

Model Implementation Conformance Statement

for the IEC 61850 interface in MRMV4

Version 2.0.u

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# INDEX

<b>INDEX.....</b>	<b>2</b>
<b>Introduction.....</b>	<b>5</b>
<b>Logical Nodes.....</b>	<b>6</b>
1.1. Logical Nodes List.....	6
1.2. Logical Node definitions.....	6
1.2.1 WW_CILO1.....	6
1.2.2 WW_CSWI1.....	6
1.2.3 WW_IHMI1.....	6
1.2.4 WW_GGIO1.....	6
1.2.5 WW_GGIO2.....	6
1.2.6 WW_LLN0CON.....	6
1.2.7 WW_LLN0PRO.....	6
1.2.8 WW_LLN0MEA.....	6
1.2.9 WW_LLN0REC.....	7
1.2.10 WW_LLN0SYS.....	7
1.2.11 WW_LPHDCON.....	7
1.2.12 WW_LPHDMEA.....	7
1.2.13 WW_LPHDPRO.....	7
1.2.14 WW_LPHDREC.....	7
1.2.15 WW_LPHDSYS.....	7
1.2.16 WW_MMXU1.....	7
1.2.17 WW_MMXU2.....	7
1.2.18 WW_MMXU3.....	7
1.2.19 WW_MSTA1.....	7
1.2.20 WW_MSTA2.....	7
1.2.21 WW_MSTA3.....	7
1.2.22 WW_PDOP1.....	7
1.2.23 WW_PDUP1.....	7
1.2.24 WW_PFRC1.....	7
1.2.25 WW_PIOC1.....	7
1.2.26 WW_PMRI1.....	7
1.2.27 WW_PMSS1.....	7

1.2.28	WW_PPAM1.....	7
1.2.29	WW_PTOC1.....	7
1.2.30	WW_PTOC3.....	7
1.2.31	WW_PTOC4.....	7
1.2.32	WW_PTOF1.....	7
1.2.33	WW_PTOV1.....	7
1.2.34	WW_PTOV2.....	7
1.2.35	WW_PTOV3.....	7
1.2.36	WW_PTTR1.....	7
1.2.37	WW_PTTR2.....	7
1.2.38	WW_PTUC1.....	7
1.2.39	WW_PTUF1.....	7
1.2.40	WW_PTUV1.....	7
1.2.41	WW_PTUV2.....	7
1.2.42	WW_PUPF1.....	7
1.2.43	WW_RBRF1.....	7
1.2.44	WW_RDRE1.....	7
1.2.45	WW_XCBR2.....	7
1.2.46	WW_XSWI1.....	7
<b>Common Data Class.....</b>	<b>8</b>	
1.3.	Common Data Class definitions.....	8
1.3.1	WW_ACD1.....	8
1.3.2	WW_ACT1.....	8
1.3.3	WW_CMV1.....	8
1.3.4	WW_DEL1.....	8
1.3.5	WW_DPC1.....	8
1.3.6	WW_DPC2.....	8
1.3.7	WW_DPL1.....	8
1.3.8	WW_INC1.....	8
1.3.9	WW_INS1.....	8
1.3.10	WW_INS2.....	8
1.3.11	WW_INS3.....	8
1.3.12	WW_INS5.....	8
1.3.13	WW_INS6.....	8
1.3.14	WW_LPL1.....	8

1.3.15	WW_LPL2.....	8
1.3.16	WW_LPL3.....	8
1.3.17	WW_MV1.....	8
1.3.18	WW_SPC1.....	8
1.3.19	WW_SPS1.....	8
1.3.20	WW_WYE1.....	8
1.4.	Common Data Attributes type definitions.....	8
1.4.1	WW_analogValue1.....	8
1.4.2	WW_Cancel1.....	8
1.4.3	WW_Oper1.....	8
1.4.4	WW_origin1.....	8
1.4.5	WW_units1.....	8
1.4.6	WW_vector1.....	8
1.5.	Enumerated type definitions.....	8
1.5.1	Beh.....	8
1.5.2	CBOpCap.....	8
1.5.3	ctlModel.....	8
1.5.4	Dbpos.....	8
1.5.5	dir.....	8
1.5.6	Health.....	8
1.5.7	Mod.....	8
1.5.8	MotorCycle.....	8
1.5.9	multiplier.....	8
1.5.10	orCategory.....	8
1.5.11	sboClass.....	8
1.5.12	SIUnit.....	8
<b>Logical Node Extensions.....</b>	<b>9</b>	
1.6.	New Logical Nodes .....	9
1.6.1	WW_PSOF1 Switch Onto Fault Protection.....	9
1.7.	Extended Logical Nodes.....	10
1.7.1	WW_MSTA1 Metering Statistics .....	10
1.7.2	WW_MSTA2 Metering Statistics .....	10
1.7.3	WW_MSTA3 Metering Statistics .....	10
1.7.4	WW_PMSS1 Motor starting time supervision.....	10

<b>Appendix – Register Maps.....</b>	<b>11</b>
1.8.    Device Planing Dependencies.....	11

# Introduction

This model implementation conformance statement is applicable to the device **MRMV4**, Release X.X (Firmware-Build **19933**).

This MICS document specifies the modelling extensions compared to IEC 61850 edition 1.

Clause 2 contains the list of implemented logical nodes.

Clause 2 contains the list of implemented common data classes.

Clause 3 describes the new and extended logical nodes and the new and extended common data classes.

Clause 4 describes the new and extended enum types.

# Logical Nodes

## 1.1. Logical Nodes List

The following table contains the list of logical nodes implemented in the device:

<b>L : System Logical Nodes</b>
<b>LLN0</b> (Logical Node device)
<b>LPHD</b> (Physical device)
<b>P : Logical Nodes for protection functions</b>
<b>PDOP</b> (Directional overpower)
<b>PDUP</b> (Directional underpower)
<b>PFRC</b> (Rate of change of frequency)
<b>PIOC</b> (Instantaneous overcurrent)
<b>PMRI</b> (Motor restart inhibition)
<b>PMSS</b> (Motor starting time supervision)
<b>PPAM</b> (Phase angle or out-of-step protection)
<b>PSOF</b> (Switch Onto Fault)
<b>PTOC</b> (Time overcurrent)
<b>PTOF</b> (Overfrequency)
<b>PTOV</b> (Overvoltage)
<b>PTTR</b> (Thermal overload protection)
<b>PTUC</b> (Undercurrent)
<b>PTUF</b> (Underfrequency)
<b>PTUV</b> (Undervoltage)
<b>PUPF</b> (Underpower factor)
<b>R : Logical Nodes for protection related functions</b>
<b>RBRF</b> (Breaker failure)
<b>RDRE</b> (Disturbance recorder function)
<b>G : Logical Nodes for generic references</b>
<b>GGIO</b> (Generic process I/O)
<b>M : Logical Nodes for metering and measurement</b>
<b>MMXU</b> (Measurement)
<b>MSTA</b> (Metering Statistics)
<b>X : Logical Nodes for switchgear</b>
<b>XCBR</b> (Circuit Breaker)

<b>XSWI</b> (Circuit Switch)
<b>C : Logical Nodes for control</b>
<b>CILO</b> (Interlocking)
<b>CSWI</b> (Switch controller)
<b>I : Logical Nodes for interfacing and archiving</b>
<b>IHMI</b> (Human machine interface)

## 1.2. Logical Node definitions

<b>LN Type</b>	<b>LN Class</b>	<b>Description</b>
WW_CILO1	CILO	Interlocking
WW_CSWI1	CSWI	Switch controller
WW_GGIO1	GGIO	Generic process I/O
WW_GGIO2	GGIO	Generic process I/O
WW_IHMI1	IHMI	Human machine interface
WW_LLN0CON	LLN0	Logical Node device
WW_LLN0MEA	LLN0	Logical Node device
WW_LLN0PRO	LLN0	Logical Node device
WW_LLN0REC	LLN0	Logical Node device
WW_LLN0SYS	LLN0	Logical Node device
WW_LPHDCON	LPHD	Physical device
WW_LPHDMEA	LPHD	Physical device
WW_LPHDPRO	LPHD	Physical device
WW_LPHDREC	LPHD	Physical device
WW_LPHDSYS	LPHD	Physical device
WW_MMXU1	MMXU	Measurement
WW_MMXU2	MMXU	Measurement
WW_MMXU3	MMXU	Measurement
WW_MSTA1	MSTA	Metering Statistics
WW_MSTA2	MSTA	Metering Statistics
WW_MSTA3	MSTA	Metering Statistics
WW_PDOP1	PDOP	Directional overpower
WW_PDUP1	PDUP	Directional underpower
WW_PFRC1	PFRC	Rate of change of frequency
WW_PIOC1	PIOC	Instantaneous overcurrent
WW_PMRI1	PMRI	Motor restart inhibition

<b>LN Type</b>	<b>LN Class</b>	<b>Description</b>
WW_PMSS1	PMSS	Motor starting time supervision
WW_PPAM1	PPAM	Phase angle or out-of-step protection
WW_PSOF1	PSOF	Switch Onto Fault
WW_PTOC1	PTOC	Time overcurrent
WW_PTOC3	PTOC	Time overcurrent
WW_PTOC4	PTOC	Time overcurrent
WW_PTOF1	PTOF	Overfrequency
WW_PTOV1	PTOV	Oversupply
WW_PTOV2	PTOV	Oversupply
WW_PTOV3	PTOV	Oversupply
WW_PTTR1	PTTR	Thermal overload protection
WW_PTTR2	PTTR	Thermal overload protection
WW_PTUC1	PTUC	Undervoltage
WW_PTUF1	PTUF	Underfrequency
WW_PTUV1	PTUV	Undervoltage
WW_PTUV2	PTUV	Undervoltage
WW_PUPF1	PUPF	Underpower factor
WW_RBPF1	RBPF	Breaker failure
WW_RDRE1	RDRE	Disturbance recorder function
WW_XCBR2	XCBR	Circuit Breaker
WW_XSWI1	XSWI	Circuit Switch

### 1.2.1 WW\_CILO1

<b>CILO class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
CILO		Interlocking		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	

NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
EnaOpn	WW_SPS1	Enable Open	M	
EnaCls	WW_SPS1	Enable Close	M	

## 1.2.2 WW\_CSWI1

<b>CSWI class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
CSWI		Switch Controller		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Loc	WW_SPS1	Local operation	O	
<b>Controls</b>				
Pos	WW_DPC2	Switch position	M	

## 1.2.3 WW\_IHMI1

<b>IHMI class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
IHMI		Human machine interface		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only

Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	

## 1.2.4 WW\_GGIO1

<b>GGIO class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
GGIO		Generic process I/O		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status information</b>				
Ind1	WW_SPS1	General indication (binary input)	O	
Ind2	WW_SPS1	General indication (binary input)	O	
Ind3	WW_SPS1	General indication (binary input)	O	
Ind4	WW_SPS1	General indication (binary input)	O	
Ind5	WW_SPS1	General indication (binary input)	O	
Ind6	WW_SPS1	General indication (binary input)	O	
Ind7	WW_SPS1	General indication (binary input)	O	
Ind8	WW_SPS1	General indication (binary input)	O	
Ind9	WW_SPS1	General indication (binary input)	O	
Ind10	WW_SPS1	General indication (binary input)	O	
Ind11	WW_SPS1	General indication (binary input)	O	
Ind12	WW_SPS1	General indication (binary input)	O	
Ind13	WW_SPS1	General indication (binary input)	O	
Ind14	WW_SPS1	General indication (binary input)	O	
Ind15	WW_SPS1	General indication (binary input)	O	

Ind16	WW_SPS1	General indication (binary input)	O	
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## 1.2.5 WW\_GGIO2

GGIO class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
GGIO		Generic process I/O		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status information				
Ind1	WW_SPS1	General indication (binary input)	O	
Ind2	WW_SPS1	General indication (binary input)	O	
Ind3	WW_SPS1	General indication (binary input)	O	
Ind4	WW_SPS1	General indication (binary input)	O	
Ind5	WW_SPS1	General indication (binary input)	O	
Ind6	WW_SPS1	General indication (binary input)	O	
Ind7	WW_SPS1	General indication (binary input)	O	
Ind8	WW_SPS1	General indication (binary input)	O	
Ind9	WW_SPS1	General indication (binary input)	O	
Ind10	WW_SPS1	General indication (binary input)	O	
Ind11	WW_SPS1	General indication (binary input)	O	
Ind12	WW_SPS1	General indication (binary input)	O	
Ind13	WW_SPS1	General indication (binary input)	O	
Ind14	WW_SPS1	General indication (binary input)	O	
Ind15	WW_SPS1	General indication (binary input)	O	
Ind16	WW_SPS1	General indication (binary input)	O	

## 1.2.6 WW\_LLN0CON

LLN0 class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
LLN0		Logical Node device		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Direct-with-normal-security
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL2	Name plate	M	

## 1.2.7 WW\_LLN0PRO

LLN0 class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
LLN0		Logical Node device		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Direct-with-normal-security
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL2	Name plate	M	

## 1.2.8 WW\_LLN0MEA

LLN0 class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
LLN0		Logical Node device		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Direct-with-normal-security
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL2	Name plate	M	

## 1.2.9 WW\_LLN0REC

LLN0 class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
LLN0		Logical Node device		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Direct-with-normal-security
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL2	Name plate	M	

## 1.2.10 WW\_LLNO SYS

<b>LLN0 class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
LLN0		Logical Node device		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Direct-with-normal-security
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL2	Name plate	M	

## 1.2.11 WW\_LPHDCON

<b>LPHD class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
LPHD		Physical device information		
<b>Data</b>				
<b>Common Logical Node Information</b>				
PhyNam	WW_DPL1	Physical device name plate	M	
PhyHealth	WW_INS3	Physical Device Health	M	
Proxy	WW_SPS1	Indicates if this LN is a proxy	M	

## **1.2.12 WW\_LPHDMEA**

<b>LPHD class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
LPHD		Physical device information		
<b>Data</b>				
<b>Common Logical Node Information</b>				
PhyNam	WW_DPL1	Physical device name plate	M	
PhyHealth	WW_INS3	Physical Device Health	M	
Proxy	WW_SPS1	Indicates if this LN is a proxy	M	

## **1.2.13 WW\_LPHDPRO**

<b>LPHD class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
LPHD		Physical device information		
<b>Data</b>				
<b>Common Logical Node Information</b>				
PhyNam	WW_DPL1	Physical device name plate	M	
PhyHealth	WW_INS3	Physical Device Health	M	
Proxy	WW_SPS1	Indicates if this LN is a proxy	M	

## **1.2.14 WW\_LPHDREC**

<b>LPHD class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
LPHD		Physical device information		
<b>Data</b>				
<b>Common Logical Node Information</b>				
PhyNam	WW_DPL1	Physical device name plate	M	
PhyHealth	WW_INS3	Physical Device Health	M	
Proxy	WW_SPS1	Indicates if this LN is a proxy	M	

## **1.2.15 WW\_LPHDSYS**

<b>LPHD class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
LPHD		Physical device information		
<b>Data</b>				
<b>Common Logical Node Information</b>				
PhyNam	WW_DPL1	Physical device name plate	M	
PhyHealth	WW_INS3	Physical Device Health	M	
Proxy	WW_SPS1	Indicates if this LN is a proxy	M	

## **1.2.16 WW\_MMXU1**

<b>MMXU class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
MMXU		Measurement		

<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Measured values</b>				
A	WW_WYE1	Phase currents (IL1, IL2, IL3)	O	

## 1.2.17 WW\_MMXU2

<b>MMXU class</b>				
<b>Attribute</b>	<b>Attribute</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
<b>Name</b>	<b>Type</b>			
MMXU		Measurement		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Measured values</b>				
PPV	WW_DEL1	Phase to phase voltages (UL12, UL23, UL31)	O	
PhV	WW_WYE1	Phase to ground voltages (UL1, UL2, UL3)	O	

## 1.2.18 WW\_MMXU3

MMXU class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
MMXU		Measurement		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Measured values				
TotW	WW_MV1	Total Active Power (Total P)	O	
TotVAr	WW_MV1	Total Reactive Power (Total Q)	O	
TotVA	WW_MV1	Total Apparent Power (Total S)	O	
TotPF	WW_MV1	Total Power factor (Total PF)	O	

## 1.2.19 WW\_MSTA1

MSTA class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
MSTA		Metering Statistics		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Metered values				
AvAPhA	WW_MV1	Average current IL1	O	
AvAPhB	WW_MV1	Average current IL2	O	

AvAPhC	WW_MV1	Average current IL3	O	
MaxAPhA	WW_MV1	Maximum current IL1	O	
MaxAPhB	WW_MV1	Maximum current IL2	O	
MaxAPhC	WW_MV1	Maximum current IL3	O	
MinAPhA	WW_MV1	Minimum current IL1	O	
MinAPhB	WW_MV1	Minimum current IL2	O	
MinAPhC	WW_MV1	Minimum current IL3	O	

## 1.2.20 WW\_MSTA2

MSTA class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
MSTA		Metering Statistics		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Metered values				
AvVPhAB	WW_MV1	Average voltage UL12	O	
AvVPhBC	WW_MV1	Average voltage UL23	O	
AvVPhCA	WW_MV1	Average voltage UL31	O	
MaxVPhAB	WW_MV1	Maximum voltage UL12	O	
MaxVPhBC	WW_MV1	Maximum voltage UL23	O	
MaxVPhCA	WW_MV1	Maximum voltage UL31	O	
MinVPhAB	WW_MV1	Minimum voltage UL12	O	
MinVPhBC	WW_MV1	Minimum voltage UL23	O	
MinVPhCA	WW_MV1	Minimum voltage UL31	O	
AvVPhA	WW_MV1	Average voltage UL1	O	
AvVPhB	WW_MV1	Average voltage UL2	O	
AvVPhC	WW_MV1	Average voltage UL3	O	
MaxVPhA	WW_MV1	Maximum voltage UL1	O	

MaxVPhB	WW_MV1	Maximum voltage UL2	O	
MaxVPhC	WW_MV1	Maximum voltage UL3	O	
MinVPhA	WW_MV1	Minimum voltage UL1	O	
MinVPhB	WW_MV1	Minimum voltage UL2	O	
MinVPhB	WW_MV1	Minimum voltage UL3	O	

### 1.2.21 WW\_MSTA3

MSTA class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
MSTA		Metering Statistics		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Metered values				
AvVA	WW_MV1	Average apparent power	O	
MaxVA	WW_MV1	Maximum apparent power	O	
MinVA	WW_MV1	Minimum apparent power	O	
Avw	WW_MV1	Average real power	O	
MaxW	WW_MV1	Maximum real power	O	
MinW	WW_MV1	Minimum real power	O	
AvVAr	WW_MV1	Average reactive power	O	
MaxVAr	WW_MV1	Maximum reactive power	O	
MinVAr	WW_MV1	Minimum reactive power	O	

## 1.2.22 WW\_PDOP1

<b>PDOP class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PDOP		Directional overpower		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.23 WW\_PDUP1

<b>PDUP class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PDUP		Directional underpower		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.24 WW\_PFRC1

<b>PFRC class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PFRC		Rate of change of frequency		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.25 WW\_PIOC1

<b>PIOC class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PIOC		Instantaneous overcurrent		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.26 WW\_PMRI1

PMRI class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PMRI		Motor restart inhibition		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Op	WW_ACT1	Operate	O	

## 1.2.27 WW\_PMSS1

PMSS class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PMSS		Motor starting time supervision		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Op	WW_ACT1	Operate	O	
MotCyc	WW_INS6	Motor Cycle	E	

## 1.2.28 WW\_PPAM1

PPAM class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PPAM1		Phase angle measuring		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.29 WW\_PTOC1

PTOC class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PTOC		Time overcurrent		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

### **1.2.30 WW\_PTOC3**

<b>PTOC class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTOC		Time overcurrent		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

### **1.2.31 WW\_PTOC4**

<b>PTOC class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTOC		Time overcurrent		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.32 WW\_PTOF1

PTOF class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PTOF		Overfrequency		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.33 WW\_PTOV1

PTOV class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PTOV		Overvoltage		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Str	WW_ACD1	Start	M	

Op	WW_ACT 1	Operate	M	
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### 1.2.34 WW\_PTOV2

PTOV class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PTOV		Overvoltage		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Str	WW_ACD 1	Start	M	
Op	WW_ACT 1	Operate	M	

### 1.2.35 WW\_PTOV3

PTOV class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PTOV		Overvoltage		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	

NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD 1	Start	M	
Op	WW_ACT1	Operate	M	

### 1.2.36 WW\_PTTR1

<b>PTTR class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTTR		Thermal overload		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Op	WW_ACT1	Operate	M	

### 1.2.37 WW\_PTTR2

<b>PTTR class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTTR		Thermal overload		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	

NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD 1	Start	O	
Op	WW_ACT1	Operate	M	

### 1.2.38 WW\_PTUC1

<b>PTUC class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTUC		Undercurrent		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD 1	Start	M	
Op	WW_ACT1	Operate	M	

### 1.2.39 WW\_PTUF1

<b>PTUF class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTUF		Underfrequency		
<b>Data</b>				
<b>Common Logical Node Information</b>				

Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.40 WW\_PTUV1

<b>PTUV class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTUV		Undervoltage		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.41 WW\_PTUV2

<b>PTUV class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PTUV		Undervoltage		
<b>Data</b>				

<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.42 WW\_PUPF1

<b>PUPF class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
PUPF		Underpower factor		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	M	
Op	WW_ACT1	Operate	M	

## 1.2.43 WW\_RBRF1

<b>RBRF class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
RBRF		Breaker failure		

<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Str	WW_ACD1	Start	O	
OpEx	WW_ACT1	Breaker failure trip	O	

## 1.2.44 WW\_RDRE1

<b>RDRE class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
RDRE		Disturbance recorder function		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
RcdMade	WW_SPS1	Recording made	M	
FltNum	WW_INS2	Fault Number	M	
GriFltNum	WW_INS2	Grid Fault Number	O	
RcdStr	WW_SPS1	Recording startet	O	

## 1.2.45 WW\_XCBR2

<b>XCBR class</b>
-------------------

<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
XCBR		Circuit Breaker		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Loc	WW_SPS1	Local operation	M	
OpCnt	WW_INS2	Operation counter	M	
<b>Status Information</b>				
CBOpCap	WW_INS5	Circuit breaker operating capability	M	
<b>Controls</b>				
Pos	WW_DPC1	Switch position	M	
BlkOpn	WW_SPC1	Block opening	M	
BlkCls	WW_SPC1	Block closing	M	

## 1.2.46 WW\_XSWI1

<b>XCBR class</b>				
<b>Attribute Name</b>	<b>Attribute Type</b>	<b>Explanation</b>	<b>M/O/E</b>	<b>Remarks</b>
XSWI		Circuit switch		
<b>Data</b>				
<b>Common Logical Node Information</b>				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Loc	WW_SPS1	Local operation	M	
OpCnt	WW_INS2	Operation counter	M	

<b>Status Information</b>				
SwTyp	WW_INS5	Switch type	M	
SwOpCap	WW_INS5	Switch operating capability	M	
<b>Controls</b>				
Pos	WW_DPC1	Switch position	M	
BlkOpn	WW_SPC1	Block opening	M	
BlkCls	WW_SPC1	Block closing	M	

# Common Data Class

## 1.3. Common Data Class definitions

The following table contains the list of Common Data Class implemented in the device:

CDC Type	CDC Class	Description
WW_ACD1	ACD	Directional Protection activation information
WW_ACT1	ACT	Protection Activation Information
WW_analogValue1	analogValue	Analogue value
WW_Cancel1	Cancel	Cancel operating
WW_CMV1	CMV	Complex measured value
WW_DEL1	DEL	Delta
WW_DPC1	DPC	Controllable Double Point
WW_DPC2	DPC	Controllable Double Point
WW_DPL1	DPL	Device name plate
WW_INC1	INC	Controllable Integer Status
WW_INS1	INS	Integer Status
WW_INS2	INS	Integer Status
WW_INS3	INS	Integer Status
WW_INS5	INS	Integer Status
WW_INS6	INS	Integer Status
WW_LPL1	LPL	Logical node name plate
WW_LPL2	LPL	Logical node name plate
WW_LPL3	LPL	Logical node name plate
WW_MV1	MV	Measured Value
WW_Oper1	Oper	Start>Select operating
WW_origin1	origin	Originator
WW_SPC1	SPC	Controllable Single Point
WW_SPS1	SPS	Single Point Status
WW_units1	units	Unit definition
WW_vector1	vector	Vector definition
WW_WYE1	WYE	Phase to ground related measured values of a three phase system

### 1.3.1 WW\_ACD1

ACD class							
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks	
General	BOOLEAN	ST	dchg		M		
dirGeneral	Enum	ST	dchg	ACDdir	M		
q	Quality	ST	qchg		M		
t	Timestamp	ST			M		

### 1.3.2 WW\_ACT1

ACT class							
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks	
General	BOOLEAN	ST	dchg		M		
q	Quality	ST	qchg		M		
t	Timestamp	ST			M		

### 1.3.3 WW\_CMV1

CMV class							
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks	
cVal	Struct	MX		WW_vector1	M		
q	Quality	ST	qchg		M		
t	Timestamp	ST			M		
instCVal	Struct	MX		WW_vector1	O		
units	Struct	CF		WW_units1	O		
db	INT32U	CF			O		

### 1.3.4 WW\_DEL1

DPC class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
phsAB	WW_CMV1					
phsBC	WW_CMV1					
phsCA	WW_CMV1					

### 1.3.5 WW\_DPC1

DPC class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	Dbpos	ST	dchg	Dbpos	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	
ctlModel	Enum	CF		ctlmodel	M	

### 1.3.6 WW\_DPC2

DPC class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
origin	Struct	ST		WW_origin1	O	
ctlNum	INT8U	ST			O	
stVal	Dbpos	ST	dchg	Dbpos	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	
stSeld	BOOLEAN	ST	dchg		O	
ctlModel	Enum	CF		ctlmodel	M	
sboTimeout	INT32U	CF			O	
sboClass	Enum	CF		sboClass	O	

cdcNs	VisString255	EX			O	
Oper	Struct	CO		WW_Oper1		
SBOw	Struct	CO		WW_Oper1		
Cancel	Struct	CO		WW_Cancel1		

### 1.3.7 WW\_DPL1

DPL class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
vendor	VISIBLE STRING255	DC			M	

### 1.3.8 WW\_INC1

INC class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	Enum	ST	dchg	Mode	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	
ctlModel	Enum	CF		ctlModel	M	

### 1.3.9 WW\_INS1

INS class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	Enum	ST	dchg	Behaviour	M	
q	Quality	ST	qchg		M	

t	Timestamp	ST			M	
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### 1.3.10 WW\_INS2

INS class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	INT32	ST	dchg		M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	

### 1.3.11 WW\_INS3

INS class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	Enum	ST	dchg	Health	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	

### 1.3.12 WW\_INS5

INS class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	Enum	ST	dchg	CBOpCap	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	

### 1.3.13 WW\_INS6

INS class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	Enum	ST	dchg	MotorCycle	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	

### 1.3.14 WW\_LPL1

LPL class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
vendor	visString255	DC			M	
swRev	visString255	DC			M	
d	visString255	DC			M	

### 1.3.15 WW\_LPL2

LPL class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
vendor	visString255	DC			M	
swRev	visString255	DC			M	
d	visString255	DC			M	
ldNs	visString255	EX				

### 1.3.16 WW\_LPL3

LPL class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
vendor	visString255	DC			M	
swRev	visString255	DC			M	
d	visString255	DC			M	
ldNs	visString255	EX				

### 1.3.17 WW\_MV1

MV class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
mag	Struct	MX		WW_analogValue1	M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	
units	Struct	CF		WW_units1	O	
db	INT32U	CF			O	
d	visString255	DC			O	
dataNs	visString255	DC			O	

### 1.3.18 WW\_SPC1

SPC class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
ctlModel	Enum	CF		ctlModel	M	

### 1.3.19 WW\_SPS1

SPS class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
stVal	BOOLEAN	ST	dchg		M	
q	Quality	ST	qchg		M	
t	Timestamp	ST			M	

### 1.3.20 WW\_WYE1

WYE class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
phsAB	WW_CMV1					
phsBC	WW_CMV1					
phsCA	WW_CMV1					
neut	WW_CMV1					

## 1.4. Common Data Attributes type definitions

### 1.4.1 WW\_analogValue1

analogvalue class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
f	FLOAT32	MX			M	

### 1.4.2 WW\_Cancel1

Cancel class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
ctlval	BOOLEAN	CO			M	
origin	Struct	ST		WW_origin1	O	
ctlNum	INT8U	ST			O	
T	Timestamp	CO			O	
Test	BOOLEAN	CO			O	

### 1.4.3 WW\_Oper1

Oper class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
ctlval	BOOLEAN	CO			M	
origin	Struct	ST		WW_origin1	O	
ctlNum	INT8U	ST			O	
T	Timestamp	CO			O	
Test	BOOLEAN	CO			O	
Check	Check	CO			O	

### 1.4.4 WW\_origin1

origin class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
orCat	Enum	ST		orCategory	M	
orldent	Octet64	ST			M	

## 1.4.5 WW\_units1

unit class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
SIUnit	Enum			SIUnit	M	
multiplier	Enum			multiplier	O	

## 1.4.6 WW\_vector1

vector class						
Attribute Name	Attribute Type	FC	TrgOp	Value/Value range	M/O/E	Remarks
mag	Struct			WW_analogValue1	M	
ang	Struct			WW_analogValue1	O	

## 1.5. Enumerated type definitions

### 1.5.1 Beh

Ordinal	Semantic
1	on
2	blocked
3	test
4	test/blocked
5	off

### **1.5.2 CBOpCap**

Ordinal	Semantic
1	None
2	Open
3	Close-Open
4	Open-Close-Open
5	Close-Open-Close-Open

### **1.5.3 ctlModel**

Ordinal	Semantic
1	status-only
2	direct-with-normal-security
3	sbo-with-normal-security
4	direct-with-enhanced-security
5	sbo-with-enhanced-security

### **1.5.4 Dbpos**

Ordinal	Semantic
1	intermediate
2	off
3	on
4	bad

### **1.5.5 dir**

Ordinal	Semantic

1	<b>unknown</b>
2	<b>forward</b>
3	<b>backward</b>
4	<b>both</b>

### 1.5.6 Health

Ordinal	Semantic
1	<b>Ok</b>
2	<b>Warning</b>
3	<b>Alarm</b>

### 1.5.7 Mod

Ordinal	Semantic
1	<b>on</b>
2	<b>blocked</b>
3	<b>test</b>
4	<b>test/block</b>
5	<b>off</b>

### 1.5.8 MotorCycle

Ordinal	Semantic
0	<b>Trip/Off</b>
1	<b>Stop</b>
2	<b>Start</b>
3	<b>Run</b>

### 1.5.9 multiplier

Ordinal	Semantic

-24	y
-21	z
-18	a
-15	f
-12	p
-9	n
-6	μ
-3	m
-2	c
-1	d
0	
1	da
2	h
3	k
6	M
9	G
12	T
15	P
18	E
21	Z
24	Y

### 1.5.10 orCategory

Ordinal	Semantic
0	not-supported
1	bay-control
2	station-control
3	remote-control
4	automatic-bay
5	automatic-station
6	automatic-remote
7	maintenance
8	process

### 1.5.11 sboClass

Ordinal	Semantic
0	operate-once
1	operate-many

### 1.5.12 SIUnit

Ordinal	Semantic
1	none
2	m
3	kg
4	s
5	A
6	K
7	mol
8	cd
9	deg
10	rad
11	sr
21	Gy
22	q
23	°C
24	Sv
25	F
26	C
27	S
28	H
29	V
30	ohm
31	J
32	N
33	Hz
34	Ix
35	Lm

36	Wb
37	T
38	W
39	Pa
41	$m^2$
42	$m^3$
43	m/s
44	$m/s^2$
45	$m^3/s$
46	$m/m^3$
47	M
48	kg/m <sup>3</sup>
49	$m^2/s$
50	W/m K
51	J/K
52	ppm
53	1/s
54	rad/s
61	VA
62	Watts
63	VAr
64	phi
65	cos(phi)
66	Vs
67	$V^2$
68	As
69	$A^2$
70	$A^2t$
71	VAh
72	Wh
73	VArh
74	V/Hz

# Logical Node Extensions

The following table use

- M : Data is mandatory in the IEC-61850-7-4.
- O: Data is optional in the IEC-61850-7-4 and is used in the device.
- E: Data is an extension to the IEC-61850-7-4.
- 

## 1.6. New Logical Nodes

New logical nodes have the InNs attribute in the Name plate. The value of InNs is a reference to the MICS document.

### 1.6.1 WW\_PSOF1 Switch Onto Fault Protection

PSOF class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
WW_PSOF1		Protection Switch Onto fault		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Status Information				
Str	WW_ACD1	Start	M	

## 1.7. Extended Logical Nodes

The following logical nodes have been extended with extra data. All extra data has been highlighted in the tables and marked as “E” (Extended), these data contains the “dataNs” attribute.

### 1.7.1 WW\_MSTA1 Metering Statistics

MSTA class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
WW_MSTA1		Metering Statistics		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Metered Values				
AvAPhA	WW_MV	Average current IL1	E	
AvAPhB	WW_MV	Average current IL2	E	
AvAPhC	WW_MV	Average current IL3	E	
MaxAPhA	WW_MV	Maximum current IL1	E	
MaxAPhB	WW_MV	Maximum current IL2	E	
MaxAPhC	WW_MV	Maximum current IL3	E	
MinAPhA	WW_MV	Minimum current IL1	E	
MinAPhB	WW_MV	Minimum current IL2	E	
MinAPhC	WW_MV	Minimum current IL3	E	

## 1.7.2 WW\_MSTA2 Metering Statistics

MSTA class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
WW_MSTA2		Metering Statistics		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Metered Values				
AvVPhAB	WW_MV	Average voltage UL12	E	
AvVPhBC	WW_MV	Average voltage UL23	E	
AvVPhCA	WW_MV	Average voltage UL31	E	
MaxVPhAB	WW_MV	Maximum voltage UL12	E	
MaxVPhBC	WW_MV	Maximum voltage UL23	E	
MaxVPhCA	WW_MV	Maximum voltage UL31	E	
MinVPhAB	WW_MV	Minimum voltage UL12	E	
MinVPhBC	WW_MV	Minimum voltage UL23	E	
MinVPhCA	WW_MV	Minimum voltage UL31	E	
AvVPhA	WW_MV	Average voltage UL1	E	
AvVPhB	WW_MV	Average voltage UL2	E	
AvVPhC	WW_MV	Average voltage UL3	E	
MaxVPhA	WW_MV	Maximum voltage UL1	E	
MaxVPhB	WW_MV	Maximum voltage UL2	E	
MaxVPhC	WW_MV	Minimum voltage UL3	E	
MinVPhA	WW_MV	Minimum voltage UL1	E	
MinVPhB	WW_MV	Minimum voltage UL2	E	
MinVPhC	WW_MV	Minimum voltage UL3	E	

### 1.7.3 WW\_MSTA3 Metering Statistics

MSTA class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
WW_MSTA3		Metering Statistics		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only
Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
Metered Values				
AvVA	WW_MV	Average apparent power	O	
MaxVA	WW_MV	Maximum apparent power	O	
MinVA	WW_MV	Minimum apparent power	O	
AvW	WW_MV	Average real power	E	
MaxW	WW_MV	Maximum real power	E	
MinW	WW_MV	Minimum real power	E	
AvVAr	WW_MV	Average reactive power	E	
MaxVAr	WW_MV	Maximum reactive power	E	
MinVAr	WW_MV	Minimum reactive power	E	

### 1.7.4 WW\_PMSS1 Motor starting time supervision

PMSS class				
Attribute Name	Attribute Type	Explanation	M/O/E	Remarks
PMSS		Motor starting time supervision		
Data				
Common Logical Node Information				
Mod	WW_INC1	Mode	M	Status-only

Beh	WW_INS1	Behaviour	M	
Health	WW_INS3	Health	M	
NamPlt	WW_LPL1	Name plate	M	
<b>Status Information</b>				
Op	WW_ACT1	Operate	O	
MotCyc	WW_INS6	Motor Cycle	E	

## Appendix – Register Maps

LDevice::CTRL

Logical Node	Data Object	Module.Name
<b>CILO1* (WW_CILO1 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	EnaOpn	SG.Interl OFF
	EnaCls	SG.Interl ON
<b>CSWI1* (WW_CSWI1 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	Loc	
	Pos	SG.Pos
<b>LLN0 (WW_LLNOCON )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
<b>LPHD1 (WW_LPHDCON )</b>		
	PhyNam	
	PhyHealth	
	Proxy	
<b>XCBR1* (WW_XCBR2 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	Loc	
	OpCnt	
	Pos	SG.Pos
	BlkOpn	

LDevice::CTRL

	BlkClS	
	CBOpCap	
<b>XSWI1* (WW_XSWI1 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	Loc	
	OpCnt	
	Pos	SG.Pos
	BlkOpn	
	BlkClS	
	SwTyp	
	SwOpCap	

LDevice::DR

Logical Node	Data Object	Module.Name
<b>LLN0 (WW_LLN0REC )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
<b>LPHD1 (WW_LPHDREC )</b>		
	PhyNam	
	PhyHealth	
	Proxy	
<b>RDRE1 (WW_RDRE1 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	RcdMade	Disturb rec.recording
	FltNum	
	GriFltNum	
	RcdStr	Disturb rec.recording

LDevice::CTRL

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LDevice::EXT

Logical Node	Data Object	Module.Name
InGGIO1 (WW_GGIO1 )		
	Mod	
	Beh	
	Health	
	NamPlt	
	Ind1	
	Ind2	
	Ind3	
	Ind4	
	Ind5	
	Ind6	
	Ind7	
	Ind8	
	Ind9	
	Ind10	
	Ind11	
	Ind12	
	Ind13	
	Ind14	
	Ind15	
	Ind16	
LLN0 (WW_LLNO0SYS )		
	Mod	
	Beh	
	Health	
	NamPlt	
LPHD1 (WW_LPHDSYS )		
	PhyNam	
	PhyHealth	
	Proxy	
OutGGIO1 (WW_GGIO2 )		
	Mod	

LDevice::CTRL

	Beh	
	Health	
	NamPlt	
	Ind1	IEC61850.VirtOut1-I
	Ind2	IEC61850.VirtOut2-I
	Ind3	IEC61850.VirtOut3-I
	Ind4	IEC61850.VirtOut4-I
	Ind5	IEC61850.VirtOut5-I
	Ind6	IEC61850.VirtOut6-I
	Ind7	IEC61850.VirtOut7-I
	Ind8	IEC61850.VirtOut8-I
	Ind9	IEC61850.VirtOut9-I
	Ind10	IEC61850.VirtOut10-I
	Ind11	IEC61850.VirtOut11-I
	Ind12	IEC61850.VirtOut12-I
	Ind13	IEC61850.VirtOut13-I
	Ind14	IEC61850.VirtOut14-I
	Ind15	IEC61850.VirtOut15-I
	Ind16	IEC61850.VirtOut16-I

LDevice::MEAS

Logical Node	Data Object	Module.Name
<b>LLN0 (WW_LLN0MEA )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
<b>LPHD1 (WW_LPHDMEA )</b>		
	PhyNam	
	PhyHealth	
	Proxy	
<b>MMXU1 (WW_MMXU2 )</b>		
	Mod	
	Beh	
	Health	

LDevice::CTRL

	NamPlt	
	PPV	VT.VL12 RMS VT.phi VL12 VT.VL23 RMS VT.phi VL23 VT.VL31 RMS VT.phi VL31
	PhV	VT.VL1 RMS VT.phi VL1 VT.VL2 RMS VT.phi VL2 VT.VL3 RMS VT.phi VL3 VT.VX meas RMS VT.phi VX meas VT.VG calc RMS VT.phi VG calc
<b>MMXU2 (WW_MMXU1 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	A	Current.IL1 RMS Current.phi IL1 Current.IL2 RMS Current.phi IL2 Current.IL3 RMS Current.phi IL3 Current.IG meas RMS Current.phi IG meas Current.IG calc RMS Current.phi IG calc
<b>MMXU3 (WW_MMXU3 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
	TotW	PQSCr.P RMS
	TotVAr	PQSCr.Q RMS
	TotVA	PQSCr.S RMS
	TotPF	PQSCr.cos phi RMS
<b>MSTA1 (WW_MSTA2 )</b>		
	Mod	

LDevice::CTRL

	Beh	
	Health	
	NamPlt	
	MaxVPhsAB	VT.VL12 max
	MaxVPhsBC	VT.VL23 max
	MaxVPhsCA	VT.VL31 max
	MinVPhsAB	VT.VL12 min
	MinVPhsBC	VT.VL23 min
	MinVPhsCA	VT.VL31 min
	MaxVPhsA	VT.VL1 max
	MaxVPhsB	VT.VL2 max
	MaxVPhsC	VT.VL3 max
	MinVPhsA	VT.VL1 min
	MinVPhsB	VT.VL2 min
	MinVPhsC	VT.VL3 min

MSTA2 (WW\_MSTA1 )

	Mod	
	Beh	
	Health	
	NamPlt	
	AvAPhsA	Current.IL1 avg
	AvAPhsB	Current.IL2 avg
	AvAPhsC	Current.IL3 avg
	MaxAPhsA	Current.IL1 max
	MaxAPhsB	Current.IL2 max
	MaxAPhsC	Current.IL3 max
	MinAPhsA	Current.IL1 min
	MinAPhsB	Current.IL2 min
	MinAPhsC	Current.IL3 min

MSTA3 (WW\_MSTA3 )

	Mod	
	Beh	
	Health	
	NamPlt	
	AvVA	PQSCr.S avg
	MaxVA	PQSCr.S max

LDevice::CTRL

	MinVA	PQSCr.S min
	AvW	PQSCr.P avg
	MaxW	PQSCr.P max
	MinW	PQSCr.P min
	AvVAr	PQSCr.Q avg
	MaxVAr	PQSCr.Q max
	MinVAr	PQSCr.Q min

LDevice::PROT

Logical Node	Data Object	Module.Name
<b>IHMI1 (WW_IHMI1 )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
<b>LLN0 (WW_LLN0PRO )</b>		
	Mod	
	Beh	
	Health	
	NamPlt	
<b>LPHD1 (WW_LPHDPRO )</b>		
	PhyNam	
	PhyHealth	
	Proxy	
<b>PDOP1* (WW_PDOP1 )</b>		
	Mod	PQS[1].active PQS[1].Blo TripCmd PQS[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[1].Alarm
	Op	PQS[1].Trip
<b>PDOP2* (WW_PDOP1 )</b>		
	Mod	PQS[2].active PQS[2].Blo TripCmd

LDevice::CTRL

		PQS[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[2].Alarm
	Op	PQS[2].Trip

PDOP3\* (WW\_PDOP1 )

	Mod	PQS[3].active PQS[3].Blo TripCmd PQS[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[3].Alarm
	Op	PQS[3].Trip

PDOP4\* (WW\_PDOP1 )

	Mod	PQS[4].active PQS[4].Blo TripCmd PQS[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[4].Alarm
	Op	PQS[4].Trip

PDOP5\* (WW\_PDOP1 )

	Mod	PQS[5].active PQS[5].Blo TripCmd PQS[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[5].Alarm
	Op	PQS[5].Trip

PDOP6\* (WW\_PDOP1 )

	Mod	PQS[6].active PQS[6].Blo TripCmd PQS[6].ExBlo TripCmd
	Beh	

LDevice::CTRL

	Health	
	NamPlt	
	Str	PQS[6].Alarm
	Op	PQS[6].Trip
<b>PDUP1* (WW_PDUP1 )</b>		
	Mod	PQS[1].active PQS[1].Blo TripCmd PQS[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[1].Alarm
	Op	PQS[1].Trip
<b>PDUP2* (WW_PDUP1 )</b>		
	Mod	PQS[2].active PQS[2].Blo TripCmd PQS[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[2].Alarm
	Op	PQS[2].Trip
<b>PDUP3* (WW_PDUP1 )</b>		
	Mod	PQS[3].active PQS[3].Blo TripCmd PQS[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[3].Alarm
	Op	PQS[3].Trip
<b>PDUP4* (WW_PDUP1 )</b>		
	Mod	PQS[4].active PQS[4].Blo TripCmd PQS[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	

LDevice::CTRL

	Str	PQS[4].Alarm
	Op	PQS[4].Trip

PDUP5\* (WW\_PDUP1 )

	Mod	PQS[5].active PQS[5].Blo TripCmd PQS[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[5].Alarm
	Op	PQS[5].Trip

PDUP6\* (WW\_PDUP1 )

	Mod	PQS[6].active PQS[6].Blo TripCmd PQS[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PQS[6].Alarm
	Op	PQS[6].Trip

PFRC1\* (WW\_PFRC1 )

	Mod	f[1].active f[1].Blo TripCmd f[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[1].Alarm
	Op	f[1].Trip

PFRC2\* (WW\_PFRC1 )

	Mod	f[2].active f[2].Blo TripCmd f[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[2].Alarm
	Op	f[2].Trip

LDevice::CTRL

PFRC3* (WW_PFRC1 )		
	Mod	f[3].active f[3].Blo TripCmd f[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[3].Alarm
	Op	f[3].Trip
PFRC4* (WW_PFRC1 )		
	Mod	f[4].active f[4].Blo TripCmd f[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[4].Alarm
	Op	f[4].Trip
PFRC5* (WW_PFRC1 )		
	Mod	f[5].active f[5].Blo TripCmd f[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[5].Alarm
	Op	f[5].Trip
PFRC6* (WW_PFRC1 )		
	Mod	f[6].active f[6].Blo TripCmd f[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[6].Alarm
	Op	f[6].Trip
PIOC1 (WW_PIOC1 )		
	Mod	Jam[1].active

LDevice::CTRL

		Jam[1].Blo TripCmd Jam[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	Jam[1].Alarm
	Op	Jam[1].Trip

PIOC2 (WW\_PIOC1 )

	Mod	Jam[2].active Jam[2].Blo TripCmd Jam[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	Jam[2].Alarm
	Op	Jam[2].Trip

PMRI1 (WW\_PMRI1 )

	Mod	MStart.active MStart.Blo TripCmd MStart.ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Op	MStart.Blo

PMSS1 (WW\_PMSS1 )

	Mod	MStart.active MStart.Blo TripCmd MStart.ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Op	MStart.Trip
	MotCyc	MStart.MotorCyc Enum

PPAM1\* (WW\_PPAM1 )

	Mod	f[1].active f[1].Blo TripCmd f[1].ExBlo TripCmd
	Beh	

LDevice::CTRL

	Health	
	NamPlt	
	Str	f[1].Alarm delta phi
	Op	f[1].Trip delta phi
<b>PPAM2* (WW_PPAM1 )</b>		
	Mod	f[2].active f[2].Blo TripCmd f[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[2].Alarm delta phi
	Op	f[2].Trip delta phi
<b>PPAM3* (WW_PPAM1 )</b>		
	Mod	f[3].active f[3].Blo TripCmd f[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[3].Alarm delta phi
	Op	f[3].Trip delta phi
<b>PPAM4* (WW_PPAM1 )</b>		
	Mod	f[4].active f[4].Blo TripCmd f[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[4].Alarm delta phi
	Op	f[4].Trip delta phi
<b>PPAM5* (WW_PPAM1 )</b>		
	Mod	f[5].active f[5].Blo TripCmd f[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	

LDevice::CTRL

	Str	f[5].Alarm delta phi
	Op	f[5].Trip delta phi

PPAM6\* (WW\_PPAM1 )

	Mod	f[6].active f[6].Blo TripCmd f[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[6].Alarm delta phi
	Op	f[6].Trip delta phi

PSOF1 (WW\_PSOF1 )

	Mod	SOTF.active SOTF.ExBlo SOTF.Ex rev Interl
	Beh	
	Health	
	NamPlt	
	Str	SOTF.enabled

PTOC1 (WW\_PTOC1 )

	Mod	I[1].active I[1].Blo TripCmd I[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I[1].Alarm
	Op	I[1].Trip

PTOC10 (WW\_PTOC3 )

	Mod	IG[4].active IG[4].Blo TripCmd IG[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	IG[4].Alarm
	Op	IG[4].Trip

PTOC11 (WW\_PTOC4 )

LDevice::CTRL

	Mod	I2>[1].active I2>[1].Blo TripCmd I2>[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I2>[1].Alarm
	Op	I2>[1].Trip

PTOC12 (WW\_PTOC4 )

	Mod	I2>[2].active I2>[2].Blo TripCmd I2>[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I2>[2].Alarm
	Op	I2>[2].Trip

PTOC2 (WW\_PTOC1 )

	Mod	I[2].active I[2].Blo TripCmd I[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I[2].Alarm
	Op	I[2].Trip

PTOC3 (WW\_PTOC1 )

	Mod	I[3].active I[3].Blo TripCmd I[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I[3].Alarm
	Op	I[3].Trip

PTOC4 (WW\_PTOC1 )

	Mod	I[4].active I[4].Blo TripCmd I[4].ExBlo TripCmd
	Beh	

LDevice::CTRL

	Beh	
	Health	
	NamPlt	
	Str	I[4].Alarm
	Op	I[4].Trip

PTOC5 (WW\_PTOC1 )

	Mod	I[5].active I[5].Blo TripCmd I[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I[5].Alarm
	Op	I[5].Trip

PTOC6 (WW\_PTOC1 )

	Mod	I[6].active I[6].Blo TripCmd I[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I[6].Alarm
	Op	I[6].Trip

PTOC7 (WW\_PTOC3 )

	Mod	IG[1].active IG[1].Blo TripCmd IG[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	IG[1].Alarm
	Op	IG[1].Trip

PTOC8 (WW\_PTOC3 )

	Mod	IG[2].active IG[2].Blo TripCmd IG[2].ExBlo TripCmd
	Beh	
	Health	

LDevice::CTRL

	NamPlt	
	Str	IG[2].Alarm
	Op	IG[2].Trip

PTOC9 (WW\_PTOC3 )

	Mod	IG[3].active IG[3].Blo TripCmd IG[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	IG[3].Alarm
	Op	IG[3].Trip

PTOF1\* (WW\_PTOF1 )

	Mod	f[1].active f[1].Blo TripCmd f[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[1].Alarm f
	Op	f[1].Trip f

PTOF2\* (WW\_PTOF1 )

	Mod	f[2].active f[2].Blo TripCmd f[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[2].Alarm f
	Op	f[2].Trip f

PTOF3\* (WW\_PTOF1 )

	Mod	f[3].active f[3].Blo TripCmd f[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[3].Alarm f

LDevice::CTRL

	Op	f[3].Trip f
<b>PTOF4* (WW_PTOF1 )</b>		
	Mod	f[4].active f[4].Blo TripCmd f[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[4].Alarm f
	Op	f[4].Trip f
<b>PTOF5* (WW_PTOF1 )</b>		
	Mod	f[5].active f[5].Blo TripCmd f[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[5].Alarm f
	Op	f[5].Trip f
<b>PTOF6* (WW_PTOF1 )</b>		
	Mod	f[6].active f[6].Blo TripCmd f[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[6].Alarm f
	Op	f[6].Trip f
<b>PTOV1* (WW_PTOV2 )</b>		
	Mod	V[1].active V[1].Blo TripCmd V[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[1].Alarm
	Op	V[1].Trip
<b>PTOV10 (WW_PTOV3 )</b>		

LDevice::CTRL

	Mod	V 012[4].active V 012[4].Blo TripCmd V 012[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V 012[4].Alarm
	Op	V 012[4].Trip

PTOV11 (WW\_PTOV3 )

	Mod	V 012[5].active V 012[5].Blo TripCmd V 012[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V 012[5].Alarm
	Op	V 012[5].Trip

PTOV12 (WW\_PTOV3 )

	Mod	V 012[6].active V 012[6].Blo TripCmd V 012[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V 012[6].Alarm
	Op	V 012[6].Trip

PTOV2\* (WW\_PTOV2 )

	Mod	V[2].active V[2].Blo TripCmd V[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[2].Alarm
	Op	V[2].Trip

PTOV3\* (WW\_PTOV2 )

	Mod	V[3].active V[3].Blo TripCmd V[3].ExBlo TripCmd

LDevice::CTRL

	Beh	
	Health	
	NamPlt	
	Str	V[3].Alarm
	Op	V[3].Trip

PTOV4\* (WW\_PTOV2 )

	Mod	V[4].active V[4].Blo TripCmd V[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[4].Alarm
	Op	V[4].Trip

PTOV5\* (WW\_PTOV2 )

	Mod	V[5].active V[5].Blo TripCmd V[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[5].Alarm
	Op	V[5].Trip

PTOV6\* (WW\_PTOV2 )

	Mod	V[6].active V[6].Blo TripCmd V[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[6].Alarm
	Op	V[6].Trip

PTOV7 (WW\_PTOV3 )

	Mod	V 012[1].active V 012[1].Blo TripCmd V 012[1].ExBlo TripCmd
	Beh	
	Health	

LDevice::CTRL

	NamPlt	
	Str	V 012[1].Alarm
	Op	V 012[1].Trip

PTOV8 (WW\_PTOV3 )

	Mod	V 012[2].active V 012[2].Blo TripCmd V 012[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V 012[2].Alarm
	Op	V 012[2].Trip

PTOV9 (WW\_PTOV3 )

	Mod	V 012[3].active V 012[3].Blo TripCmd V 012[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V 012[3].Alarm
	Op	V 012[3].Trip

PTTR1 (WW\_PTTR2 )

	Mod	ThR.active ThR.Blo TripCmd ThR.ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	ThR.Alarm
	Op	ThR.Trip

PTTR2 (WW\_PTTR1 )

	Mod	RTD.active RTD.Blo TripCmd RTD.ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Op	RTD.Trip

LDevice::CTRL

PTUC1 (WW_PTUC1 )		
	Mod	I<[1].active I<[1].Blo TripCmd I<[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I<[1].Alarm
	Op	I<[1].Trip
PTUC2 (WW_PTUC1 )		
	Mod	I<[2].active I<[2].Blo TripCmd I<[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I<[2].Alarm
	Op	I<[2].Trip
PTUC3 (WW_PTUC1 )		
	Mod	I<[3].active I<[3].Blo TripCmd I<[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	I<[3].Alarm
	Op	I<[3].Trip
PTUF1* (WW_PTUF1 )		
	Mod	f[1].active f[1].Blo TripCmd f[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[1].Alarm f
	Op	f[1].Trip f
PTUF2* (WW_PTUF1 )		
	Mod	f[2].active

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		f[2].Blo TripCmd f[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[2].Alarm f
	Op	f[2].Trip f

PTUF3\* (WW\_PTUF1 )

	Mod	f[3].active f[3].Blo TripCmd f[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[3].Alarm f
	Op	f[3].Trip f

PTUF4\* (WW\_PTUF1 )

	Mod	f[4].active f[4].Blo TripCmd f[4].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[4].Alarm f
	Op	f[4].Trip f

PTUF5\* (WW\_PTUF1 )

	Mod	f[5].active f[5].Blo TripCmd f[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	f[5].Alarm f
	Op	f[5].Trip f

PTUF6\* (WW\_PTUF1 )

	Mod	f[6].active f[6].Blo TripCmd f[6].ExBlo TripCmd

LDevice::CTRL

	Beh	
	Health	
	NamPlt	
	Str	f[6].Alarm f
	Op	f[6].Trip f

PTUV1\* (WW\_PTUV2 )

	Mod	V[1].active V[1].Blo TripCmd V[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[1].Alarm
	Op	V[1].Trip

PTUV2\* (WW\_PTUV2 )

	Mod	V[2].active V[2].Blo TripCmd V[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[2].Alarm
	Op	V[2].Trip

PTUV3\* (WW\_PTUV2 )

	Mod	V[3].active V[3].Blo TripCmd V[3].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[3].Alarm
	Op	V[3].Trip

PTUV4\* (WW\_PTUV2 )

	Mod	V[4].active V[4].Blo TripCmd V[4].ExBlo TripCmd
	Beh	
	Health	

LDevice::CTRL

	NamPlt	
	Str	V[4].Alarm
	Op	V[4].Trip
<b>PTUV5* (WW_PTUV2 )</b>		
	Mod	V[5].active V[5].Blo TripCmd V[5].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[5].Alarm
	Op	V[5].Trip
<b>PTUV6* (WW_PTUV2 )</b>		
	Mod	V[6].active V[6].Blo TripCmd V[6].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	V[6].Alarm
	Op	V[6].Trip
<b>PUPF1 (WW_PUPF1 )</b>		
	Mod	PF[1].active PF[1].Blo TripCmd PF[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PF[1].Alarm
	Op	PF[1].Trip
<b>PUPF2 (WW_PUPF1 )</b>		
	Mod	PF[2].active PF[2].Blo TripCmd PF[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	PF[2].Alarm

LDevice::CTRL

	Op	PF[2].Trip
<b>RBRF1 (WW_RBRF1 )</b>		
	Mod	CBF.active CBF.ExBlo CBF.ExBlo
	Beh	
	Health	
	NamPlt	
	Str	CBF.running
	OpEx	CBF.Alarm
<b>VePTOV1* (WW_PTOV1 )</b>		
	Mod	VG[1].active VG[1].Blo TripCmd VG[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	VG[1].Alarm
	Op	VG[1].Trip
<b>VePTOV2* (WW_PTOV1 )</b>		
	Mod	VG[2].active VG[2].Blo TripCmd VG[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	VG[2].Alarm
	Op	VG[2].Trip
<b>VePTUV1* (WW_PTUV1 )</b>		
	Mod	VG[1].active VG[1].Blo TripCmd VG[1].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	VG[1].Alarm
	Op	VG[1].Trip
<b>VePTUV2* (WW_PTUV1 )</b>		

### LDevice::CTRL

	Mod	VG[2].active VG[2].Blo TripCmd VG[2].ExBlo TripCmd
	Beh	
	Health	
	NamPlt	
	Str	VG[2].Alarm
	Op	VG[2].Trip

\* Logical Node is dependent from settings in the “Device Planing”. (See 1.8 Device Planing Dependencies)

## 1.8. Device Planing Dependencies

Depending on the settings in the Device Planing section Logical Node instances will be available in the generated ICD file. The following list will give you an overview about the different selections for each Module which have an effect on the existence of a Logical Node.

Module.Name	Value
CILO1	
SG.SwitchgearType	Controlled SG
	Controlled Make Break SG
CSWI1	
	Controlled SG
	Controlled Make Break SG
XCBR1	
	Monitored Make Break SG
	Controlled Make Break SG
XSWI1	
	Monitored SG
	Controlled SG
PDOP1	
PQS[1].Mode	P>
	Pr>
	Q>
	Qr>

<b>Module.Name</b>	<b>Value</b>
	S>
	P>
	Pr>
	Q>
	Qr>
PDOP2	
PQS[2].Mode	P>
	Pr>
	Q>
	Qr>
	S>
	P>
	Pr>
	Q>
	Qr>
PDOP3	
PQS[3].Mode	P>
	Pr>
	Q>
	Qr>
	S>
	P>
	Pr>
	Q>
	Qr>
PDOP4	
PQS[4].Mode	P>
	Pr>
	Q>
	Qr>
	S>
	P>
	Pr>
	Q>
	Qr>
PDOP5	
PQS[5].Mode	P>

<b>Module.Name</b>	<b>Value</b>
	Pr>
	Q>
	Qr>
	S>
	P>
	Pr>
	Q>
	Qr>
PDOP6	
PQS[6].Mode	P>
	Pr>
	Q>
	Qr>
	S>
	P>
	Pr>
	Q>
	Qr>
PDUP1	
PQS[1].Mode	P<
	Q<
	S<
	P<
	Pr<
	Q<
	Qr<
PDUP2	
PQS[2].Mode	P<
	Q<
	S<
	P<
	Pr<
	Q<
	Qr<
PDUP3	
PQS[3].Mode	P<
	Q<

<b>Module.Name</b>	<b>Value</b>
	S<
	P<
	Pr<
	Q<
	Qr<
PDUP4	
PQS[4].Mode	P<
	Q<
	S<
	P<
	Pr<
	Q<
	Qr<
PDUP5	
PQS[5].Mode	P<
	Q<
	S<
	P<
	Pr<
	Q<
	Qr<
PDUP6	
PQS[6].Mode	P<
	Q<
	S<
	P<
	Pr<
	Q<
	Qr<
PFRC1	
f[1].Mode	f< and df/dt
	f> and df/dt
	f< and DF/DT
	f> and DF/DT
	df/dt
PFRC2	
f[2].Mode	f< and df/dt

<b>Module.Name</b>	<b>Value</b>
	f> and df/dt
	f< and DF/DT
	f> and DF/DT
	df/dt
PFRC3	
f[3].Mode	f< and df/dt
	f> and df/dt
	f< and DF/DT
	f> and DF/DT
	df/dt
PFRC4	
f[4].Mode	f< and df/dt
	f> and df/dt
	f< and DF/DT
	f> and DF/DT
	df/dt
PFRC5	
f[5].Mode	f< and df/dt
	f> and df/dt
	f< and DF/DT
	f> and DF/DT
	df/dt
PFRC6	
f[6].Mode	f< and df/dt
	f> and df/dt
	f< and DF/DT
	f> and DF/DT
	df/dt
PPAM1	
f[1].Mode	delta phi
PPAM2	
f[2].Mode	delta phi
PPAM3	
f[3].Mode	delta phi
PPAM4	
f[4].Mode	delta phi
PPAM5	

<b>Module.Name</b>	<b>Value</b>
f[5].Mode	delta phi
PPAM6	
f[6].Mode	delta phi
PTOF1	
f[1].Mode	f>
PTOF2	
f[2].Mode	f>
PTOF3	
f[3].Mode	f>
PTOF4	
f[4].Mode	f>
PTOF5	
f[5].Mode	f>
PTOF6	
f[6].Mode	f>
PTOV1	
V[1].Mode	V>
PTOV2	
V[2].Mode	V>
PTOV3	
V[3].Mode	V>
PTOV4	
V[4].Mode	V>
PTOV5	
V[5].Mode	V>
PTOV6	
V[6].Mode	V>
PTUF1	
f[1].Mode	f<
PTUF2	
f[2].Mode	f<
PTUF3	
f[3].Mode	f<
PTUF4	
f[4].Mode	f<
PTUF5	
f[5].Mode	f<

<b>Module.Name</b>	<b>Value</b>
PTUF6	
f[6].Mode	f<
PTUV1	
V[1].Mode	V<
	use
	V(t)<
PTUV2	
V[2].Mode	V<
	use
	V(t)<
PTUV3	
V[3].Mode	V<
	use
	V(t)<
PTUV4	
V[4].Mode	V<
	use
	V(t)<
PTUV5	
V[5].Mode	V<
	use
	V(t)<
PTUV6	
V[6].Mode	V<
	use
	V(t)<
VePTOV1	
VG[1].Mode	V>
VePTOV2	
VG[2].Mode	V>
VePTUV1	
VG[1].Mode	V<
	use
VePTUV2	
VG[2].Mode	V<
	use