

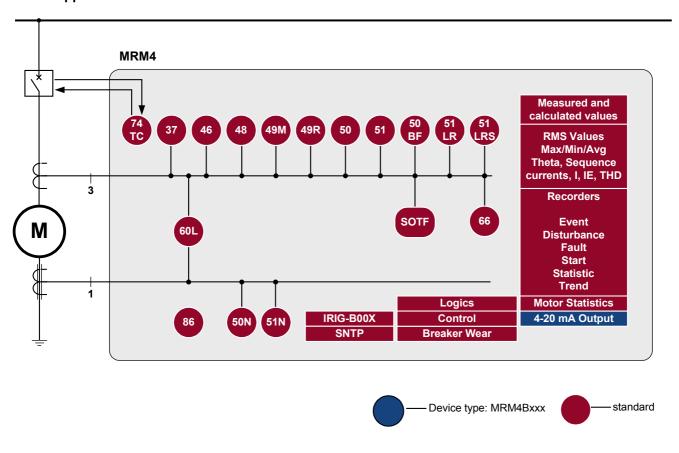


MRM4 HighPROTEC

Motor Protection

Device Manual DOK-HB-MRM4E

MRM4 Application Overview



Page 2 EN MRM4 12/11

Order Code

Motor F	Protection				MRM4				
Digital Inputs	Binary output re- lays	Analog. In/out	Housing	Large display					
8 4	6 4	0/0 0/1	B1 B1	-		A B			
	e variant 2 rrent 5A/1A,	Ground Cu	urrent 5A/	1A			0		
Door mou	-		ng)					А В	
Door mounting 19" (flush mounting) Communication protocol Protocol/without protocol Modbus RTU, IEC60870-5-103, RS485/terminals, Modbus TCP, IEC61850 prepared, Ethernet 100 MB/RJ45 connector*1, Profibus-DP, optic fibre Profibus-DP, RS485/D-SUB Modbus RTU, IEC60870-5-103, Optic fiber, Modbus RTU, IEC60870-5-103, RS485/D-SUB, IEC61850, Ethernet 100MB/RJ45 *2						A B C D E F G H			
	g from avail English/Ger			es					

The parameterizing- and disturbance analyzing software is included in delivery of HighPROTEC devices.

ANSI: 46, 49M, 49R, 49S, 50J, 37, 50, 51, 50N, 51N, 60L, 86, 50BF, 74TC.

^{*1} Additional costs for the software update tool IEC61850 per device are 250.- €. The devices can be updated via device front interface (RS232) and PC locally. Please ask for availability

^{*2} Please ask for availability

Comments on the Manual	
Information Concerning Liability and Warranty	
MPORTANT DEFINITIONS	
Scope of Delivery	
Storage	
Important Information	
Symbols	. 12
General Conventions	
Load Reference Arrow System	
Device	
Device Planning	
Device Planning Parameters of the Device	
nstallation and Connection	
Three-Side-View - 19" Three-Side-View - 7-Push-button Version	
Three-Side-View - 8-Push-button Version	
Installation Diagram 7-Push-button Version	
Installation Diagram 8-Push-button Version	
Assembly Groups	
Grounding	
Legend for Wiring Diagrams	
Slot X1: Power Supply Card with Digital Inputs	
Slot X2: Relay Output Card	
Slot X3: Current Transformer Measuring Inputs	
Slot X100: Ethernet Interface	51
Slot X101: IRIG-B00X	53
Slot X103: Data Communication	54
nput, Output, and LED Settings	.62
Configuration of the Digital Inputs	62
DI-8P X	
DI-4P X	
Relay Output Contacts Settings	
OR-3AI X	
LED configuration	
Navigation - Operation	
Basic Menu Control	
Smart View Keyboard Commands	
Smart View	
Installation of Smart View.	
Uninstalling Smart View	120
Switching the Language of the Graphical User Interface	121
Setting up the Connection PC - Device	121
Loading of Device Data When Using Smart View	
Restoring of Device Data When Using Smart View	120
Offline Device Planning Via Smart View	131
Veasuring Values	
Read Out Measured Values	
Current - Measured Values	
Configuration of the Analog Outputs	136
Available Elements:	
Statistics	
Read Out Statistics	
Statistics (Configuration).	
Direct Commands	
Global Protection Parameters of the Statistics Module	
States of the Inputs of the Statistics Module	
Signals of the Statistics Module.	
Counters of the Module Statistics	
Acknowledgments	
Manual Acknowledgment	

Manual Acknowledgment Via Smart View	
External Acknowledgments	
External Acknowledge Via Smart View	
External LED - Acknowledgment Signals	147
Manual Resets	
Manual Resets Via Smart View	
Reset to Factory Defaults	
Reset	
Status Display	152
Status Display Via Smart View	152
Operating Panel (HMI)	153
Special Parameters of the Panel	153
Direct Commands of the Panel	153
Global Protection Parameters of the Panel	153
Recorder	154
Disturbance Recorder	
Fault Recorder	
Event Recorder	166
Trend Recorder	168
Motor Start Recorder	
Statistic Recorder	183
Communication Protocols	184
SCADA Interface	184
Modbus®	184
Profibus	
IEC60870-5-103	
IEC61850	
Time Synchronisation	211
SNTP	
IRIG-B00X	
Parameters	225
Parameter Definitions	225
Operational Modes (Access Authorization)	239
Password	240
Changing of Parameters - Example	241
Changing of Parameters When Using the Smart View - Example	242
Protection Parameters	
Setting Groups	244
Comparing Parameter Files Via Smart View	
Converting Parameter Files Via Smart View	
Program Mode	
Device Parameters	
Date and Time	
Synchronize Date and Time Via Smart View	
Version	
Version Via Smart View	
TCP/IP Settings	25/
Direct Commands of the System Module	
Global Protection Parameters of the System	
System Module Input States	
System Module Signals	
Special Values of the System Module	
System Parameters	
General System Parameters	
System Parameters – Current Related	
Blockings	
Permanent Blocking	
Temporary Blocking	265
To Activate or Deactivate the Tripping Command of a Protection Module	
Activate, Deactivate respectively Block Temporarily Protection Functions	
Module: Protection (Prot)	∠/∪

Direct Commands of the Protection Module	277
Global Protection Parameters of the Protection Module	277
Protection Module Input States	278
Protection Module Signals (Output States)	278
Protection Module Values	
Switchgear/Breaker – Manager	279
Single Line Diagram	279
Switchgear Configuration.	
Switchgear Wear	
Control Parameters	
Control - Example: Switching of a Breaker	
Protective Elements	
MStart - Motor Starting and Control [48,66]	
I>> - IOC Function	
I - Overcurrent Protection [50, 51,51Q, 51V]	
12 and %I2/I1> - Unbalanced Load [46]	
Special Notes on Earth Fault Current Transformers	
IG> - Earth Fault [50N/G, 51N/G]	
I< - Undercurrent [37]	
JAM [51LR]	
LRC - Locked Rotor during Start	
MLS - Mechanical Load Shedding	
Theta - Thermal Model [49M, 49Ř]	
UTC - Ultimate Trip Current	
SOTF - Switch Onto Fault	415
ExP - External Protection	420
Supervision	425
CBF- Breaker Failure [50BF]	425
TCS - Trip Circuit Supervision [74TC]	
CTS - Current Transformer Supervision [60L]	
O 10 Outlone transformer outlong [oot]	
Self Supervision	452
Self Supervision	452
	452 454
Self Supervision	452 4 54 455
Self SupervisionCommissioning	452 454 455
Self Supervision	452 454 455 456
Self Supervision	452 454 455 456 457
Self Supervision	452 454 455 457 480
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device	452 454 456 457 480 480 480
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic.	452 454 456 457 480 480 480
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description	452 454 456 457 480 480 489 489
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View	
Self Supervision. Commissioning	
Self Supervision. Commissioning	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs*	
Self Supervision. Commissioning	
Self Supervision Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device. Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs* Forcing Analog Outputs*	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data	
Self Supervision	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data	
Self Supervision. Commissioning. Commissioning/Protection Test. Putting Out of Operation – Plug Out the Relay. RTD Protection Module. URTDII Module Interface* Principle – General Use. URTDII Module Fiber Optic Connection to the Protective Device. Programmable Logic. General Description. Programmable Logic at the Panel. Programmable Logic via Smart View. Service and Commissioning Support. Forcing the Relay Output Contacts. Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data. Climatic Environmental Conditions. Degree of Protection EN 60529. Routine Test.	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device. Programmable Logic General Description. Programmable Logic at the Panel Programmable Logic via Smart View. Service and Commissioning Support Forcing the Relay Output Contacts Forcing Analog Outputs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data Climatic Environmental Conditions Degree of Protection EN 60529	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data Climatic Environmental Conditions Degree of Protection EN 60529 Routine Test Housing Current and Earth Current Measurement	
Self Supervision. Commissioning. Commissioning/Protection Test	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing He Relay Output Contacts Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data Climatic Environmental Conditions Degree of Protection EN 60529 Routine Test Housing Current and Earth Current Measurement Voltage Supply	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device Programmable Logic General Description Programmable Logic at the Panel Programmable Logic via Smart View Service and Commissioning Support Forcing the Relay Output Contacts Forcing RTDs* Forcing Analog Outputs* Forcing Analog Outputs* Failure Simulator (Sequencer)* Technical Data Climatic Environmental Conditions Degree of Protection EN 60529 Routine Test Housing Current and Earth Current Measurement Voltage Supply Power Consumption	
Self Supervision Commissioning Commissioning/Protection Test Putting Out of Operation – Plug Out the Relay RTD Protection Module URTDII Module Interface* Principle – General Use URTDII Module Fiber Optic Connection to the Protective Device. Programmable Logic General Description Programmable Logic at the Panel. Programmable Logic via Smart View Service and Commissioning Support Forcing Analog Output Contacts Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data Climatic Environmental Conditions Degree of Protection EN 60529 Routine Test Housing Current and Earth Current Measurement Voltage Supply Power Consumption Display	
Self Supervision. Commissioning. Commissioning/Protection Test. Putting Out of Operation – Plug Out the Relay. RTD Protection Module. URTDII Module Interface* Principle – General Use. URTDII Module Fiber Optic Connection to the Protective Device. Programmable Logic. General Description. Programmable Logic at the Panel. Programmable Logic via Smart View. Service and Commissioning Support. Forcing the Relay Output Contacts. Forcing Analog Outputs* Forcing Analog Inputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data. Climatic Environmental Conditions. Degree of Protection EN 60529. Routine Test. Housing. Current and Earth Current Measurement. Voltage Supply. Power Consumption. Display Front Interface RS232.	
Self Supervision. Commissioning. Commissioning/Protection Test. Putting Out of Operation – Plug Out the Relay. RTD Protection Module. URTDII Module Interface* Principle – General Use. URTDII Module Fiber Optic Connection to the Protective Device. Programmable Logic. General Description. Programmable Logic at the Panel. Programmable Logic via Smart View. Service and Commissioning Support. Forcing the Relay Output Contacts. Forcing RTDs* Forcing Analog Outputs* Forcing Analog Inputs* Failure Simulator (Sequencer)* Technical Data. Climatic Environmental Conditions. Degree of Protection EN 60529. Routine Test. Housing. Current and Earth Current Measurement. Voltage Supply. Power Consumption. Display. Front Interface RS232. Real Time Clock.	

Analog Output	525
RS485*	
Fiber Optic*	
URTD-Interface*	
Boot phase	
Standards	
Approvals	
Design Standards	
High Voltage Tests (IEC 60255-6)	
EMC Immunity Tests	
EMC Emission Tests	
Environmental Tests	
Mechanical Tests	
Tolerances	531
Real Time Clock Tolerances	
Measured Value Acquisition Tolerances	
Protection Stages Tolerances	
Assignment ListAssignment List	
Signals of the Digital Inputs and Logic	

This manual applies to devices (version):

Version 2.0.a

Build: 16101

Comments on the Manual

This manual explains in general the tasks of device planning, parameter setting, installation, commissioning, operation and maintenance of the HighPROTEC devices.

The manual serves as working basis for:

- Engineers in the protection field,
- commissioning engineers,
- people dealing with setting, testing and maintenance of protection and control devices,
- as well as trained personnel for electrical installations and power stations.

All functions concerning the type code will be defined. Should there be a description of any functions, parameters or inputs/outputs which do not apply to the device in use, please ignore that information.

All details and references are explained to the best of our knowledge and are based on our experience and observations.

This manual describes the (optionally) full featured versions of the devices.

All technical information and data included in this manual reflect their state at the time this document was issued. We reserve the right to carry out technical modifications in line with further development without changing this manual and without previous notice. Hence no claim can be brought based on the information and descriptions this manual includes.

Text, graphic and formulae do not always apply to the actual delivery scope. The drawings and graphics are not true to scale. We do not accept any liability for damage and operational failures caused by operating errors or disregarding the directions of this manual.

No part of this manual is allowed to be reproduced or passed on to others in any form, unless *Woodward Kempen GmbH* have approved in writing.

This User manual is part of the delivery scope when purchasing the device. In case the device is passed on (sold) to a third party, the manual has to be handed over as well.

Any repair work carried out on the device requires skilled and competent personnel who need to be well aware especially of the local safety regulations and have the necessary experience for working on electronic protection devices and power installations (provided by evidence).

Information Concerning Liability and Warranty

Woodward does not accept any liability for damage resulting from conversions or changes carried out on the device or planning (projecting) work, parameter setting or adjustment changes done by the customer.

The warranty expires after a device has been opened by others than Woodward specialists.

Warranty and liability conditions stated in *Woodward* General Terms and Conditions are not supplemented by the above mentioned explanations.

Page 8 EN MRM4 12/11

IMPORTANT DEFINITIONS

The signal definitions shown below serve the safety of life and limb as well as for the appropriate operating life of the device.



DANGER indicates a hazardous situation which, if not avoided, will result in death or serious injury.



WARNING indicates a hazardous situation which, if not avoided, could result in death or serious injury.



CAUTION, used with the safety alert symbol, indicates a hazardous situation which, if not avoided, could result in minor or moderate injury.



NOTICE is used to address practices not related to personal injury.

CAUTION

CAUTION, without the safety alert symbol, is used to address practices not related to personal injury.



FOLLOW INSTRUCTIONS

Read this entire manual and all other publications pertaining to the work to be performed before installing, operating, or servicing this equipment. Practice all plant and safety instructions and precautions. Failure to follow instructions can cause personal injury and/or property damage.



PROPER USE

Any unauthorized modifications to or use of this equipment outside its specified mechanical, electrical, or other operating limits may cause personal injury and/or property damage, including damage to the equipment. Any such unauthorized modifications: (1) constitute "misuse" and/or "negligence" within the meaning of the product warranty thereby excluding warranty coverage for any resulting damage, and (2) invalidate product certifications or listings.

The programmable devices subject to this manual are designed for protection and also control of power installations and operational devices that are fed by voltage sources with a fixed frequency, i.e. fixed at 50 or 60 Hertz. They are not intended for use with Variable Frequency Drives. The devices are further designed for installation in low-voltage (LV) compartments of medium voltage (MV) switchgear panels or in decentralized protection panels. The programming and parametrization has to meet all requirements of the protection concept (of the equipment that is to be protected). You must ensure that the device will properly recognize and manage (e.g. switch off the breaker) on the basis of your programming and parametrization all operational conditions (failures). The proper use requires a backup protection by an additional protective device. Before starting any operation and after any modification of the programming (parametrization) test make a documentary proof that your programming and parametrization meets the requirements of your protection concept.

Typical applications for this product family/device line are for instance:

- · Feeder protection
- Mains protection
- Machine protection
- · Transformer Differential Protection

Any usage beyond these applications the devices are not designed for. This applies also to the use as a partly completed machinery. The manufacturer cannot be held liable for any resulting damage, the User alone bears the risk for this. As to the appropriate use of the device: The technical data and tolerances specified by *Woodward* have to be met.

A WARNING

OUT-OF-DATE PUBLICATION

This publication may have been revised or updated since this copy was produced. To verify that you have the latest revision, please visit the download section of our website:

www.woodward.com

If your publication is not there, please contact your customer service representative to get the latest copy.

CAUTION

Electrostatic Discharge Awareness

All electronic equipment is electro-static sensitive, some components more than others. To protect these components from electro-static damage, you must take special precautions to minimize or eliminate electrostatic discharges.

Follow these precautions when working with or near the control.

- 1. Before doing maintenance on the electronic control, discharge the static electricity on your body to ground by touching and holding a grounded metal object (pipes, cabinets, equipment, etc.).
- 2. Avoid the build-up of static electricity on your body by not wearing clothing made of synthetic materials. Wear cotton or cotton-blend materials as much as possible because these do not store static electric charges as much as synthetics.
- Keep plastic, vinyl, and Styrofoam materials (such as plastic or Styrofoam cups, cup holders, cigarette packages, cellophane wrappers, vinyl books or folders, plastic bottles, and plastic ash trays) away from the control, the modules, and the work area as much as possible.

Page 10 EN MRM4 12/11

- 4. Do not remove any printed circuit board (PCB) from the control cabinet unless absolutely necessary. If you must remove the PCB from the control cabinet, follow these precautions:
 - Verify the safe isolation from supply. All connectors have to be unplugged.
 - Do not touch any part of the PCB except the edges.
 - Do not touch the electrical conductors, the connectors, or the components with conductive devices or with your hands.
 - When replacing a PCB, keep the new PCB in the plastic antistatic protective bag it comes in until you are ready to install it. Immediately after removing the old PCB from the control cabinet, place it in the antistatic protective bag.

To prevent damage to electronic components caused by improper handling, read and observe the precautions in Woodward manual 82715, Guide for Handling and Protection of Electronic Controls, Printed Circuit Boards, and Modules.

Woodward reserves the right to update any portion of this publication at any time. Information provided by Woodward is believed to be correct and reliable. However, no responsibility is assumed by Woodward unless otherwise expressly undertaken.

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Scope of Delivery

The delivery scope does not include the fastening material, but includes all connection terminals, except communication connectors. Please check the consignment for completeness on arrival (delivery note).

Please ascertain whether the type plate, connection diagram, type code and description of the device tally. If you have any doubts please contact our Service Department (contact address to be found on the reverse of the manual).

Storage

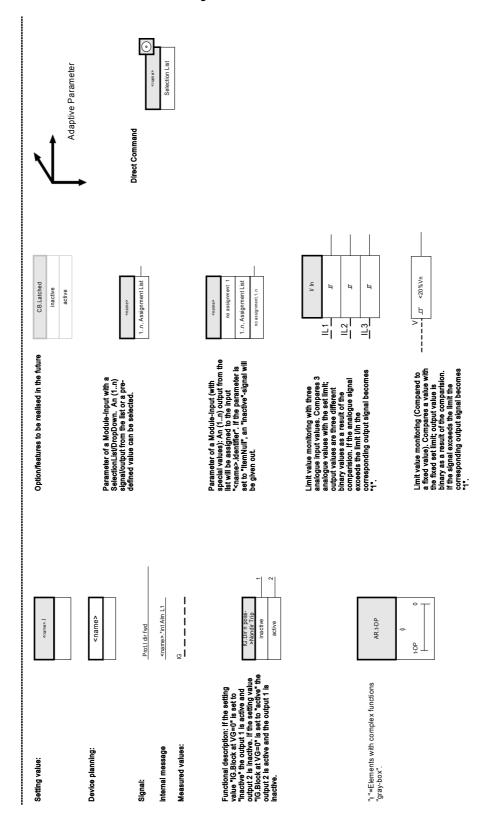
The devices must not be stored outdoors. The storing facilities have to be sufficiently ventilated and must be dry (see Technical Data).

Important Information

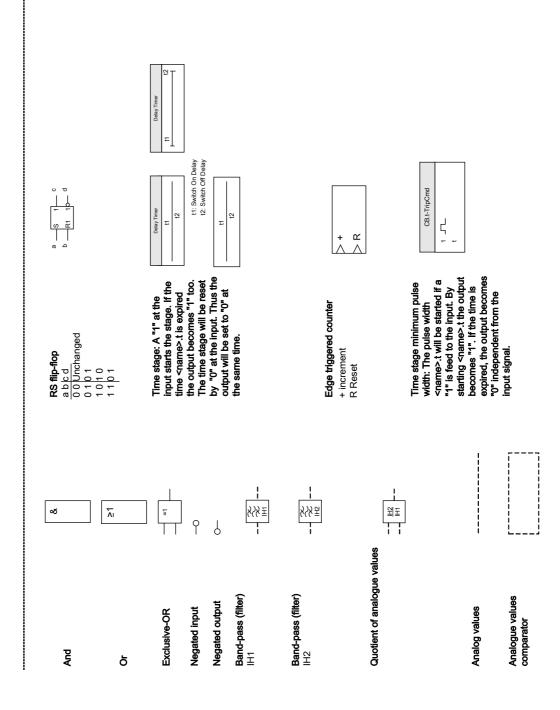


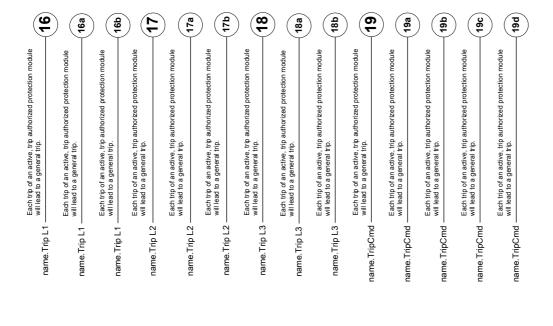
In line with the customer's requirement the devices are combined in a modular way (in compliance with the order code). The terminal assignment of the device can be found on the top of the device (wiring diagram).

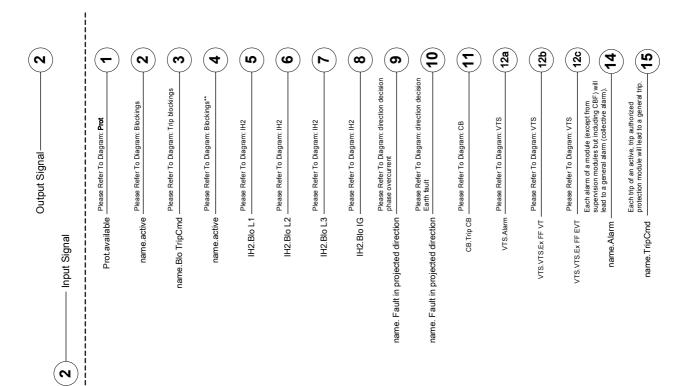
Symbols



Page 12 EN MRM4 12/11







Page 14 EN MRM4 12/11

27b

27c

27a

Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).

name.Alarm

name.Alarm

name.Alarm

name.Alarm

Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).

Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).

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Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).

name.Alarm L2

name.Alarm L3

name.Alarm

Prot.Blo TripCmd

name.Alarm L1

Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).

78

27d

62

30

(2)

Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).

32

33

Please Refer To Diagram: CB.CB Manager

CB.Pos

35

Please Refer To Diagram: CB.CB Manager

CB.Pos OFF

36

Please Refer To Diagram: CB.CB Manager

CB.Pos Indeterm

Please Refer To Diagram: CB.CB Manager

CB.Pos Disturb

37

38a

38b

Please Refer To Diagram: LOP.Ex FF VT

LOP.Ex FF VT

Please Refer To Diagram: LOP.LOP

LOP.LOP Blo

် ဆွ

Please Refer To Diagram: LOP.Ex FF EVT

LOP.Ex FF EVT

(X)

Please Refer To Diagram: CB.CB Manager

CB.Pos ON

	3 (5	2 3		5 6	24 a	24p	(A)	25a) (5	25b) (H) (26b	27
Each trip of an active, trip authorized protection module will lead to a general trip.	Each trip of an active, trip authorized protection module will lead to a general trip.	Each trip of an active, trip authorized protection module will lead to a general trip.	Each trip of an active, trip authorized protection module will lead to a general trip.	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).	Each phase selective alarm of a module (I, IG, V, VX depending on the device type) will lead to a phase selective general alarm (collective alarm).
name.Trio L1	name Trio	name.Trip L3	name Trip	- Flame Alarm	name.Alam L1	name.Alarm L1	name Alarm I 2	name Alam I 2	name.Alarm L2	name Alarm I.3		name.Alarm L3	name.Alarm -

65) {	7) (4	3
Please Refer To Diagram : Q -> & V <.Decoupling Energy Resource	Please ReferTo Diagram : CTS.Alarm	Please ReferTo Diagram: S.G.ProtON	Please ReferTo Diagram: SG.ON Cm d	Please Refer To Diagram: Analog values	Please Refer To Diagram : Analog values	Please Refer To Diagram: Analog values
Q ->&V<.Decoupling Energy Resource	C T S . A la rm		S G . O N C m d	An In [1]. V a lu e	An In [2]. Value	An In [n]. V a lu e

Page 16 EN MRM4 12/11

General Conventions

»Parameters are indicated by right and left double arrow heads and written in italic.«
»SIGNALS are indicated by right and left double arrow heads and small caps.«
[Paths are indicated by brackets.]
Software and Device names are written in italic.
Module and Instance (Element) names are displayed italic and underlined.
»Pushbuttons, Modes and Menu entries are indicated by right and left double arrow heads.«
1 2 3 Image References (Squares)

Load Reference Arrow System

In general, the "Load Reference Arrow System" is used for loads (consumed energy) and the "Generator Reference System" is used for generators (generated energy).

Within the HighPROTEC the "Load Reference Arrow System" is used exclusively.

This applies to directions and phase angles. The phase angle is defined as the angle from the current phasor to the voltage phasor. Current and voltage arrows are to be counted positive in the direction of the arrow. The advantage of determining the load reference arrow system as the standard is that it is not necessary to change the direction of the current arrow if there is a transition from motor to generator.

Page 18 EN MRM4 12/11

Device

MRM4

Device Planning

Planning of a device means to reduce the functional range to a degree that suits the protection task to be fulfilled, i.e. the device shows only those functions you really need. If you, for example, deactivate the voltage protection function, all parameter branches related to this function do not appear in the parameter tree any more. All corresponding events, signals etc. will be deactivated too. By this the parameter trees become very transparent. Planning also involves adjustment of all basic system data (frequency etc.).



But it has to be taken into account that by deactivating, for instance, protective functions, you also change the functionality of the device. If you cancel the directional feature of the overcurrent protections then the device no longer trips in a directional way but merely in a non-directional way.

The manufacturer does not accept liability for any personal or material damage as a result of wrong planning.

A planning service is also offered by Woodward Kempen GmbH.



Beware of inadvertent deactivating protective functions/modules

If you are deactivating modules within the device planning all parameters of those modules will be set on default.

If you are activating one of these modules again all parameters of those reactivated modules will be set on default.

Device Planning Parameters of the Device

Parameter	Description	Options	Default	Menu path
Hardware Variant 1	Optional Hardware Extension	»A« 8 digital inputs 6 binary relay output contacts IRIG-B,	binary relay output contacts	[MRM4]
		»B« 4 Digital Inputs 4 Relay Output Contacts , Analog Output IRIG-B	IRIG-B	
Hardware Variant 2	Optional Hardware Extension	»0« Phase Current 5A/1A, Ground Current 5A/1A,	Phase Current 5A/1A, Ground Current 5A/1A	[MRM4]
		»1« Phase Current 5A/1A, Sensitive Ground Current 5A/1A		
Housing	Mounting form	»A« Flush mounting,	Flush mounting	[MRM4]
		»B« 19 inch mounting (semi-flush),		
		»H« Customized Version 1		

Parameter	Description	Options	Default	Menu path
Communica	Communication	»A« Without,	RS 485: Modbus	[MRM4]
tion	F	»B« RS 485: Modbus RTU IEC 60870-5- 103,	RTU IEC 60870-5-103	
		»C« Ethernet: Modbus TCP,		
		»D« Fiber Optics: Profibus-DP,		
		»E« D-SUB: Profibus-DP,		
		»F« Fiber Optics: RS 485: Modbus RTU IEC 60870-5-103,		
		»G« RS 485/D-SUB: Modbus RTU IEC 60870-5-103,		
		»H« IEC61850		

Page 20 EN MRM4 12/11

Installation and Connection

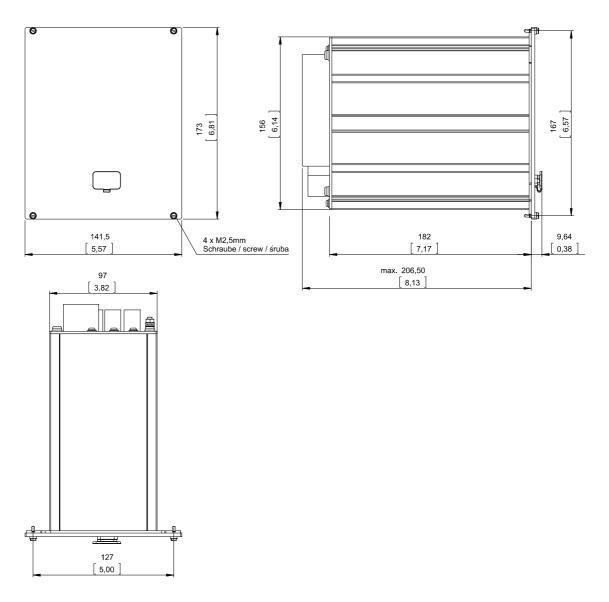
Three-Side-View - 19"

NOTICE

Dependent on the connection method of the SCADA system used the needed space (depth) differs. If, for instance, a D-Sub-Plug is used, it has to be added to the depth dimension.

NOTICE

The three-side-view shown in this section is exclusively valid for 19" devices.



3-Side-View B1 Housing (19" Devices)



The housing must be carefully grounded. Connect a ground cable (4 to 6 mm^2 / AWG 12-10) / 1,7 Nm [15 In-lb]) to the housing, using the screw, which is marked with the ground symbol (at the rear side of the device).

The power supply card needs a separate ground connection (2.5 mm² (AWG 14) at terminal X1 (0.56-0.79 Nm [5-7 In-lb]).

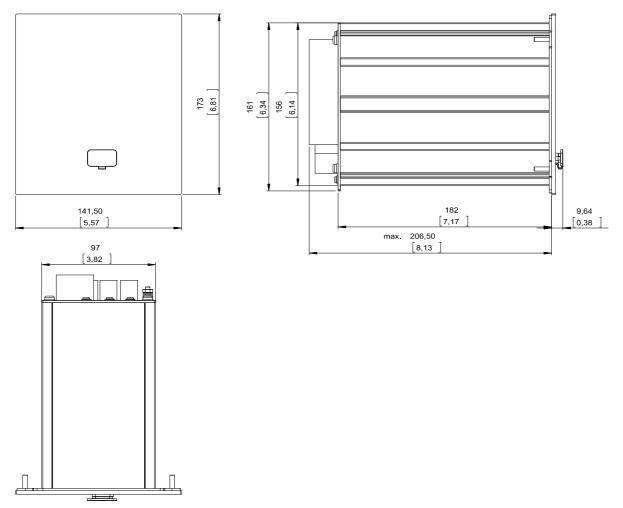
Three-Side-View - 7-Push-button Version

NOTICE

Dependent on the connection method of the SCADA system used the needed space (depth) differs. If, for instance, a D-Sub-Plug is used, it has to be added to the depth dimension.

NOTICE

The installation diagram shown in this section is exclusively valid for devices with 7 push-buttons at the front side of the HMI. (INFO-, C-, OK-Push-button and 4 Softkeys (Push-buttons)).



3-Side-View B1 Housing (Devices with 7 Softkeys)



The housing must be carefully grounded. Connect a ground cable (4 to 6 mm² / AWG 12-10) / 1,7 Nm [15 In-lb]) to the housing, using the screw, which is marked with the ground symbol (at the rear side of the device).

The power supply card needs a separate ground connection (2.5 mm² (AWG 14) at terminal X1 (0.56-0.79 Nm [5-7 In-lb]).

Page 22 EN MRM4 12/11

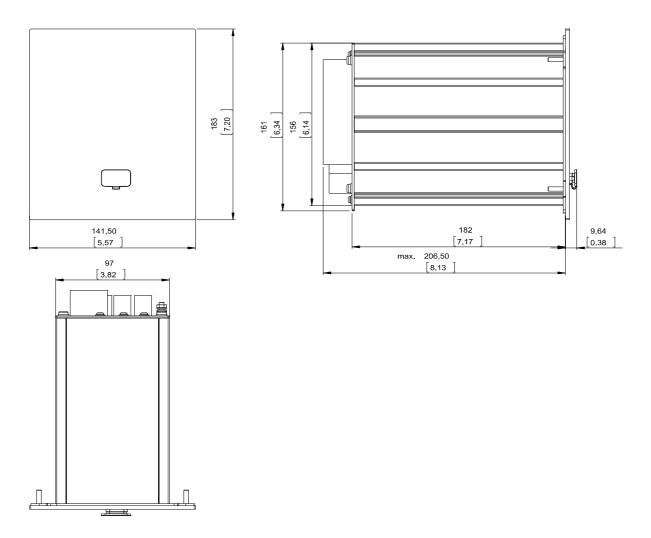
Three-Side-View - 8-Push-button Version

NOTICE

Dependent on the connection method of the SCADA system used the needed space (depth) differs. If, for instance, a D-Sub-Plug is used, it has to be added to the depth dimension.

NOTICE

The installation diagram shown in this section is exclusively valid for devices with 8 push-buttons at the front side of the HMI. (INFO-, C-, OK-, CTRL-Push-button and 4 Softkeys (Push-buttons)).



3-Side-View B1 Housing (Devices with 8 Softkeys)



The housing must be carefully grounded. Connect a ground cable (4 to 6 mm² / AWG 12-10) / 1,7 Nm [15 In-lb]) to the housing, using the screw, which is marked with the ground symbol (at the rear side of the device).

The power supply card needs a separate ground connection (2.5 mm² (AWG 14) at terminal X1 (0.56-0.79 Nm [5-7 In-lb]).

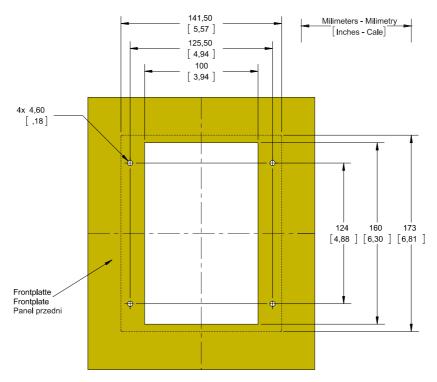
Installation Diagram 7-Push-button Version



Even when the auxiliary voltage is switched-off, unsafe voltages might remain at the device connections.

NOTICE

The installation diagram shown in this section is exclusively valid for devices with 7 push-buttons at the front side of the HMI. (INFO-, C-, OK-Push-button and 4 Softkeys (Push-buttons)).



B1 Housing Door Cut-out (7-Push-button Version)



The housing must be carefully earthed. Connect a ground cable (4 to 6 mm^2 / AWG 12-10) / 1,7 Nm [15 In-lb]) to the housing, using the screw, which is marked with the ground symbol (at the rear side of the device).

The power supply card needs a separate ground connection (2.5 mm² (AWG 14) at terminal X1 (0.56-0.79 Nm [5-7 In-Ib]).



Be careful. Do not over-tighten the mountings nuts of the relay (M4 metric 4 mm). Check the torque by means of a torque wrench (1,7 Nm [15 ln-lb]). Over-tightening the mounting nuts could due to personal injury or damage the relay.

Page 24 EN MRM4 12/11

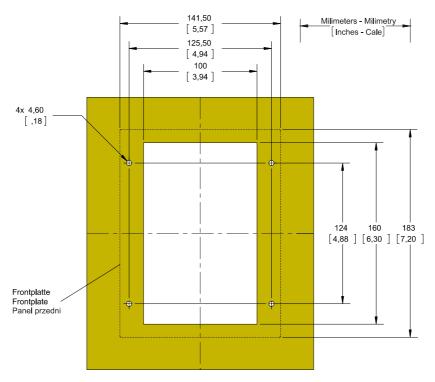
Installation Diagram 8-Push-button Version



Even when the auxiliary voltage is switched-off, unsafe voltages might remain at the device connections.

NOTICE

The installation diagram shown in this section is exclusively valid for devices with 8 push-buttons at the front side of the HMI. (INFO-, C-, OK-, CTRL-Push-button and 4 Softkeys (Push-buttons)).



B1 Housing Door Cut-out (8-Push-button Version)



The housing must be carefully earthed. Connect a ground cable (4 to 6 mm² / AWG 12-10) / 1,7 Nm [15 In-lb]) to the housing, using the screw, which is marked with the ground symbol (at the rear side of the device).

The power supply card needs a separate ground connection (2.5 mm² (AWG 14) at terminal X1 (0.56-0.79 Nm [5-7 In-lb]).

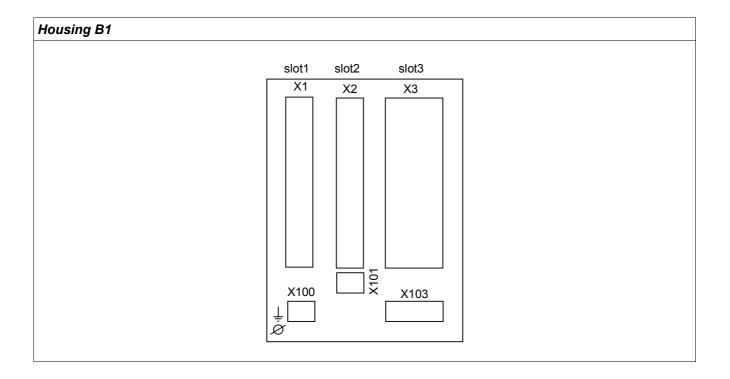


Be careful. Do not over-tighten the mountings nuts of the relay (M4 metric 4 mm). Check the torque by means of a torque wrench (1,7 Nm [15 ln-lb]). Over-tightening the mounting nuts could due to personal injury or damage the relay.

Assembly Groups



In line with the customer's requirement the devices are combined in a modular way (in compliance with the order code). In each of the slots an assembly-group may be integrated. In the following the terminal assignment of the individual assembly-groups are shown. The exact installation place of the individual modules can be learned from the connection diagram fixed at the top of your device.



B1 housing - schematic diagram

Grounding



The housing must be carefully grounded. Connect a ground cable (4 to 6 mm^2 / AWG 12-10) / 1,7 Nm [15 In-lb]) to the housing, using the screw, which is marked with the ground symbol (at the rear side of the device).

The power supply card needs a separate ground connection (2.5 mm² (AWG 14) at terminal X1 (0.56-0.79 Nm [5-7 In-lb]).

Page 26 EN MRM4 12/11

CAUTION

The devices are very sensitive to electro-static discharges.

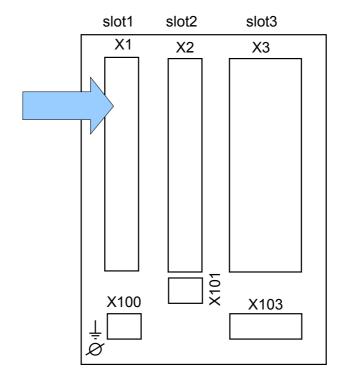
Legend for Wiring Diagrams

In this legend designations of various device types are listed, e. g. transformer protection, motor protection, generator protection, etc. Therefor it can occur that you will not find each designation on the wiring diagram of your device.

Designation	Meaning
FE	Connection of functional earth
Power Supply	Connection for auxiliary power supply
IL1	Phase current input L1
IL2	Phase current input L2
IL3	Phase current input L3
IG	Earth current input IG
I L1 W1	Phase current input L1, winding side 1
I L2 W1	Phase current input L2, winding side 1
I L3 W1	Phase current input L3, winding side 1
I G W1	Earth current input IG, winding side 1
I L1 W2	Phase current input L1, winding side 2
I L2 W2	Phase current input L2, winding side 2
I L3 W2	Phase current input L3, winding side 2
I G W2	Earth current input IG, winding side 2
V L1	Phase voltage L1
V L2	Phase voltage L2
V L3	Phase voltage L3
V 12	Phase to phase voltage V 12
V 23	Phase to phase voltage V 23
V 31	Phase to phase voltage V 31
VX	Forth voltage measuring input for measuring residual voltage or for Sync-check
ВО	Contact output, change over contact
NO	Contact output, normally open
DI	Digital input
СОМ	Common connection of digital inputs
Out+	Analog output + (0/420 mA or 010 V)
IN-	Analog input + (0/420 mA or 010 V)
N.C.	Not connected
DO NOT USE	Do not use
SC	Self supervision contact
GND	Ground
HF SHIELD	Connection cable shield
Fiber Connection	Fiber optic connection
Only for use with external galvanic decoupled CTs. See chapter Current Transformers of the manual.	Only for use with external galvanic decoupled CTs. See chapter Current Transformers of the manual.
Caution Sensitive Current Inputs	Caution Sensitive Current Inputs
Connection Diagram see specification	Connection Diagram see specification

Page 28 EN MRM4 12/11

Slot X1: Power Supply Card with Digital Inputs



Rear side of the device (Slots)

The type of power supply card and the number of digital inputs on it used in this slot is dependent on the ordered device type. The different variants have a different scope of functions.

Available assembly groups in this slot:

- (DI8-X1): This assembly group comprises a wide-range power supply unit; and two non-grouped digital inputs and six (6) digital inputs (grouped).
- (DI4-X1): This assembly group comprises a wide-range power supply unit and four (4) digital inputs (grouped).



The available combinations can be gathered from the ordering code.

DI8-X Power Supply and Digital Inputs



Make sure, that the tightening torque is 0.56-0.79 Nm [5-7 In-Ib].

This assembly group comprises:

- a wide-range power supply unit
- 6 digital inputs, grouped
- 2 digital inputs, non-grouped
- 24V DC (for options with Woodward Devices only)

Auxiliary voltage supply

• The aux. voltage inputs (wide-range power supply unit) are non-polarized. The device could be provided with AC or DC voltage.

Digital inputs



For each digital input group the related voltage input range has to be configured. Wrong switching thresholds can result in malfunctions/wrong signal transfer times.

The digital inputs are provided with different switching thresholds (can be configured) (two AC and five DC input ranges). For the six grouped (connected to common potential) inputs and the two non-grouped inputs the following switching levels can be defined:

- 24V DC
- 48V DC / 60V DC
- 110 V AC/DC
- 230 V AC/DC

If a voltage >80% of the set switching threshold is applied at the digital input, the state change is recognized (physically "1"). If the voltage is below 40% of the set switching threshold, the device detects physically "0".

CAUTION

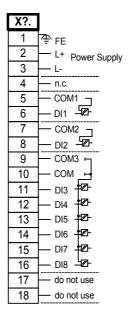
The ground terminal has to be connected to the »-pole« when using DC supply.

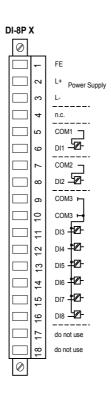
CAUTION

Use of the 24 V DC Output is prohibited. This output is exclusively for factory testing and commissioning.

Page 30 EN MRM4 12/11

Terminals





DI-4 X - Power Supply and Digital Inputs



Make sure, that the tightening torque is 0.56-0.79 Nm [5-7 In-lb].

This assembly group comprises:

- a wide-range power supply unit
- 4 digital inputs, grouped
- 24V DC (for options with Woodward Devices only)

Auxiliary voltage supply

• The aux. voltage inputs (wide-range power supply unit) are non-polarized. The device could be provided with AC or DC voltage.

Digital inputs



For each digital input group the related voltage input range has to be configured. Wrong switching thresholds can result in malfunctions/wrong signal transfer times.

The digital inputs are provided with different switching thresholds (can be configured) (two AC and five DC input ranges). For the six grouped (connected to common potential) inputs and the two non-grouped inputs the following switching levels can be defined:

- 24V DC
- 48V DC / 60V DC
- 110 V AC/DC
- 230 V AC/DC

If a voltage >80% of the set switching threshold is applied at the digital input, the state change is recognized (physically "1"). If the voltage is below 40% of the set switching threshold, the device detects physically "0".

CAUTION

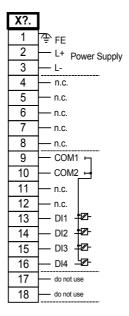
The ground terminal has to be connected to the »-pole« when using DC supply.

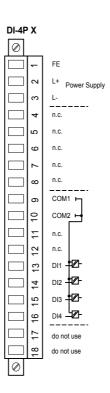


Use of the 24 V DC Output is prohibited. This output is exclusively for factory testing and commissioning.

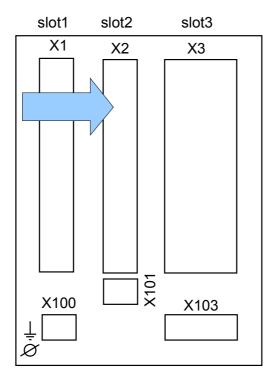
Page 32 EN MRM4 12/11

Terminals





Slot X2: Relay Output Card



Rear side of the device (Slots)

The type of card in this slot is dependent on the ordered device type. The different variants have a different scope of functions.

Available assembly groups in this slot:

- (OR-5 X2): Assembly Group with 5 Changeover (CO), Supervision Contact (SC)
- (OR-3Al X2): Assembly Group with 2 Normally Open (Form A), 1 Changeover (Form C), SC, Analog Output IRIG-B



The available combinations can be gathered from the ordering code.

Page 34 EN MRM4 12/11

Binary Relay Output Contacts and System Contact

The number of the binary relay output contact contacts is related to the type of the device or type code. The binary relay output contacts are potential-free change-over contacts. In chapter [Assignment/binary outputs] the assignment of the binary relay output contacts is specified. The changeable signals are listed in the **assignment list* which can be found in the appendix.



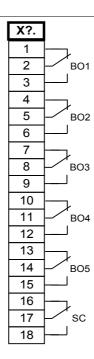
Make sure that the tightening torque is 0.56-0.79 Nm [5-7 In-lb].

CAUTION

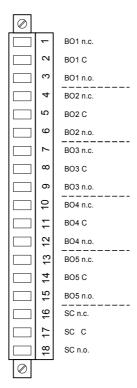
Please duly consider the current carrying capacity of the binary relay output contacts. Please refer to the Technical Data.

The System-OK contact (SC relay) cannot be configured. The system contact is a changeover contact that picks up when the device is free from internal faults. While the device is booting up, the System OK relay (SC) remains dropped-off (unenergized). As soon as the system is properly started (and protection is active), the System Contact picks up and the assigned LED is activated accordingly (please refer to the Self Supervision chapter).

Terminals



BO-5 X



Page 36 EN MRM4 12/11

OR - 3Al X - Relay Output Contacts and System Contact

The number of the binary relay output contact contacts is related to the type of the device or type code. The binary relay output contacts are dry-type contacts. In chapter [Assignment/binary outputs] the assignment of the binary relay output contacts is specified. The changeable signals are listed in the *assignment list* which can be found in the appendix.



Make sure that the tightening torque is 0.56-0.79 Nm [5-7 In-lb].

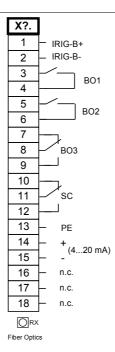
CAUTION

Please duly consider the current carrying capacity of the binary relay output contacts. Please refer to the Technical Data.

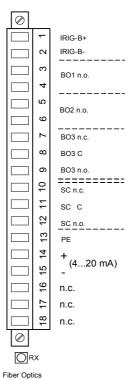
The System-OK contact (SC relay) cannot be configured. The system contact is a changeover contact that picks up when the device is free from internal faults. While the device is booting up, the System OK relay (SC) remains dropped-off (unenergized). As soon as the system is properly started (and protection is active), the System Contact picks up and the assigned LED is activated accordingly (please refer to the Self Supervision chapter).

For details on the Analog Output please refer to the Technical Data.

Terminals

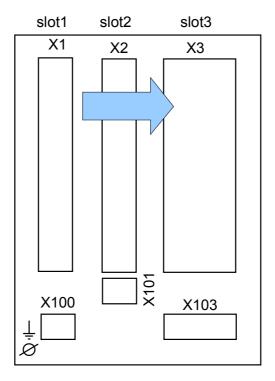


OR-3AI X



Page 38 EN MRM4 12/11

Slot X3: Current Transformer Measuring Inputs



Rear side of the device (Slots)

This slot contains the current transformer measuring inputs.

TI X- Standard Phase and Ground Current Measuring Input Card

The device is provided with 4 current measuring inputs: three for measuring the phase currents and one for measuring of the earth current. Each of the current measuring inputs has a measuring input for 1 A and 5 A.

The input for earth current measuring either can be connected to a cable-type current transformer or alternatively it is possible to connect the summation current path of the phase current transformer to this input (Holmgreen connection).



Current transformers have to be earthed on their secondary side.



Interrupting the secondary circuits of current transformers causes hazardous voltages.

The secondary side of the current transformers have to be short circuited before the current circuit to the device is opened.



The current measuring inputs may exclusively be connected to current measuring transformers (with galvanic separation).



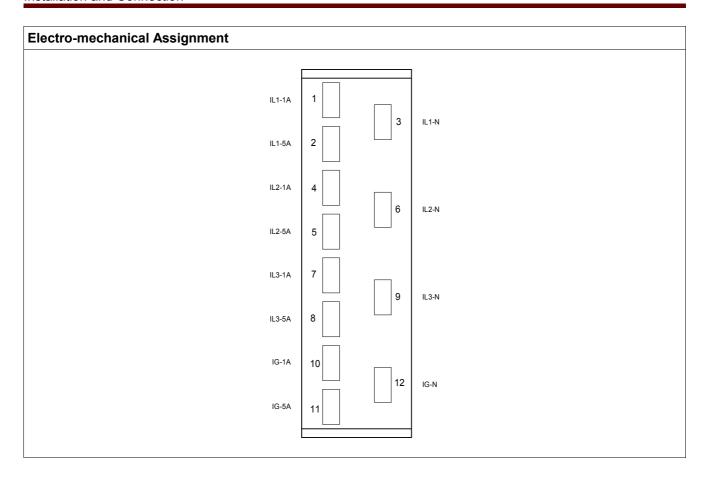
- Do not interchange the inputs (1 A/5 A)
- Make sure the transformation ratios and the power of the CTs are correctly rated. If the rating of the CTs is not right (overrated), then the normal operational conditions may not be recognized. The pickup value of the measuring unit amounts approx. 3% of the rated current of the device. Also the CTs need a current greater than approx 3% of the rated current to ensure sufficient accuracy. Example: For a 600 A CT (primary current) any currents below 18 A cannot be detected any more.
- Overloading can result in destruction of the measuring inputs or faulty signals. Overloading means that in case of a short-circuit the currentcarrying capacity of the measuring inputs could be exceeded.



Make sure, that the tightening torque is 2 Nm [17.7 In-lb].

Page 40 EN MRM4 12/11

1 1A 1A 5A 3 E IL1
4 1A 5 5A 6 N 7 1A 8 5A 8 5A 8 1L3 9 N 10 1A 11 5A 12 N



Current Transformers (CT)

Check the installation direction.



It is imperative that the secondary sides of measuring transformers be grounded.



The current measuring inputs may exclusively be connected to current measuring transformers (with galvanic separation).



CT secondary circuits must always to be low burdened or short-circuited during operation.

NOTICE

For current and voltage sensing function external wired and appropriate current and voltage transformer shall be used, based on the required input measurement ratings. Those devices provide the necessary insulation functionality.

All current measuring inputs can be provided with 1 A or 5 A nominal. Make sure that the wiring is correct.

Sensitive Ground Current Measurement

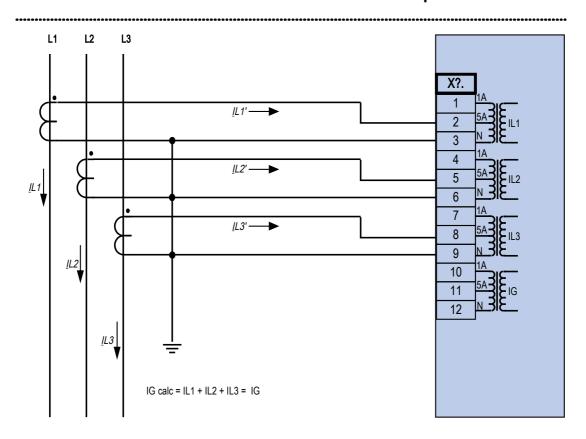
The proper use of sensitive current measuring inputs is the measurement of small currents like they could occur in isolated and high resistance grounded networks.

Page 42 EN MRM4 12/11

Due to the sensitiveness of these measuring inputs don't use them for the measurement of ground short circuit currents like they occur in solidly earthed networks.

If a sensitive measuring input should be used for the measurement of ground short circuit currents, it has to be ensured, that the measuring currents are transformed by a matching transformer according to the technical data of the protective device.

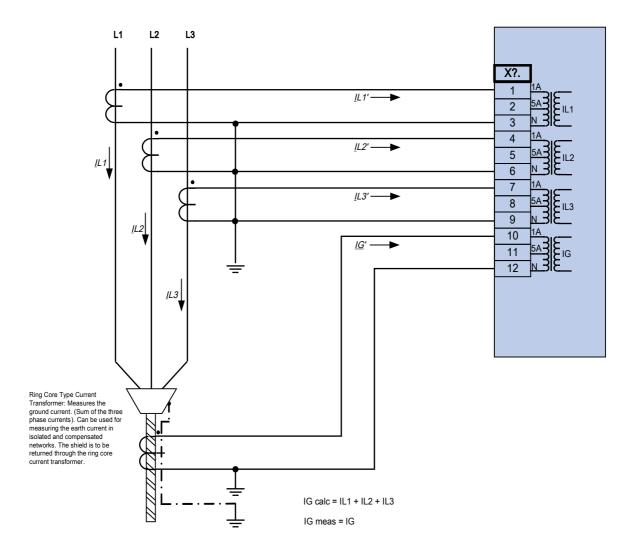
Current Transformer Connection Examples



Three phase current measurement; In secondary = 5 A.

Page 44 EN MRM4 12/11

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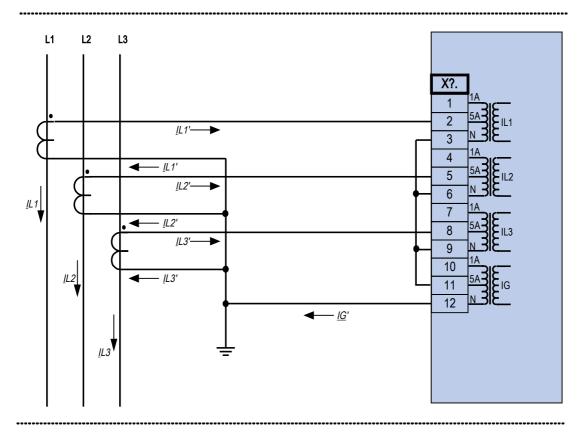
Three phase current measurement; In secondary = 1 A.

Earth-current measuring via cable-type current transformer; IGnom secondary = 1 A.



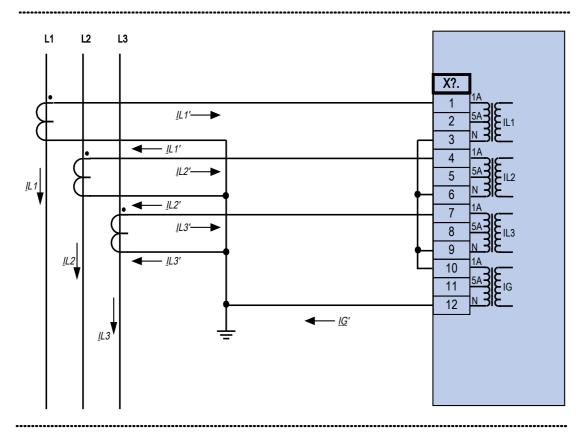
Warning!

The shielding at the dismantled end of the line has to be put through the cable-type current transformer and has to be grounded at the cable side.

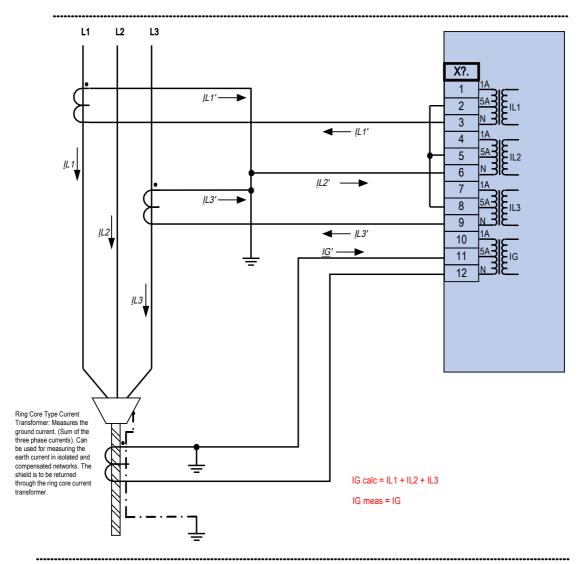


Three phase current measurement; In secondary = 5 A. Earth-current measuring via Holmgreen-connection; IGnom secondary = 5 A.

Page 46 EN MRM4 12/11



Three phase current measurement; In secondary = 1 A. Earth-current measuring via Holmgreen-connection; IGnom secondary = 1 A.



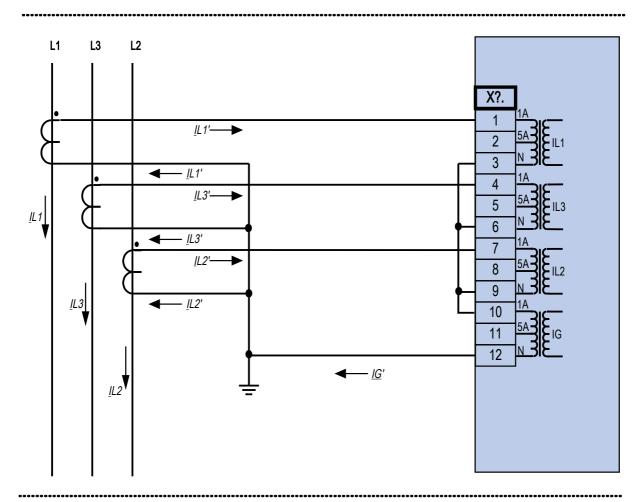
Two phase current measurement (Open Delta); In secondary = 5 A. Earth-current measuring via cable-type current transformer; IGnom secondary = 5 A.



Warning!

The shielding at the dismantled end of the line has to be put through the cable-type current transformer and has to be grounded at the cable side.

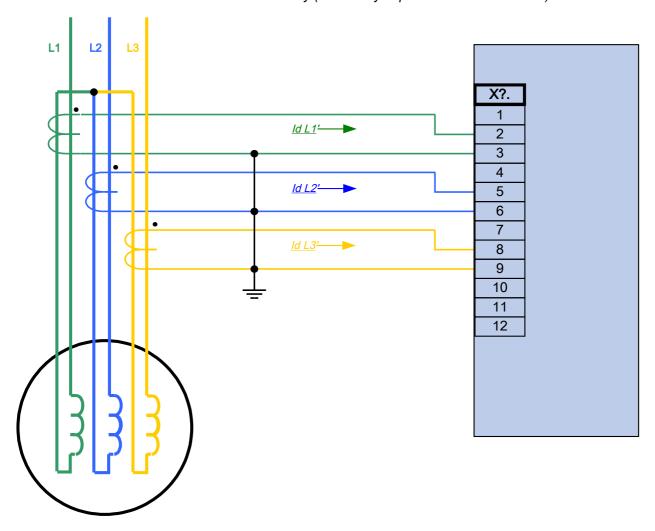
Page 48 EN MRM4 12/11



Three phase current measurement; In secondary = 1 A.

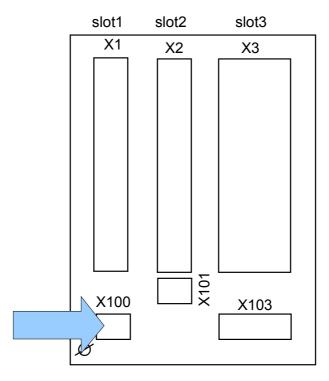
Earth-current measuring via Holmgreen-connection; IGnom secondary = 1 A.

Differential Protection Variant for Electrical Machinery (Availability depends on ordered device)



Page 50 EN MRM4 12/11

Slot X100: Ethernet Interface



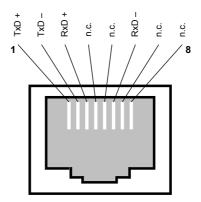
Rear side of the device (Slots)

An Ethernet interface may be available depending on the device type ordered.

NOTICE

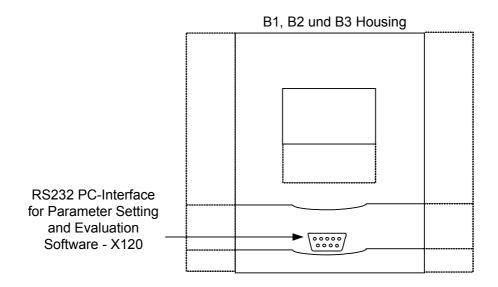
The available combinations can be gathered from the ordering code.

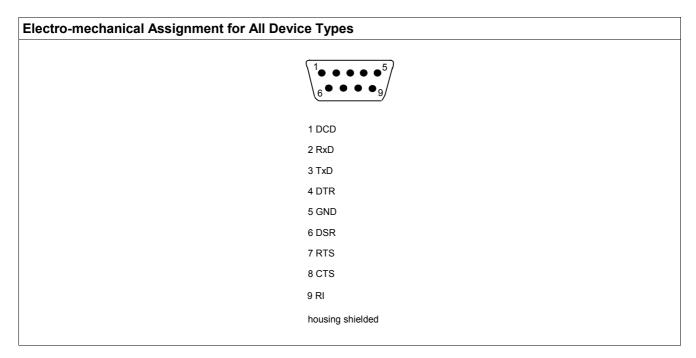
Ethernet - RJ45



PC Interface - X120

9-pole D-Sub at all device fronts





Assignment of the Null Modem Cable

Assignment of the fully wired null modem cable

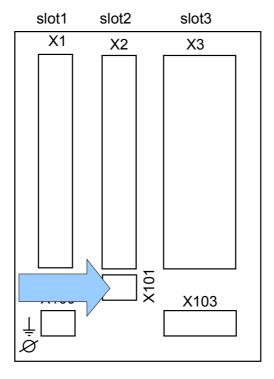
Dsub -9 (female)	Signal	Dsub -9 (female)	Signal
2	RxD	3	TxD
3	TxD	2	RxD
4	DTR	6,1	DSR, DCD
6,1	DSR, DCD	4	DTR
7	RTS	8	CTS
8	CTS	7	RTS
5	GND (Ground)	5	GND (Ground)
9	Ring signal	9	Ring signal

Page 52 EN MRM4 12/11



The connection cable must be shielded.

Slot X101: IRIG-B00X



Rear side of the device (Slots)

If the device is equipped with an IRIG-B00X interface is dependent on the ordered device type.



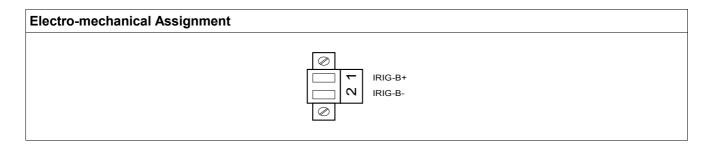
The available combinations can be gathered from the ordering code.

IRIG-B00X

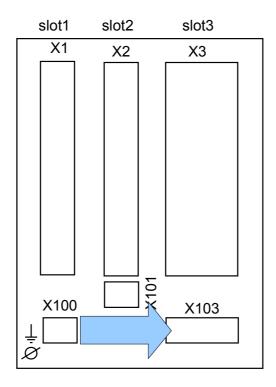


Make sure that the tightening torque is 5-7 In-lb [0.56-0.79 Nm].

Terminal Markings		
	X101 IRIG-B+	
	2 IRIG-B-	



Slot X103: Data Communication



Rear side of the device (Slots)

The data communication interface in the **X103** slot is dependent on the ordered device type. The scope of functions is dependent on the type of data communication interface.

Available assembly groups in this slot:

- RS485 Terminals for Modbus and IEC
- LWL Interface for Modbus, IEC and Profibus
- D-SUB Interface for Modbus and IEC
- D-SUB Interface for Profibus



The available combinations can be gathered from the ordering code.

Page 54 EN MRM4 12/11

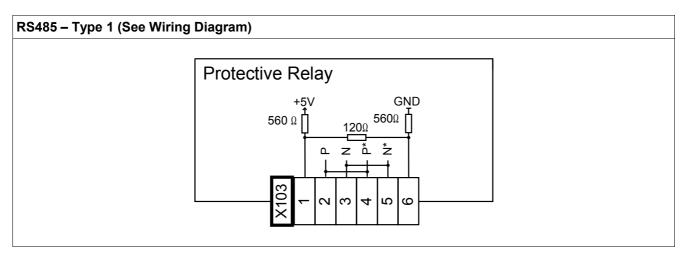
Modbus® RTU / IEC 60870-5-103 via RS485

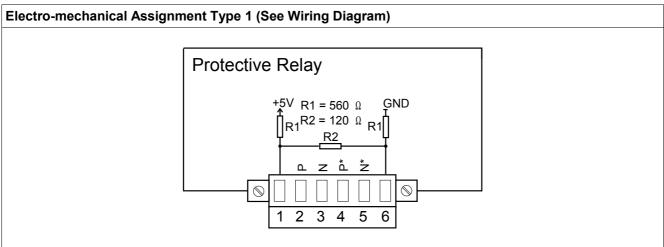


There are two different versions of the RS485 interface. By means of the wiring diagram on the top of your device, you have to find out which version is built in your device (Type1 or Type2).



Make sure that the tightening torque is 0.22-0.45 Nm [2-4 In-Ib].

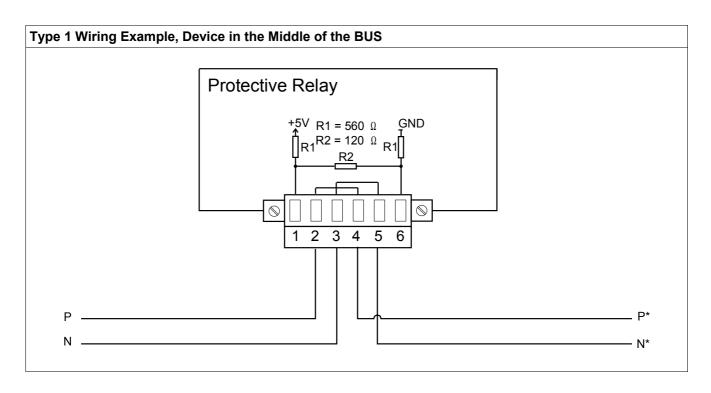


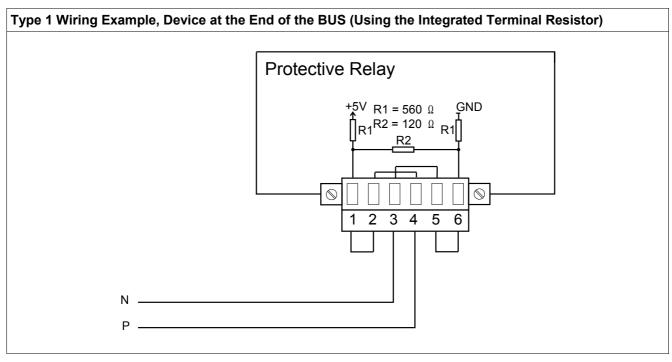


NOTICE

The Modbus® / IEC 60870-5-103 connection cable must be shielded. The shielding has to be fixed at the screw which is marked with the ground symbol at the rear side of the device.

The communication is Halfduplex.





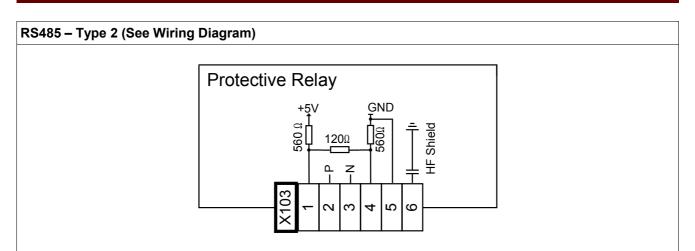
▲ WARNING

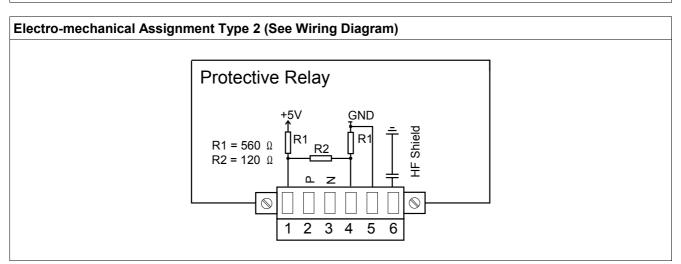
There are two different versions of the RS485 interface. By means of the wiring diagram on the top of your device, you have to find out which version is built in your device (Type1 or Type2).



Make sure that the tightening torque is 0.22-0.45 Nm [2-4 In-lb].

Page 56 EN MRM4 12/11

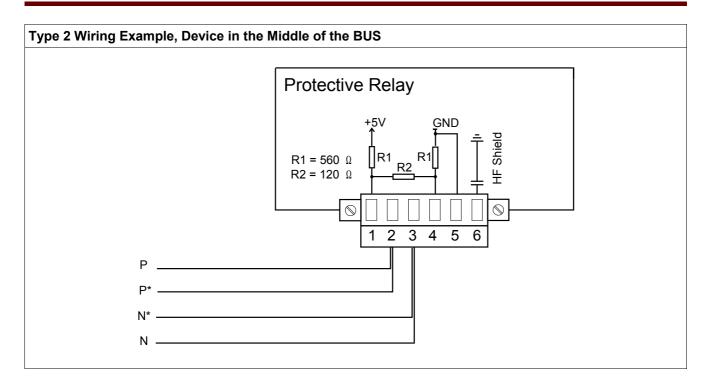


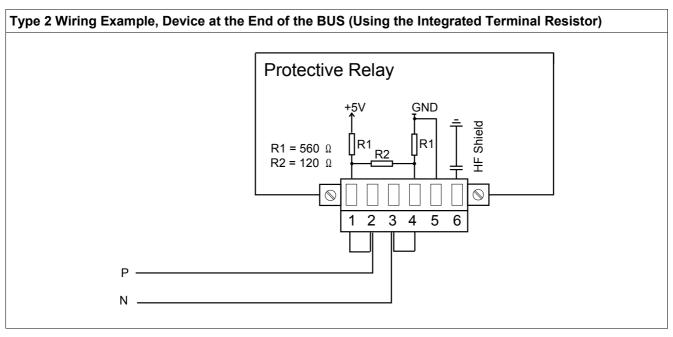


NOTICE

The Modbus® / IEC 60870-5-103 connection cable must be shielded. The shielding has to be fixed at the screw which is marked with the ground symbol at the rear side of the device.

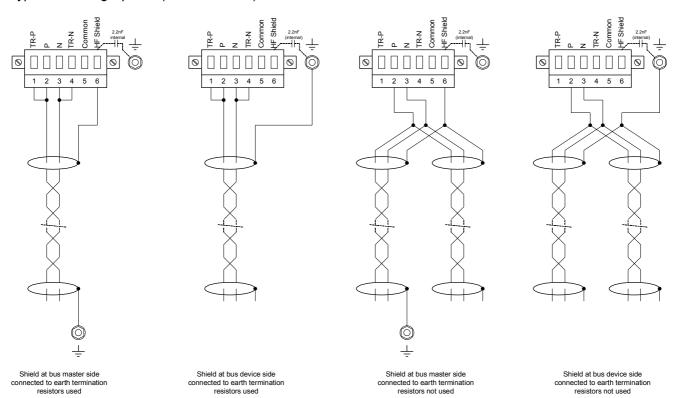
The communication is Halfduplex.



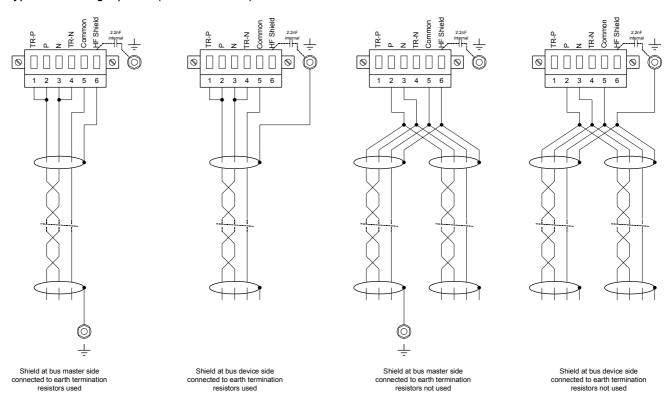


Page 58 EN MRM4 12/11

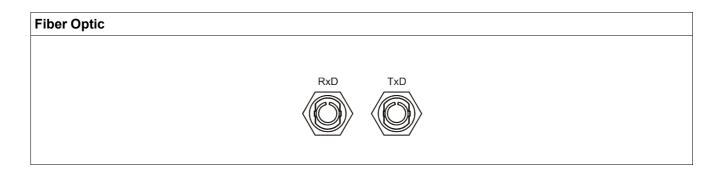
Type 2 Shielding Options (2-wire + Shield)



Type 2 Shielding Options (3-wire + Shield)

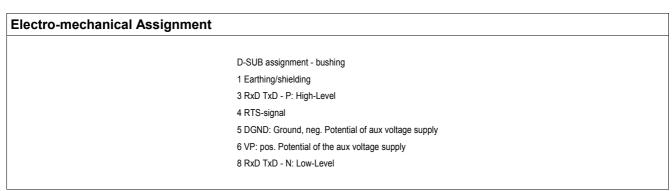


Profibus DP/ Modbus® RTU / IEC 60870-5-103 via fiber optic



Modbus® RTU / IEC 60870-5-103 via D-SUB

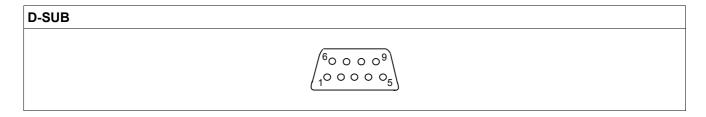
D-SUB		
	$\begin{pmatrix} 6 & 0 & 0 & 0 & 9 \\ 10 & 0 & 0 & 0 & 0 & 5 \end{pmatrix}$	



NOTICE

The connection cable must be shielded. The shielding has to be fixed at the screw which is marked with the ground symbol at the back side of the device.

Profibus DP via D-SUB



Page 60 EN MRM4 12/11

Electro-mechanical Assignment

D-SUB assignment - bushing

1 Earthing/shielding

3 RxD TxD - P: High-Level

4 RTS-signal

5 DGND: Ground, neg. Potential of aux voltage supply

6 VP: pos. Potential of the aux voltage supply

8 RxD TxD - N: Low-Level

NOTICE

The connection cable must be shielded. The shielding has to be fixed at the screw which is marked with the ground symbol at the back side of the device.

Input, Output, and LED Settings

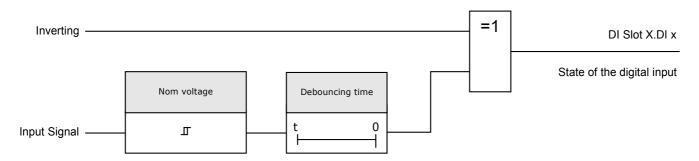
Configuration of the Digital Inputs

CAUTION

Based on the »assignment list«, the states of digital inputs are allocated to the module inputs.

Set the following parameters for each of the digital inputs:

- »Nominal voltage«
- *»Debouncing time«*: A state change will only be adopted by the digital input after the debouncing time has expired.
- »Inverting« (where necessary)



CAUTION

The debouncing time will be started each time the state of the input signal alternates.

CAUTION

In addition to the debouncing time that can be set via software, there is always a hardware debouncing time (approx 12 ms) that cannot be turned off.

DI-8P X

DI Slot X1

Device Parameters of the Digital Inputs on DI-8P X

Parameter	Description	Setting range	Default	Menu path
Nom voltage	Nominal voltage of the digital inputs	24 V DC,	24 V DC	[Device Para
		48 V DC,		/Digital Inputs
		60 V DC,		/DI Slot X1
		110 V DC,		/Group 1]
		230 V DC,		
		110 V AC,		
		230 V AC		

Page 62 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Inverting 1	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
				/Group 1]
Debouncing A change of the state of a digital input will onl	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 1	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
		50 ms,		/DI Slot X1
·	100 ms		/Group 1]	
Nom voltage	Nominal voltage of the digital inputs	24 V DC,	24 V DC	[Device Para
	48 V DC,		/Digital Inputs	
		60 V DC,		/DI Slot X1
		110 V DC,		/Group 2]
		230 V DC,		
		110 V AC,		
	230 V AC			
Inverting 2 Inverting the input signals.	nverting the input signals.	inactive,	inactive	[Device Para
	active		/Digital Inputs	
			/DI Slot X1	
				/Group 2]
time 2 recognized after the debouncing time has expired (become effective). Thus, transient signals will not	no debouncing time,	no debouncing	[Device Para	
	20 ms,	time	/Digital Inputs	
	50 ms,		/DI Slot X1	
		100 ms		/Group 2]
Nom voltage	Nominal voltage of the digital inputs	24 V DC,	24 V DC	[Device Para
		48 V DC,		/Digital Inputs
		60 V DC,		/DI Slot X1
		110 V DC,		/Group 3]
		230 V DC,		
		110 V AC,		
		230 V AC		
nverting 3	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
				/Group 3]
Debouncing	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
ime 3	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
	be misinterpreted.	50 ms,		/DI Slot X1
		100 ms		/Group 3]

Parameter	Description	Setting range	Default	Menu path
Inverting 4	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
			/Group 3]	
	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 4	(become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
		50 ms,		/DI Slot X1
		100 ms		/Group 3]
Inverting 5	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
				/Group 3]
Debouncing	change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 5	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
	be misinterpreted.	50 ms,		/DI Slot X1
		100 ms		/Group 3]
Inverting 6	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
				/Group 3]
	A change of the state of a digital input will only be		no debouncing	[Device Para
time 6	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
	e misinterpreted.	50 ms,		/DI Slot X1
		100 ms		/Group 3]
Inverting 7	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
				/Group 3]
Debouncing	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 7	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
,	be misinterpreted.	50 ms,		/DI Slot X1
		100 ms		/Group 3]
Inverting 8	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1
				/Group 3]

Page 64 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Debouncing time 8	A change of the state of a digital input will only be recognized after the debouncing time has expired (become effective). Thus, transient signals will not be misinterpreted. 8	no debouncing time, 20 ms, 50 ms,	time	[Device Para /Digital Inputs /DI Slot X1
		100 ms		/Group 3]

Signals of the Digital Inputs on DI-8P X

Name	Description
DI 1	Signal: Digital Input
DI 2	Signal: Digital Input
DI 3	Signal: Digital Input
DI 4	Signal: Digital Input
DI 5	Signal: Digital Input
DI 6	Signal: Digital Input
DI 7	Signal: Digital Input
DI 8	Signal: Digital Input

DI-4P X

DI Slot X1

Device Parameters of the Digital Inputs on DI-4P X

Parameter	Description	Setting range	Default	Menu path
Nom voltage	Nominal voltage of the digital inputs	24 V DC,	24 V DC	[Device Para
		48 V DC,		/Digital Inputs
		60 V DC,		/DI Slot X1]
		110 V DC,		
		230 V DC,		
		110 V AC,		
		230 V AC		
Inverting 1	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1]
Debouncing	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 1	time 1 recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
	be misinterpreted.	50 ms,		/DI Slot X1]
		100 ms		

Parameter	Description	Setting range	Default	Menu path
Inverting 2	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1]
Debouncing	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 2	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
	be misinterpreted.	50 ms,		/DI Slot X1]
		100 ms		
Inverting 3	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1]
Debouncing	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 3	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
	be misinterpreted.	50 ms,		/DI Slot X1]
		100 ms		
Inverting 4	Inverting the input signals.	inactive,	inactive	[Device Para
		active		/Digital Inputs
				/DI Slot X1]
Debouncing	A change of the state of a digital input will only be	no debouncing time,	no debouncing	[Device Para
time 4	recognized after the debouncing time has expired (become effective). Thus, transient signals will not	20 ms,	time	/Digital Inputs
be misinterpreted.		50 ms,		/DI Slot X1]
		100 ms		

Signals of the Digital Inputs on DI-4P X

Name	Description
DI 1	Signal: Digital Input
DI 2	Signal: Digital Input
DI 3	Signal: Digital Input
DI 4	Signal: Digital Input

Relay Output Contacts Settings

BO-5 X2

The conditions of module outputs and signals/protective functions (such as reverse interlocking) can be passed by means of alarm relays. The alarm relays are dry-type contacts (which can be used as opening or closing contact). Each alarm relay can be assigned up to 7 functions out of the **assignment list*.

Set the following parameters for each of the binary relay output contacts:

Up to 7 signals from the »assignment list« (OR-connected)

Page 66 EN MRM4 12/11

- Each of the assigned signals can be inverted.
- The (collective) state of the binary relay output contact can be inverted (open or closed circuit current principle)
- By the Operating Mode it can be determined whether the relay output works in working current or closed-circuit principle.
- »Latched« active or inactive
 - »Latched = inactive«:
 If the latching function is **ninactive**, the alarm relay respectively the alarm contact will adopt the state of those alarms that were assigned.
 - »Latched = active«
 If the »latching function« is »active«, the state of the alarm relay respectively alarm contact that was set by the alarms will be stored.

The alarm relay can only be acknowledged after reset of those signals that had initiated setting of the relay and after expiry of the minimum retention time.

• *»Hold time«*: At signal changes, the minimal latching time ensures that the relay will be maintained picked-up or released for at least this period.

CAUTION

If binary outputs are configured »Latched=active«, they will keep (return into) their position even if there is a break within the power supply.

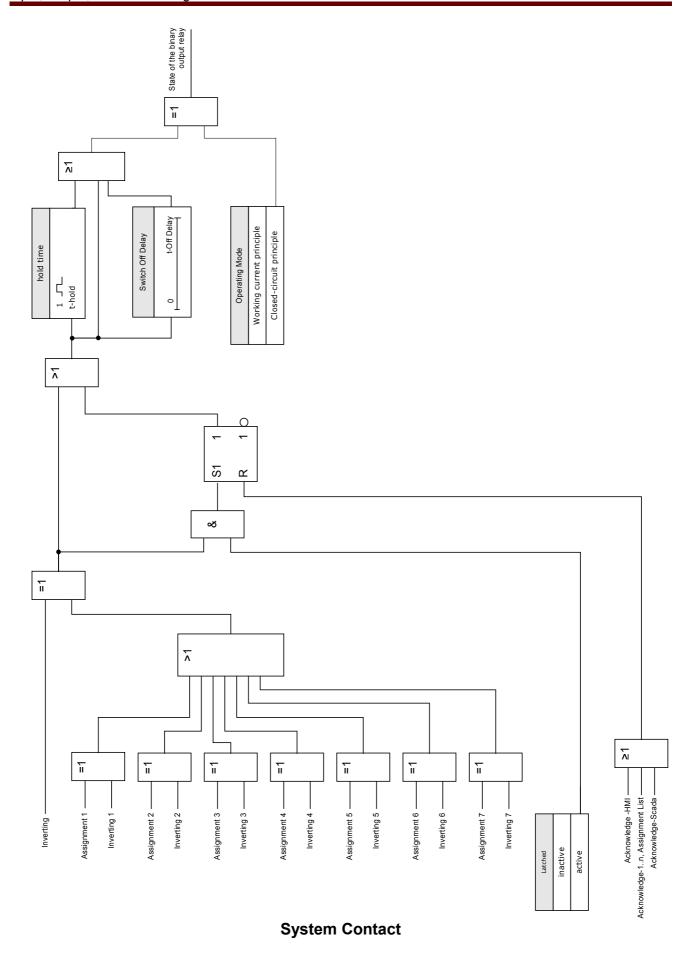
If binary relay output contacts are configured »Latched=active«, The binary output will also retain, if the binary output is reprogrammed in another way. This applies also if »Latched is set to inactive«. Resetting a binary output that has latched a signal will always require an acknowledgment.

NOTICE

The »System OK Relay« (watchdog) cannot be configured.



Relay output contacts can be set by force or disarmed (for commisioning support, please refer to the "Service/Disarming the Relay Output Contact Contacts" and "Service/Forcing the Relay Output Contact Contacts" sections).



Page 68 EN MRM4 12/11

The System OK alarm relay (SC) is the devices »LIFE CONTACT«. Its installation location depends on the housing type. Please refer to the wiring diagram of the device (WDC-contact).

The *System-OK relay (SC)* cannot be configured. The system contact is an operating current contact that picks-up, when the device is free from internal faults. While the device is booting up, the *System OK relay (SC)* remains dropped-off. As soon as the system was duly started up, the relay picks up and the assigned LED is activated accordingly (please refer to chapter Self Supervision).

Direct Commands of OR-5 X

Parameter	Description	Setting range	Default	Menu path
DISARMED	This is the second step, after the "DISARMED Ctrl" has been activated, that is required to DISARM the relay output contacts. This will DISARM those relay output contacts that are currently not latched and that are not on "hold" by a pending minimum hold time. CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: Zone Interlocking and Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance.	inactive, active	inactive	[Service /Test Mode (Prot inhibit) /DISARMED /BO-5 X2]
Force all Outs	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state. Forcing all outputs relays of an entire assembly group is superior to forcing a single relay output contact.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]
Force OR1	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]
Force OR2	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]
Force OR3	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]

Parameter	Description	Setting range	Default	Menu path
Force OR4	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]
Force OR5	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]

Device Parameters of the Binary Relay Output Contacts on OR-5 X

Parameter	Description	Setting range	Default	Menu path
Operating Mode	Operating Mode	Working current principle,	Working current principle	[Device Para
		Closed-circuit principle		/Binary Outputs
				/BO-5 X2
				/BO 1]
t-hold	To clearly identify the state transition of a binary relay output contact, the "new state" is being hold, at least for the duration of the hold time.	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 1]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 1]
Latched	Defines whether the Relay Output will be latched when it picks up.	inactive,	active	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Acknowledg ment	Acknowledgment Signal - An acknowledgment signal (that acknowledges the corresponding binary relay output contact) can be assigned to each relay output contact. The acknowledgment-signal is only effective if the parameter "Latched" is set to active.	1n, Assignment List		[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 1]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]

Page 70 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List	SG.TripCmd	[Device Para
1				/Binary Outputs
				/BO-5 X2
				/BO 1]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/Binary Outputs
				/BO-5 X2
				/BO 1]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
3		-		/Binary Outputs
				/BO-5 X2
				/BO 1]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/Binary Outputs
				/BO-5 X2
				/BO 1]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
J		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/BO-5 X2
				/BO 1]

Parameter	Description	Setting range	Default	Menu path
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/BO-5 X2
				/BO 1]
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/BO-5 X2
				/BO 1]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 1]
Operating	Operating Mode	Working current principle,	Working current principle	[Device Para
Mode		Closed-circuit principle		/Binary Outputs
				/BO-5 X2
				/BO 2]
t-hold	To clearly identify the state transition of a binary relay output contact, the "new state" is being hold, at least for the duration of the hold time.	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 2]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 2]
Latched	Defines whether the Relay Output will be latched when it picks up.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 2]

Page 72 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Acknowledg ment	Acknowledgment Signal - An acknowledgment signal (that acknowledges the corresponding binary relay output contact) can be assigned to each relay output contact. The acknowledgment-signal is only effective if the parameter "Latched" is set to active.	1n, Assignment List		[Device Para /Binary Outputs /BO-5 X2 /BO 2]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive, active	inactive	[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Assignment 1	Assignment	1n, Assignment List	Prot.Alarm	[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Inverting 1	Inverting of the state of the assigned signal.	inactive, active	inactive	[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Assignment 2	Assignment	1n, Assignment List		[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Inverting 2	Inverting of the state of the assigned signal.	inactive, active	inactive	[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Assignment 3	Assignment	1n, Assignment List		[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Inverting 3	Inverting of the state of the assigned signal.	inactive, active	inactive	[Device Para /Binary Outputs /BO-5 X2 /BO 2]
Assignment 4	Assignment	1n, Assignment List		[Device Para /Binary Outputs /BO-5 X2 /BO 2]

Parameter	Description	Setting range	Default	Menu path
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/BO-5 X2
				/BO 2]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/BO-5 X2
				/BO 2]
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/BO-5 X2
				/BO 2]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 2]
Operating	Operating Mode	Working current principle,	Working current	[Device Para
Mode		Closed-circuit principle	principle	/Binary Outputs
				/BO-5 X2
				/BO 3]
t-hold	To clearly identify the state transition of a binary	0.00 – 300.00 s	0.00 s	[Device Para
	relay output contact, the "new state" is being hold, at least for the duration of the hold time.			/Binary Outputs
	at 1930t for the daration of the field time.			/BO-5 X2
				/BO 3]

Page 74 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 3]
Latched	Defines whether the Relay Output will be latched	inactive,	inactive	[Device Para
	when it picks up.	active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Acknowledg	Acknowledgment Signal - An acknowledgment	1n, Assignment List		[Device Para
ment	signal (that acknowledges the corresponding binary relay output contact) can be assigned to			/Binary Outputs
	each relay output contact. The acknowledgment-			/BO-5 X2
	signal is only effective if the parameter "Latched" is set to active.			/BO 3]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List	SG.ON Cmd	[Device Para
1				/Binary Outputs
				/BO-5 X2
				/BO 3]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/Binary Outputs
				/BO-5 X2
				/BO 3]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/Binary Outputs
				/BO-5 X2
				/BO 3]

Parameter	Description	Setting range	Default	Menu path
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
4				/Binary Outputs
				/BO-5 X2
				/BO 3]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/BO-5 X2
				/BO 3]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/BO-5 X2
				/BO 3]
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/BO-5 X2
				/BO 3]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 3]

Page 76 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Operating	Operating Mode	Working current principle,	Working current	[Device Para
Mode		Closed-circuit principle	principle	/Binary Outputs
				/BO-5 X2
				/BO 4]
t-hold	To clearly identify the state transition of a binary	0.00 – 300.00 s	0.00 s	[Device Para
	relay output contact, the "new state" is being hold, at least for the duration of the hold time.			/Binary Outputs
	at load to the duration of the held time.			/BO-5 X2
				/BO 4]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 4]
Latched	Defines whether the Relay Output will be latched	inactive,	inactive	[Device Para
	when it picks up.	active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Acknowledg	Acknowledgment Signal - An acknowledgment	1n, Assignment List		[Device Para
ment	signal (that acknowledges the corresponding binary relay output contact) can be assigned to			/Binary Outputs
	each relay output contact. The acknowledgment-			/BO-5 X2
	signal is only effective if the parameter "Latched" is set to active.			/BO 4]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Assignment	Assignment	1n, Assignment List	SG.OFF Cmd	[Device Para
1				/Binary Outputs
				/BO-5 X2
				/BO 4]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/Binary Outputs
				/BO-5 X2
				/BO 4]

Parameter	Description	Setting range	Default	Menu path
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/Binary Outputs
				/BO-5 X2
				/BO 4]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/Binary Outputs
				/BO-5 X2
				/BO 4]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/BO-5 X2
				/BO 4]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/BO-5 X2
				/BO 4]
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]

Page 78 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/BO-5 X2
				/BO 4]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 4]
Operating	Operating Mode	Working current principle,	Working current	[Device Para
Mode		Closed-circuit principle	principle	/Binary Outputs
				/BO-5 X2
				/BO 5]
t-hold	To clearly identify the state transition of a binary	0.00 – 300.00 s	0.00 s	[Device Para
	relay output contact, the "new state" is being hold, at least for the duration of the hold time.			/Binary Outputs
	at least for the daration of the field time.			/BO-5 X2
				/BO 5]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 5]
Latched	Defines whether the Relay Output will be latched	inactive,	inactive	[Device Para
	when it picks up.	active		/Binary Outputs
				/BO-5 X2
				/BO 5]
Acknowledg	Acknowledgment Signal - An acknowledgment	1n, Assignment List		[Device Para
ment	signal (that acknowledges the corresponding binary relay output contact) can be assigned to			/Binary Outputs
	each relay output contact. The acknowledgment-			/BO-5 X2
	signal is only effective if the parameter "Latched" is set to active.			/BO 5]
	Only available if I ataked - active			
Inverting	Only available if: Latched = active Inverting of the Binary Relay Output Contact.	inactive,	inactive	[Device Para
mverting	involving of the binary relay output contact.	active	HIGOLIVE	/Binary Outputs
		active		/Binary Outputs /BO-5 X2
				/BO-5 A2 /BO 5]
Accianment	Assignment	1 n Assignment List	MStart Pla	-
Assignment 1	Assignment	1n, Assignment List	MStart.Blo	[Device Para
				/Binary Outputs
				/BO-5 X2
				/BO 5]

Parameter	Description	Setting range	Default	Menu path
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 5]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/Binary Outputs
				/BO-5 X2
				/BO 5]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 5]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/Binary Outputs
				/BO-5 X2
				/BO 5]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 5]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/Binary Outputs
				/BO-5 X2
				/BO 5]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 5]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/BO-5 X2
				/BO 5]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/BO-5 X2
				/BO 5]

Page 80 EN MRM4 12/11

	Setting range	Default	Menu path
Assignment	1n, Assignment List		[Device Para
			/Binary Outputs
			/BO-5 X2
			/BO 5]
Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
	active		/Binary Outputs
			/BO-5 X2
			/BO 5]
Assignment	1n, Assignment List		[Device Para
			/Binary Outputs
			/BO-5 X2
			/BO 5]
Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
	active		/Binary Outputs
			/BO-5 X2
			/BO 5]
Enables and disables the disarming of the relay	inactive,	inactive	[Service
process, to inhibit the operation or the relay output	active		/Test Mode (Prot inhibit)
second step.			/DISARMED
			/BO-5 X2]
CAUTION!RELAYS DISARMED in order to safely	permanent,	permanent	[Service
taking an entire process off-line. (Note: The	Timeout		/Test Mode (Prot inhibit)
MUST ENSURE that the relays are ARMED			/DISARMED
AGAIN after maintenance.			/BO-5 X2]
The relays will be armed again after expiring of	0.00 – 300.00 s	0.03 s	[Service
this time.			/Test Mode (Prot inhibit)
Only available if: Mode = Timeout DISARM			/DISARMED
			/BO-5 X2]
By means of this function the normal Relay Output	permanent,	permanent	[Service
Contact States can be overwritten (forced) in case that the Relay is not in a disarmed state. The	Timeout		/Test Mode (Prot inhibit)
works according to the assigned signals) to "force			/Force OR
energized" or "force de-energized" state.			/BO-5 X2]
	Inverting of the state of the assigned signal. Assignment Inverting of the state of the assigned signal. Enables and disables the disarming of the relay output contacts. This is the first step of a two step process, to inhibit the operation or the relay output contacts. Please refer to "DISARMED" for the second step. CAUTION!RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance. The relays will be armed again after expiring of this time. Only available if: Mode = Timeout DISARM By means of this function the normal Relay Output Contact States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force	Inverting of the state of the assigned signal. Assignment 1n, Assignment List Inverting of the state of the assigned signal. Inverting of the state of the assigned signal. Inverting of the state of the assigned signal. Inactive, active Enables and disables the disarming of the relay output contacts. This is the first step of a two step process, to inhibit the operation or the relay output contacts. Please refer to "DISARMED" for the second step. CAUTION!RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance. The relays will be armed again after expiring of this time. Only available if: Mode = Timeout DISARM By means of this function the normal Relay Output Contact States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force	Inverting of the state of the assigned signal. Assignment 1n, Assignment List Inverting of the state of the assigned signal. Inverting of the state of the assigned signal. Enables and disables the disarming of the relay output contacts. This is the first step of a two step process, to inhibit the operation or the relay output contacts. This is the first step of a two step process, to inhibit the operation or the relay output contacts. Please refer to "DISARMED" for the second step. CAUTIONIRELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance. The relays will be armed again after expiring of this time. Only available if: Mode = Timeout DISARM By means of this function the normal Relay Output Contact States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force"

Parameter	Description	Setting range	Default	Menu path
Force	The Output State will be set by force for the duration of this time. That means for the duration of this time the Relay Output Contact does not show the state of the signals that are assigned on it. Only available if: Mode = Timeout DISARM	0.00 – 300.00 s	0.03 s	[Service /Test Mode (Prot inhibit) /Force OR /BO-5 X2]

Input States of the Binary Relay Output Contacts on OR-5 X

Name	Description	Assignment via
BO1.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]
BO1.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]
BO1.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]
BO1.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]
BO1.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]
BO1.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]
BO1.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 1]

Page 82 EN MRM4 12/11

Name	Description	Assignment via
Ack signal BO 1	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/BO-5 X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 1]
BO2.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
BO2.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
BO2.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
BO2.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
BO2.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
BO2.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
BO2.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 2]
Ack signal BO 2	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/BO-5 X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 2]

Name	Description	Assignment via
BO3.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
BO3.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
BO3.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
BO3.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
BO3.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
BO3.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
BO3.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 3]
Ack signal BO 3	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/BO-5 X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 3]
BO4.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]

Page 84 EN MRM4 12/11

Name	Description	Assignment via
BO4.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]
BO4.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]
BO4.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]
BO4.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]
BO4.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]
BO4.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 4]
Ack signal BO 4	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/BO-5 X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 4]
BO5.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]
BO5.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]

Name	Description	Assignment via
BO5.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]
BO5.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]
BO5.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]
BO5.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]
BO5.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/BO-5 X2
		/BO 5]
Ack signal BO 5	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/BO-5 X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 5]

Signals of the Binary Relay Output Contacts on OR-5 X

Name	Description
BO 1	Signal: Binary Relay Output Contact
BO 2	Signal: Binary Relay Output Contact
BO 3	Signal: Binary Relay Output Contact
BO 4	Signal: Binary Relay Output Contact
BO 5	Signal: Binary Relay Output Contact
DISARMED!	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
Outs forced	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.

Page 86 EN MRM4 12/11

OR-3AI X

<u>OR-3AI X2</u>

Direct Commands of OR-3AIX

Parameter	Description	Setting range	Default	Menu path
DISARMED	This is the second step, after the "DISARMED Ctrl" has been activated, that is required to DISARM the relay output contacts. This will DISARM those relay output contacts that are currently not latched and that are not on "hold" by a pending minimum hold time. CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: Zone Interlocking and Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance.	inactive, active	inactive	[Service /Test Mode (Prot inhibit) /DISARMED /OR-3AI X2]
Force all Outs	Only available if: DISARMED Ctrl = active By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state. Forcing all outputs relays of an entire assembly group is superior to forcing a single relay output contact.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /OR-3AI X2]
Force OR1	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /OR-3AI X2]
Force OR2	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /OR-3AI X2]
Force OR3	By means of this function the normal Relay Output Contact State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	Normal, De-Energized, Energized	Normal	[Service /Test Mode (Prot inhibit) /Force OR /OR-3AI X2]

Device Parameters of the Binary Relay Output Contacts on OR- 3AI X

Parameter	Description	Setting range	Default	Menu path
Operating	Operating Mode	Working current principle,	Working current	[Device Para
Mode		Closed-circuit principle	principle	/Binary Outputs
				/OR-3AI X2
				/BO 1]
t-hold	To clearly identify the state transition of a binary	0.00 – 300.00 s	0.00 s	[Device Para
	relay output contact, the "new state" is being hold, at least for the duration of the hold time.			/Binary Outputs
	at least for the duration of the note time.			/OR-3AI X2
				/BO 1]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Latched	Defines whether the Relay Output will be latched	inactive,	active	[Device Para
	when it picks up.	active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Acknowledg	Acknowledgment Signal - An acknowledgment	1n, Assignment List		[Device Para
ment	signal (that acknowledges the corresponding binary relay output contact) can be assigned to			/Binary Outputs
	each relay output contact. The acknowledgment-			/OR-3AI X2
	signal is only effective if the parameter "Latched" is set to active.			/BO 1]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Assignment	Assignment	1n, Assignment List	SG.TripCmd	[Device Para
1				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]

Page 88 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List		[Device Para
2				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/OR-3AI X2
				/BO 1]

Parameter	Description	Setting range	Default	Menu path
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/OR-3AI X2
				/BO 1]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 1]
Operating	Operating Mode	Working current principle,	Working current	[Device Para
Mode		Closed-circuit principle	principle	/Binary Outputs
				/OR-3AI X2
				/BO 2]
t-hold	To clearly identify the state transition of a binary relay output contact, the "new state" is being hold, at least for the duration of the hold time.		0.00 s	[Device Para
				/Binary Outputs
				/OR-3AI X2
				/BO 2]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Latched	Defines whether the Relay Output will be latched	inactive,	inactive	[Device Para
	when it picks up.	active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Acknowledg	Acknowledgment Signal - An acknowledgment	1n, Assignment List		[Device Para
ment	signal (that acknowledges the corresponding binary relay output contact) can be assigned to			/Binary Outputs
	each relay output contact. The acknowledgment-			/OR-3AI X2
	signal is only effective if the parameter "Latched" is set to active.			/BO 2]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]

Page 90 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List	SG.ON Cmd	[Device Para
1				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
5				/Binary Outputs
				/OR-3AI X2
				/BO 2]

Parameter	Description	Setting range	Default	Menu path
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/OR-3AI X2
				/BO 2]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 2]
Operating	Operating Mode	Working current principle,	Working current	[Device Para
Mode		Closed-circuit principle principle	principle	/Binary Outputs
				/OR-3AI X2
				/BO 3]
t-hold	To clearly identify the state transition of a binary	0.00 – 300.00 s	0.00 s	[Device Para
	relay output contact, the "new state" is being hold, at least for the duration of the hold time.			/Binary Outputs
	at look to the duration of the held time.			/OR-3AI X2
				/BO 3]
t-Off Delay	Switch Off Delay	0.00 – 300.00 s	0.00 s	[Device Para
				/Binary Outputs
				/OR-3AI X2
				/BO 3]
Latched	Defines whether the Relay Output will be latched	inactive,	inactive	[Device Para
	when it picks up.	active		/Binary Outputs
				/OR-3AI X2
				/BO 3]

Page 92 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Acknowledg ment	Acknowledgment Signal - An acknowledgment signal (that acknowledges the corresponding binary relay output contact) can be assigned to each relay output contact. The acknowledgment-signal is only effective if the parameter "Latched" is set to active.	1n, Assignment List		[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
	Only available if: Latched = active			
Inverting	Inverting of the Binary Relay Output Contact.	inactive, active	inactive	[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Assignment 1	Assignment	1n, Assignment List	SG.OFF Cmd	[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Inverting 1	Inverting of the state of the assigned signal.	inactive, active	inactive	[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Assignment 2	Assignment	1n, Assignment List		[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Inverting 2	Inverting of the state of the assigned signal.	inactive, active	inactive	[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Assignment 3	Assignment	1n, Assignment List		[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Inverting 3	Inverting of the state of the assigned signal.	inactive, active	inactive	[Device Para /Binary Outputs /OR-3AI X2 /BO 3]
Assignment 4	Assignment	1n, Assignment List		[Device Para /Binary Outputs /OR-3AI X2 /BO 3]

Parameter	Description	Setting range	Default	Menu path
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Binary Outputs
				/OR-3AI X2
				/BO 3]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
6				/Binary Outputs
				/OR-3AI X2
				/BO 3]
Inverting 6	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 3]
Assignment	Assignment	1n, Assignment List		[Device Para
7				/Binary Outputs
				/OR-3AI X2
				/BO 3]
Inverting 7	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/Binary Outputs
				/OR-3AI X2
				/BO 3]
DISARMED	Enables and disables the disarming of the relay	inactive,	inactive	[Service
Ctrl	output contacts. This is the first step of a two step process, to inhibit the operation or the relay output contacts. Please refer to "DISARMED" for the	active		/Test Mode (Prot inhibit)
	second step.			/DISARMED
				/OR-3AI X2]
Disarm Mode	CAUTION!RELAYS DISARMED in order to safely	permanent,	permanent	[Service
	perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Supervision Contact cannot be disarmed). YOU	Timeout		/Test Mode (Prot inhibit)
	MUST ENSURE that the relays are ARMED			/DISARMED
	AGAIN after maintenance.			/OR-3AI X2]

Page 94 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
t-Timeout DISARM	, , ,	0.00 – 300.00 s	0.03 s	[Service
DISARIVI	this time. Only available if: Mode = Timeout DISARM			/Test Mode (Prot inhibit)
	Only available ii. Mode — Timeout Bio/itti			/DISARMED
				/OR-3AI X2]
Force Mode	By means of this function the normal Relay Output	permanent,	permanent	[Service
	Contact States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force	Timeout		/Test Mode (Prot inhibit)
				/Force OR
	energized" or "force de-energized" state.			/OR-3AI X2]
t-Timeout	The Output State will be set by force for the	0.00 – 300.00 s	0.03 s	[Service
Force	duration of this time. That means for the duration of this time the Relay Output Contact does not show the state of the signals that are assigned on it.			/Test Mode (Prot inhibit)
				/Force OR
	Only available if: Mode = Timeout DISARM			/OR-3AI X2]

Input States of the Binary Relay Output Contacts on OR- 3Al X

Name	Description	Assignment via
BO1.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]
BO1.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]
BO1.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]
BO1.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]
BO1.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]

Name	Description	Assignment via
BO1.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]
BO1.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 1]
Ack signal BO 1	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/OR-3AI X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 1]
BO2.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]
BO2.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]
BO2.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]
BO2.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]
BO2.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]
BO2.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]

Page 96 EN MRM4 12/11

Name	Description	Assignment via
BO2.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 2]
Ack signal BO 2	Module input state: Acknowledgment signal	[Device Para
	for the binary relay output contact. If latching is set to active, the binary relay	/Binary Outputs
	output contact can only be acknowledged if	/OR-3AI X2
	those signals that initiated the setting are fallen back and the hold time is expired.	/BO 2]
BO3.1	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]
BO3.2	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]
BO3.3	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]
BO3.4	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]
BO3.5	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]
BO3.6	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]
BO3.7	Module input state: Assignment	[Device Para
		/Binary Outputs
		/OR-3AI X2
		/BO 3]

Name	Description	Assignment via
Ack signal BO 3	Module input state: Acknowledgment signal for the binary relay output contact. If latching is set to active, the binary relay output contact can only be acknowledged if those signals that initiated the setting are fallen back and the hold time is expired.	[Device Para /Binary Outputs /OR-3AI X2 /BO 3]

Signals of the Binary Relay Output Contacts on OR-3AI X

Name	Description
BO 1	Signal: Binary Relay Output Contact
BO 2	Signal: Binary Relay Output Contact
BO 3	Signal: Binary Relay Output Contact
DISARMED!	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
Outs forced	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.

LED configuration

The LEDs can be configured within menu:

[Device Para/LEDs/Group X]

CAUTION

Attention must be paid that there are no overlapping functions due to double or multiple LED assignment of colors and flashing codes.

CAUTION

If LEDs are configured »Latched=active«, they will keep (return into) their blink code/color even if there is a break within the power supply.

If LEDs are configured »Latched=active«, The LED blink code will also retain, if the LED is reprogrammed in another way. This applies also if »Latched is set to inactive«. Resetting a LED that has latched a signal will always require an acknowledgment.

NOTICE

This chapter contains information on the LEDs that are placed on the left hand of the display (group A).

If your device is also equipped with LEDs on the right hand of the display (group B), the information in this chapter is valid analog. The only difference is "group A" and "group B" within the menu paths.

Via push button »INFO« it is always possible to display the current alarms/alarm texts that are assigned to an LED. Please refer to chapter *Navigation* (description of the »INFO-key«).

Page 98 EN MRM4 12/11

Set the following parameters for each LED:

- »Latching/self holding function«: If »Latching« is set to »active«, the state that is set by the alarms will be stored. If latching »Latching« is set to »inactive«, the LED always adopts the state of those alarms that were assigned.
- »Acknowledgment« (signal from the »assignment list«)
- *»LED active color«*, LED lights up in this color in case that at least one of the allocated functions is valid (red, red flashing, green, green flashing, off).
- *»LED inactive color«*, LED lights up in this color in case that none of the allocated functions is valid (red, red flashing, green, green flashing, off).
- Apart from the LED for System OK, each LED can be assigned up to five functions/alarms out of the »assignment list«.
- »Inverting« (of the signals), if necessary.

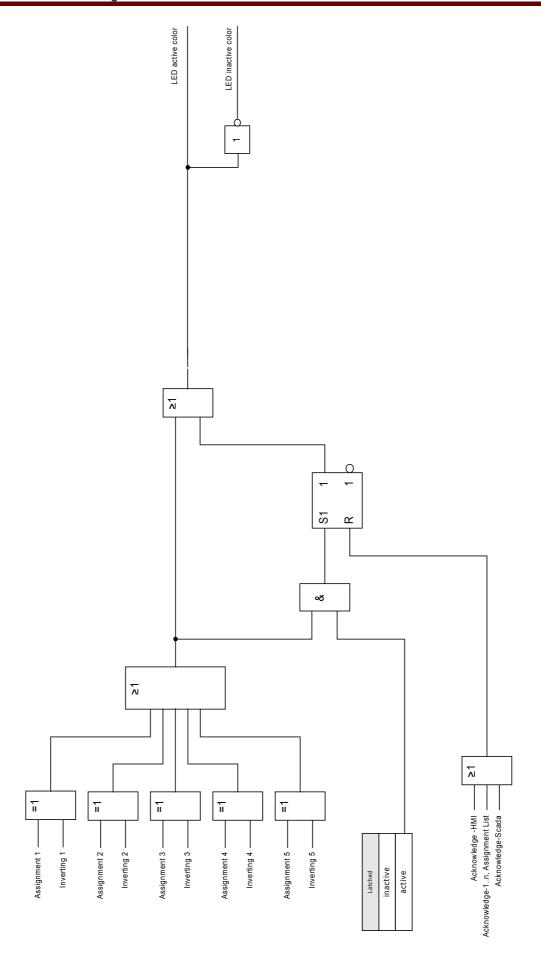
Acknowledgment options

LEDs can be acknowledged by:

- Via the push-button »C« at the operating panel.
- Each LED can be acknowledged by a signal of the »assignment list« (If »Latched = active«).
- Via the module »Ex Acknowledge« all LEDs can be acknowledged at once, if the signal for external
 acknowledgment that was selected from the »assignment list« becomes true (e.g. the state of a digital
 input).
- Via SCADA, all LEDs can be acknowledged at once.



The Product-CD that is delivered with the device contains a PDF-Template in order to create and print out self adhesive films for LED assignment texts (front foil) by means of a laser printer. Recommendation: (AVERY Zweckform Art.Nr.3482)



Page 100 EN MRM4 12/11

The »System OK« LED

This LED flashes green while the device is booting. After completed booting, the LED for *System OK* lights up in green thus signalizing that the *protection* (function) is **activated***. If, however, in spite of successful booting, or after the third unsuccessful reboot caused by the module self supervision the *System OK – LED* flashes in red or is red illuminated, please contact the *Woodward Kempen GmbH* – Service Dept (See also chapter Self Supervision).

LED System OK cannot be configured.

Global Protection Parameters of the LED Module

<u>LED</u>

Parameter	Description	Setting range	Default	Menu path
Latched	Defines whether the LED will be latched when it picks up.	inactive,	active	[Device Para
		active		/LEDs
				/LED 1]
Ack signal	Acknowledgment signal for the LED. If latching is	1n, Assignment List		[Device Para
	set to active the LED can only be acknowledged if those signals that initiated the setting are no			/LEDs
	longer present.			/LED 1]
	Dependency Only available if: Latched = active			
LED active	The LED lights up in this color if the state of the	green,	red	[Device Para
color	OR-assignment of the signals is true.	red,		/LEDs
		red flash,		/LED 1]
		green flash,		
		-		
LED inactive	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	green,	-	[Device Para
color		red,		/LEDs
		red flash,		/LED 1]
		green flash,		
		-		
Assignment	Assignment	1n, Assignment List	SG.TripCmd	[Device Para
1				/LEDs
				/LED 1]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 1]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/LEDs
				/LED 1]

Page 102 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 1]
	Assignment	1n, Assignment List		[Device Para
3				/LEDs
				/LED 1]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 1]
	Assignment	1n, Assignment List		[Device Para
4				/LEDs
				/LED 1]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 1]
	Assignment	1n, Assignment List		[Device Para
5		-		/LEDs
				/LED 1]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 1]
	Defines whether the LED will be latched when it picks up.	inactive,	active	[Device Para
	ρίσκο αφ.	active		/LEDs
				/LED 2]
	Acknowledgment signal for the LED. If latching is set to active the LED can only be acknowledged if	1n, Assignment List		[Device Para
	those signals that initiated the setting are no			/LEDs
	longer present.			/LED 2]
	Only available if: Latched = active			
	The LED lights up in this color if the state of the	green,	red	[Device Para
color	OR-assignment of the signals is true.	red,		/LEDs
		red flash,		/LED 2]
		green flash,		
		-		
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	green,	-	[Device Para
color		red,		/LEDs
		red flash,		/LED 2]
		green flash,		
		-		

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List	Prot.Alarm	[Device Para
1				/LEDs
				/LED 2]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 2]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/LEDs
				/LED 2]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 2]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
3				/LEDs
				/LED 2]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 2]
Assignment	Assignment	1n, Assignment List		[Device Para
4		-		/LEDs
				/LED 2]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 2]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
5				/LEDs
				/LED 2]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 2]
Latched	Defines whether the LED will be latched when it	inactive,	active	[Device Para
	picks up.	active		/LEDs
				/LED 3]
Ack signal	Acknowledgment signal for the LED. If latching is	1n, Assignment List	-,-	[Device Para
	set to active the LED can only be acknowledged if those signals that initiated the setting are no			/LEDs
	longer present.			/LED 3]
	Only available if: Latched = active			
	Only available ii. Latoried - active			

Page 104 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
LED active color	The LED lights up in this color if the state of the OR-assignment of the signals is true.	green,	red	[Device Para
		red,		/LEDs
		red flash,		/LED 3]
		green flash,		
		-		
LED inactive	The LED lights up in this color if the state of the	green,	-	[Device Para
color	OR-assignment of the signals is untrue.	red,		/LEDs
		red flash,		/LED 3]
		green flash,		
		-		
Assignment	Assignment	1n, Assignment List	ThR.Alarm	[Device Para
1				/LEDs
				/LED 3]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 3]
Assignment	Assignment	1n, Assignment List	I[1].Alarm	[Device Para
2				/LEDs
				/LED 3]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 3]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/LEDs
				/LED 3]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 3]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/LEDs
				/LED 3]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 3]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
5				/LEDs
				/LED 3]

Parameter	Description	Setting range	Default	Menu path
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 3]
Latched	Defines whether the LED will be latched when it	inactive,	active	[Device Para
	picks up.	active		/LEDs
				/LED 4]
Ack signal	Acknowledgment signal for the LED. If latching is	1n, Assignment List		[Device Para
	set to active the LED can only be acknowledged if			/LEDs
	those signals that initiated the setting are no longer present.			/LED 4]
	Only available if: Latched = active			
LED active	The LED lights up in this color if the state of the	green,	red	[Device Para
color	OR-assignment of the signals is true.	red,		/LEDs
		red flash,		/LED 4]
		green flash,		
		-		
LED inactive	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	green,	-	[Device Para
color		red,		/LEDs
		red flash,		/LED 4]
		green flash,		
		-		
Assignment	Assignment	1n, Assignment List	MStart.Blo	[Device Para
1				/LEDs
				/LED 4]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 4]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/LEDs
				/LED 4]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 4]
Assignment	Assignment	1n, Assignment List		[Device Para
3		-		/LEDs
				/LED 4]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
J		active		/LEDs
				/LED 4]

Page 106 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List		[Device Para
4				/LEDs
				/LED 4]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 4]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/LEDs
				/LED 4]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
- I		active		/LEDs
				/LED 4]
Latched	Defines whether the LED will be latched when it	inactive,	inactive	[Device Para
	picks up.	active		/LEDs
				/LED 5]
Ack signal	Acknowledgment signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	1n, Assignment List	-,-	[Device Para
Ü				/LEDs
				/LED 5]
LED	Only available if: Latched = active		and the ab	ID. in Deep
LED active color	The LED lights up in this color if the state of the OR-assignment of the signals is true.	green,	red flash	[Device Para
		red,		/LEDs
		red flash,		/LED 5]
		green flash,		
LEDiane	The LED Follows is this color of the color of the	-		ID. in Deep
LED inactive color	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	green,	-	[Device Para
	or accignment or the digital of a marco	red,		/LEDs
		red flash,		/LED 5]
		green flash,		
		-		
Assignment 1	Assignment	1n, Assignment List	MStart.Start	[Device Para
•				/LEDs
				/LED 5]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 5]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/LEDs
				/LED 5]

Parameter	Description	Setting range	Default	Menu path
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 5]
Assignment 3	Assignment	1n, Assignment List		[Device Para
				/LEDs
				/LED 5]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 5]
Assignment 4	Assignment	1n, Assignment List		[Device Para
				/LEDs
				/LED 5]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 5]
Assignment 5	Assignment	1n, Assignment List		[Device Para
				/LEDs
				/LED 5]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 5]
Latched	Defines whether the LED will be latched when it picks up.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 6]
Ack signal	Acknowledgment signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	1n, Assignment List		[Device Para
				/LEDs
				/LED 6]
				-
	Only available if: Latched = active	aroon	rod	[Davies Dars
LED active color	The LED lights up in this color if the state of the OR-assignment of the signals is true.	green,	red	[Device Para
		red,		/LEDs
		red flash,		/LED 6]
		green flash,		
LED inactive	The LED lights up in this color if the state of the	- aroon		[Davino Para
LED inactive color	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	green,	-	[Device Para
		red,		/LEDs
		red flash,		/LED 6]
		green flash,		

Page 108 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List	MStart.Run	[Device Para
1				/LEDs
				/LED 6]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 6]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/LEDs
				/LED 6]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 6]
Assignment	Assignment	1n, Assignment List		[Device Para
3	-			/LEDs
				/LED 6]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 6]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/LEDs
				/LED 6]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 6]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/LEDs
				/LED 6]
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 6]
Latched	Defines whether the LED will be latched when it	inactive,	inactive	[Device Para
	picks up.	active		/LEDs
				/LED 7]
Ack signal	Acknowledgment signal for the LED. If latching is	1n, Assignment List		[Device Para
-	set to active the LED can only be acknowledged if			/LEDs
	those signals that initiated the setting are no longer present.			/LED 7]
	Only available if: Latched = active			

Parameter	Description	Setting range	Default	Menu path
LED active	The LED lights up in this color if the state of the	green,	green	[Device Para
color	OR-assignment of the signals is true.	red,		/LEDs
		red flash,		/LED 7]
		green flash,		
		-		
LED inactive	The LED lights up in this color if the state of the	green,	-	[Device Para
color	OR-assignment of the signals is untrue.	red,		/LEDs
		red flash,		/LED 7]
		green flash,		
		-		
Assignment	Assignment	1n, Assignment List	MStart.Stop	[Device Para
1				/LEDs
				/LED 7]
Inverting 1	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 7]
Assignment	Assignment	1n, Assignment List		[Device Para
2				/LEDs
				/LED 7]
Inverting 2	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 7]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/LEDs
				/LED 7]
Inverting 3	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 7]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/LEDs
				/LED 7]
Inverting 4	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 7]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/LEDs
				/LED 7]

Page 110 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Inverting 5	Inverting of the state of the assigned signal.	inactive,	inactive	[Device Para
		active		/LEDs
				/LED 7]

LED Module Input States

Name	Description	Assignment via
LED1.1	Module input state: LED	[Device Para
		/LEDs
		/LED 1]
LED1.2	Module input state: LED	[Device Para
		/LEDs
		/LED 1]
LED1.3	Module input state: LED	[Device Para
		/LEDs
		/LED 1]
LED1.4	Module input state: LED	[Device Para
		/LEDs
		/LED 1]
LED1.5	Module input state: LED	[Device Para
		/LEDs
		/LED 1]
Acknow Sig 1	(only for automatic acknowledgment)	[Device Para
	(only for automatic acknowledgment)	/LEDs
		/LED 1]
LED2.1	Module input state: LED	[Device Para
		/LEDs
		/LED 2]
LED2.2	Module input state: LED	[Device Para
		/LEDs
		/LED 2]
LED2.3	Module input state: LED	[Device Para
		/LEDs
		/LED 2]
LED2.4	Module input state: LED	[Device Para
		/LEDs
		/LED 2]

Name	Description	Assignment via
LED2.5	Module input state: LED	[Device Para
		/LEDs
		/LED 2]
Acknow Sig 2	Module input state: Acknowledgment Signal	[Device Para
	(only for automatic acknowledgment)	/LEDs
		/LED 2]
LED3.1	Module input state: LED	[Device Para
		/LEDs
		/LED 3]
LED3.2	Module input state: LED	[Device Para
		/LEDs
		/LED 3]
LED3.3	Module input state: LED	[Device Para
		/LEDs
		/LED 3]
LED3.4	Module input state: LED	[Device Para
		/LEDs
		/LED 3]
LED3.5	Module input state: LED	[Device Para
		/LEDs
		/LED 3]
Acknow Sig 3	Module input state: Acknowledgment Signal	[Device Para
	(only for automatic acknowledgment)	/LEDs
		/LED 3]
LED4.1	Module input state: LED	[Device Para
		/LEDs
		/LED 4]
LED4.2	Module input state: LED	[Device Para
		/LEDs
		/LED 4]
LED4.3	Module input state: LED	[Device Para
		/LEDs
		/LED 4]
LED4.4	Module input state: LED	[Device Para
		/LEDs
		/LED 4]
LED4.5	Module input state: LED	[Device Para
		/LEDs
		/LED 4]

Page 112 EN MRM4 12/11

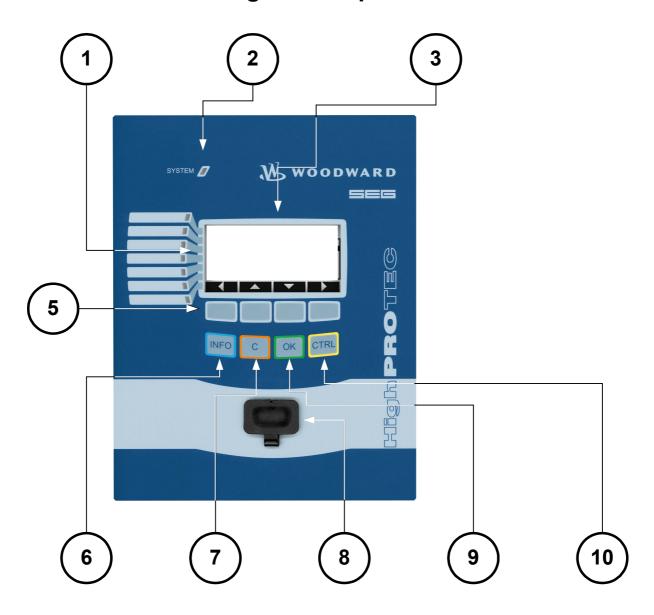
Name	Description	Assignment via
Acknow Sig 4	Module input state: Acknowledgment Signal	[Device Para
	(only for automatic acknowledgment)	/LEDs
		/LED 4]
LED5.1	Module input state: LED	[Device Para
		/LEDs
		/LED 5]
LED5.2	Module input state: LED	[Device Para
		/LEDs
		/LED 5]
LED5.3	Module input state: LED	[Device Para
		/LEDs
		/LED 5]
LED5.4	Module input state: LED	[Device Para
		/LEDs
		/LED 5]
LED5.5	Module input state: LED	[Device Para
		/LEDs
		/LED 5]
Acknow Sig 5	Module input state: Acknowledgment Signal	[Device Para
	(only for automatic acknowledgment)	/LEDs
		/LED 5]
LED6.1	Module input state: LED	[Device Para
		/LEDs
		/LED 6]
LED6.2	Module input state: LED	[Device Para
		/LEDs
		/LED 6]
LED6.3	Module input state: LED	[Device Para
		/LEDs
		/LED 6]
LED6.4	Module input state: LED	[Device Para
		/LEDs
		/LED 6]
LED6.5	Module input state: LED	[Device Para
		/LEDs
		/LED 6]
Acknow Sig 6	(only for automatic acknowledgment)	[Device Para
		/LEDs
		/LED 6]

Input, Output, and LED Settings

Name	Description	Assignment via
LED7.1	Module input state: LED	[Device Para
		/LEDs
		/LED 7]
LED7.2	Module input state: LED	[Device Para
		/LEDs
		/LED 7]
LED7.3	Module input state: LED	[Device Para
		/LEDs
		/LED 7]
LED7.4	Module input state: LED	[Device Para
		/LEDs
		/LED 7]
LED7.5	Module input state: LED	[Device Para
		/LEDs
		/LED 7]
Acknow Sig 7	(only for automatic acknowledgment)	[Device Para
		/LEDs
		/LED 7]

Page 114 EN MRM4 12/11

Navigation - Operation



1		LEDs	Messages inform you about operational conditions, system data or other device particulars. They additionally provide you with information regarding failures and functioning of the device as well as other states of the device and the equipment. Alarm signals can be freely allocated to LEDs out of the "ASSIGNMENT LIST". An overview about all alarm signals available in the device can be obtained from the "ASSIGNMENT LIST" which can be found in the appendix.
	SYSTEM	LED »System OK«	Should LED »System OK« flash red during operation, contact the Service Dept. immediately.
3		Display	Via the display you can read- out operational data and edit parameters.
5		Softkeys	The function of the »SOFTKEYS« are contextual. On the bottom line of the display the present function is displayed/symbolized. Possible functions are: Navigation Parameter decrement/increment. Scrolling up/down a menu page Moving to a digit Change into the parameter setting mode »wrench symbol«.

Page 116 EN MRM4 12/11

6	INFO	INFO Key (Signals/Messa ges)	Looking through the present LED assignment. The direct select key can be actuated at any time. If the INFO key is actuated again you will leave the LED menu. Here only the first assignments of the LEDs will be shown. Every three seconds the "SOFTKEYs" will be shown (flashing). Displaying the multiple Assignments If the INFO-Button is pressed only the first assignments of any LED is shown. Every three seconds the "SOFTKEYs" will be shown (flashing). If there is more than one signal assigned to a LED (indicated by three dots) you can check the state of the multiple assignments if you proceed as follows. In order to show all (multiple) assignments select a LED by means of the "SOFTKEYs" "up" and "down" Via the "Softkey" "right" call up a Sub-menu of this LED that gives you detailed information on the state of all signals assigned to this LED. An arrow symbol points to the LED whose assignments are currently displayed. Via the "SOFTKEYs" "up" and "down" you can call up the next / previous LED. In order to leave the LED menu press the "SOFTKEY" "left" multiple times.
7	C	»C Key«	To abort changes and to acknowledge messages. In order to reset please press the Softkey »wrench« and enter the password. The reset menu can be left by pressing the Softkey »Arrowleft«

8		RS232 Interface (<i>Smart View</i> Connection)	Connection to software <i>Smart View</i> is done via the RS232 interface.
9	ОК	»OK Key«	When using the »OK« key parameter changes are temporarily stored. If the »OK« key is pressed again, those changes are stored definitely.
10	CTRL	»CTRL Key«*	For future applications

Softkey	Description
•	 Via »SOFTKEY« »up« you will come to the prior menu point/one parameter up by scrolling upwards.
	Via »SOFTKEY« »left« you will go one step back.
T	 Via »SOFTKEY« »down« you will change to the next menu point/one parameter down by scrolling downwards.
>	Via »SOFTKEY« »right« you will come to a sub-menu
+	Via »SOFTKEY« »Top of list« you will jump directly to the top of a list.
+	Via »SOFTKEY« »Bottom of list« you will jump directly to the end of a list.
+	Via »SOFTKEY« »+« the related digit will be incremented. (Continuous pressure -> fast).
-	Via »SOFTKEY« »-« the related digit will be decremented. (Continuous pressure -> fast)
←	Via »SOFTKEY« »left« you will go one digit to the left.
\rightarrow	Via »SOFTKEY« »right« you will go one digit to the right.
¥	Via »SOFTKEY« »Parameter setting« you will call up the parameter setting mode.
Х	Via »SOFTKEY« »delete« data will be deleted.

Key	Description
$\uparrow \uparrow$	Moving up within the navigation tree or parameter list.
\	Moving down within the navigation tree or parameter list.
←	Collapse the tree item or select a folder on a higher level.
\rightarrow	Expands the tree item or selects a sub-folder
Numpad +	Expands the tree item.
Numpad -	Collapses the tree item.
Home	Moves to the top of the active window.

Page 118 EN MRM4 12/11

End	Moves to the bottom of the active window.
Ctrl+O	Opens the file opening dialog. Browsing through the file system for an existing device file.
Ctrl+N	Creates a new parameter file file by means of a template.
Ctrl+S	Saves actual loaded parameter file.
F1	Displays the online help information.
F2	Load Device Data
F5	Reloads the displayed data of a device.
Ctrl+F5	Enables automatic refresh.
Ctrl+Shift+T	Back to the navigation window.
Ctrl+F6	Walks through the tabular forms (detail windows).
Page ↑	Previous value (parameter setting).
Page ↓	Next value (parameter setting).

Smart View

Smart View is a parameter setting and evaluation software.

- Menu-controlled parameter setting incl. validity checks
- Offline configuration of all relay types
- Reading and evaluating of statistical data and measuring values
- Setting into operation assistance
- Display of the device status
- Fault analysis via event- and fault recorder



Smart View 3.0 or higher supports reading parameter files generated by older versions of Smart View. Parameter files generated by Smart View 3.0 and higher cannot be read by older versions of Smart View.

Installation of Smart View



Port 52152 must not be blocked by a Firewall



If the Windows Vista User Access Control pops up while installing Smart View, please "Allow" all installation requirements concerning Smart View.

System requirements:

Windows 2000 or compatible (e.g. Windows XP, Windows Vista or Windows 7)

- Double-click on the installation file with the left mouse button.
- Select a language for the installation procedure.
- Confirm by pressing the »Continue« button in the INFO frame.
- Select an installation path or confirm the standard installation path by mouse click on the »Continue« button.
- Confirm the entry for the suggested installation folder by mouse click on the »Continue« button.
- By mouse click on the »Install« button, the installation routine is started.
- Close the installation procedure by mouse click on the »Complete« button.

Now you can call up the program via [Start>Programs>Woodward>HighPROTEC>Smart View].

Uninstalling Smart View

Via the menu [Start>System Control >Software] the Smart View can be removed from your computer.

Page 120 EN MRM4 12/11

Switching the Language of the Graphical User Interface

Within the menu Settings/Language, you can change the language of the graphical User interface.

Setting up the Connection PC - Device

Set-up a Connection via Ethernet - TCP/IP

NOTICE

Establishing a connection via TCP/IP to the device is only possible if your device is equipped with an Ethernet Interface (RJ45).

Contact your IT administrator in order to establish the network connection.

Part 1: Set the TCP/IP Parameters at the panel (Device)

Call up the menu »Device parameter/TCP/IP« at the HMI (panel) and set the following parameters:

- TCP/IP address
- Subnetmask
- Gateway

Part 2: Setting the IP address within Smart View

- Call up the menu Settings/Device Connection within Smart View.
- Set radio button Network Connection.
- Enter the IP-Address of the device that should be connected.

Set-up a Connection Via Serial Interface Under Windows 2000

After installation of the software, the »Connection PC/Notebook to the Device« has to be configured once, so that you are able to read device data or re-write them into the device by means of the software *Smart View*.

NOTICE

For connection of your PCs/notebooks with the device you need a special zero-modem cable (no serial cable!/please refer to chapter »Null Modem Cable«).

NOTICE

If your PC/notebook does not have a serial interface, you need a special *USB-to-serial-adapter*. Only if the *USB-to-serial-adapter* is correctly installed - aided by the provided CD – the communication with the device can be established. (see next chapter).

NOTICE

The connection Notebook/PC to the device must not be protected/encrypted via a smartcard.

If the network connection wizard asks you, to encrypt the connection via a smartcard or not, please choose »Do not use the smartcard«.

Setting up/Configuring the Connection

- Connect your PC/notebook with the device via a zero-modem cable.
- Start the software Smart View.
- Select the menu point »Device Connection« in menu »Settings«.
- Click on »Serial Connection«.
- Click button »Settings«.
- When initially setting up the connection, a dialogue window appears with the information that, so far, a
 direct connection with your protection device has not been established. Click on »Yes«.
- If, so far, a location has not been set up on your PC, your location information has to be put in. Confirm the pop-up window »Telephone and Modem Options« with »OK«.
- The Windows network connection assistant appears after the location information is set up. Select the connection type »Establish direct connection to another computer«.
- Select the serial interface (COM-Port) where the device shall be connected to.
- Select »To be used for all Users« in window »Availability of the connection«.
- Do not change the connection name appearing in window »Name of the connection« and click the button »Complete«.
- Finally you arrive again in window »Device Installation« from where you started establishing the connection. Confirm the adjustments by clicking the »OK« button.

NOTICE

Due to an bug in Windows 2000 it is possible that the automatically made communication settings are not correctly adopted. In order to overcome this problem, proceed as follows after setting up the serial connection:

- Select menu point »Device Connection« in menu »Settings«.
- Select »Serial Connection«.
- Click button »Settings«.
- Change to register card »General«.
- Ensure that »Communication cable between two computers Com X is selected in the »Drop Down Menu«. X = interface number where you have connected the zero-modem cable to.
- Click button »Configure«.
- Ensure that the »Hardware Flowing Control« is activated.
- Ensure that baud rate »115200« is selected.

Page 122 EN MRM4 12/11

Set up a Connection via Serial Interface under Windows XP

After installation of the software, the »Connection PC/Notebook to the Device« has to be configured once so that you are able to read device data or re-write them into the device by means of the software *Smart View*.

NOTICE

For connection of your PCs/notebooks with the device, you need a zero-modem cable (no serial cable!/please refer to chapter »Null Modem Cable«).



If your PC/notebook does not have a serial interface, you need a special »USB-to-serial-adapter«. Only if the »USB-to-serial-adapter« is correctly installed - aided by the provided CD – the communication with the device can be established. (see next chapter).

Setting up/Configuring the connection

- Connect your PC/notebook with the device via a zero-modem cable.
- Start the software Smart View.
- Select the menu point »Device Connection« in menu »Settings«.
- Click on »Serial Connection«.
- Click button »Settings«.
- When initially setting up the connection, a dialogue window appears with the information that, so far, a
 direct connection with your protection device has not been established. Click on »Yes«.
- If, so far, a location has not been set up on your PC, your location information has to be put in. Confirm the following pop-up window »Telephone and Modem Options« with »OK«.
- The Windows network connection assistant appears after the location information is set up. Select the connection type »Establish direct connection to another computer«.
- Select the serial interface (COM-Port) where the device shall be connected to.
- Select »To be used for all Users« in window »Availability of the connection«.
- Do not change the connection name appearing in window »Name of the connection« and click the button »Complete«.
- Finally you arrive again in window »Device Installation« from where you started establishing the connection. Confirm the adjustments by clicking the »OK« button.

Set up a Connection Via Serial Interface Under Windows Vista or Windows 7

Establishing the connection between Smart View and the device is a three step procedure.

- 1. Installing Smart View (the application itself)
- 2. Installing a (virtual) modem (that is a precondition for TCP/IP communication via a zero-modem cable)/ ((to be done within the Windows Phone and Modem dialog).
- 3. Establishing a network connection between Smart View and the device (to be done within Smart View).

- Installation of Smart View (the application itself).
 Please see above.
- 2. Installation of the (virtual) modem
- Open the Windows Start menu and type "Phone and Modem" and RETURN. This opens the "Phone and Modem" Dialog
- Go to Tab »Modem«
- Click on the »Add« button
- The Hardware Wizard window Install New Modem pops up
- Set the check box Don't detect my modem; I will select it from a list
- Click on the »Next« button
- Select Communications cable between two computers
- Click on the »Next« button
- Choose the correct COM-Port
- Click on the »Next« button
- Click on the »Finish« button
- Select the new added modem and click on the »Properties« button
- Go to Tab »General«
- Click on the »Change settings« button
- Go to Tab »Modem«
- Set within the Drop-Down Menu the correct baud rate = 115200
- Close this dialog with the »OK« button
- Close the Phone and Modem dialog with the »OK« button
- You have to reboot your computer now!
- 3. Establishing a network connection between Smart View and the device
- Connect the device to the PC/notebook via a correct Zero-Modem-Cable.
- Run Smart View.
- Call up »Device Connection« within the menu »Device Connection«.
- Click on the »Settings« button.

Page 124 EN MRM4 12/11

- A connection wizard will pop up asking you How do you want to connect.
- Choose »Dial-up«.
- The Telephone number must not be empty. Please enter any number (e.g. 1).
- Don't care about the User name and password.
- Click on the »OK« button.

Connected to the Device and Calling Up Websites at the Same Time

In principle, it is possible to call up websites while there is an active connection to the device.

If your computer has no direct connection to the internet, that means, that it is placed behind a proxy server, the device connection has to be modified in certain circumstances. The device connection has to be provided with the proxy settings.

Internet Explorer

For each connection the proxy settings have to be set manually. Please proceed as follows:

- Start your Internet Explorer.
- Call up the »Tools« menu.
- Call up the menu »Internet options«.
- Call up the tab »Connections«.
- Click with the left hand mouse key on the button »Settings« on the right of the »HighPROTEC-Device-Connection«.
- Set the check box »Use Proxy Server for this connection.
- Enter the proxy settings that are available by your network administrator.
- Confirm the settings by pressing »OK«.

Firefox

The proxy settings are centrally managed, so there is no need to modify any settings.

Establishing the Connection Via a USB-/RS232-Adapter

If your PC/notebook is not provided with a serial interface, this can be compensated by a special *USB-/RS232-Adapter+Null Modem-Cable*.



Only an adapter accepted by *Woodward Kempen GmbH* may be used. First install the adapter (with the related driver that you can find on the CD) and then establish the connection (*Smart View => Device*). The adapters must support very high speed.

Set-up a Connection Via Ethernet - TCP/IP



Warning: Mixing up IP-Addresses (In case that there is more than one protective device within the TCP/IP network). Establishing an unintentional wrong connection to a protective device based on a wrong entered IP-Address. Transferring parameters into a wrong protective device might lead to death, personal injury or damage of electrical equipment.

In order to prevent faulty connections the User has to document and maintain a list with the IP addresses of any switchboard/protective device.

The User has to double check the IP addresses of the connection that is to be established. That means, the User must first read out the IP address at the HMI of the device (within menu [Device para/TCP IP] then compare the IP address with the list. If the addresses are identical, establish the connection. If not, DO not connect.



Establishing a connection via TCP/IP to the device is only possible if your device is equipped with an Ethernet Interface (RJ45).

Contact your IT administrator in order to establish the network connection.

Part 1: Set the TCP/IP Parameters at the panel (Device)

Call up the menu »Device parameter/TCP/IP« at the HMI (panel) and set the following parameters:

- TCP/IP address
- Subnet mask
- Gateway

Part 2: Setting the IP address within Smart View

- Call up the menu Settings/Device Connection within Smart View.
- Set radio button Network Connection.
- Enter the IP-Address of the device that should be connected.

Smart View Troubleshooting (XP and Windows 2000)

- Make sure whether the Windows service *Telephony* is started. In [Start>System Control >Administration >Services] the service »Telephony« must be visible and must have also been started. If not, the service has to be started.
- For establishing the connection, you need to have sufficient rights (administration rights).
- If a firewall is installed on your computer, TCP/IP port 52152 must have been released.
- If your computer is not provided with a serial interface, you need a *USB-to-serial-adapter*, accepted by *Woodward Kempen GmbH*. This adapter has to be properly installed.

Page 126 EN MRM4 12/11

 Ensure that a zero-modem cable is used (a standard serial cable without control wires does not enable communication).

NOTICE

If »WINDOWS 2000 is running on your computer and a serial interface for direct connection to another computer has not been established so far, the following problem can arise:

If you have selected a serial interface in the connection assistant, it may happen that this is not entered correctly in the dial-up network due to an bug in the Windows operating system. Your attention is drawn to this problem by the operational software and the error message »Warning, invalid connection setting« will be shown.

To solve this problem, you need administration rights.

Please proceed as follows:

- Select menu point »Device Connection« in menu »Settings«.
- Select »Serial Connection«.
- Click button »Settings«.
- Change to register card »General«.
- Ensure that »Communication cable between two computers (Com X)" is selected in the »Drop Down Menu«. »X« = interface number where you have connected the zero-modem cable to.

NOTICE

If the message »Warning, invalid connection settings« appears during establishing the connection, this indicates that the connection adjustments you have chosen are not correct.

On this warning you can react as follows:

»Yes«: (to set up the connection completely new).

By this, all adjustments are canceled and the connection assistant is opened again for renewed adjustment of the connection to the device.

This procedure is advisable in case basic adjustments cannot be modified via the characteristics dialogue (e.g. if a new additional serial interface has been installed on the system).

»No«: (to modify the existing dial-up network entry).

Opens the dialogue for characteristics of the connection settings. During the dialogue it is possible to correct invalid settings (e.g. the recommended baud rate).

»Cancel«:

The warning is ignored and the connection adjustments remain as they are. This procedure is accepted for a limited time, but in such a case, the User is obliged to establish a correct connection later on.

Smart View Persistent Connection Problems

In case of persistent connection problems you should remove all connection settings and establish them again afterwards. In order to remove all connection settings please proceed as follows:

- Remove the settings for the Dial-up Network
- Close Smart View
- Call up the »Control Panel«
- Choose »Network & Internet«
- On the left side click on »Manage Network Connections«
- Click on HighPROTEC Direct Connection with the right hand mouse key
- Choose Delete from the shortcut menu
- Click on the OK button
- 2. Remove the virtual modem
- Call up the »Control Panel«
- Choose »Hardware & Sound«
- Choose »Phone & Modem Options«
- Go to Tab Modem
- Click on the correct (in case there is more than one) entry Connection cable between two computers
- Click on the Remove button

Loading of Device Data When Using Smart View

- Starting of the Smart View.
- Make sure the connection has been established properly.
- Connect your PC with the device via a zero-modem cable.
- Select »Receiving Data From The Device« in menu »Device«.

Page 128 EN MRM4 12/11

Restoring of Device Data When Using Smart View



Via the button »Transfer only modified parameters into the device« only modified parameters are transmitted into the device.

Parameter modifications are indicated by a red "star symbol" in front of the parameter.

The star symbol (in the device tree window) indicates that parameters in the opened file (within smart view) differ from parameters stored on your local hard disk.

Via the button »Transfer only modified parameters into the device«, you can transmit all parameters that are marked by this symbol.

If a parameter file is saved on your local hard drive, these parameters are no longer classified to be modified and cannot be transmitted via the button »Transfer only modified parameters into the device«.

In case that you have loaded and modified a parameter file from the device and saved it to your local hard drive without transferring the parameters into the device beforehand, you cannot use the button »Transfer only modified parameters into the device«. In a case like that, use »Transfer all parameters into the device«.

NOTICE

The button »Transfer only modified parameters into the device« only works if modified parameters are available in the *Smart View*.

In contrast to that, all parameters of the device are transferred when the button »Transfer all parameters into the device« is pressed (provided all device parameters are valid).

- In order to (re-)transfer changed parameters into the device, please select »Transfer all parameters into the device« in menu »Device«.
- Confirm the safety inquiry »Shall the parameters be overwritten into the device?"«.
- Enter the password for setting parameters in the pop-up window.
- Thereafter the changed data is transferred to the device and adopted.
- Confirm the inquiry »Parameters successfully updated. It is recommended to save the parameters into a local file on your hard drive. Shall The Data Be Saved Locally?"« with »Yes« (recommended). Select a suitable folder on your hard disk.
- Confirm the chosen folder by clicking »Save«.
- The changed parameter data is now saved in the folder chosen by you.

Backup and Documentation When Using Smart View

How to save device data on a PC:

Click on »Save as ... « in menu »File «. Specify a name, choose a folder on your hard disk and save the device data accordingly.

Printing of Device Data When Using Smart View (Setting List)

The »Printing menu« offers the following options:

- Printer setting
- Page preview
- Printing
- Export the selected printing range into a txt-file.

The printing menu of the Smart View software offers contextual different types of printing ranges.

- Printing of the complete parameter tree:
 All values and parameters of the present parameter file are printed.
- Printing of the displayed working window:
 Only the data shown on the relevant working window are printed, i.e. this applies, if at least one window is opened.
- Printing of all opened working windows:
 The data shown on all windows are printed, i.e. this applies only if more than one window is opened.
- Printing of the device parameter tree as from a shown position on:

 All data and parameters of the device parameter tree are printed as from the position/marking in the navigation window. Below this selection the complete name of the marking is additionally displayed.

Saving Data as a txt File Via Smart View

Within the print menu [File>Print] you can choose »Export into File« in order to export the device data into a text-file.



Only the actual selected printing range will be exported into a text-file. That means: If you have chosen the "Complete device parameter tree" then the "Complete device parameter tree" will be exported. But, if you have chosen "Actual working window", only this window will be exported.

You can print out operating data but not export them.



If you export a txt-file, the content of this file is encoded as Unicode. That means that, if you want to edit this file, your application must support Unicode encoded files (e.g. Microsoft Office 2003 or higher).

Page 130 EN MRM4 12/11

Offline Device Planning Via Smart View

NOTICE

In order to be able to transmit a parameter file (e.g. offline created) into the device the following issues must comply:

- Type Code (written on the top of the device/type label) and
- Version of the device model (can be found in menu [Device Parameters\Version].

The *Smart View* software enables also to parametrize offline. The advantage is: By using device models you can do planning jobs for a device and set parameters in advance.

You can also read the parameter file out of the device, further process it offline (e.g. from your office) and finally re-transfer it to the device.

You can either:

- load an existing parameter file from a device (please refer to chapter [Loading device data when using Smart View]).
- create a new parameter file (see below),
- open a locally saved parameter file (backup).

In order to create a new device/parameter file by way of a device template offline:

- In order to create a new offline parameter file please choose within the »file-menu« »create new parameter file«.
- A working window pops up. Please make sure, that you select the right device type with the correct version and configuration.
- Finally click on »Apply«
- In order to save the device configuration select »Save« out of the »File-Menu«.
- Within the menu »Modify Device Configuration (Typecode)« you can modify the device configuration or simply find out the type code of your current selection.

If you want to transfer the parameter file into a device, please refer to chapter "Restoring of device data when using Smart View".

Measuring Values

Read Out Measured Values

In menu »Operation/Measured Values« both measured and calculated values can be viewed. The measured values are ordered by »Standard values« and »special values« (depending on the type of device).

Read Out of Measured Values Via Smart View

- In case Smart View is not running please start it.
- If the device data were not yet loaded select »Receive Data From The Device« from menu »Device«.
- Double click on icon »Operation« in the navigation tree.
- Double click on icon »Measured Values« within the navigation tree »Operation«.
- Double click the »Standard Values« or special values within the »Measured values«.
- The measured and calculated values are shown now in tabular form on the window.



To have the measuring data read in a cyclic manner, select »Auto refresh« in menu »View«. The measured values are read out about every two seconds.

Measurement Display

Menu [Device Para\Measurem Display] offers options to change the display of measured values within the HMI and Smart View.

Scaling of Measured Values

By means of the parameter »Scaling« the User can determine how measured values are to be displayed within the HMI and *Smart View*:

- Primary quantities
- Secondary quantities
- Per Unit quantities

Power Units (applies only for devices with power measurement)

By means of the parameter »*Power Units«* the User can determine how measured values are to be displayed within the HMI and *Smart View*:

- Power Auto Scaling
- kW, kVAr or kVA
- MW, MVAr or MVA
- GW, GVAr or GVA

Page 132 EN MRM4 12/11

Energy Units (applies only for devices with energy measurement)

By means of the parameter »*Energy Units*« the User can determine how measured values are to be displayed within the HMI and *Smart View*:

- Energy Auto Scaling
- kWh, kVArh or kVAh
- MWh, MVArh or MVAh
- GWh, GVArh or GVAh

Temperature Unit (applies only for devices with temperature measurement)

By means of the parameter » Temperatur Unit« the User can determine how measured values are to be displayed within the HMI and Smart View:

- ° Celsius
- ° Fahrenheit

Cutoff Level

In order to suppress noise within measured values that are close to zero the User has the option to set cutoff levels. By means of the cutoff levels, measuring quantities that are close to zero will be displayed as zero. These parameters have no impact on recorded values.

Current - Measured Values

Current

If the device is not equipped with an voltage measuring card the first measuring input on the first current measuring card (slot with the lowest number) will be used as the reference angle (» IL1«).

Value	Description	Menu path
IL1	Measured value: Phase current	[Operation
	(fundamental)	/Measured values
		/Current]
IL2	Measured value: Phase current	[Operation
	(fundamental)	/Measured values
		/Current]
IL3 Measured value: Phase current		[Operation
	(fundamental)	/Measured values
		/Current]
IG meas	Measured value (measured): IG	[Operation
	(fundamental)	/Measured values
		/Current]
IG calc	Measured value (calculated): IG	[Operation
	(fundamental)	/Measured values
		/Current]

Value	Description	Menu path
10	Measured value (calculated): Zero current	[Operation
	(fundamental)	/Measured values
		/Current]
11	Measured value (calculated): Positive	[Operation
	phase sequence current (fundamental)	/Measured values
		/Current]
12	Measured value (calculated): Unbalanced	[Operation
	load current (fundamental)	/Measured values
		/Current]
IL1 H2	Measured value: 2nd	[Operation
	harmonic/fundamental harmonic of IL1	/Measured values
		/Current]
IL2 H2	Measured value: 2nd	[Operation
	harmonic/fundamental harmonic of IL2	/Measured values
		/Current]
IL3 H2	Measured value: 2nd	[Operation
	harmonic/fundamental harmonic of IL3	/Measured values
		/Current]
IG H2 meas	Measured value: 2nd	[Operation
	harmonic/fundamental harmonic of IG (measured)	/Measured values
	(measureu)	/Current]
IG H2 calc	Measured value (calculated): 2nd	[Operation
	harmonic/fundamental harmonic of IG (calculated)	/Measured values
	(Galcalatea)	/Current]
phi IL1	Measured value (calculated): Angle of	[Operation
	Phasor IL1	/Measured values
		/Current]
phi IL2	Measured value (calculated): Angle of	[Operation
	Phasor IL2	/Measured values
		/Current]
phi IL3	Measured value (calculated): Angle of	[Operation
	Phasor IL3	/Measured values
		/Current]
phi IG meas	Measured value (calculated): Angle of	[Operation
	Phasor IG meas	/Measured values
		/Current]
phi IG calc	Measured value (calculated): Angle of	[Operation
	Phasor IG calc	/Measured values
		/Current]

Page 134 EN MRM4 12/11

Value	Description	Menu path
phi I0	Measured value (calculated): Angle Zero	[Operation
	Sequence System	/Measured values
		/Current]
phi I1	Measured value (calculated): Angle of	[Operation
	Positive Sequence System	/Measured values
		/Current]
phi I2	Measured Value (calculated): Angle of	[Operation
	Negative Sequence System	/Measured values
		/Current]
IL1 RMS	Measured value: Phase current (RMS)	[Operation
		/Measured values
		/Current RMS]
IL2 RMS	Measured value: Phase current (RMS)	[Operation
		/Measured values
		/Current RMS]
IL3 RMS	Measured value: Phase current (RMS)	[Operation
		/Measured values
		/Current RMS]
IG meas RMS	Measured value (measured): IG (RMS)	[Operation
		/Measured values
		/Current RMS]
IG calc RMS	Measured value (calculated): IG (RMS)	[Operation
		/Measured values
		/Current RMS]
%IL1 THD	Measured value (calculated): IL1 Total	[Operation
	Harmonic Distortion	/Measured values
		/Current RMS]
%IL2 THD	Measured value (calculated): IL2 Total	[Operation
	Harmonic Distortion	/Measured values
		/Current RMS]
%IL3 THD	Measured value (calculated): IL3 Total	[Operation
	Harmonic Distortion	/Measured values
		/Current RMS]
IL1 THD	Measured value (calculated): IL1 Total	[Operation
	Harmonic Current	/Measured values
		/Current RMS]
IL2 THD	Measured value (calculated): IL2 Total	[Operation
	Harmonic Current	/Measured values
		/Current RMS]

Value	Description	Menu path
,	[Operation	
	Harmonic Current	/Measured values
		/Current RMS]
%(I2/I1)	Measured value (calculated): I2/I1, phase	[Operation
	sequence will be taken into account automatically.	/Measured values
	,	/Current]

Configuration of the Analog Outputs

Available Elements: Analog Output

The Analog Outputs can be programmed to output for three different ranges of either »0- 20mA«, »4- 20 mA«, or »0-10 Volts«.

These outputs can be configured by the User to represent the status of User programmed parameters that are available from the relay. The User will find the configuration menu for this feature under the [Device Para/ Analog Outputs] menu option. Here the User can define to which parameter the output will correlate.

Once the assignment has been made, the User can select the expected range of the parameter that will correlate to the analog output. The User will be required to enter a »Range min«, and »Range max«. The »Range min« will determine the value at which value the transmission will start. Likewise, the »Range max« value will determine the value that will result in the end value of the transmission.

Page 136 EN MRM4 12/11

Global Protection Parameters of the Analog Outputs

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	-,	-	[Device Para
		IL1 RMS,		/Analog Outputs
		IL2 RMS,		/Analog Output]
		IL3 RMS,		
		IG meas RMS,		
		IG calc RMS,		
		IL1 lb,		
		IL2 lb,		
		IL3 lb,		
		I3 P (%Ib) avg,		
		I3P Fla Demand,		
		I2T Used,		
		I2T Remained,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		Windg1,		
		Windg2,		
		Windg3,		
		Windg4,		
		Windg5,		
		Windg6,		
		MotBear1,		
		MotBear2,		
		LoadBear1,		
		LoadBear2,		
		Aux1,		
		Aux2,		
		RTD Max,		
		HottestWindingTemp		
Range	Adjustable range	020mA,	020mA	[Device Para
		420mA,		/Analog Outputs
		010V		/Analog Output]
Range max	Adjustable range maximum. Enter the measuring value as Max value that corresponds to the	-999999.00 - 999999.00%	1.00%	[Device Para
	maximum of analog output value (use the unit			/Analog Outputs
	which is displayed for measuring values while "Per unit values" scaling is active).			/Analog Output]

Parameter	Description	Setting range	Default	Menu path
Range min	Adjustable range minimum. Enter the measuring value as Min value that corresponds to the minimum of analog output value (use the unit which is displayed for measuring values while "Per unit values" scaling is active).	-999999.00 - 999999.00%	0.00%	[Device Para /Analog Outputs /Analog Output]
Force Mode	For commissioning purposes or for maintenance, Analog Outputs can be set by force. By means of this function the normal Analog Outputs can be overwritten (forced).	permanent, Timeout	permanent	[Service /Test Mode (Prot inhibit) /Analog Outputs /Analog Output]
t-Timeout Force	The Analog Output Value will be set by force for the duration of this time. That means for the duration of this time the Analog Output does not show the value of the signals that are assigned on it. Only available if: Force Mode = active	0.00 – 300.00 s	0.03 s	[Service /Test Mode (Prot inhibit) /Analog Outputs /Analog Output]

Direct Commands of the Analog Outputs

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of	inactive,	inactive	[Service
	module/stage.	active		/Test Mode (Prot inhibit)
				/Analog Outputs
				/Analog Output]
Force Value	By means of this function the Analog Output Value	0.00 - 100.00%	0%	[Service
	can be overwritten (forced).			/Test Mode (Prot inhibit)
				/Analog Outputs
				/Analog Output]

Signals of the Analog Outputs

Name	Description
	For commissioning purposes or for maintenance, Analog Outputs can be set by force. By means of this function the normal Analog Outputs can be overwritten (forced).

Page 138 EN MRM4 12/11

Statistics

Statistics

In menu »Operation/Statistics« the min., max. and mean values of the measured and calculated measured quantities can be found. The statistics are ordered by »Standard values« and »special values« (depending on the type of device and the device planning).

In menu »Device Parameter/Statistics « you can either set a fixed synchronization time and a calculation interval or start and stop the statistics via a function (e.g. digital input).

Read Out Statistics

- Call up the main menu.
- Call up the sub-menu »Operation/Statistics«.
- Call up the »Standard values« or »Special values«

Statistics to Be Read Out Via Smart View

- In case Smart View is not running please start it.
- If device data have not yet been loaded click »Receive Data From The Device« in menu »Device«.
- Double click on icon »Operation« in the navigation tree
- Double click on icon »Statistics« within the navigation tree »Operation«
- Double click on icon »Standard values« or »Special values«
- In the window the statistical data is shown in tabular form

The values can be read out cyclically. For this purpose, please select »Auto Refresh« out of the menu »View«.

Statistics (Configuration)

The Statistic-module can be configured within the menu »Device Parameter/Statistics«.

The time interval, that is taken into account for the calculation of the statistics can either be limited by a fixed duration or it can be limited by a start function (freely assignable signal from the **assignment list**).

Fixed duration:

If the statistic module is set to a fixed duration/time interval, the minimum, maximum and average values will be calculated and displayed continuously on the basis of this duration/time interval.

Start function (flexible duration):

If the statistic module is to be started by a start function the <u>statistics</u> will be updated not until the start function becomes true (rising edge). At the same time a new time interval will be started.

Statistics (Configuration) Via Smart View

In case Smart View is not running - please start it

- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device«
- Double click on icon »Device Parameter« in the navigation tree
- Double click on icon »Statistics« within the navigation tree »Device Parameter«
- Configure the <u>Statistics</u>-module

Direct Commands

Parameter	Description	Setting range	Default	Menu path
ResFc all	Resetting of all Statistic values (Current Demand, Power Demand, Min, Max)	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]
ResFc I Demand	Resetting of Statistics - Current Demand (avg, peak avg)	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]
ResFc P Demand	Resetting of Statistics - Power Demand (avg, peak avg)	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]
ResFc Min	Resetting of all Minimum values	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]
ResFc Max	Resetting of all Maximum values	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]

Page 140 EN MRM4 12/11

Global Protection Parameters of the Statistics Module

Parameter	Description	Setting range	Default	Menu path
ResFc Max	Resetting of all Maximum values	1n, Assignment List		[Device Para
				/Statistics
				/Min / Max]
ResFc Min	Resetting of all Minimum values	1n, Assignment List		[Device Para
				/Statistics
				/Min / Max]
Min Max	Definition of the time base that is used for	Sliding window,	Peak values	[Device Para
Definition	calculating the minimum and maximum values.	Peak values		/Statistics
				/Min / Max]
Start I	Start Current demand by:	Duration,	Duration	[Device Para
Demand via:		StartFct		/Statistics
				/Demand
				/Current Demand]
Start I	Start of the calculation, if the assigned signal	1n, Assignment List		[Device Para
Demand Fc	becomes true.			/Statistics
	Only available if: Start I Demand via: = StartFct			/Demand
				/Current Demand]
ResFc I	Resetting of Statistics - Current Demand (avg,	1n, Assignment List		[Device Para
Demand	peak avg)			/Statistics
				/Demand
				/Current Demand]

Parameter	Description	Setting range	Default	Menu path
Duration I	Recording time	2 s,	15 s	[Device Para
Demand	Only available if: Start I Demand via: = Duration	5 s,		/Statistics
	only available in clark i bonnana via.	10 s,		/Demand
		15 s,		/Current Demand]
		30 s,		
		1 min,		
		5 min,		
		10 min,		
		15 min,		
		30 min,		
		1 h,		
		2 h,		
		6 h,		
		12 h,		
		1 d,		
		2 d,		
		5 d,		
		7 d,		
		10 d,		
		30 d		
Window I	Window configuration	sliding,	fixed	[Device Para
Demand		fixed		/Statistics
				/Demand
				/Current Demand]

States of the Inputs of the Statistics Module

Name	Description	Assignment via
StartFc 1-I	State of the module input: Start of Statistics 1	[Device Para
		/Statistics
		/Demand
		/Current Demand]
StartFc 2-I	State of the module input: Start of Statistics 2	
ResFc I Demand-I	State of the module input: Resetting of Statistics - Current Demand (avg, peak avg)	[Device Para
		/Statistics
		/Demand
		/Current Demand]

Page 142 EN MRM4 12/11

Name	Description	Assignment via
ResFc P Demand-I	State of the module input: Resetting of Statistics - Power Demand (avg, peak avg)	0
ResFc Max-I	State of the module input: Resetting of all	[Device Para
	Maximum values	/Statistics
		/Min / Max]
ResFc Min-I	State of the module input: Resetting of all	[Device Para
	Minimum values	/Statistics
		/Min / Max]

Signals of the Statistics Module

Name	Description
ResFc all	Signal: Resetting of all Statistic values (Current Demand, Power Demand, Min, Max)
ResFc I Demand	Signal: Resetting of Statistics - Current Demand (avg, peak avg)
ResFc P Demand	Signal: Resetting of Statistics - Power Demand (avg, peak avg)
ResFc Max	Signal: Resetting of all Maximum values
ResFc Min	Signal: Resetting of all Minimum values

Counters of the Module Statistics

Value	Description	Menu path
MeasPointNo	Each measuring point that is taken over by the statistics increments this counter. By means of this counter, the User can check whether the statistics are alive and if data are being acquired.	[Operation
		/Count and RevData
		/Statistics]
the statistics means of this whether the s	Each measuring point that is taken over by	[Operation
	the statistics increments this counter. By means of this counter, the User can check whether the statistics are alive and if data are being acquired.	/Count and RevData
		/Statistics]
Res Cr I Demand	Number of resets since last booting. The time-stamp shows date and time of the last reset.	[Operation
		/Statistics
		/Demand
		/Current Demand]
Res Cr Min values	Number of resets since last booting. The time-stamp shows date and time of the last reset.	[Operation
		/Statistics
		/Min
		/Current]

Statistics

Value	Description	Menu path
	Number of resets since last booting. The	[Operation
time-stamp show reset.	time-stamp shows date and time of the last reset.	/Statistics
		/Max
		/Current]

Page 144 EN MRM4 12/11

Acknowledgments

Collective Acknowledgments for latched signals:

Collective Acknowledgments					
	LEDs	Binary Relay Output Contacts	SCADA	Pending Trip Command	LEDs+ Binary Relay Output Contacts+
					SCADA+ Pending Trip Command
Via Smart View or at the panel all can be acknowledged. At the panel, the menu [Operation\ Acknowledge] can directly be accessed via the »C« key	All LEDs at once: Where? [Operation\ Acknowledge]	All Binary Relay Output Contacts at once: Where? [Operation\ Acknowledge]	All SCADA signals at once: Where? [Operation\ Acknowledge]	All pending trip commands at once: Where? [Operation\ Acknowledge]	All at once: Where? [Operation\ Acknowledge]
External Acknowledgme nt*: Via a signal from the assignment list (e.g. a digital Input) all can be acknowledged.	All LEDs at once: Where? Within the menu Ex Acknowledge	All Binary Relay Output Contacts at once: Where? Within the menu Ex Acknowledge	All SCADA signals at once: Where? Within the menu Ex Acknowledge	All pending trip commands at once: Where? Within the menu Ex Acknowledge	

^{*}The External Acknowledgment might be disabled if parameter » Ex Ack «is set to »inactive« within menu [Device Para/Ex Acknowledge]. This blocks also the acknowledgment via Communication (e.g. Modbus).

Options for individual acknowledgments for latched signals:

Individual Acknowledgment				
	LEDs	Binary Relay Output Contacts	Pending Trip Command	
Via a signal from the assignment list (e.g.:a digital Input) a single can be acknowledged.	Single LED: Where? Within the configuration menu of this single LED.	Binary Relay Output Contact: Where? Within the configuration menu of this single Binary Relay Output Contact.	Pending Trip Command. Where? Within the module <i>TripControl</i>	

NOTICE

As long as you are within the parameter setting mode, you cannot acknowledge.



In case of a fault during parameter setting via the operating panel, you must first leave the parameter mode by pressing either push-button »C« or »OK« before you may access to menu »Acknowledgments« via push-button.

Manual Acknowledgment

- Press the C-Button at the panel.
- Select the item to be acknowledged via the Softkeys:
 - Binary relay output contacts,
 - LEDs,
 - SCADA,
 - a pending trip command or
 - all (above) mentioned items at once.
- Press the Softkey with the »Wrench-Symbol«.
- Enter your password.

Manual Acknowledgment Via Smart View

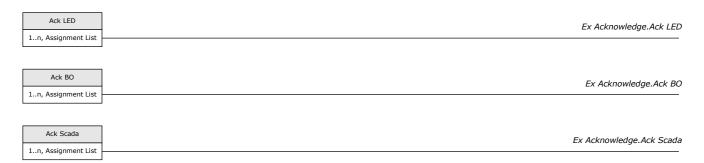
- In case Smart View is not running please start it
- If the device data were not yet loaded select »Receive Data From The Device« from menu »Device«
- Double click on icon »Operation« in the navigation tree.
- Double click on icon »Acknowledgment« within the operation menu.
- Double click the entry within the pop-up that is to be acknowledged.
- Press the button »Execute immediately«.
- Enter your password.

External Acknowledgments

Within the menu [Ex Acknowledge] you can assign a signal (e.g. the state of a digital input) from the assignment list that:

- acknowledges all (acknowledgeable) LEDs at once;
- acknowledges all (acknowledgeable) binary outputs at once:
- acknowledges all (acknowledgeable) SCADA-signals at once.

Page 146 EN MRM4 12/11



Within the menu [Protection Para\Global Prot Para\TripControl] you can assign a signal that:

acknowledges a pending trip command.

For details, please refer to chapter » TripControl«.

External Acknowledge Via Smart View

In case Smart View is not running – please start it.

- If the device data were not yet loaded select »Receive Data From The Device« from menu »Device«
- Double click on icon »Device Parameter« in the navigation tree
- Double click on icon »Ex Acknowledge« within the operation menu
- In the working window you can assign now each one signal that resets all acknowledgeable LEDs, a signal that resets all binary outputs, a signal that resets the SCADA-signals respectively a signal that acknowledges a pending trip command.

External LED - Acknowledgment Signals

The following signals can be used for external acknowledgment of latched LEDs.

Name	Description
7-7	No assignment
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input

Name	Description
Modbus.Scada Cmd 1	Scada Command
Modbus.Scada Cmd 2	Scada Command
Modbus.Scada Cmd 3	Scada Command
Modbus.Scada Cmd 4	Scada Command
Modbus.Scada Cmd 5	Scada Command
Modbus.Scada Cmd 6	Scada Command
Modbus.Scada Cmd 7	Scada Command
Modbus.Scada Cmd 8	Scada Command
Modbus.Scada Cmd 9	Scada Command
Modbus.Scada Cmd 10	Scada Command
Modbus.Scada Cmd 11	Scada Command
Modbus.Scada Cmd 12	Scada Command
Modbus.Scada Cmd 13	Scada Command
Modbus.Scada Cmd 14	Scada Command
Modbus.Scada Cmd 15	Scada Command



NOTICE

Page 148 EN MRM4 12/11

Reset

Current - Statistic Values

Value	Description	Menu path
I1 max	Maximum value positive phase sequence	[Operation
	current (fundamental)	/Statistics
		/Max
		/Current]
I1 min	Minimum value positive phase sequence	[Operation
	current (fundamental)	/Statistics
		/Min
		/Current]
I2 max	Maximum value unbalanced load	[Operation
	(fundamental)	/Statistics
		/Max
		/Current]
I2 min	Minimum value unbalanced load current	[Operation
	(fundamental)	/Statistics
		/Min
		/Current]
IL1 max RMS	IL1 maximum value (RMS)	[Operation
		/Statistics
		/Max
		/Current]
IL1 avg RMS	IL1 average value (RMS)	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL1 min RMS	IL1 minimum value (RMS)	[Operation
		/Statistics
		/Min
		/Current]
IL2 max RMS	IL2 maximum value (RMS)	[Operation
		/Statistics
		/Max
		/Current]

Value	Description	Menu path
IL2 avg RMS	IL2 average value (RMS)	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL2 min RMS	IL2 minimum value (RMS)	[Operation
		/Statistics
		/Min
		/Current]
IL3 max RMS	IL3 maximum value (RMS)	[Operation
		/Statistics
		/Max
		/Current]
IL3 avg RMS	IL3 average value (RMS)	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL3 min RMS	IL3 minimum value (RMS)	[Operation
		/Statistics
		/Min
		/Current]
IG meas max RMS	Measured value: IG maximum value (RMS)	[Operation
		/Statistics
		/Max
		/Current]
IG meas min RMS	Measured value: IG minimum value (RMS)	[Operation
		/Statistics
		/Min
		/Current]
IG calc max RMS	Measured value (calculated):IG maximum	[Operation
	value (RMS)	/Statistics
		/Max
		/Current]
IG calc min RMS	Measured value (calculated):IG minimum	[Operation
	value (RMS)	/Statistics
		/Min
		/Current]

Page 150 EN MRM4 12/11

Value	Description	Menu path
%(I2/I1) max	Measured value (calculated): I2/I1	[Operation
	maximum value, phase sequence will be taken into account automatically	/Statistics
		/Max
		/Current]
%(I2/I1) min	Measured value (calculated): I2/I1 minimum	[Operation
	value, phase sequence will be taken into account automatically	/Statistics
		/Min
		/Current]
IL1 Peak demand	IL1 Peak value, RMS value	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL2 Peak demand	IL2 Peak value, RMS value	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL3 Peak demand	IL3 Peak value, RMS value	[Operation
		/Statistics
		/Demand
		/Current Demand]

Status Display

In the status display within the »Operation« menu, the present state of all signals can be viewed. This means the User is able to see if the individual signals are active or inactive at that moment. The User can see all signals sorted by protective elements/modules.

State of the module input/signal is	Is shown at the panel as





NOTICE

Page 152 EN MRM4 12/11

Operating Panel (HMI)

<u>HMI</u>

Special Parameters of the Panel

This menu »Device Parameter/HMI« is used to define the contrast of the display, the maximum admissible edit time and the menu language (after expiry of which, all unsaved parameter changes will be rejected).

Direct Commands of the Panel

Parameter	Description	Setting range	Default	Menu path
Contrast	Contrast	30 - 60	50	[Device Para
				/HMI]

Global Protection Parameters of the Panel

Parameter	Description	Setting range	Default	Menu path
t-max Edit	If no other key(s) is pressed at the panel, after expiration of this time, all cached (changed) parameters are canceled.	20 - 3600s	180s	[Device Para /HMI]
Menu	Selection of the language	English,	English	[Device Para
language		German,		/HMI]
		Russian,		
		Polish		

Recorder

Disturbance Recorder

Available elements: Disturb rec

The disturbance recorder works with 32 samples per cycle. The disturbance recorder can be started by one of eight start events (selection from the »assignment list«/OR-Logic). The disturbance record contains the measuring values inclusively pre-trigger-time. By means of *Smart View/Datavisualizer* (option) the oscillographic curves of the analogue (current, voltage) and digital channels/traces can be shown and evaluated in a graphical form. The disturbance recorder has a storage capacity of 120s. The disturbance recorder is able to record up to 10 s (adjustable) per record. The amount of records depends on the file size of each record.

The disturbance recorder can be configured in the menu »Device Parameter/Recorder/Disturb rec«.

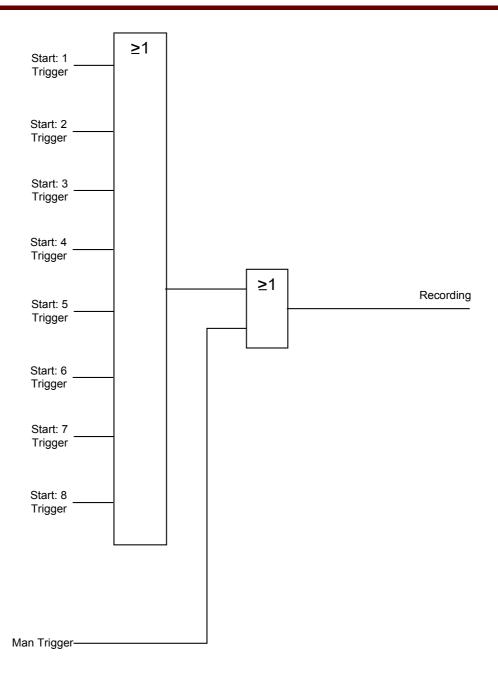
Determine the max. recording time to register a disturbance event. The max. total length of a recording is 10s (inclusive pre-trigger and post-trigger time).

To trigger the disturbance recorder, up to 8 signals can be selected from the »assignment list«. The trigger events are OR-linked. If a disturbance record is written, a new disturbance record cannot be triggered until all trigger signals, which have triggered the previous disturbance record, are gone. Recording is only done for the time the assigned event exists (event controlled), plus the time for the pre- and post-trigger, but not longer than 10s. The time for forward run and tracking of the disturbance recorder is shown in percent of the total recording length.

NOTICE

The post-trigger time will be up to "Post-trigger time" depending on the duration of the trigger signal. The post-trigger will be the remaining time of the "Max file size" but at maximum "Post-trigger time"

Page 154 EN MRM4 12/11

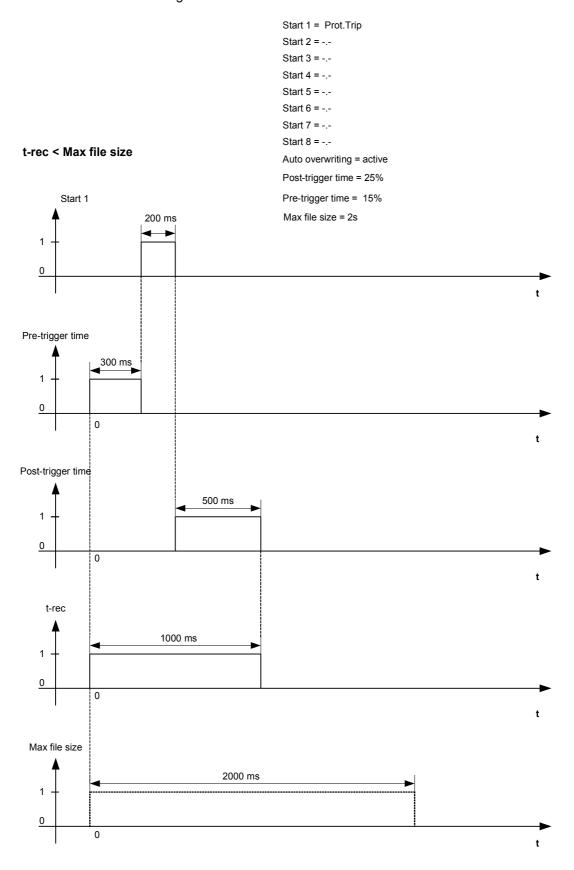


Example

The disturbance recorder is started by the general activation facility. After the fault has been canceled (+ follow-up time), the recording process is stopped (but after 10s at the latest).

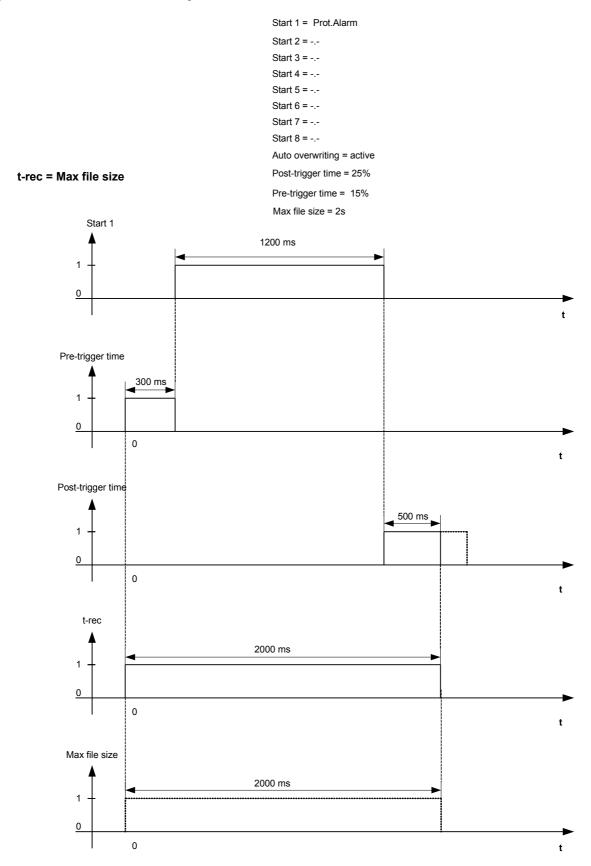
The parameter »Auto Delete« defines how the device shall react if there is no saving place available. In case »Auto Delete« is »active«, the first recorded disturbance will be overwritten according to the FIFO principle. If the parameter is set to »inactive«, recording of the disturbance events will be stopped until the storage location is released manually.

Example Disturbance Recorder Timing Chart I



Page 156 EN MRM4 12/11

Example Disturbance Recorder Timing Chart II



Read Out Disturbance Records

Within the Menu Operation/Disturb rec you can

Detect accumulated Disturbance Records.



Within the Menu »Operation/Recorders/Man Trigger« you can trigger the disturbance recorder manually.

Disturbance Recorder to Be Read Out by Smart View

- In case Smart View is not running please start it.
- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device«.
- Double click the »Operation« icon in the navigation tree.
- Double click the »Recorders« icon in the navigation tree.
- Double click the »Disturb rec-Icon«.
- In the window the disturbance records are shown in tabular form.
- A pop-up will be appear by a double click onto a disturbance record. Choose a folder where the
 disturbance record is to be saved to.
- You can analyze the disturbance records by means of the optionally available Data Visualizer by clicking
 on Yes when you are asked "Shall the received disturbance record be opened by the Data Visualizer?"

Deleting Disturbance Records

Within the Menu Operation/Disturb rec you can

- Delete Disturbance Records.
- Choose via »SOFTKEY« »up« and »SOFTKEY« »down« the disturbance record that is to be deleted.
- Call up the detailed view of the disturbance record via »SOFTKEY« »right«.
- Confirm by pressing »SOFTKEY« »delete«
- Enter your password followed by pressing the key »OK«
- Choose whether only the current of whether all disturbance records should be deleted.
- Confirm by pressing »SOFTKEY« »OK«

Deleting Disturbance Records Via Smart View

In case Smart View is not running – please start it.

Page 158 EN MRM4 12/11

- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device«.
- Double click the »Operation« icon in the navigation tree.
- Double click the »Recorders« icon in the navigation tree.
- Double click the »Disturb rec-Icon«.
- In the window the disturbance records are shown in tabular form.
- In order to delete a disturbance record double click on:



(the red x) in front of the disturbance record and confirm.

Direct Commands of the Disturbance Recorder

Parameter	Description	Setting range	Default	Menu path
Man Trigger	Manual Trigger	False,	False	[Operation
		True		/Recorders
				/Man Trigger]
Res all rec	Reset all records	inactive,	inactive	[Operation
		active		/ Reset/Acknowledg e
				/Reset]

Global Protection Parameters of the Disturbance Recorder

Parameter	Description	Setting range	Default	Menu path
Start: 1	Start recording if the assigned signal is true.	1n, Assignment List	Prot.Alarm	[Device Para
				/Recorders
				/Disturb rec]
Start: 2	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Disturb rec]
Start: 3	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Disturb rec]
Start: 4	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Disturb rec]

Parameter	Description	Setting range	Default	Menu path
Start: 5	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Disturb rec]
Start: 6	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Disturb rec]
Start: 7	Start recording if the assigned signal is true.	1n, Assignment List	-,-	[Device Para
				/Recorders
				/Disturb rec]
Start: 8	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Disturb rec]
Auto	If there is no more free memory capacity left, the oldest file will be overwritten.	inactive,	active	[Device Para
overwriting		active		/Recorders
				/Disturb rec]
Post-trigger	The post trigger time is settable up to a maximum	0 - 50%	20%	[Device Para
time	of 50% of the Maximum file size setting. The post-trigger will be the remaining time of the "Max file size" but at maximum "Post-trigger time"			/Recorders
				/Disturb rec]
Pre-trigger	The pre-trigger time is settable up to a maximum of 50% of the Maximum file size setting.	0 - 50%	20%	[Device Para
time				/Recorders
				/Disturb rec]
Max file size	The maximum storage capacity per record is 10	0.1 – 10.0 s	2 s	[Device Para
	seconds, including pre-trigger and post-trigger time. The disturbance recorder has a total storage capacity of 120 seconds.			/Recorders
				/Disturb rec]

Disturbance Recorder Input States

Name	Description	Assignment via
Start1-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Disturb rec]
Start2-I	State of the module input:: Trigger event / start recording if:	[Device Para
		/Recorders
		/Disturb rec]
Start3-I	start recording if:	[Device Para
		/Recorders
		/Disturb rec]

Page 160 EN MRM4 12/11

Name	Description	Assignment via
Start4-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Disturb rec]
Start5-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Disturb rec]
Start6-I	State of the module input:: Trigger event / start recording if:	[Device Para
		/Recorders
		/Disturb rec]
Start7-I	State of the module input:: Trigger event / start recording if:	[Device Para
		/Recorders
		/Disturb rec]
Start8-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Disturb rec]

Disturbance Recorder Signals

Name	Description
recording	Signal: Recording
memory full	Signal: Memory full
Clear fail	Signal: Clear failure in memory
Res all records	Signal: All records deleted
Res rec	Signal: Delete record
Man Trigger	Signal: Manual Trigger

Special Parameters of the Disturbance Recorder

Value	Description	Default	Size	Menu path
Rec state	Recording state	Ready	Ready,	[Operation
			Recording,	/Status display
			Writing file,	/Recorders
			Trigger Blo	/Disturb rec]
Error code	Error code	OK	OK,	[Operation
			Write err,	/Status display
			Clear fail,	/Recorders
			Calculation err,	/Disturb rec]
			File not found,	
			Auto overwriting off	

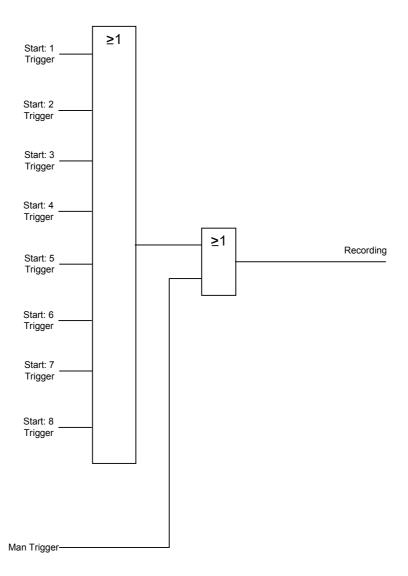
Fault Recorder

Fault rec

The fault recorder can be started by one of eight start events (selection from the »assignment list«/OR-Logic). The fault recorder can register up to 20 faults. The last of the recorded faults is stored in a fail-safe manner.

If one of the assigned trigger events becomes true, the fault recorder will be started. Each fault is saved inclusive module and name, fault number, mains fault number and record number at that time one of the trigger events becomes true. To each of the faults the measuring values (at the time when the trigger event became true) can be viewed.

Up to 8 signals to trigger the fault recorder can be selected from following list. The trigger events are OR-linked.



The parameter »Auto Delete« defines how the device shall react if there is no saving place available. In case »Auto Delete« is »active«, the first recorded fault will be overwritten according to the FIFO principle. If the parameter is set to »inactive«, recording of the fault events will be stopped until the storage location is released manually.

Page 162 EN MRM4 12/11

Read Out the Fault Recorder

The measured values at the time of tripping are saved (failure safe) within the fault recorder. If there is no more memory free, the oldest record will be overwritten (FIFO).

In order to read out a failure record:

- call up the main menu,
- call up the sub-menu Operation/Recorders/Fault rec.,
- select a fault record,
- analyze the corresponding measured values.

Read Out the Fault Recorder Via Smart View

- In case Smart View is not running please start it.
- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device«.
- Double click the »Operation« icon in the navigation tree.
- Double click the »Fault Rec« icon within the tree »Operation/Recorders«.
- In the window the fault recordings are shown in tabular form.
- In order to receive more detailed information on a fault double click the selected item in the list.

NOTICE

Via the print menu you can export the data into a file. Please proceed as follows:

- Call up the data as described above.
- · Call up the menu [File/Print].
- Choose »Print Actual Working Window« within the pop-up
- Press the »Print« button.
- Press the »Export to File« button.
- Enter a file name.
- Choose a location where to save the file.
- Confirm the »Save« button.

Direct Commands of the Fault Recorder

Parameter	Description	Setting range	Default	Menu path
Res all rec	Reset all records	inactive,	inactive	[Operation
		active		/ Reset/Acknowledg e /Reset]
Man Trigger	Manual Trigger	False, True	False	[Operation /Recorders /Man Trigger]

Global Protection Parameters of the Fault Recorder

Parameter	Description	Setting range	Default	Menu path
Start: 1	Start recording if the assigned signal is true.	1n, Assignment List	Prot.Alarm	[Device Para
				/Recorders
				/Fault rec]
Start: 2	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Fault rec]
Start: 3	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Fault rec]
Start: 4	Start recording if the assigned signal is true.	1n, Assignment List	-,-	[Device Para
				/Recorders
				/Fault rec]
Start: 5	Start recording if the assigned signal is true.	1n, Assignment List	-,-	[Device Para
				/Recorders
				/Fault rec]
Start: 6	Start recording if the assigned signal is true.	1n, Assignment List	-,-	[Device Para
				/Recorders
				/Fault rec]
Start: 7	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Fault rec]
Start: 8	Start recording if the assigned signal is true.	1n, Assignment List		[Device Para
				/Recorders
				/Fault rec]

Page 164 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Auto	If there is no more free memory capacity left, the	inactive,	active	[Device Para
overwriting	overwriting oldest file will be overwritten.	active		/Recorders
				/Fault rec]

Fault Recorder Input States

Name	Description	Assignment via
Start1-I	State of the module input:: Trigger event / start recording if:	[Device Para
		/Recorders
		/Fault rec]
Start2-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Fault rec]
Start3-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Fault rec]
Start4-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Fault rec]
Start5-I	start recording if:	[Device Para
		/Recorders
		/Fault rec]
Start6-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Fault rec]
Start7-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Fault rec]
Start8-I	State of the module input:: Trigger event /	[Device Para
	start recording if:	/Recorders
		/Fault rec]

Fault Recorder Signals

Name	Description
Res rec	Signal: Delete record
Man Trigger	Signal: Manual Trigger

Event Recorder

Event rec

The event recorder can register up to 300 events and the last (minimum) 50 saved events are recorded fail-safe. The following information is provided for any of the events:

Events are logged as follows:

Record No.	Fault No.	No of grid faults	Date of Record	Module.Name	State
Sequential Number	Number of the ongoing fault	A grid fault No. can have several Fault No.	Time stamp	What has changed?	Changed Value
	This counter will be incremented by each General Alarm (Prot.Alarm).	This counter will be incremented by each General Alarm. (Exception AR: this applies only to devices that offer auto reclosing).			

There are three different classes of events:

- Alternation of binary states are shown as:
 - 0->1 if the signal changes physically from »0« to »1«.
 - 1->0 if the signal changes physically from »1« to »0«.
- Counters increment is shown as:
 - Old Counter state -> New Counter state (e.g. 3->4)
- Alternation of multiple states are shown as:
 - Old state -> New state (e.g. 0->2)

Read Out the Event Recorder

- Call up the »main menu«.
- Call up the sub-menu »Operation/Recorders/Event rec«.
- Select an event.

Read Out the Event Recorder Via Smart View

- In case Smart View is not running please start it.
- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device.
- Double click the »Operation« icon in the navigation tree.

Page 166 EN MRM4 12/11

- Double click the »Event Rec« icon within the »Operation/Recorders« menu.
- In the window the events are shown in tabular form.

NOTICE

To have the event recorder up-dated in a cyclic manner, select »Automatic Up-Date« in menu *View.*

Smart View is able to record more events than the device itself, if the window of the event recorder is opened and »Automatic Up-Date« is set to active.

NOTICE

Via the print menu you can export the data into a file. Please proceed as follows:

- Call up the data as described above.
- Call up the menu [File/Print].
- Choose »Print Actual Working Window« within the pop-up
- Press the »Print« button.
- Press the »Export to File« button.
- Enter a file name.
- Choose a location where to save the file.
- Confirm the »Save« button.

Direct Commands of the Event Recorder

Parameter	Description	Setting range	Default	Menu path
Res all rec	Reset all records	inactive,	inactive	[Operation
		active		/ Reset/Acknowledg e
				/Reset]

Event Recorder Signals

Name	Description
Res all records	Signal: All records deleted

Trend Recorder

Available Elements: <u>Trend rec</u>

Functional Description

The Trend Data are data points stored by the Trend Recorder on the relay device over fixed intervals of time, and can be downloaded from the device using *Smart View*. A Trend Record is viewable using the *Data Visualizer* software by selecting files saved by *Smart View* with a file extension of ".ErTr". The list of available trend recorder data is viewable by selecting [Operation/ Recorders/Trend Recorder].

When viewed within the *Data Visualizer*, the trend record will show the observed values (up to 10) that the User has specified. The values available in the <u>Trend Recorder</u> depend on the type of the connected device and the configuration of the <u>Trend Recorder</u>.

Managing Trend Records

To download information from the Trend Recorder, select [Operation/Recorder/Trend Rec] from the menu tree. The User will find three options within the Trend Recorder window that will allow the User to:

- Receive Trend Records.
- Refresh the Trend Recorder, and
- Delete Trend Records.

Selecting the »Receive Trend Record« button will download data from the relay to the User's PC. By selecting the »Refresh Trend Recorder«", Smart View updates the list of the Trend Recorder. The »Delete Trend Records« function will clear all trend data from the relay. Trend Recorder data previously stored on the User's PC remains untouched.

After having received trend data from the device, the User can view the data in the *Data Visualizer* by double-clicking on the received ".ErTr" file stored on the PC. Once the ".ErTr" file is open, the User will see the "Analog Channels" that are monitored by the Trend Recorder. By clicking on the "Analog Channels", all monitored parameters are listed. To view a channel, the User must click on the left mouse key, then drag and drop the channel onto the right side of the *Data Visualizer* screen. The channel is then listed under the "Displayed Channels".

To remove a channel from view, the User must select the Trend Data to be removed in the »*Displayed Channels«* menu tree, then click on the right mouse button to bring up the menu options. Here, the User will find the »Remove« menu option that, when selected, will remove the trend data.

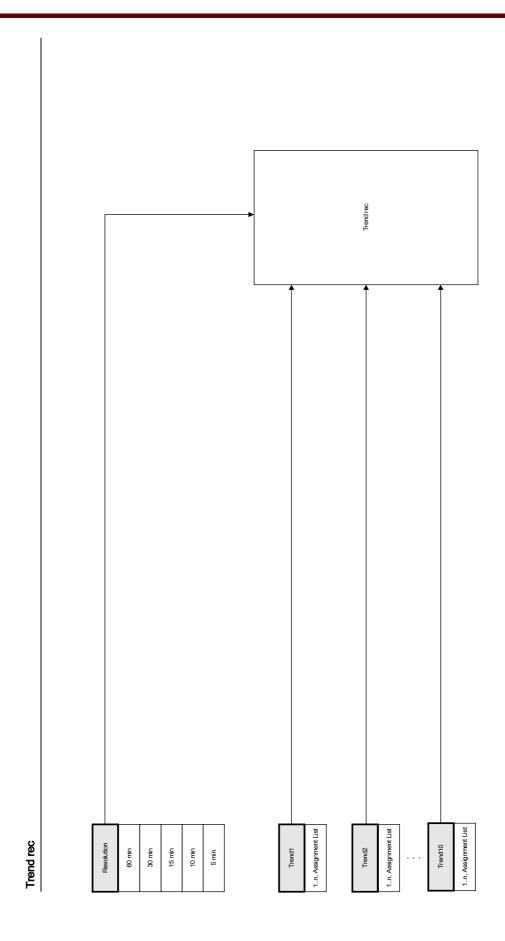
Configuring the Trend Recorder

The Trend Recorder is to be configured within [Device Para/Recorders/Trend Recorder] menu.

The User has to set the time interval. This defines the distance between two measuring points.

The User can select up to ten values that will be recorded.

Page 168 EN MRM4 12/11



Global Protection Parameters of the Trend Recorder

Parameter	Description	Setting range	Default	Menu path
Resolution	Resolution (recording frequency)	60 min,	15 min	[Device Para
		30 min,		/Recorders
		15 min,		/Trend rec]
		10 min,		
		5 min		

Page 170 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Trend1	Observed Value1	-,	IL1 RMS	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas ,		
		IG calc ,		
		10 ,		
		I1 ,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Parameter	Description	Setting range	Default	Menu path
Frend2	Observed Value2	-,	IL2 RMS	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas,		
		IG calc,		
		10,		
		l1 ,		
		12,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
	%(I2/I1),			
	%(I2/I1) max,			
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Page 172 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Trend3	Observed Value3	-,	IL3 RMS	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas ,		
		IG calc ,		
		10 ,		
		11 ,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Parameter	Description	Setting range	Default	Menu path
Trend4	Observed Value4	-,	IG meas	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas,		
		IG calc,		
		10 ,		
		11,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Page 174 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Trend5	Observed Value5	-,	%(I2/I1)	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas,		
		IG calc,		
		10,		
		l1 ,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Parameter	Description	Setting range	Default	Menu path
Trend6	Observed Value6	-,	-	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas,		
		IG calc,		
		10,		
		l1 ,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
	I2T Used,			
	HottestWindingTemp,			
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Page 176 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Trend7	Observed Value7	-,	-	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas ,		
		IG calc,		
		10 ,		
		I1 ,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(12/11),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Parameter	Description	Setting range	Default	Menu path
Trend8	Observed Value8	-,	-	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas,		
		IG calc,		
		10,		
		11,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Page 178 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Trend9	Observed Value9	-,	-	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas ,		
		IG calc,		
		10,		
		I1 ,		
		12,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
		I2T Used,		
		HottestWindingTemp,		
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Parameter	Description	Setting range	Default	Menu path
Trend10	Observed Value10	-,	-	[Device Para
		IL1 RMS,		/Recorders
		IL2 RMS,		/Trend rec]
		IL3 RMS,		
		IG meas,		
		IG calc,		
		10 ,		
		11,		
		12 ,		
		IL1 THD,		
		IL2 THD,		
		IL3 THD,		
		%(I2/I1),		
		%(I2/I1) max,		
		IL1 lb,		
	I2T Used,			
	HottestWindingTemp,			
		Hottest MotBearTemp,		
		Hottest LoadBearTemp,		
		Hottest Aux Temp,		
		Windg1,		
		Windg1 max,		
		Windg2,		
		Windg2 max,		
		Windg3,		
		Windg3 max,		
		Windg4,		
		Windg4 max,		
		Windg5,		
		Windg5 max,		
		Windg6,		
		Windg6 max,		
		MotBear1,		
		MotBear1 max,		
		MotBear2,		
		MotBear2 max,		
		LoadBear1,		
		LoadBear1 max,		
		LoadBear2,		

Page 180 EN MRM4 12/11

Trend Recorder Signals (Output States)

Name	Description
Hand Reset	Hand Reset

Direct Commands of the Trend Recorder

Parameter	Description	Setting range	Default	Menu path
Reset	Delete all entries	inactive,	inactive	[Operation
		active		/ Reset/Acknowledg e
				/Reset]

Motor Start Recorder

Available Elements:

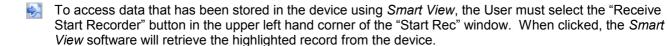
Start rec

The Motor Start Recorder is accessed using *Smart View* or via the front panel interface of the relay. This feature provides information recorded at the time of each start of the motor such as:

- Date of the motor start event;
- Record number;
- Maximum RMS phase current of each phase at the time of start;
- Current unbalance;
- TSTI and TSTR values;
- Thermal capacity used (I2T Used); and
- Number of successful starts.

Managing Start Records

The start recorder data can be downloaded from the device when the User has selected the "Start Rec" feature. To navigate to this feature, the User must go to the [Operation / Recorders] menu. Here the User will find the »Start Rec« menu item. By selecting »Start Rec«, the Start Recorder Window will appear.



A summary of the Start Recorder data can be retrieved by selecting the "Receive Summary Data" button in the upper left hand corner of the "Start Rec" window.

A list of all currently available Start Records is viewable by selecting the "Refresh Start Recorder" button on the start recorder.



It is possible to delete individual records. First, select "Receive Start Recorder", and then select the record to be deleted by clicking on the record number, or record date followed by the selection of the "Delete Start Record" button in the upper left hand corner of the "Start Rec" window.



To permanently remove all start records within a device's start recorder, select the "Delete All Start Records" button also located in the upper left hand corner of the "Start Rec" window. This will remove all previously stored start records within the device to which the User is presently connected.



Open a Start Record from a local storage device. Please note that it is possible to compare an archived Start Record against archived Parameter settings that are also stored on a local device. Read the "Caution Information at the end of this chapter".

When using Smart View to view the Start Recorder data, the Start Recorder features can also be found by right clicking anywhere within the "Start Rec" window.

Displaying Start Records

When a Start Record is called up, a window with the following options pops up.



View motor start data graphically in the <u>Data visualizer</u> software. In the <u>Data visualizer</u> software the User can view the RMS value of the phase currents, thermal capacity used, and temperatures measured by the URTD module if a URTD is installed and attached to the relay.



View motor start data overlayed with the Motor Protection Curves (Starting Profile Plot versus Protection limits). The User can view the average current recorded during the motor start versus protection elements such as 50P, or the Thermal Model. Please note, that protective elements, that are not projected within the device planning wont be visible. The User has the option to alter the displayed setting groups.

The Starting Profile Plot offers two User Scenarios:

- 1. Adapting the protection settings to the recorded start curve. The User will see the impact of parameter changes in the Profile Plot. By means of this he can decide if the relay settings match the protection requirements.
- 2. Analyzing a Start Record. Since a Start Record does not include the relay settings, the User has to ensure, that backups of the relay settings are available that were valid at the time of recording.



Please Note that the Starting Profile Plot shows the recorded average current versus the current relay settings. The relay settings itself are not part of a Start Record.

Adaptive parameters and their impacts wont be visible within the Starting Profile.

Blockings wont be visible within the Starting Profile.

In order to analyze a Start Record, the User has to ensure that the displayed settings match those at the time of recording. That means he has to use archived data in case that the relay settings have changed in between.

Global Protection Parameters of the Motor Start Recorder

Parameter	Description	Setting range	Default	Menu path
Resolution	Resolution (recording frequency)	50ms,	50ms	[Device Para
		100ms,		/Recorders
		1s		/Start rec]

Motor Start Recorder Module Input States

Name	Description	Assignment via
MotorStart	Module input state: Start of recorder	
MotorRun	Module input state: Motor is in run mode	
Motor Speed2	Module input state: Motor operates in speed 2	
ITransit	Module input state: Motor operations transition on current	

Motor Start Recorder Signals (Output States)

Name	Description
Storing	Signal: Data are saved

Direct Commands of the Motor Start Recorder

Parameter	Description	Setting range	Default	Menu path
ClearStartRe c	Delete all start recorder records	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]
ClearStatistic Rec	Delete all statistic recorder records (start trending)	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]

Statistic Recorder

The Statistic Recorder shows motor specific statistical data on a monthly base.

The Statistic Recorder can record up to 24 monthly reports. The reports are power fail-safe stored. In order to view information from the Statistic Recorder, the User has to select [Operation/Recorder/Statisticrec] from the menu tree.

By double clicking on the »Date of Record« statistics information can be viewed such as the number of starts, the number of successful starts, the average start time, the »average I2T« value during any start, and the average of all maximum currents value seen during each start.

Communication Protocols

SCADA Interface

X103

Device Planning Parameters of the Serial Scada Interface

Parameter	Description	Options	Default	Menu path
Protocol	Caution! Changing the protocol will cause a restart	-,	Modbus	[Device planning]
	of the device	Modbus,		
		IEC60870-5-103,		
		Profibus		

Global Protection Parameters of the Serial Scada Interface

Parameter	Description	Setting range	Default	Menu path
Optical rest	Optical rest position	Light off,	Light on	[Device Para
position		Light on		/X103]

Modbus®

Modbus

Modbus® Protocol Configuration

The time-controlled Modbus® protocol is based on the Master-Slave working principle. This means that the substation control and protection system sends an enquiry or instruction to a certain device (slave address) which will then be answered or carried out accordingly. If the enquiry/instruction cannot be answered/carried out (e.g. because of an invalid slave address), a failure message is returned to the master.

The Master (substation control and protection system) can <u>query</u> information <u>from</u> the device, such as:

- Type of unit version
- Measuring values/Statistical measured values
- Switch operating position (in preparation)
- State of device
- Time and date
- State of the device's digital inputs
- Protection-/State alarms

The Master (control system) can give commands/instructions to the device, such as:

- Control of switchgear (where applicable, i.e. each acc. to the applied device version)
- Change-over of parameter set
- Reset and acknowledgment of alarms/signals
- Adjustment of date and time
- Control of alarm relays

Page 184 EN MRM4 12/11

For detailed information on data point lists and error handling, please refer to the Modbus® documentation.

To allow configuration of the devices for Modbus® connection, some default values of the control system must be available.

Modbus RTU

Part 1: Configuration of the Devices

Call up »Device parameter/Modbus« and set the following communication parameters there:

- Slave-address, to allow clear identification of the device.
- Baud-Rate

Also, select below indicated RS485 interface-related parameters from there, such as:

- Number of data bits
- One of the following supported communication variants: Number of data bits, even, odd, parity or no parity, number of stop bits.
- »t-timeout«: communication errors are only identified after expiry of a supervision time »t-timeout«.
- Response time (defining the period within which an inquiry from the master has to be answered).

Part 2: Hardware Connection

- For hardware connection to the control system, there is an RS485 interface at the rear side of the device (RS485, fiber optic or terminals).
- Connect bus and device (wiring).

Error Handling - Hardware Errors

Information on physical communication errors, such as:

- Baud rate Error
- Parity Error ...

can be obtained from the event recorder.

Error Handling - Errors on protocol level

If, for example, an invalid memory address is enquired, error codes will be returned by the device that need to be interpreted.

Modbus TCP



Establishing a connection via TCP/IP to the device is only possible if your device is equipped with an Ethernet Interface (RJ45).

Contact your IT administrator in order to establish the network connection.

Part 1: Setting the TCP/IP Parameters

Call up »Device parameter/TCP/IP« at the HMI (panel) and set the following parameters:

- TCP/IP address
- Subnetmask
- Gateway

Part 2: Configuration of the Devices

Call up »Device parameter/Modbus« and set the following communication parameters:

- Setting a Unit Identifier is only necessary if a TCP network should be coupled to a RTU network.
- If a different port than the default port 502 should be used please proceed as follows:
 - Choose "Private" within the TCP-Port-Configuration.
 - Set the port-number.
- Set the maximum accepted time of "no communication". If this time has expired without any communication, the device concludes a failure within the master system.
- Allow or disallow the blocking of SCADA commands.

Part 3: Hardware Connection

- There is a RJ45 interface at the rear side of the device for the hardware connection to the control system.
- Establish the connection to the device by means of a proper Ethernet cable.

Direct Commands of the Modbus®

Parameter	Description	Setting range	Default	Menu path
Res Diagn Cr	All Modbus Diagnosis Counters will be reset.	inactive,	inactive	[Operation
		active		/ Reset/Acknowledg e
				/Reset]

Page 186 EN MRM4 12/11

Global Protection Parameters of the Modbus®

Device address (Slave ID) within the bus system. Each device address has to be unique within a	01.01.47	1	
bus system.		'	[Device Para /Modbus]
Only available if:Device planning = RTU	1 255	255	[Device Para
parameter is to be set, if a Modbus RTU and a Modbus TCP network should be coupled.	1 - 200	255	/Modbus]
Only available if:Device planning = TCP			
TCP Port Configuration. This parameter is to be	Default,	Default	[Device Para
be used.	Private		/Modbus]
Only available if:Device planning = TCP			
Port number	502 - 65535	502	[Device Para
Only available if:Device planning = TCP And Only available if: TCP Port Config = Private			/Modbus]
Within this time the answer has to be received by the SCADA system, otherwise the request will be disregarded. In that case the Scada system detects a communication failure and the Scada System has to send a new request.	0.01 – 10.00 s	1 s	[Device Para /Modbus]
Only available if:Device planning = RTU			
Baud rate	1200,	19200	[Device Para
Only available if:Device planning = RTU	2400,		/Modbus]
	4800,		
	9600,		
	19200,		
	38400		
Digit 1: Number of bits. Digit 2: E=even parity,	8E1,	8E1	[Device Para
bits. More information on the parity: It is possible			/Modbus]
	8N1,		
errors. The parity bit ensures that with even parity ("EVEN") always an even number of bits with valence "1" or with odd parity ("ODD") an odd number of "1" valence bits are transmitted. But it is also possible to transmit no parity bits (here the setting is "Parity = None"). More information on the stop-bits: The end of a data byte is terminated by the stop-bits.	8N2		
	The Unit Identifier is used for routing. This parameter is to be set, if a Modbus RTU and a Modbus TCP network should be coupled. Only available if:Device planning = TCP TCP Port Configuration. This parameter is to be set only if the default modbus TCP Port should not be used. Only available if:Device planning = TCP Port number Only available if:Device planning = TCP And Only available if: TCP Port Config = Private Within this time the answer has to be received by the SCADA system, otherwise the request will be disregarded. In that case the Scada system detects a communication failure and the Scada System has to send a new request. Only available if:Device planning = RTU Baud rate Only available if:Device planning = RTU Baud rate Only available if:Device planning = RTU Digit 1: Number of bits. Digit 2: E=even parity, O=odd parity, N=no parity. Digit 3: Number of stop bits. More information on the parity: It is possible that the last data bit is followed by a parity bit which is used for recognition of communication errors. The parity bit ensures that with even parity ("EVEN") always an even number of bits with valence "1" or with odd parity ("ODD") an odd number of "1" valence bits are transmitted. But it is also possible to transmit no parity bits (here the setting is "Parity = None"). More information on the stop-bits: The end of a data byte is terminated	The Unit Identifier is used for routing. This parameter is to be set, if a Modbus RTU and a Modbus TCP network should be coupled. Only available if:Device planning = TCP TCP Port Configuration. This parameter is to be set only if the default modbus TCP Port should not be used. Only available if:Device planning = TCP Port number Only available if:Device planning = TCP And Only available if: TCP Port Config = Private Within this time the answer has to be received by the SCADA system, otherwise the request will be disregarded. In that case the Scada system detects a communication failure and the Scada System has to send a new request. Only available if:Device planning = RTU Baud rate 1200, 2400, 4800, 9600, 19200, 38400 BE1, 801, 8N1, 8N1 NN2	The Unit Identifier is used for routing. This parameter is to be set, if a Modbus RTU and a Modbus TCP network should be coupled. Only available if:Device planning = TCP TCP Port Configuration. This parameter is to be set only if the default modbus TCP Port should not be used. Only available if:Device planning = TCP Port number Only available if:Device planning = TCP And Only available if:TcP Port Config = Private Within this time the answer has to be received by the SCADA system, otherwise the request will be disregarded. In that case the Scada system detects a communication failure and the Scada System has to send a new request. Only available if:Device planning = RTU Baud rate Only available if:Device planning = RTU Baud ra

Parameter	Description	Setting range	Default	Menu path
t-call	If there is no request telegram sent from Scada to the device after expiry of this time - the device concludes a communication failure within the Scada system.	1 - 3600s	10s	[Device Para /Modbus]
Scada CmdBlo	Activating (allowing)/ Deactivating (disallowing) the blocking of the Scada Commands	inactive, active	inactive	[Device Para /Modbus]
Disable Latching	Disable Latching: If this parameter is active (true), none of the Modbus states will be latched. That means that trip signals wont be latched by Modbus.	inactive, active	inactive	[Device Para /Modbus]
AllowGap	If this parameter is active (True), the User can request a set of modbus register without getting an exception, because of invalid address in the requested array. The invalid addresses have a special value 0xFAFA, but the User is responsible for ignoring invalid addresses. Attention: This special value can be valid, if address is valid.	inactive, active	inactive	[Device Para /Modbus]

Modbus® Signals (Output States)

NOTICE

Some signals (that are for a short time active only) have to be acknowledged separately (e.g. Trip signals) by the Communication System.

Name	Description
Transmission	Signal: SCADA active
Scada Cmd 1	Scada Command
Scada Cmd 2	Scada Command
Scada Cmd 3	Scada Command
Scada Cmd 4	Scada Command
Scada Cmd 5	Scada Command
Scada Cmd 6	Scada Command
Scada Cmd 7	Scada Command
Scada Cmd 8	Scada Command
Scada Cmd 9	Scada Command
Scada Cmd 10	Scada Command
Scada Cmd 11	Scada Command
Scada Cmd 12	Scada Command
Scada Cmd 13	Scada Command
Scada Cmd 14	Scada Command
Scada Cmd 15	Scada Command
Scada Cmd 16	Scada Command

Page 188 EN MRM4 12/11

Modbus® Values

Value	Description	Default	Size	Menu path
NoOfRequestsTotal	Total number of requests. Includes requests for other slaves.	0	0 - 9999999999	[Operation /Count and RevData /Modbus]
NoOfRequestsForM e	Total Number of requests for this slave.	0	0 - 9999999999	[Operation /Count and RevData /Modbus]
NoOfResponse	Total number of requests having been responded. Only available if:Device planning = TCP	0	0 - 999999999	[Operation /Count and RevData /Modbus]
NoOfResponsTimeO verruns	Total number of requests with exceeded response time. Physically corrupted Frame. Only available if:Device planning = RTU	0	0 - 9999999999	[Operation /Count and RevData /Modbus]
NoOfOverrunErros	Total Number of Overrun Failures. Physically corrupted Frame. Only available if:Device planning = RTU	0	0 - 999999999	[Operation /Count and RevData /Modbus]
NoOfParityErrors	Total number of parity errors. Physically corrupted Frame. Only available if:Device planning = RTU	0	0 - 9999999999	[Operation /Count and RevData /Modbus]
NoOfFrameErrors	Total Number of Frame Errors. Physically corrupted Frame. Only available if:Device planning = RTU	0	0 - 999999999	[Operation /Count and RevData /Modbus]
NoOfBreaks	Number of detected communication aborts Only available if:Device planning = RTU	0	0 - 999999999	[Operation /Count and RevData /Modbus]
NoOfQueryInvalid	Total number of Request errors. Request could not be interpreted	0	0 - 999999999	[Operation /Count and RevData /Modbus]
NoOfInternalError	Total Number of Internal errors while interpreting the request.	0	0 - 9999999999	[Operation /Count and RevData /Modbus]

Profibus

Profibus

Part 1: Configuration of the Devices

Call up »Device parameter/Profibus« and set the following communication parameter:

Slave-address, to allow clear identification of the device.

In addition to that the Master has to be provided with the GSD-file. The GSD-file can be taken from the Product-CD.

Part 2: Hardware Connection

- For hardware connection to the control system, there is optional an D-SUB interface at the rear side of the device.
- Connect bus and device (wiring).
- Up to 123 slaves can be connected.
- Terminate the Bus by means of an Terminate Resistor.

Error Handling

Information on physical communication errors, such as:

Baud Rate Error

can be obtained from the event recorder or the status display.

Error Handling – Status LED at the rear side

The Profibus D-SUB interface at the rear side of the device is equipped with an status LED.

- Baud Search -> red flashing
- Baud Found -> green flashing
- Data Exchange -> green
- No Profibus/Unplugged, not connected -> red

Direct Commands of the Profibus

Parameter	Description	Setting range	Default	Menu path
Reset Comds	All Profibus Commands will be reset.	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]

Page 190 EN MRM4 12/11

Global Protection Parameters of the Profibus

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
1				/Profibus
				/Assignment 1-16]
atched 1	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	City available in Eatonea addition			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
2				/Profibus
				/Assignment 1-16]
atched 2	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
3				/Profibus
				/Assignment 1-16]
atched 3	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
4				/Profibus
				/Assignment 1-16]
_atched 4	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Profibus
				/Assignment 1-16]
atched 5	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	City available in Eatonea addition			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
5				/Profibus
				/Assignment 1-16]
atched 6	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	Sing available in Eatoriou donvo			/Assignment 1-16]

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
7				/Profibus
				/Assignment 1-16]
Latched 7	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	Only available ii. Eateried – active			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
8				/Profibus
				/Assignment 1-16]
Latched 8	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	only available in Eastered active			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
9				/Profibus
				/Assignment 1-16]
Latched 9	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
10				/Profibus
				/Assignment 1-16]
Latched 10	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	·			/Assignment 1-16]
Assignment 11	Assignment	1n, Assignment List		[Device Para
11				/Profibus
				/Assignment 1-16]
Latched 11	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 1-16]
Assignment 12	Assignment	1n, Assignment List		[Device Para
12				/Profibus
				/Assignment 1-16]
Latched 12	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 1-16]
Assignment 13	Assignment	1n, Assignment List		[Device Para
13				/Profibus
				/Assignment 1-16]

Page 192 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Latched 13	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	Offity available ii. Laterieu – active			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
14				/Profibus
				/Assignment 1-16]
Latched 14	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	City available ii. Eatoried dolive			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
15				/Profibus
				/Assignment 1-16]
Latched 15	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	0 m, 0 m 200 m 200 m			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
16				/Profibus
				/Assignment 1-16]
Latched 16	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 1-16]
Assignment	Assignment	1n, Assignment List		[Device Para
17				/Profibus
				/Assignment 17-32]
Latched 17	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment 18	Assignment	1n, Assignment List		[Device Para
10				/Profibus
				/Assignment 17-32]
Latched 18	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment 19	Assignment	1n, Assignment List		[Device Para
15				/Profibus
				/Assignment 17-32]
Latched 19	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]

Parameter	Description	Setting range	Default	Menu path
Assignment	Assignment	1n, Assignment List		[Device Para
20				/Profibus
				/Assignment 17-32]
Latched 20	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	City available in Eaterlea addition			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List		[Device Para
21				/Profibus
				/Assignment 17-32]
Latched 21	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment	Assignment	1n, Assignment List		[Device Para
22				/Profibus
				/Assignment 17-32]
Latched 22	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List		[Device Para
23				/Profibus
				/Assignment 17-32]
Latched 23	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment 24	Assignment	1n, Assignment List		[Device Para
24				/Profibus
				/Assignment 17-32]
Latched 24	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment 25	Assignment	1n, Assignment List		[Device Para
20				/Profibus
				/Assignment 17-32]
Latched 25	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment 26	Assignment	1n, Assignment List		[Device Para
				/Profibus
				/Assignment 17-32]

Page 194 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Latched 26	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	Offity available ii. Latched – active			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List		[Device Para
27				/Profibus
				/Assignment 17-32]
Latched 27	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	Only available ii. Eatoned active			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
28				/Profibus
				/Assignment 17-32]
Latched 28	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List		[Device Para
29				/Profibus
				/Assignment 17-32]
Latched 29	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
30				/Profibus
				/Assignment 17-32]
Latched 30	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
	,			/Assignment 17-32]
Assignment	Assignment	1n, Assignment List	-,-	[Device Para
31				/Profibus
				/Assignment 17-32]
Latched 31	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]
Assignment 32	Assignment	1n, Assignment List		[Device Para
32				/Profibus
				/Assignment 17-32]
Latched 32	Defines whether the Input is latched.	inactive,	inactive	[Device Para
	Only available if: Latched = active	active		/Profibus
				/Assignment 17-32]

Parameter	Description	Setting range	Default	Menu path
Slave ID	Device address (Slave ID) within the bus system. Each device address has to be unique within a bus system.	2 - 125	2	[Device Para /Profibus /Bus parameters]

Inputs of the Profibus

Name	Description	Assignment via
Assignment 1-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 2-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 3-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 4-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 5-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 6-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 7-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 8-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 9-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 10-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]

Page 196 EN MRM4 12/11

Name	Description	Assignment via
Assignment 11-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 12-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 13-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 14-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 15-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 16-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 1-16]
Assignment 17-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 18-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 19-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 20-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 21-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 22-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 23-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]

Name	Description	Assignment via
Assignment 24-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 25-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 26-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 27-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 28-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 29-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 30-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 31-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]
Assignment 32-I	Module input state: Scada Assignment	[Device Para
		/Profibus
		/Assignment 17-32]

Profibus Signals (Output States)

Name	Description
Data OK	Data within the Input field are OK (Yes=1)
SubModul Err	Assignable Signal, Failure in Sub-Module, Communication Failure.
Connection active	Connection active
Scada Cmd 1	Scada Command
Scada Cmd 2	Scada Command
Scada Cmd 3	Scada Command
Scada Cmd 4	Scada Command
Scada Cmd 5	Scada Command

Page 198 EN MRM4 12/11

Name	Description
Scada Cmd 6	Scada Command
Scada Cmd 7	Scada Command
Scada Cmd 8	Scada Command
Scada Cmd 9	Scada Command
Scada Cmd 10	Scada Command
Scada Cmd 11	Scada Command
Scada Cmd 12	Scada Command
Scada Cmd 13	Scada Command
Scada Cmd 14	Scada Command
Scada Cmd 15	Scada Command
Scada Cmd 16	Scada Command

Profibus Values

Value	Description	Default	Size	Menu path
Master ID	Device address (Master ID) within the bus			[Operation
	system. Each device address has to be unique within a bus system.			/Status display
	anique mami a sue ejecem			/Profibus
				/State]
HO ld PSub	Handoff Id of PbSub	0	0 - 999999999	[Operation
				/Status display
				/Profibus
				/State]
t-WatchDog	t-WatchDog The Profibus Chip detects a communication 0	0	0 - 999999999	[Operation
	issue if this timer is expired without any communication (Parameterising telegram).			/Status display
	communication (i arameterising telegram).			/Profibus
				/State]
Slave State	Communication State between Slave and Baud Search		Baud Search,	[Operation
	Master.		Baud Found,	/Status display
			PRM OK,	/Profibus
			PRM REQ,	/State]
			PRM Fault,	
			CFG Fault,	
			Clear Data,	
			Data exchange	

Value	Description	Default	Size	Menu path
Baud rate	The baud rate that has been detected lastly,		12 Mb/s,	[Operation
	will still be shown after a connection issue.		6 Mb/s,	/Status display
			3 Mb/s,	/Profibus
			1.5 Mb/s,	/State]
			0.5 Mb/s,	
			187500 baud,	
			93750 baud,	
			45450 baud,	
			19200 baud,	
			9600 baud,	
			-,-	
PNO Id	PNO Identification Number. GSD	0C50 h	0C50 h	[Operation
	Identification Number.			/Status display
				/Profibus
				/State]

IEC60870-5-103

IEC 103

IEC60870-5-103 Protocol Configuration

In order to use the IEC60870-5-103 protocol it has to be assigned to the X103 Interface within the Device Planning. The device will reboot after setting this parameter.



The parameter X103 is only available if the device is at the rear side equipped with an interface like RS485 or Fiber Optic.



If the device is equipped with an Fiber Optic Interface, the Optical Rest Position has to be set within the Device Parameters .

The time-controlled IEC60870-5-103 protocol is based on the Master-Slave working principle. This means that the substation control and protection system sends an enquiry or instruction to a certain device (slave address) which will then be answered or carried out accordingly.

The device meets the compatibility mode 2. Compatibility mode 3 is not supported.

The following IEC60870-5-103-functions will be supported:

- Initialization (Reset)
- Time Synchronization
- Reading out of time stamped, instantaneous signals
- General Queries
- Cyclic Signals
- General Commands

Page 200 EN MRM4 12/11

Transmission of Disturbance Data

Initialization

The communication has to be reset by a Reset Command each time that the device is turned on or that communication parameters have been changed. The "Reset CU" Command resets. The relay acts on both Reset Commands (Reset CU or Reset FCB).

The relay acts on the reset command by an identification signal ASDU 5 (Application Service Data Unit), as a reason (Cause Of Transmission, COT) for the transmission of the answer either a "Reset CU" or a "Reset FCB" will be sent depending on the type of the reset command. This information can be part of the data section of the ASDU-signal.

Name of the Manufacturer

The section for the identification of the software contains three digits of the device code for the identification of the device type. Beside the upper mentioned identification number the device generates a communication start event.

Time Synchronization

Time and date of the relay can be set by means of the time synchronization function of the IEC60870-5-103 protocol. If the time synchronization signal is send out with a confirmation request, the device will answer with a confirmation signal.

Spontaneous Events

The events that are generated by the device will be forwarded to the master with numbers for standard function types / standard information. The data point list comprises all events that can be generated by the device.

Cyclic Measurement

The device generates on a cyclic base measured values by means of ASDU 9. They can be read out via a class 2 query. Please take into account that the measured values will be send out as multiples (1.2 or 2.4 times the rated value). How to set 1.2 or 2.4 as multiplier for a value can be taken from the data point list.

The parameter "Transm priv meas val" defines if additional measurement values should be transmitted in the private part. Public and private measured values are transmitted by ASDU9. That means that either a "private" or a "public" ASDU9 will be transmitted. If this parameter is set, the ASDU9 will contain additional measured values that are an enhancement of the standard. The "private" ASDU9 is send with a fixed function type and information number that does not depend the type of device. Please refer to the data point list.

Commands

The data point list comprises a list of the supported commands. Any command will be responded by the device with a positive or negative confirmation. If the command is executable, the execution with the corresponding reason for the transmission (COT) will be lead in at first, and subsequently the execution will be confirmed with COT1 within a ASDU9.

Disturbance Recording

The disturbances recorded by the device can be read out by means described in standard IEC60870-5-103. The device is in compliance with the VDEW-Control System by transmission of an ASDU 23 without disturbance records at the beginning of an GI-Cycle.

A disturbance record contains the following information:

- Analog Measured Values, IL1, IL2, IL3, IN, Voltages VL1, VL2, VL3, VEN;
- Binary States, transmitted as marks, e.g. Alarms and Trips.
- The Transmission ratio will not be supported. The transmission ratio is included in the "Multiplier".

Blocking the Transmission Direction

The relay does not support functions to block the transmission in a certain direction (supervision direction).

Global Protection Parameters of the IEC60870-5-103

Parameter	Description	Setting range	Default	Menu path
Slave ID	Device address (Slave ID) within the bus system. Each device address has to be unique within a bus system.	1 - 247	1	[Device Para /IEC 103]
t-call	If there is no request telegram sent from Scada to the device after expiry of this time - the device concludes a communication failure within the Scada system.	1 – 3600 s	60 s	[Device Para /IEC 103]
Transm priv	Transmit additional (private) measuring values	inactive,	inactive	[Device Para
meas val		active		/IEC 103]
Baud rate	Baud rate	1200,	19200	[Device Para
		2400,		/IEC 103]
		4800,		
		9600,		
		19200,		
		38400,		
		57600		
Physical	Digit 1: Number of bits. Digit 2: E=even parity,	8E1,	8E1	[Device Para
Settings	O=odd parity, N=no parity. Digit 3: Number of stop bits. More information on the parity: It is possible	801,		/IEC 103]
	that the last data bit is followed by a parity bit	8N1,		
	which is used for recognition of communication errors. The parity bit ensures that with even parity ("EVEN") always an even number of bits with valence "1" or with odd parity ("ODD") an odd number of "1" valence bits are transmitted. But it is also possible to transmit no parity bits (here the setting is "Parity = None"). More information on the stop-bits: The end of a data byte is terminated by the stop-bits.	8N2		

IEC60870-5-103 Signals (Output States)

Name	Description
Scada Cmd 1	Scada Command
Scada Cmd 2	Scada Command
Scada Cmd 3	Scada Command
Scada Cmd 4	Scada Command
Scada Cmd 5	Scada Command
Scada Cmd 6	Scada Command
Scada Cmd 7	Scada Command
Scada Cmd 8	Scada Command
Scada Cmd 9	Scada Command

Page 202 EN MRM4 12/11

Name	Description
Scada Cmd 10	Scada Command
Transmission	Signal: SCADA active
Fail phy Interf	Failure in the physical interface
Failure Event lost	Failure event lost

IEC60870-5-103 Values

Value	Description	Default	Size	Menu path
Internal errors	Internal errors	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NReceived	Total Number of received Messages	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NSent	Total Number of sent Messages	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NBadFramings	Number of bad Messages	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NBadParities	Number of Parity Errors	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NBreakSignals	Number of Communication Interrupts	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NInternalError	Number of Internal Errors	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC 103]
NBadCharChecksu	Number of Checksum Errors	0	0 - 999999999	[Operation
m				/Count and RevData
				/IEC 103]

IEC61850

IEC61850

Introduction

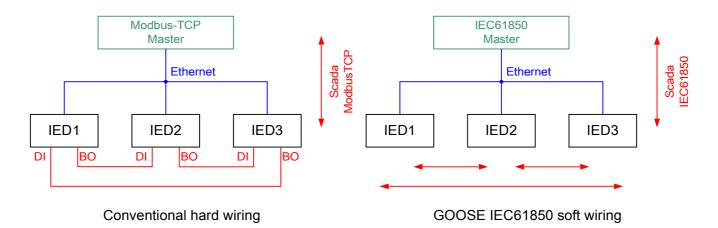
To understand the functioning and mode of operation of a substation in an IEC61850 automation environment, it is useful to compare the commissioning steps with those of a conventional substation in a Modbus TCP environment.

In a conventional substation the individual IEDs (Intelligent Electronic Devices) communicate in vertically direction with the higher level control center via SCADA. The horizontal communication is exclusively realized by wiring relay output contacts (OR) and digital inputs (DI) among each other.

In an IEC61850 environment communication between the IEDs takes place digitally (via Ethernet) by a service called GOOSE (Generic Object Oriented Substation Event). By means of this service information about events is submitted between each IED. Therefore each IED has to know about the functional capability of all other connected IEDs.

Each IEC61850 capable device includes a description of it's own functionality and communications skills (IED Capability Description, *.ICD).

By means of a Substation Configuration Tool to describe the structure of the substation, assignment of the devices to the primary technique, etc. a virtual wiring of the IEDs among each other and with other switch gear of the substation can be done. A description of the substation configuration will be generated in form of a *.SCD file. At last this file has to be submitted to each device. Now the IEDs are able to communicate closed among each other, react to interlockings and operate switch gear.



Commissioning steps for a conventional substation with modbus TCP environment:

- · Parameter setting of the IEDs
- Ethernet installation
- TCP/IP settings for the IEDs
- · Wiring according to wiring scheme

Commissioning steps for a substation with IEC61850 environment:

- Parameter setting of the IEDs Ethernet installation TCP/IP settings for the IEDs
- 2. IEC61850 configuration (software wiring)
 - a) Exporting an ICD file from each device
 - b) Configuration of the substation (generating a SCD file)
 - c) Transmit SCD file to each device

Generation/Export of a Device Specific ICD File

Each device of the HighPROTEC line includes a description of it's own functionality and communications skills in form of an *.ICD file (IED Capability Description). This file can be exported as follows and be used for the configuration of the substation.



 A change of the devices parameters has an influence on the content of the ICD file.

Page 204 EN MRM4 12/11

- 1. Connect the device with your PC/Notebook.
- 2.Start Smart View.
- 3.Click on »Receive data from Device« in the menu »Device«.
- 4.Click on »IEC61850« in the menu »Device Para«.
- 5. Click on the ICD icon in the IEC61850 window.
- 6. Select a drive and file name for the ICD file and click "save".
- 7. Repeat the steps 1 to 6 for all connected devices in this IEC61850 environment.

Generation/Export of a SCD File

Each device of the HighPROTEC can create an export it's own functionality and communications skills in form of a *.SCD file.

- 1. Connect the device with your PC/Notebook.
- 2.Start Smart View.
- 3.Click on »Receive data from Device« in the menu »Device«.
- 4.Click on »IEC61850« in the menu »Device Para«.
- 5.Click on the SCD icon in the IEC61850 window.
- 6. Select a drive and file name for the SCD file and click "save".
- 7. Repeat the steps 1 to 6 for all connected devices in this IEC61850 environment.

Substation Configuration, Generation of .SCD file (Station Configuration Description)

The substation configuration, i. e. connection of all logical nodes of protection and control devices, as well as switch gear usually is done with a "Substation Configuration Tool". Therefore the ICD files of all connected IEDs in the IEC61850 environment have to be available. The result of the station wide "software wiring" can be exported in the form of a SCD file (Station Configuration Description).

Suitable Substation Configuration Tools (SCT) are available by the following Companies:

H&S, Hard- & Software Technologie GmbH & Co. KG, Dortmund (Germany) (www.hstech.de). Applied Systems Engineering Inc. (www.ase-systems.com)
Kalki Communication Technologies Limited (www.kalkitech.com)

Import of the .SCD File Into the Device

When the substation configuration is completed, the .SCD file has to be transmitted to all connected devices. This is has to be done as follows:

- 1. Connect the device with your PC/Notebook.
- 2.Start Smart View.
- 3.Click on »Receive data from Device« in the menu »Device«.
- 4.Click on »IEC61850« in the menu »Device Para«.
- 5.Switch the parameter »IEC61850 Communication« to »OFF« and submit the changed parameter set into the device.
- 6.Click on the IEC icon in the IEC61850 window.
- 7.Select the folder, where the .SCD file is stored. Select the .SCD file and click "open".
- 8.Now a password is requested. Enter the same password, which you use for parameter setting of the device (4 digits).
- 9.Acc. to step 5 switch on again the IEC Communication and submit the changed parameter set into the device.
- 10. Repeat the steps 1 to 9 for all devices connected to this IEC61850 environment.
- 11. If no error message occurs, the configuration has been completed successfully.



When changing the substation configuration, usually a new .SCD file has to be generated. This .SCD file has to be mandatory transmitted to all

- devices by means of Smart View. For the case, that this step will be forgotten, IEC61850 malfunctions will be the result
- Provided that parameters of the devices are changed after the substation configuration completion, changes in the corresponding .ICD file may result – this in turn may make an update of the .SCD file necessary.

IEC 61850 Virtual Outputs

Additionally to the standardized logical node status information up to 16 free configurable status information can be assigned to 16 Virtual Outputs. This can be done in the menu [Device Para/IEC61850].

Device Planning Parameters of the IEC 61850

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Direct Commands of the IEC 61850

Parameter	Description	Setting range	Default	Menu path	
ResetStatistic	Reset of all IEC61850 diagnostic counters	inactive,	inactive	[Operation	
		active		/Reset/Acknowledge	
				/Reset]	

Global Protection Parameters of the IEC 61850

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive, active	inactive	[Device Para /IEC61850]
VirtualOutput1	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List	-,-	[Device Para /IEC61850]
VirtualOutput2	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput3	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput4	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput5	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput6	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]

Page 206 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
VirtualOutput7	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput8	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput9	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput10	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput11	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput12	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput13	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput14	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput15	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]
VirtualOutput16	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	1n, Assignment List		[Device Para /IEC61850]

States of the Inputs of the IEC 61850

Name	Description	Assignment via
VirtOut1-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut2-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut3-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut4-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut5-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]

Name	Description	Assignment via
VirtOut7-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut8-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut9-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut10-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut11-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut12-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut13-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut14-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut15-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]
VirtOut16-I	Module input state: Binary state of the	[Device Para
	Virtual Output (GGIO)	/IEC61850]

IEC 61850 Module Signals (Output States)

Name	Description
VirtInp1	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp2	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp3	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp4	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp5	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp6	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp7	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp8	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp9	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp10	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp11	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp12	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp13	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp14	Signal: Virtual Input (IEC61850 GGIO Ind)
VirtInp15	Signal: Virtual Input (IEC61850 GGIO Ind)

Page 208 EN MRM4 12/11

Name	Description
VirtInp16	Signal: Virtual Input (IEC61850 GGIO Ind)

IEC 61850 Module Values

Value	Description	Default	Size	Menu path
NoOfGooseRxAll	Total number of received GOOSE messages including messages for other devices (subscribed and not subscribed messages).	0	0 - 9999999999	[Operation
				/Count and RevData
	(Casconsoa and not casconsoa moscagos).			/IEC61850]
NoOfGooseRxSubsc	Total Number of subscribed GOOSE	0	0 - 9999999999	[Operation
ribed	messages including messages with incorrect content.			/Count and RevData
	oonone.			/IEC61850]
NoOfGooseRxCorre	Total Number of subscribed and correctly	0	0 - 9999999999	[Operation
ct	received GOOSE messages.			/Count and RevData
				/IEC61850]
NoOfGooseRxNew	Number of subscribed and correctly received	0	0 - 9999999999	[Operation
	GOOSE messages with new content.			/Count and RevData
				/IEC61850]
NoOfGooseTxAll	Total Number of GOOSE messages that have been published by this device.	0	0 - 999999999	[Operation
				/Count and RevData
				/IEC61850]
NoOfGooseTxNew	Total Number of new GOOSE messages (modified content) that have been published by this device.	0	0 - 999999999	[Operation
				/Count and RevData
	by this device.			/IEC61850]
NoOfServerRequest	Total number of MMS Server requests	0	0 - 9999999999	[Operation
sAll	including incorrect requests.			/Count and RevData
				/IEC61850]
NoOfDataReadAll	Total Number of values read from this device including incorrect requests.	0	0 - 9999999999	[Operation
				/Count and RevData
				/IEC61850]
	Total Number of correctly read values from	0	0 - 9999999999	[Operation
ct	this device.			/Count and RevData
				/IEC61850]
NoOfDataWrittenAll	Total Number of values written by this device	0	0 - 999999999	[Operation
	including incorrect ones.			/Count and RevData
				/IEC61850]
NoOfDataWrittenCor	Total Number of correctly written values by	0	0 - 9999999999	[Operation
rect	this device.			/Count and RevData
				/IEC61850]

Value	Description	Default	Size	Menu path
NoOfDataChangeNo tification	Number of detected changes within the data sets that are published with GOOSE	0	0 - 9999999999	[Operation
uncation	messages.			/Count and RevData
				/IEC61850]

Values of the IEC 61850

Value	Description	Default	Size	Menu path
GoosePublisherStat	State of the GOOSE Publisher (on or off)	Off	Off,	[Operation
е			On,	/Status display
			Error	/IEC61850]
	State of the GOOSE Subscriber (on or off)	Off	Off,	[Operation
te			On,	/Status display
			Error	/IEC61850]
MmsServerState	State of MMS Server (on or off)	Off	Off,	[Operation
			On,	/Status display
			Error	/IEC61850]

Page 210 EN MRM4 12/11

Time Synchronisation

Time Zones

The User has the possibility to synchronise the device with a central time generator. This offers the following advantages:

- The time does not drift off from the reference time. A continuously accumulating deviation from the reference time thereby will be balanced. Also refer to the chapter Specifications (Tolerances Real Time Clock).
- All time synchronised devices operate with the same time. Thus logged events of the individual devices
 can be compared exactly and be evaluated in conjunction (single events of the event recorder,
 disturbance records).

The device's time can be synchronised via the following protocols:

- IRIG-B
- SNTP
- Communications-Protocol Modbus (RTU or TCP)
- Communications-Protocol IEC60870-5-103

The provided protocols use different hardware interfaces and differ also in their achieved time accuracy. Further information can be found in the chapter Specifications.

Used Protocol	Hardware-Interface	Recommended Application
Without time synchronisation		Not recommended
IRIG-B	IRIG-B Terminal	Recommended, if interface available
SNTP	RJ45 (Ethernet)	Recommended alternative to IRIG-B, especially when using IEC 61850 or Modbus TCP
Modbus RTU	RS485, D-SUB or Fibre Optic	Recommended when using Modbus RTU communication protocol and when no IRIG-B code generator is available
Modbus TCP	RJ45 (Ethernet)	Limited recommendation when Modbus TCP communication protocol is used and no IRIG-B code generator or SNTP-Server is available
IEC 60870-5-103	RS485, D-SUB or Fibre Optic	Recommended when using IEC 10870-5-103 communication protocol and no IRIG-B code generator is available

Accuracy of Time Synchronisation

The accuracy of the device's synchronised system time depends on several factors:

- accuracy of the connected time generator
- used synchronisation protocol
- when using Modbus TCP or SNTP: Network load and data package transmission times



Please consider the accuracy of the used time generator. Fluctuations of the time generator's time will cause the same fluctuations of the protection relay's system time.

Selection of Time zone and Synchronisation Protocol

The protection relay masters both UTC and local time. This means that the device can be synchronised with UTC time while using local time for User display.

Time Synchronisation with UTC time (recommended):

Time synchronisation is usually done using UTC time. This means for example that an IRIG-B time generator is sending UTC time information to the protection relay. This is the recommended use case, since here a continuous time synchronisation can be ensured. There are no "leaps in time" through change of summer- and wintertime.

To achieve that the device shows the current local time, the time zone and the change between summer- and wintertime can be configured.

Please carry out the following parametrization steps under [Device Para/ Time]:

- 1. Select your local time zone in the time zone menu.
- 2. There also configure the switching of daylight saving time.
- 3. Select the used time synchronisation protocol in the TimeSync menu (e.g. "IRIG-B").
- 4. Set the parameters of the synchronisation protocol (refer to the according chapter).

Time Synchronisation with local time:

Should the time synchronisation however be done using local time, then please leave the time zone to »*UTC+0 London«* and do not use switching of daylight saving time.



The synchronisation of the relay's system time is exclusively done by the synchronisation protocol selected in the menu [Device Para/ Time/ TimeSync/ Used Protocol].

Without Time Synchronisation:

To achieve that the device shows the current local time, the time zone and the change between summer- and wintertime can be configured.

Please carry out the following parametrization steps under [Device Para/ Time]:

- 1. Select your local time zone in the time zone menu.
- 2. There also configure the switching of daylight saving time.
- 3.Select »manual« as your used protocol in the TimeSync menu.
- 4.Set date and time.

Global Protection Parameters of the Time Synchronization

Parameter	Description	Setting range	Default	Menu path
DST offset	Difference to wintertime	-180 – 180 min	60 min	[Device Para
				/Time
				/Timezone]
DST manual	Manual setting of the Daylight Saving Time	inactive,	active	[Device Para
		active		/Time
				/Timezone]
Summertime	Daylight Saving Time	inactive,	inactive	[Device Para
	Only available if: DST manual = active	active		/Time
	5, 5			/Timezone]

Page 212 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Summertime m	Month of clock change summertime	January,	March	[Device Para
	Only available if: DST manual = active	February,		/Time
	3, a.a	March,		/Timezone]
		April,		
		May,		
		June,		
		July,		
		August,		
		September,		
1		October,		
		November,		
		December		
Summertime d	Day of clock change summertime	Sunday,	Saturday	[Device Para
	Only available if: DST manual = active	Monday,		/Time
	3, a.a	Tuesday,		/Timezone]
		Wednesday,		
		Thursday,		
		Friday,		
		Saturday,		
		General day		
Summertime w	Place of selected day in month (for clock	First,	Last	[Device Para
	change summertime)	Second,		/Time
	Only available if: DST manual = active	Third,		/Timezone]
		Fourth,		
		Last		
Summertime h	Hour of clock change summertime	0 – 23 h	2 h	[Device Para
	Only available if: DST manual = active			/Time
	,			/Timezone]
Summertime	Minute of clock change summertime	0 – 59 min	0 min	[Device Para
min	Only available if: DST manual = active			/Time
				/Timezone]

Parameter	Description	Setting range	Default	Menu path
Wintertime m	Month of clock change wintertime	January,	October	[Device Para
	Only available if: DST manual = active	February,		/Time
	Ciny diamage in 201 mandar deare	March,		/Timezone]
		April,		
		May,		
		June,		
		July,		
		August,		
		September,		
		October,		
		November,		
		December		
Wintertime d	Day of clock change wintertime	Sunday,	Saturday	[Device Para
	Only available if: DST manual = active	Monday,		/Time
		Tuesday,		/Timezone]
		Wednesday,		
		Thursday,		
		Friday,		
		Saturday,		
		General day		
Wintertime w	Place of selected day in month (for clock	First,	Last	[Device Para
	change wintertime)	Second,		/Time
	Only available if: DST manual = active	Third,		/Timezone]
		Fourth,		
		Last		
Wintertime h	Hour of clock change wintertime	0 – 23 h	3 h	[Device Para
	Only available if: DST manual = active			/Time
	,			/Timezone]
Wintertime min	Minute of clock change wintertime	0 – 59 min	0 min	[Device Para
	Only available if: DST manual = active			/Time
				/Timezone]

Page 214 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Time Zones	Time Zones	UTC+14 Kiritimati,	UTC+0 London	[Device Para
		UTC+13 Rawaki,		/Time
		UTC+12.75 Chatham Island,		/Timezone]
		UTC+12 Wellington,		
		UTC+11.5 Kingston,		
		UTC+11 Port Vila,		
		UTC+10.5 Lord Howe Island,		
		UTC+10 Sydney,		
		UTC+9.5 Adelaide,		
		UTC+9 Tokyo,		
		UTC+8 Hong Kong,		
		UTC+7 Bangkok,		
		UTC+6.5 Rangoon,		
		UTC+6 Colombo,		
		UTC+5.75 Kathmandu,		
		UTC+5.5 New Delhi,		
		UTC+5 Islamabad,		
		UTC+4.5 Kabul,		
		UTC+4 Abu Dhabi,		
		UTC+3.5 Tehran,		
		UTC+3 Moscow,		
		UTC+2 Athens,		
		UTC+1 Berlin,		
		UTC+0 London,		
		UTC-1 Azores,		
		UTC-2 Fern. d. Noronha,		
		UTC-3 Buenos Aires,		
		UTC-3.5 St. John's,		
		UTC-4 Santiago,		
		UTC-5 New York,		
		UTC-6 Chicago,		
		UTC-7 Salt Lake City,		
		UTC-8 Los Angeles,		
		UTC-9 Anchorage,		
		UTC-9.5 Taiohae,		
		UTC-10 Honolulu,		
		UTC-11 Midway Islands		

Parameter	Description	Setting range	Default	Menu path
TimeSync	Time synchronization	manual,	manual	[Device Para
		SNTP,		/Time
		IRIG-B,		/TimeSync
		Modbus,		/TimeSync]
		IEC60870-5-103		

SNTP

SNTP

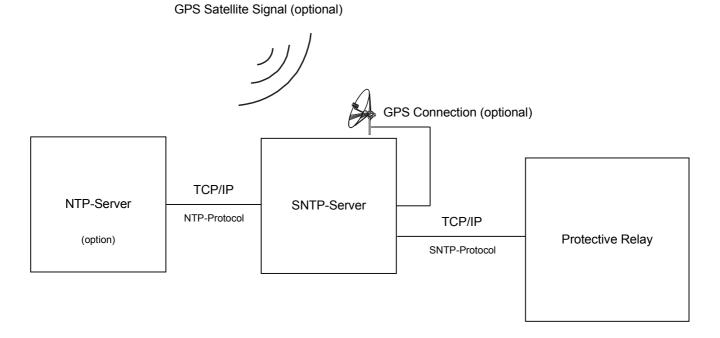


Important pre-condition: The protective relay needs to have access to a SNTP server via the connected network. This server preferably should be installed locally.

Principle - General Use

SNTP is a standard protocol for time synchronisation via a network. For this at least one SNTP server has to be available within the network. The device can be configured for one or two SNTP servers.

The protection relay's system time will be synchronised with the connected SNTP server 1-4 times per minute. In turn the SNTP server synchronises its time via NTP with other NTP servers. This is the normal case. Alternatively it can synchronise its time via GPS, radio controlled clock or the like.



Accuracy

The accuracy of the used SNTP server and the excellence of its reference clock influences the accuracy of the protection relay's clock.

For further information about accuracy refer to chapter Specifications.

Page 216 EN MRM4 12/11

With each transmitted time information, the SNTP server also sends information about its accuracy:

- Stratum: The stratum indicates over how many interacting NTP-Servers the used SNTP server is connected to an atomic or radio controlled clock.
- Precision: This indicates the accuracy of the system time provided by the SNTP server.

Additionally the performance of the connected network (traffic and data package transmission times) has an influence on the accuracy of the time synchronisation.

Recommended is a locally installed SNTP server with an accuracy of ≤200 µsec. If this cannot be realised, the connected server's excellence can be checked in the menu [Operation/Status Display/TimeSync]:

- The server quality gives information about the accuracy of the used server. The quality should be GOOD or SUFFICIENT. A server with BAD quality should not be used, because this could cause fluctuations in time synchronisation.
- The network quality gives information about the network's load and data package transmission time. The
 quality should be GOOD or SUFFICIENT. A network with BAD quality should not be used, because this
 could cause fluctuations in time synchronisation.

Using Two SNTP Servers

When configuring two SNTP servers, the device selects the server with the lower stratum value, because this generally provides a more precise time synchronisation. If the servers have the same stratum value, the device selects the server with the better precision. It does not matter, which of the servers is configured as server 1 or server 2.

When the last used server fails, the device automatically switches to the other server. Should the first server recover after some time, the device switches back to this (better) server automatically.

SNTP Commissioning

Activate the SNTP time synchronization by means of the menu [Device Para/ Time/ TimeSync]:

- Select » SNTP« in the time synchronisation menu.
- Set the IP address of the first server in the SNTP menu.
- Set the IP address of the second server, if available.
- Set all configured servers to "active".

Fault Analysis

If there is no SNTP signal for more than 120 sec, the SNTP status changes from "active" to "inactive" and an entry in the Event Recorder will be created.

The SNTP functionality can be checked in the menu [Operation/Status Display/TimeSync/Sntp]: If the SNTP status is not indicated as being "active", please proceed as follows:

- Check if the wiring is correct (Ethernet-cable connected).
- Check if a valid IP address is set in the device (Device Para/TCP/IP).
- Check if the Ethernet connection is active (Device Para/TCP/IP/Link = Up?).
- Check if both the SNTP server and the protection device answer to a Ping.
- Check if the SNTP server is up and working.

Device Planning Parameters of the SNTP

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Direct Commands of the SNTP

Parameter	Description	Setting range	Default	Menu path
Res Counter	Reset all Counters.	inactive,	inactive	[Operation
		active		/ Reset/Acknowledg e
				/Reset]

Global Protection Parameters of the SNTP

Parameter	Description	Setting range	Default	Menu path
Server1	Server 1	inactive,	inactive	[Device Para
		active		/Time
				/TimeSync
				/SNTP]
IP Byte1	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]
IP Byte2	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]
IP Byte3	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]
IP Byte4	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]

Page 218 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Server2	Server 2	inactive,	inactive	[Device Para
		active		/Time
				/TimeSync
				/SNTP]
IP Byte1	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]
IP Byte2	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]
IP Byte3	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]
IP Byte4	IP1.IP2.IP3.IP4	0 - 255	0	[Device Para
				/Time
				/TimeSync
				/SNTP]

Signals of the SNTP

Name	Description
SNTP active	Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.

SNTP Counters

Value	Description	Default	Size	Menu path
NoOfSyncs	Total Number of Synchronizations.	0	0 - 9999999999	[Operation
				/Count and RevData
				/TimeSync
				/SNTP]
NoOfConnectLost	Total Number of lost SNTP Connections (no	0	0 - 9999999999	[Operation
	sync for 120 sec).			/Count and RevData
				/TimeSync
				/SNTP]

Value	Description	Default	Size	Menu path
NoOfSmallSyncs	Service counter: Total Number of very small	0	0 - 999999999	[Operation
	Time Corrections.			/Count and RevData
				/TimeSync
				/SNTP]
NoOfNormSyncs	Service counter: Total Number of normal	0	0 - 999999999	[Operation
	Time Corrections			/Count and RevData
				/TimeSync
				/SNTP]
NoOfBigSyncs	Service counter: Total Number of big Time	0	0 - 999999999	[Operation
	Corrections			/Count and RevData
				/TimeSync
				/SNTP]
NoOfFiltSyncs	Service counter: Total Number of filtered	0	0 - 999999999	[Operation
	Time Corrections			/Count and RevData
				/TimeSync
				/SNTP]
NoOfSlowTrans	Service counter: Total Number of slow	0	0 - 999999999	[Operation
	Transfers.			/Count and RevData
				/TimeSync
				/SNTP]
NoOfHighOffs	Service counter: Total Number of high Offsets.	0	0 - 999999999	[Operation
	Olisets.			/Count and RevData
				/TimeSync
				/SNTP]
NoOfIntTimeouts	Service counter: Total Number of internal timeouts.	0	0 - 999999999	[Operation
	timeouts.			/Count and RevData
				/TimeSync
				/SNTP]
StratumServer1	Stratum of Server 1	0	0 - 999999999	[Operation
				/Status display
				/TimeSync
				/SNTP]
StratumServer2	Stratum of Server 2	0	0 - 999999999	[Operation
				/Status display
				/TimeSync
				/SNTP]

Page 220 EN MRM4 12/11

SNTP Values

Value	Description	Default	Size	Menu path
Used Server	Which Server is used for SNTP	None	Server1,	[Operation
	synchronization.		Server2,	/Status display
			None	/TimeSync
				/SNTP]
PrecServer1	Precision of Server 1	0 ms	0 – 1000.00000 ms	[Operation
				/Status display
				/TimeSync
				/SNTP]
PrecServer2	Precision of Server 2	0 ms	0 – 1000.00000 ms	[Operation
				/Status display
				/TimeSync
				/SNTP]
ServerQlty	Quality of Server used for Synchronization	-	GOOD,	[Operation
	(GOOD, SUFFICIENT, BAD)		SUFFICENT,	/Status display
			BAD,	/TimeSync
			-	/SNTP]
NetConn	Quality of Network Connection (GOOD,	-	GOOD,	[Operation
	SUFFICIENT, BAD).		SUFFICENT,	/Status display
			BAD,	/TimeSync
			-	/SNTP]

IRIG-B00X

IRIG-B



Requirement: An IRIG-B00X time code generator is needed. IRIG-B004 and higher will support/transmit the "year information".

If you are using an IRIG time code that does not support the "year information" (IRIG-B000, IRIG-B001, IRIG-B002, IRIG-B003), you have to set the "year" manually within the device. In these cases the correct year information is a precondition for a properly working IRIG-B.

Principle - General Use

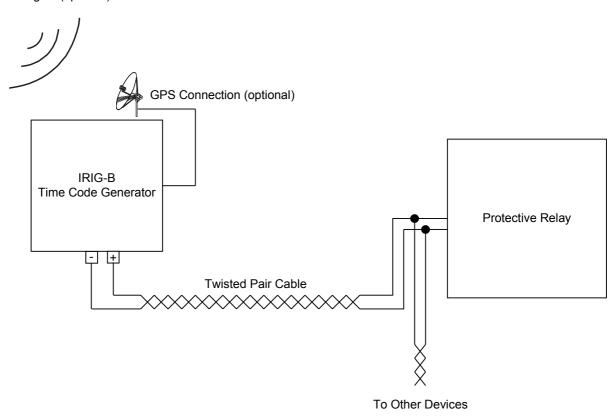
The IRIG-B standard is the most used standard to synchronize the time of protection devices in medium voltage applications.

The protection device supports IRIG-B according to the IRIG STANDARD 200-04. This means that all time synchronization formats IRIG-B00X (IRIG-B000 / B001 / B002 / B003 / B004 / B005 / B006 / B007) are supported. It is recommended to use IRIG-B004 and higher which also transmits the "year information".

The system time of the protection device is being synchronized with the connected IRIG-B code generator once

a second. The accuracy of the used IRIG-B code generator can be increased by connecting a GPS-receiver to it.

GPS Satellite Signal (optional)



The location of the IRIG-B interface depends to the device type. Please refer to the wiring diagram supplied with the protective device.

IRIG-B Commissioning

Activate the IRIG-B synchronization within menu [Device Para/ Time/ TimeSync]:

- Select »IRIG-B« in the time synchronisation menu.
- Set the time synchronization in the IRIG-B menu to »Active«.
- Select the IRIG-B type (choose B000 through B007).

Fault Analysis

If the device does not receive any IRIG-B time code for more than 60 s, the IRIG-B status switches from *»active«* to *»inactive«* and there is created an entry within the Event Recorder.

Check the IRIG-B functionality through the menu [Operation/ Status display/ TimeSync/ IRIG-B]: Should the IRIG-B status not be reported as being *>active«*, please proceed as follows:

- To begin with check the IRIG-B wiring.
- Check, if the correct IRIG-B00X type is configured.

Page 222 EN MRM4 12/11

IRIG-B Control Commands

In addition to the date and time information, the IRIG-B code offers the option to transmit up to 18 control commands that can be processed by the protective device. They have to be set and issued by the IRIG-B code generator.

The protective device offers up to 18 IRIG-B assignment options for those control commands in order to carry out the assigned action. If there is a control command assigned to an action, this action is being triggered as soon as the control command is transmitted as being true. As an example there can be triggered the start of statistics or the street lighting can be switched on through a relay.

Device Planning Parameters of the IRIG-B00X

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Direct Commands of the IRIG-B00X

Parameter	Description	Setting range	Default	Menu path
Res IRIG-B Cr	Resetting of the Diagnosis Counters: IRIG-B	inactive, active	inactive	[Operation / Reset/Acknowledg e /Reset]

Global Protection Parameters of the IRIG-B00X

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of	inactive,	inactive	[Device Para
	module/stage.	active		/Time
				/TimeSync
				/IRIG-B]
IRIG-B00X	Determination of the Type: IRIG-B00X. IRIG-B	IRIGB-000,	IRIGB-000	[Device Para
	types differ in types of included "Coded Expressions" (year, control-functions, straight-	IRIGB-001,		/Time
	binary-seconds).	IRIGB-002,		/TimeSync
		IRIGB-003,		/IRIG-B]
		IRIGB-004,		
		IRIGB-005,		
		IRIGB-006,		
		IRIGB-007		

Signals of the IRIG-B00X (Output States)

Name	Description
active	Signal: active
inverted	Signal: IRIG-B inverted
Control Signal1	Signal: IRIG-B Control Signal
Control Signal2	Signal: IRIG-B Control Signal
Control Signal4	Signal: IRIG-B Control Signal
Control Signal5	Signal: IRIG-B Control Signal
Control Signal6	Signal: IRIG-B Control Signal
Control Signal7	Signal: IRIG-B Control Signal
Control Signal8	Signal: IRIG-B Control Signal
Control Signal9	Signal: IRIG-B Control Signal
Control Signal10	Signal: IRIG-B Control Signal
Control Signal11	Signal: IRIG-B Control Signal
Control Signal12	Signal: IRIG-B Control Signal
Control Signal13	Signal: IRIG-B Control Signal
Control Signal14	Signal: IRIG-B Control Signal
Control Signal15	Signal: IRIG-B Control Signal
Control Signal16	Signal: IRIG-B Control Signal
Control Signal17	Signal: IRIG-B Control Signal
Control Signal18	Signal: IRIG-B Control Signal

IRIG-B00X Values

Value	Description	Default	Size	Menu path
NoOfFramesOK	Total Number valid Frames.	0	0 - 65535	[Operation
				/Count and RevData
				/TimeSync
				/IRIG-B]
NoOfFrameErrors Total Number of Frame.	Total Number of Frame Errors. Physically	0	0 - 65535	[Operation
	corrupted Frame.			/Count and RevData
				/TimeSync
				/IRIG-B]
Edges	Edges	0	0 - 65535	[Operation
				/Count and RevData
				/TimeSync
				/IRIG-B]

Page 224 EN MRM4 12/11

Parameters

Parameter setting and planning can be done:

- directly at the device or
- by way of the Smart View software.

Parameter Definitions

Device Parameters

Device Parameters are part of the Device Parameter Tree. By means of them you can (depending on the type of device):

- Set cutoff levels,
- Configure Digital Inputs,
- Configure Relay Output Contacts,
- Assign LEDs,
- Assign Acknowledgment Signals,
- Configure Statistics,
- Configure Protocol Parameters,
- Adapt HMI Settings,
- Configure Recorders (reports),
- Set Date and Time,
- Change Passwords,
- Check the version (build) of the device.

System Parameters

System Parameters are part of the Device Parameter Tree. System Parameters comprise the essential, basic settings of your switchboard such as rated frequency, transformer ratios.

Protection Parameters

Protection Parameters are part of the Device Parameter Tree. This tree comprises:

- Global Protection Parameters are part of the Protection Parameters: All settings and assignments that are done within the Global Parameter Tree are valid independent of the Setting Groups. They have to be set once only. In addition to that they comprise the Bkr. Management.
- The Parameter Setting Switch is part of the Protection Parameters: You can either direct switch
 onto a certain parameter setting group or you can determine the conditions for switching onto another
 parameter setting group.
- Setting Group Parameters are part of the Protection Parameters: By means of the Parameter Setting Group Parameters you can individually adapt you protective device to the current conditions or grid conditions. They can be individually set in each Setting group.

Device Planning Parameters

Device Planning Parameters are part of the Device Parameter Tree.

- Improving the Usability (clearness): All protection modules that are currently not needed can be
- de-protected (switched to invisible) by means of Device Planning. In Menu Device Planning you can
 adapt the scope of functionality of the protective device exactly to your needs. You can improve the
 usability by de-projecting all modules that are currently not needed.
- Adapting the device to your application: For those modules that you need, determine how they should work (e.g. directional, non-directional, <, >...).

Direct Commands

Direct Commands are part of the Device Parameter Tree but they are **NOT** part of the parameter file. They will be executed directly (e.g. Resetting of a Counter).

State of the Module Inputs

Module Inputs are part of the Device Parameter Tree. The State of the Module Input is context-dependent.

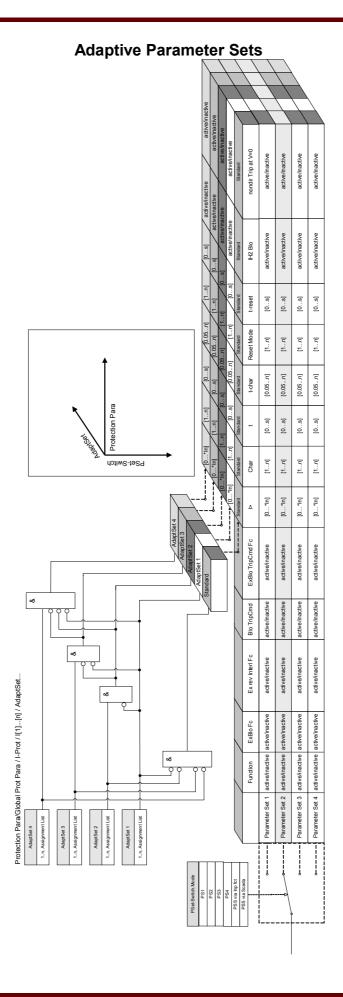
By means of the Module Inputs influence can be taken on the Modules. You can assign Signals onto **Module Inputs**. The state of the signals that are assigned to an input can be taken from the Status Display. Module Inputs can be identified by an **"-I"** at the end of the name.

Signals

Signals are part of the Device Parameter Tree. The state of the signal is context-dependent.

- Signals represent the state of your installation/equipment (e.g. Position Indicators of the Breaker).
- **Signals** are assessments of the state of the grid and the equipment (System OK, Transformer failure detected...).
- **Signals** represent decisions that are taken by the device (e.g. Trip command) based on your parameter settings.

Page 226 EN MRM4 12/11



Adaptive Parameter Sets are part of the Device Parameter Tree.

By means of *Adaptive Parameter Sets* you can temporarily modify single parameters within the parameter setting groups.

NOTICE

Adaptive Parameters fall back automatically, if the acknowledged signal, that has activated them, has fallen back. Please take into account that Adaptive Set 1 is dominant to Adaptive Set 2. Adaptive Set 2 is dominant to Adaptive Set 3. Adaptive Set 3 is dominant to Adaptive Set 4.

NOTICE

In order to increase the usability (clearness) Adaptive Parameter Sets become visible if an corresponding activation signals has been assigned (Smart View 2.0 and higher).

Example: In order to use Adaptive Parameters within Protective Element I[1] please proceed as follows:

- Assign within the Global Parameter tree within Protective Element I[1] an activation signal for AdaptiveParameterSet 1.
- AdaptiveParameterSet 1 becomes now visible within the Protection Parameter Sets for element I[1].

By means of additional activation signals further Adaptive Parameter Sets can be used.

The functionality of the IED (relay) can be enhanced / adapted by means of **Adaptive Parameters** in order to meet the requirements of modified states of the grid or the power supply system respectively to manage unpredictable events.

Moreover, the adaptive parameter can also be used to realize various special protective functions or to expand the existing function modules in a simple way without to redesign the existing hardware or software platform costly.

The *Adaptive Parameter* feature allows, besides a standard parameter set, one of the four parameter sets labeled from 1 to 4, to be used for example in a time overcurrent element under the control of the configurable Set Control Logic. The dynamical switch-over of the adaptive parameter set is only active for a particular element when its adaptive set control logic is configured and only as long as the activation signal is true.

For some protection elements such as time overcurrent and instantaneous overcurrent (50P, 51P, 50G, 51G...), besides the "default" setting there exist another 4 "alternative" settings for pickup value, curve type, time dial, reset mode set values which can dynamically be switched-over by means of the configurable adaptive setting control logic in the single set parameter.

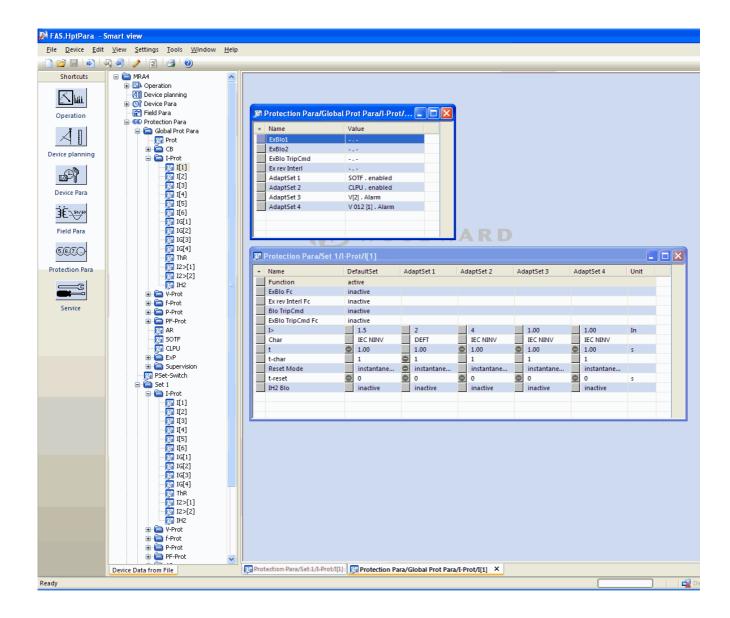
If the *Adaptive Parameter* feature is not used, the adaptive set control logic will not be selected (assigned). The protective elements work in this case just like a normal protection using the "Default" settings. If one of the *Adaptive Set* Control logic" is assigned to a logic function, the protective element will be "switched-over" to the corresponding adaptive settings if the assigned logic function is asserted and will fall back to the "Default" Setting if the assigned signal that has activated the *Adaptive Set* has fallen back.

Application Example

During a Switch-OnTo-Fault condition, it is usually requested to make the embedded protective function tripping the faulted line faster, instantaneously or sometimes non-directionally.

Page 228 EN MRM4 12/11

Such a Switch-OnTo-Fault application can easily be realized using the **Adaptive Parameter** features above mentioned: The standard time overcurrent protection element (e.g. 51P) normally works with an inverse curve type (e.g. ANSI Type A), while in case of \underline{SOTF} condition, it should trip instantaneously. If the \underline{SOTF} logic function »SOTF ENABLED« is detecting a manual breaker close condition the relay switches to **AdaptiveSet1** if the signal »SOTF.ENABLED« is assigned to **AdaptiveSet1**. The corresponding **AdaptiveSet1** will become active and that means e.g. »curve type = DEFT« and »t = 0« sec.



The screen shot above shows the adaptive setting configurations following applications based on only one simple overcurrent protection element:

- 1. Standard Set: Default settings
- 2. Adaptive Set 1: SOTF application (Switch-Onto-Fault)
- 3. Adaptive Set 2: <u>CLPU</u> application (Cold Load Pickup)
- 4. Adaptive Set 3: Voltage-Controlled time overcurrent protection (ANSI 51V)
- 5. Adaptive Set 4: Negative- Phase- Sequence- Voltage-Controlled time overcurrent protection

Application Examples

- The output signal of the <u>Switch Onto Fault</u> module can be used to activate an **Adaptive Parameter Set** that sensibilizes the overcurrent protection.
- The output signal of the <u>Cold Load Pickup</u> module can be used to activate an **Adaptive Parameter Set** that desensitizes the overcurrent protection.
- By means of *Adaptive Parameter Sets* an Adaptive <u>Auto Reclosure</u> can be realized. After a reclosure attempt the tripping thresholds or tripping curves of the overcurrent protection can be adapted.
- Depending on undervoltage the overcurrent protection can be modified (Voltage Controlled).
- The earth overcurrent protection can be modified by the residual voltage.
- Matching the ground current protective settings dynamically and automatically according to the singlephase load diversity (Adaptive relay Setting – Normal Setting/Alternative Setting)



Adaptive Parameter Sets are only available for devices with current protection modules.

Adaptive Parameter Set Activation Signals

Name	Description
	No assignment
SOTF.enabled	Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input
Logic.LE1.Gate Out	Signal: Output of the logic gate
Logic.LE1.Timer Out	Signal: Timer Output
Logic.LE1.Out	Signal: Latched Output (Q)
Logic.LE1.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE2.Gate Out	Signal: Output of the logic gate
Logic.LE2.Timer Out	Signal: Timer Output

Page 230 EN MRM4 12/11

Name	Description
Logic.LE2.Out	Signal: Latched Output (Q)
Logic.LE2.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE3.Gate Out	Signal: Output of the logic gate
Logic.LE3.Timer Out	Signal: Timer Output
Logic.LE3.Out	Signal: Latched Output (Q)
Logic.LE3.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE4.Gate Out	Signal: Output of the logic gate
Logic.LE4.Timer Out	Signal: Timer Output
Logic.LE4.Out	Signal: Latched Output (Q)
Logic.LE4.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE5.Gate Out	Signal: Output of the logic gate
Logic.LE5.Timer Out	Signal: Timer Output
Logic.LE5.Out	Signal: Latched Output (Q)
Logic.LE5.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE6.Gate Out	Signal: Output of the logic gate
Logic.LE6.Timer Out	Signal: Timer Output
Logic.LE6.Out	Signal: Latched Output (Q)
Logic.LE6.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE7.Gate Out	Signal: Output of the logic gate
Logic.LE7.Timer Out	Signal: Timer Output
Logic.LE7.Out	Signal: Latched Output (Q)
Logic.LE7.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE8.Gate Out	Signal: Output of the logic gate
Logic.LE8.Timer Out	Signal: Timer Output
Logic.LE8.Out	Signal: Latched Output (Q)
Logic.LE8.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE9.Gate Out	Signal: Output of the logic gate
Logic.LE9.Timer Out	Signal: Timer Output
Logic.LE9.Out	Signal: Latched Output (Q)
Logic.LE9.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE10.Gate Out	Signal: Output of the logic gate
Logic.LE10.Timer Out	Signal: Timer Output
Logic.LE10.Out	Signal: Latched Output (Q)
Logic.LE10.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE11.Gate Out	Signal: Output of the logic gate
Logic.LE11.Timer Out	Signal: Timer Output
Logic.LE11.Out	Signal: Latched Output (Q)
Logic.LE11.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE12.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE12.Timer Out	Signal: Timer Output
Logic.LE12.Out	Signal: Latched Output (Q)
Logic.LE12.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE13.Gate Out	Signal: Output of the logic gate
Logic.LE13.Timer Out	Signal: Timer Output
Logic.LE13.Out	Signal: Latched Output (Q)
Logic.LE13.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE14.Gate Out	Signal: Output of the logic gate
Logic.LE14.Timer Out	Signal: Timer Output
Logic.LE14.Out	Signal: Latched Output (Q)
Logic.LE14.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE15.Gate Out	Signal: Output of the logic gate
Logic.LE15.Timer Out	Signal: Timer Output
Logic.LE15.Out	Signal: Latched Output (Q)
Logic.LE15.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE16.Gate Out	Signal: Output of the logic gate
Logic.LE16.Timer Out	Signal: Timer Output
Logic.LE16.Out	Signal: Latched Output (Q)
Logic.LE16.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE17.Gate Out	Signal: Output of the logic gate
Logic.LE17.Timer Out	Signal: Timer Output
Logic.LE17.Out	Signal: Latched Output (Q)
Logic.LE17.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE18.Gate Out	Signal: Output of the logic gate
Logic.LE18.Timer Out	Signal: Timer Output
Logic.LE18.Out	Signal: Latched Output (Q)
Logic.LE18.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE19.Gate Out	Signal: Output of the logic gate
Logic.LE19.Timer Out	Signal: Timer Output
Logic.LE19.Out	Signal: Latched Output (Q)
Logic.LE19.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE20.Gate Out	Signal: Output of the logic gate
Logic.LE20.Timer Out	Signal: Timer Output
Logic.LE20.Out	Signal: Latched Output (Q)
Logic.LE20.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE21.Gate Out	Signal: Output of the logic gate
Logic.LE21.Timer Out	Signal: Timer Output
Logic.LE21.Out	Signal: Latched Output (Q)
Logic.LE21.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 232 EN MRM4 12/11

Name	Description
Logic.LE22.Gate Out	Signal: Output of the logic gate
Logic.LE22.Timer Out	Signal: Timer Output
Logic.LE22.Out	Signal: Latched Output (Q)
Logic.LE22.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE23.Gate Out	Signal: Output of the logic gate
Logic.LE23.Timer Out	Signal: Timer Output
Logic.LE23.Out	Signal: Latched Output (Q)
Logic.LE23.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE24.Gate Out	Signal: Output of the logic gate
Logic.LE24.Timer Out	Signal: Timer Output
Logic.LE24.Out	Signal: Latched Output (Q)
Logic.LE24.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE25.Gate Out	Signal: Output of the logic gate
Logic.LE25.Timer Out	Signal: Timer Output
Logic.LE25.Out	Signal: Latched Output (Q)
Logic.LE25.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE26.Gate Out	Signal: Output of the logic gate
Logic.LE26.Timer Out	Signal: Timer Output
Logic.LE26.Out	Signal: Latched Output (Q)
Logic.LE26.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE27.Gate Out	Signal: Output of the logic gate
Logic.LE27.Timer Out	Signal: Timer Output
Logic.LE27.Out	Signal: Latched Output (Q)
Logic.LE27.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE28.Gate Out	Signal: Output of the logic gate
Logic.LE28.Timer Out	Signal: Timer Output
Logic.LE28.Out	Signal: Latched Output (Q)
Logic.LE28.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE29.Gate Out	Signal: Output of the logic gate
Logic.LE29.Timer Out	Signal: Timer Output
Logic.LE29.Out	Signal: Latched Output (Q)
Logic.LE29.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE30.Gate Out	Signal: Output of the logic gate
Logic.LE30.Timer Out	Signal: Timer Output
Logic.LE30.Out	Signal: Latched Output (Q)
Logic.LE30.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE31.Gate Out	Signal: Output of the logic gate
Logic.LE31.Timer Out	Signal: Timer Output
Logic.LE31.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE31.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE32.Gate Out	Signal: Output of the logic gate
Logic.LE32.Timer Out	Signal: Timer Output
Logic.LE32.Out	Signal: Latched Output (Q)
Logic.LE32.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE33.Gate Out	Signal: Output of the logic gate
Logic.LE33.Timer Out	Signal: Timer Output
Logic.LE33.Out	Signal: Latched Output (Q)
Logic.LE33.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE34.Gate Out	Signal: Output of the logic gate
Logic.LE34.Timer Out	Signal: Timer Output
Logic.LE34.Out	Signal: Latched Output (Q)
Logic.LE34.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE35.Gate Out	Signal: Output of the logic gate
Logic.LE35.Timer Out	Signal: Timer Output
Logic.LE35.Out	Signal: Latched Output (Q)
Logic.LE35.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE36.Gate Out	Signal: Output of the logic gate
Logic.LE36.Timer Out	Signal: Timer Output
Logic.LE36.Out	Signal: Latched Output (Q)
Logic.LE36.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE37.Gate Out	Signal: Output of the logic gate
Logic.LE37.Timer Out	Signal: Timer Output
Logic.LE37.Out	Signal: Latched Output (Q)
Logic.LE37.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE38.Gate Out	Signal: Output of the logic gate
Logic.LE38.Timer Out	Signal: Timer Output
Logic.LE38.Out	Signal: Latched Output (Q)
Logic.LE38.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE39.Gate Out	Signal: Output of the logic gate
Logic.LE39.Timer Out	Signal: Timer Output
Logic.LE39.Out	Signal: Latched Output (Q)
Logic.LE39.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE40.Gate Out	Signal: Output of the logic gate
Logic.LE40.Timer Out	Signal: Timer Output
Logic.LE40.Out	Signal: Latched Output (Q)
Logic.LE40.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE41.Gate Out	Signal: Output of the logic gate
Logic.LE41.Timer Out	Signal: Timer Output

Page 234 EN MRM4 12/11

Name	Description
Logic.LE41.Out	Signal: Latched Output (Q)
Logic.LE41.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE42.Gate Out	Signal: Output of the logic gate
Logic.LE42.Timer Out	Signal: Timer Output
Logic.LE42.Out	Signal: Latched Output (Q)
Logic.LE42.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE43.Gate Out	Signal: Output of the logic gate
Logic.LE43.Timer Out	Signal: Timer Output
Logic.LE43.Out	Signal: Latched Output (Q)
Logic.LE43.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE44.Gate Out	Signal: Output of the logic gate
Logic.LE44.Timer Out	Signal: Timer Output
Logic.LE44.Out	Signal: Latched Output (Q)
Logic.LE44.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE45.Gate Out	Signal: Output of the logic gate
Logic.LE45.Timer Out	Signal: Timer Output
Logic.LE45.Out	Signal: Latched Output (Q)
Logic.LE45.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE46.Gate Out	Signal: Output of the logic gate
Logic.LE46.Timer Out	Signal: Timer Output
Logic.LE46.Out	Signal: Latched Output (Q)
Logic.LE46.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE47.Gate Out	Signal: Output of the logic gate
Logic.LE47.Timer Out	Signal: Timer Output
Logic.LE47.Out	Signal: Latched Output (Q)
Logic.LE47.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE48.Gate Out	Signal: Output of the logic gate
Logic.LE48.Timer Out	Signal: Timer Output
Logic.LE48.Out	Signal: Latched Output (Q)
Logic.LE48.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE49.Gate Out	Signal: Output of the logic gate
Logic.LE49.Timer Out	Signal: Timer Output
Logic.LE49.Out	Signal: Latched Output (Q)
Logic.LE49.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE50.Gate Out	Signal: Output of the logic gate
Logic.LE50.Timer Out	Signal: Timer Output
Logic.LE50.Out	Signal: Latched Output (Q)
Logic.LE50.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE51.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE51.Timer Out	Signal: Timer Output
Logic.LE51.Out	Signal: Latched Output (Q)
Logic.LE51.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE52.Gate Out	Signal: Output of the logic gate
Logic.LE52.Timer Out	Signal: Timer Output
Logic.LE52.Out	Signal: Latched Output (Q)
Logic.LE52.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE53.Gate Out	Signal: Output of the logic gate
Logic.LE53.Timer Out	Signal: Timer Output
Logic.LE53.Out	Signal: Latched Output (Q)
Logic.LE53.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE54.Gate Out	Signal: Output of the logic gate
Logic.LE54.Timer Out	Signal: Timer Output
Logic.LE54.Out	Signal: Latched Output (Q)
Logic.LE54.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE55.Gate Out	Signal: Output of the logic gate
Logic.LE55.Timer Out	Signal: Timer Output
Logic.LE55.Out	Signal: Latched Output (Q)
Logic.LE55.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE56.Gate Out	Signal: Output of the logic gate
Logic.LE56.Timer Out	Signal: Timer Output
Logic.LE56.Out	Signal: Latched Output (Q)
Logic.LE56.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE57.Gate Out	Signal: Output of the logic gate
Logic.LE57.Timer Out	Signal: Timer Output
Logic.LE57.Out	Signal: Latched Output (Q)
Logic.LE57.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE58.Gate Out	Signal: Output of the logic gate
Logic.LE58.Timer Out	Signal: Timer Output
Logic.LE58.Out	Signal: Latched Output (Q)
Logic.LE58.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE59.Gate Out	Signal: Output of the logic gate
Logic.LE59.Timer Out	Signal: Timer Output
Logic.LE59.Out	Signal: Latched Output (Q)
Logic.LE59.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE60.Gate Out	Signal: Output of the logic gate
Logic.LE60.Timer Out	Signal: Timer Output
Logic.LE60.Out	Signal: Latched Output (Q)
Logic.LE60.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 236 EN MRM4 12/11

Name	Description
Logic.LE61.Gate Out	Signal: Output of the logic gate
Logic.LE61.Timer Out	Signal: Timer Output
Logic.LE61.Out	Signal: Latched Output (Q)
Logic.LE61.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE62.Gate Out	Signal: Output of the logic gate
Logic.LE62.Timer Out	Signal: Timer Output
Logic.LE62.Out	Signal: Latched Output (Q)
Logic.LE62.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE63.Gate Out	Signal: Output of the logic gate
Logic.LE63.Timer Out	Signal: Timer Output
Logic.LE63.Out	Signal: Latched Output (Q)
Logic.LE63.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE64.Gate Out	Signal: Output of the logic gate
Logic.LE64.Timer Out	Signal: Timer Output
Logic.LE64.Out	Signal: Latched Output (Q)
Logic.LE64.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE65.Gate Out	Signal: Output of the logic gate
Logic.LE65.Timer Out	Signal: Timer Output
Logic.LE65.Out	Signal: Latched Output (Q)
Logic.LE65.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE66.Gate Out	Signal: Output of the logic gate
Logic.LE66.Timer Out	Signal: Timer Output
Logic.LE66.Out	Signal: Latched Output (Q)
Logic.LE66.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE67.Gate Out	Signal: Output of the logic gate
Logic.LE67.Timer Out	Signal: Timer Output
Logic.LE67.Out	Signal: Latched Output (Q)
Logic.LE67.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE68.Gate Out	Signal: Output of the logic gate
Logic.LE68.Timer Out	Signal: Timer Output
Logic.LE68.Out	Signal: Latched Output (Q)
Logic.LE68.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE69.Gate Out	Signal: Output of the logic gate
Logic.LE69.Timer Out	Signal: Timer Output
Logic.LE69.Out	Signal: Latched Output (Q)
Logic.LE69.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE70.Gate Out	Signal: Output of the logic gate
Logic.LE70.Timer Out	Signal: Timer Output
	Signal. Timer Output

Name	Description
Logic.LE70.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE71.Gate Out	Signal: Output of the logic gate
Logic.LE71.Timer Out	Signal: Timer Output
Logic.LE71.Out	Signal: Latched Output (Q)
Logic.LE71.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE72.Gate Out	Signal: Output of the logic gate
Logic.LE72.Timer Out	Signal: Timer Output
Logic.LE72.Out	Signal: Latched Output (Q)
Logic.LE72.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE73.Gate Out	Signal: Output of the logic gate
Logic.LE73.Timer Out	Signal: Timer Output
Logic.LE73.Out	Signal: Latched Output (Q)
Logic.LE73.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE74.Gate Out	Signal: Output of the logic gate
Logic.LE74.Timer Out	Signal: Timer Output
Logic.LE74.Out	Signal: Latched Output (Q)
Logic.LE74.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE75.Gate Out	Signal: Output of the logic gate
Logic.LE75.Timer Out	Signal: Timer Output
Logic.LE75.Out	Signal: Latched Output (Q)
Logic.LE75.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE76.Gate Out	Signal: Output of the logic gate
Logic.LE76.Timer Out	Signal: Timer Output
Logic.LE76.Out	Signal: Latched Output (Q)
Logic.LE76.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE77.Gate Out	Signal: Output of the logic gate
Logic.LE77.Timer Out	Signal: Timer Output
Logic.LE77.Out	Signal: Latched Output (Q)
Logic.LE77.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE78.Gate Out	Signal: Output of the logic gate
Logic.LE78.Timer Out	Signal: Timer Output
Logic.LE78.Out	Signal: Latched Output (Q)
Logic.LE78.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE79.Gate Out	Signal: Output of the logic gate
Logic.LE79.Timer Out	Signal: Timer Output
Logic.LE79.Out	Signal: Latched Output (Q)
Logic.LE79.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE80.Gate Out	Signal: Output of the logic gate
Logic.LE80.Timer Out	Signal: Timer Output

Page 238 EN MRM4 12/11

Name	Description
Logic.LE80.Out	Signal: Latched Output (Q)
Logic.LE80.Out inverted	Signal: Negated Latched Output (Q NOT)
Sys.AFRMS active	Signal: Arcflash Reduction Maintenance active
Sys.AFRMS inactive	Signal: Arcflash Reduction Maintenance inactive

Operational Modes (Access Authorization)

Operational Mode - »Display Only«

- The protection is activated.
- All data, measuring values, records and counters/meters can be viewed.

Operation Mode - »Parameter Setting and Planning«

In this mode you are able to:

- edit and set parameters.
- change device planning details and
- parametrize and reset operational data (event recorder/fault recorder/power meter/switching cycles).



If the device was not active within the parameter setting mode for a longer time (can be set between 20 – 3600 seconds) it changes automatically into »Display Only« mode. (Please refer to the appendix *Module Panel*).

NOTICE

As long as you are within the parameter setting mode you cannot acknowledge.

In order to change into operation mode »Parameter Setting« please proceed as follows:

- 1. Mark in the device display the parameter you want to change.
- 2.Press the softkey »Wrench« to change temporarily into the parameter setting mode.
- 3.Enter the parameter password.
- 4. Change the parameter.
- 5. Change perhaps additional parameters.

NOTICE

As long as you are within the parameter setting mode a wrench icon will be shown in the upper right corner of the display.



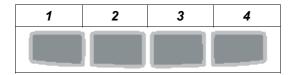
- 6. For saving the altered parameter:
 - press the »OK« key,
 - confirm by pressing the softkey »Yes«.

7. Then the device changes into mode »Display Only«.

Password

Password Entry at the Panel

Passwords can be entered by way of the softkeys.



Example: For password (3244) press successively:

- Softkey 3
- Softkey 2
- Softkey 4
- Softkey 4

Password Changes

Passwords can be changed at the device in menu »Device Para/Password« or by means of the *Smart View* software.



A password must be a User-defined combination of the numerics 1, 2, 3 and 4. All other characters and keys won't be accepted.

The password of operation mode »Parameter setting and planning« enables you to transfer parameters from the *Smart View* software into the device.

When you want to change a password, the existing one has to be entered firstly. The new password (up to 8 digits) is then to be confirmed twice. Please proceed as follows:

- In order to change the password please enter your old password followed by pressing the »OK«-key.
- Afterwards enter the new password and press the »OK«-key.
- Finally confirm your new password and press the »OK-key«.

Password Forgotten

By pressing the »C« key during cold booting a reset menu will be called up. By selecting »Reset All Passwords?« and confirming with »Yes« all passwords will be reset to the defaults »1234«.

Page 240 EN MRM4 12/11

Changing of Parameters - Example

- Move to the parameter you want to change by using the softkeys.
- Press the softkey »Wrench«.
- Enter the password for parameter setting.
- Edit/change the parameter.

Now you can:

- save the change you made and have them adopted by the system or:
- change additional parameters and save finally all the altered parameters and have them adopted by the system.

To save parameter changes immediately,

• press the »OK« key for saving changed parameters directly and to have them adopted by the device. Confirm the parameter changes by pressing the »Yes« softkey or dismiss by pressing »No«.

To change additional parameters and save afterwards,

move to other parameters and change them

NOTICE

A star symbol in front of the changed parameters indicates that the modifications have only temporarily been saved, they are not yet finally stored and adopted by the device.

In order to make things easier to follow, especially where complex parameter changes are involved, on every superior/higher-ranking menu level the intended change of the parameter is indicated by the star symbol (star trace). This makes it possible to control or follow from the main menu level at any time where parameter changes have been made and have not finally been saved.

In addition to the star trace to the temporarily saved parameter changes, a general parameter changing symbol is faded-in at the left corner of the display, and so it is possible from each point of the menu tree to see that there are parameter changes still not adopted by the device.

Press the »OK« key to initiate the final storage of all parameter changes. Confirm the parameter changes by pressing the »Yes« softkey or dismiss by pressing Softkey »No«.

NOTICE

Plausibility check: In order to prevent obvious wrong settings the device monitors constantly all temporarily saved parameter changes. If the device detects an implausibility, this is indicated by a question mark in front of the respective parameter.

In order to make things easier to follow, especially where complex parameter changes are involved, on every superior/higher-ranking menu level, above the temporarily saved parameters an invalidity is indicated by the question mark (plausibility trace). This makes it possible to control or follow from the main menu level at any time where implausibilities are intended to be saved.

In addition to the question mark trace to the temporarily saved implausible parameter changes a general implausibility symbol/question mark is faded-in at the left corner of the display, and so it is possible to see from each point of the menu tree that implausibilities have been detected by the device.

A star/parameter change indication is always overwritten by the question mark/implausibility symbol.

If a device detects an implausibility, it rejects saving and adopting of the parameters.

Changing of Parameters When Using the Smart View - Example

Example: Changing of a protective parameter (to alter the characteristic for the overcurrent protection function I[1] in parameter set 1).

- In case Smart View is not in operation start this software.
- In case the device data has not been loaded select »Data To Be Received From The Device« in menu »Device«.
- Double-click the »Protection Para Icon« in the navigation tree.
- Double-click the »Protection Para Set Icon« in the navigation tree.
- Double-click the »Set 1 Icon« in the navigation tree.
- Double-click the »protection stage I[1]« in the navigation tree.
- In the working window a tabulated overview appears, showing the parameters assigned to this protective function.
- In this table double-click the value/parameter you want to change (here: »Char«).
- Another window (pop-up) is opened where you can select the required characteristic.
- Close this window by clicking the »OK« key.

Page 242 EN MRM4 12/11

NOTICE

A star symbol in front of the changed parameters indicates that the alterations have only temporarily been saved. They are not yet finally stored and adopted by the software/device.

In order to make things easier to follow, especially where complex parameter changes are involved, on every superior/higher menu level, the intended change of the parameter is indicated by the star symbol (star trace). This makes it possible to control or follow from the main menu level at any time where parameter changes have been made and have not finally been saved.

NOTICE

Plausibility check: In order to prevent obvious wrong settings the software monitors constantly all temporarily saved parameter changes. If it detects an implausibility, this is indicated by a question mark in front of the respective parameter.

In order to make things easier to follow, especially where complex parameter changes are involved, on every superior/higher menu level above of the temporarily saved parameters, an implausibility is indicated by a question mark (plausibility trace). This makes it possible to control or follow from the main menu level at any time where implausibilities exist.

So it is possible to see from each point of the menu tree that implausibilities have been detected by the software.

A star/parameter change indication is always overwritten by the question mark/implausibility symbol.

If the software detects an implausibility it rejects saving and adopting of the parameters.

- · Additional parameters can be changed if required.
- In order to transfer changed parameters into the device, please select »Transfer all parameters into the device« in menu »Device«.
- Confirm the safety inquiry »Shall The Parameters Be Overwritten?«.
- Enter the password for setting parameters in the pop-up window.
- Confirm the inquiry »Shall The Data Be Saved Locally?« with »Yes« (recommended). Select a suitable storing location on your hard disk.
- Confirm the chosen storing location by clicking »Save«.
- The changed parameter data is now saved in the data file chosen by you. Thereafter the changed data is transferred to the device and adopted.

NOTICE

Once you have entered the parameter setting password, Smart View wont ask you again for the password for at least 10 minutes. This time interval will start again, each time parameters are transmitted into the device. If for more than 10 minutes no parameters are transmitted into the device, Smart View will ask you again for the password, when you are trying to transmit parameters into the device.

Protection Parameters



It has to be taken into account that by deactivating, for instance, protective functions, you also change the functionality of the device.

The manufacturer does not accept liability for any personal or material damage as a result of wrong planning.

A planning/parameter setting service is also offered by *Woodward Kempen GmbH*.

The protection parameters include the following protection parameter trees:

- Global Protection Parameters: »Global Prot Para«: Here you can find all protection parameters that are universally valid, that means that they are valid independent of the protection parameter sets.
- Setting Group Parameters: »Set1..4«: The protection parameters that you set within a parameter set are only valid, if the parameter set where you set them is switched to active.

Setting Groups

Setting Group Switch

Within the menu »Protection Para/P-Set Switch« you have the following possibilities:

- To set one of the four setting groups active manually.
- To assign a signal to each setting group that sets this group to active.
- Scada switches the setting groups.

Option	Setting Group Switch
Manual Selection	Switch over, if another setting group is chosen manually within the menu »Protection Para/P-Set Switch«
Via Input Function	Switch over not until the request is clear.
(e.g. Digital Input)	That means, if there is more or less than one request signal active, no switch over will be executed.
	Example::
	DI3 is assigned onto Parameter set 1. DI3 is active "1".
	DI4 is assigned onto Parameter set 2. DI4 is inactive "0".
	Now the device should switch from parameter set 1 to parameter set 2. Therefore at first DI3 has to become inactive "0". Than DI4 has to be active "1".
	If DI4 becomes again inactive "0", parameter set 2 will remain active "1" as long as there is no clear request (e.g. DI3 becomes active "1", all the other assignments are inactive "0")
Via Scada	Switch over if there is a clear SCADA request.
	Otherwise no switch over will be executed.

Page 244 EN MRM4 12/11



The description of the parameters can be found within chapter System Parameters.

Signals That Can Be Used for PSS

Name	Description
- - -	No assignment
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input
Logic.LE1.Gate Out	Signal: Output of the logic gate
Logic.LE1.Timer Out	Signal: Timer Output
Logic.LE1.Out	Signal: Latched Output (Q)
Logic.LE1.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE2.Gate Out	Signal: Output of the logic gate
Logic.LE2.Timer Out	Signal: Timer Output
Logic.LE2.Out	Signal: Latched Output (Q)
Logic.LE2.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE3.Gate Out	Signal: Output of the logic gate
Logic.LE3.Timer Out	Signal: Timer Output
Logic.LE3.Out	Signal: Latched Output (Q)
Logic.LE3.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE4.Gate Out	Signal: Output of the logic gate
Logic.LE4.Timer Out	Signal: Timer Output
Logic.LE4.Out	Signal: Latched Output (Q)
Logic.LE4.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE5.Gate Out	Signal: Output of the logic gate
Logic.LE5.Timer Out	Signal: Timer Output
Logic.LE5.Out	Signal: Latched Output (Q)
Logic.LE5.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE6.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE6.Timer Out	Signal: Timer Output
Logic.LE6.Out	Signal: Latched Output (Q)
Logic.LE6.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE7.Gate Out	Signal: Output of the logic gate
Logic.LE7.Timer Out	Signal: Timer Output
Logic.LE7.Out	Signal: Latched Output (Q)
Logic.LE7.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE8.Gate Out	Signal: Output of the logic gate
Logic.LE8.Timer Out	Signal: Timer Output
Logic.LE8.Out	Signal: Latched Output (Q)
Logic.LE8.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE9.Gate Out	Signal: Output of the logic gate
Logic.LE9.Timer Out	Signal: Timer Output
Logic.LE9.Out	Signal: Latched Output (Q)
Logic.LE9.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE10.Gate Out	Signal: Output of the logic gate
Logic.LE10.Timer Out	Signal: Timer Output
Logic.LE10.Out	Signal: Latched Output (Q)
Logic.LE10.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE11.Gate Out	Signal: Output of the logic gate
Logic.LE11.Timer Out	Signal: Timer Output
Logic.LE11.Out	Signal: Latched Output (Q)
Logic.LE11.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE12.Gate Out	Signal: Output of the logic gate
Logic.LE12.Timer Out	Signal: Timer Output
Logic.LE12.Out	Signal: Latched Output (Q)
Logic.LE12.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE13.Gate Out	Signal: Output of the logic gate
Logic.LE13.Timer Out	Signal: Timer Output
Logic.LE13.Out	Signal: Latched Output (Q)
Logic.LE13.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE14.Gate Out	Signal: Output of the logic gate
Logic.LE14.Timer Out	Signal: Timer Output
Logic.LE14.Out	Signal: Latched Output (Q)
Logic.LE14.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE15.Gate Out	Signal: Output of the logic gate
Logic.LE15.Timer Out	Signal: Timer Output
Logic.LE15.Out	Signal: Latched Output (Q)
Logic.LE15.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 246 EN MRM4 12/11

Name	Description
Logic.LE16.Gate Out	Signal: Output of the logic gate
Logic.LE16.Timer Out	Signal: Timer Output
Logic.LE16.Out	Signal: Latched Output (Q)
Logic.LE16.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE17.Gate Out	Signal: Output of the logic gate
Logic.LE17.Timer Out	Signal: Timer Output
Logic.LE17.Out	Signal: Latched Output (Q)
Logic.LE17.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE18.Gate Out	Signal: Output of the logic gate
Logic.LE18.Timer Out	Signal: Timer Output
Logic.LE18.Out	Signal: Latched Output (Q)
Logic.LE18.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE19.Gate Out	Signal: Output of the logic gate
Logic.LE19.Timer Out	Signal: Timer Output
Logic.LE19.Out	Signal: Latched Output (Q)
Logic.LE19.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE20.Gate Out	Signal: Output of the logic gate
Logic.LE20.Timer Out	Signal: Timer Output
Logic.LE20.Out	Signal: Latched Output (Q)
Logic.LE20.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE21.Gate Out	Signal: Output of the logic gate
Logic.LE21.Timer Out	Signal: Timer Output
Logic.LE21.Out	Signal: Latched Output (Q)
Logic.LE21.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE22.Gate Out	Signal: Output of the logic gate
Logic.LE22.Timer Out	Signal: Timer Output
Logic.LE22.Out	Signal: Latched Output (Q)
Logic.LE22.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE23.Gate Out	Signal: Output of the logic gate
Logic.LE23.Timer Out	Signal: Timer Output
Logic.LE23.Out	Signal: Latched Output (Q)
Logic.LE23.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE24.Gate Out	Signal: Output of the logic gate
Logic.LE24.Timer Out	Signal: Timer Output
Logic.LE24.Out	Signal: Latched Output (Q)
Logic.LE24.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE25.Gate Out	Signal: Output of the logic gate
Logic.LE25.Timer Out	Signal: Timer Output
Logic.LE25.Out	Signal: Latched Output (Q)

Name E	Description
Logic.LE25.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE26.Gate Out	Signal: Output of the logic gate
Logic.LE26.Timer Out S	Signal: Timer Output
Logic.LE26.Out S	Signal: Latched Output (Q)
Logic.LE26.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE27.Gate Out S	Signal: Output of the logic gate
Logic.LE27.Timer Out S	Signal: Timer Output
Logic.LE27.Out S	Signal: Latched Output (Q)
Logic.LE27.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE28.Gate Out	Signal: Output of the logic gate
Logic.LE28.Timer Out S	Signal: Timer Output
Logic.LE28.Out S	Signal: Latched Output (Q)
Logic.LE28.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE29.Gate Out S	Signal: Output of the logic gate
Logic.LE29.Timer Out S	Signal: Timer Output
Logic.LE29.Out S	Signal: Latched Output (Q)
Logic.LE29.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE30.Gate Out S	Signal: Output of the logic gate
Logic.LE30.Timer Out S	Signal: Timer Output
Logic.LE30.Out S	Signal: Latched Output (Q)
Logic.LE30.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE31.Gate Out S	Signal: Output of the logic gate
Logic.LE31.Timer Out S	Signal: Timer Output
Logic.LE31.Out S	Signal: Latched Output (Q)
Logic.LE31.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE32.Gate Out	Signal: Output of the logic gate
Logic.LE32.Timer Out S	Signal: Timer Output
Logic.LE32.Out S	Signal: Latched Output (Q)
Logic.LE32.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE33.Gate Out	Signal: Output of the logic gate
Logic.LE33.Timer Out S	Signal: Timer Output
Logic.LE33.Out S	Signal: Latched Output (Q)
Logic.LE33.Out inverted S	Signal: Negated Latched Output (Q NOT)
Logic.LE34.Gate Out S	Signal: Output of the logic gate
Logic.LE34.Timer Out S	Signal: Timer Output
Logic.LE34.Out S	Signal: Latched Output (Q)
Logic.LE34.Out inverted S	2' IN (II (I I O (I (O NOT)
	Signal: Negated Latched Output (Q NOT)
Logic.LE35.Gate Out	Signal: Negated Latched Output (Q NOT) Signal: Output of the logic gate

Page 248 EN MRM4 12/11

Name	Description
Logic.LE35.Out	Signal: Latched Output (Q)
Logic.LE35.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE36.Gate Out	Signal: Output of the logic gate
Logic.LE36.Timer Out	Signal: Timer Output
Logic.LE36.Out	Signal: Latched Output (Q)
Logic.LE36.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE37.Gate Out	Signal: Output of the logic gate
Logic.LE37.Timer Out	Signal: Timer Output
Logic.LE37.Out	Signal: Latched Output (Q)
Logic.LE37.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE38.Gate Out	Signal: Output of the logic gate
Logic.LE38.Timer Out	Signal: Timer Output
Logic.LE38.Out	Signal: Latched Output (Q)
Logic.LE38.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE39.Gate Out	Signal: Output of the logic gate
Logic.LE39.Timer Out	Signal: Timer Output
Logic.LE39.Out	Signal: Latched Output (Q)
Logic.LE39.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE40.Gate Out	Signal: Output of the logic gate
Logic.LE40.Timer Out	Signal: Timer Output
Logic.LE40.Out	Signal: Latched Output (Q)
Logic.LE40.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE41.Gate Out	Signal: Output of the logic gate
Logic.LE41.Timer Out	Signal: Timer Output
Logic.LE41.Out	Signal: Latched Output (Q)
Logic.LE41.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE42.Gate Out	Signal: Output of the logic gate
Logic.LE42.Timer Out	Signal: Timer Output
Logic.LE42.Out	Signal: Latched Output (Q)
Logic.LE42.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE43.Gate Out	Signal: Output of the logic gate
Logic.LE43.Timer Out	Signal: Timer Output
Logic.LE43.Out	Signal: Latched Output (Q)
Logic.LE43.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE44.Gate Out	Signal: Output of the logic gate
Logic.LE44.Timer Out	Signal: Timer Output
Logic.LE44.Out	Signal: Latched Output (Q)
Logic.LE44.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE45.Gate Out	Signal: Output of the logic gate

Logic LE45 Timer Out Logic LE45 Out Signal: Timer Output (Q) Logic LE45 Out inverted Logic LE46 Sout inverted Logic LE46 Sout of the logic gate Logic LE46 Timer Out Logic LE46 Charmer Out Logic LE47 Get Out Logic LE47 Charmer Out Logic LE47 Out Logic LE48 Get Out Logic LE48 Charmer Out Logic LE48 Out Logic LE49 Out Logic LE49 Out Logic LE49 Charmer Out Logic LE49 Out Logic LE50 Charmer Out Logic LE50 Charmer Out Logic LE50 Charmer Out Logic LE50 Out Inverted Logic LE50 Out Inverted Logic LE50 Out Logic LE51 Out Inverted Logic LE51 Out Logic LE52 Charmer Out Logic LE52 Out Signal: Negated Latched Output (O NOT) Logic LE52 Charmer Out Logic LE52 Charmer Out Logic LE53 Charmer Out Logic LE54 Charmer Out Logic LE55 Charmer Out Logic LE55 Charmer Out Logic LE55 Charmer Out Logic LE55 Charmer Out Logic LE54 Gate Out Logic LE55 Charmer Out Logic LE54 Gate Out Logic LE55 Charmer Out Logic LE54 Charmer Out Logic LE54 Charmer	Name	Description
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Page 250 EN MRM4 12/11

Logic LE55 Gate Out Logic LE55 Gate Out Logic LE55 Out Winverted Logic LE55 Gate Out Logic LE55 Gate Out Logic LE56 Gate Out Logic LE56 Gate Out Logic LE56 Timer Out Logic LE56 Out Inverted Logic LE56 Out Logic LE56 Out Inverted Logic LE56 Out Logic LE56 Out Inverted Logic LE56 Out Logic LE57 Gate Out Logic LE57 Gate Out Logic LE57 Gate Out Logic LE57 Gate Out Logic LE57 Cate Out Logic LE57 Cate Out Logic LE57 Cate Out Logic LE57 Out inverted Logic LE57 Out inverted Logic LE57 Out inverted Logic LE58 Gate Out Logic LE57 Out inverted Logic LE58 Gate Out Logic LE57 Sout inverted Logic LE58 Gate Out Logic LE58 Out inverted Logic LE58 Out inverted Logic LE58 Out inverted Logic LE59 Gate Out Logic LE59 Out Inverted Logic LE59 Out Signal: Negated Latched Output (Q NOT) Logic LE59 Gate Out Logic LE59 Out Inverted Logic LE59 Out Logic Latched Output (Q NOT) Logic LE59 Out Logic LE59 Out Signal: Latched Output (Q NOT) Logic LE59 Out Logic LE50 Out Inverted Logic LE50 Out	Name	Description
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Logic.LE64.Timer Out Signal: Timer Output	Logic.LE63.Out inverted	Signal: Negated Latched Output (Q NOT)
	Logic.LE64.Gate Out	Signal: Output of the logic gate
Logic.LE64.Out Signal: Latched Output (Q)	Logic.LE64.Timer Out	Signal: Timer Output
	Logic.LE64.Out	Signal: Latched Output (Q)

Logic LE63 Cate Out Logic LE65 Timer Out Logic LE65 Timer Out Logic LE65 Timer Out Logic LE65 Timer Out Logic LE65 Cate Out Logic LE66 Cate Out Logic LE67 Cate Out Logic LE67 Cate Out Logic LE67 Cate Out Logic LE67 Cate Out Logic LE68 Cate Out Logic LE69 Cate Out Lo	Name	Description
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Logic LE65 Out inverted Signal: Negated Latched Output (Q NOT) Logic LE66. Timer Out Logic LE66 Timer Out Logic LE66 Out Signal: Timer Output (Q NOT) Logic LE66 Out Logic LE66 Out Signal: Timer Output (Q NOT) Logic LE66 Out Logic LE66 Out Inverted Signal: Timer Output (Q) Logic LE67. Gate Out Signal: Timer Output (Q NOT) Logic LE67. Gate Out Signal: Output of the logic gate Logic LE67. Timer Out Logic LE67. Out Inverted Signal: Negated Latched Output (Q NOT) Logic LE67. Out Inverted Signal: Negated Latched Output (Q NOT) Logic LE68. Out Inverted Signal: Negated Latched Output (Q NOT) Logic LE68. Out Output of the logic gate Logic LE68. Out Signal: Negated Latched Output (Q NOT) Logic LE68. Out Signal: Timer Output Logic LE68. Out Signal: Latched Output (Q NOT) Logic LE68. Out Inverted Signal: Latched Output (Q NOT) Logic LE69. Gate Out Signal: Logic Lefe8. Output of the logic gate Logic LE69. Out inverted Signal: Logic Untre Output Logic LE69. Out Signal: Logic Untre Output Logic LE69. Out Signal: Logic Output of the logic gate Logic LE69. Out Signal: Logic Output of the logic gate Logic LE69. Out Inverted Signal: Negated Latched Output (Q NOT) Logic LE69. Out Inverted Signal: Output of the logic gate Logic LE70. Out Signal: Latched Output (Q NOT) Logic LE70. Out Signal: Latched Output (Q NOT) Logic LE70. Out Signal: Latched Output (Q NOT) Logic LE71. Out inverted Signal: Negated Latched Output (Q NOT) Logic LE71. Out Signal: Latched Output (Q NOT) Logic LE71. Out inverted Signal: Negated Latched Output (Q NOT) Logic LE71. Out inverted Signal: Latched Output (Q NOT) Logic LE72. Out inverted Signal: Negated Latched Output (Q NOT) Logic LE73. Out Signal: Latched Output (Q NOT) Logic LE73. Out Not Signal: Latched Output (Q NOT) Logic LE73. Out Signal: Latched Output (Q NOT) Logic LE73. Out Not Signal: Latched Output (Q NOT) Logic LE73.	Logic.LE65.Gate Out	Signal: Output of the logic gate
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Logic.LE73.Timer Out Logic.LE73.Out Signal: Timer Output Signal: Latched Output (Q) Logic.LE73.Out inverted Signal: Negated Latched Output (Q NOT) Logic.LE74.Gate Out Signal: Output of the logic gate	Logic.LE72.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE73.Out Signal: Latched Output (Q) Logic.LE73.Out inverted Signal: Negated Latched Output (Q NOT) Logic.LE74.Gate Out Signal: Output of the logic gate	Logic.LE73.Gate Out	Signal: Output of the logic gate
Logic.LE73.Out inverted Signal: Negated Latched Output (Q NOT) Logic.LE74.Gate Out Signal: Output of the logic gate	Logic.LE73.Timer Out	Signal: Timer Output
Logic.LE74.Gate Out Signal: Output of the logic gate	Logic.LE73.Out	Signal: Latched Output (Q)
	Logic.LE73.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE74.Timer Out Signal: Timer Output	Logic.LE74.Gate Out	Signal: Output of the logic gate
	Logic.LE74.Timer Out	Signal: Timer Output

Page 252 EN MRM4 12/11

Name	Description
Logic.LE74.Out	Signal: Latched Output (Q)
Logic.LE74.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE75.Gate Out	Signal: Output of the logic gate
Logic.LE75.Timer Out	Signal: Timer Output
Logic.LE75.Out	Signal: Latched Output (Q)
Logic.LE75.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE76.Gate Out	Signal: Output of the logic gate
Logic.LE76.Timer Out	Signal: Timer Output
Logic.LE76.Out	Signal: Latched Output (Q)
Logic.LE76.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE77.Gate Out	Signal: Output of the logic gate
Logic.LE77.Timer Out	Signal: Timer Output
Logic.LE77.Out	Signal: Latched Output (Q)
Logic.LE77.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE78.Gate Out	Signal: Output of the logic gate
Logic.LE78.Timer Out	Signal: Timer Output
Logic.LE78.Out	Signal: Latched Output (Q)
Logic.LE78.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE79.Gate Out	Signal: Output of the logic gate
Logic.LE79.Timer Out	Signal: Timer Output
Logic.LE79.Out	Signal: Latched Output (Q)
Logic.LE79.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE80.Gate Out	Signal: Output of the logic gate
Logic.LE80.Timer Out	Signal: Timer Output
Logic.LE80.Out	Signal: Latched Output (Q)
Logic.LE80.Out inverted	Signal: Negated Latched Output (Q NOT)
Sys.AFRMS active	Signal: Arcflash Reduction Maintenance active
Sys.AFRMS inactive	Signal: Arcflash Reduction Maintenance inactive

Setting Group Switch Via Smart View

- In case Smart View is not running please start it.
- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device«.
- Double click the »Protection Para« icon in the navigation tree.
- Double click the »P-Set Switch« within the protection parameters.
- Configure the Setting Group Switch respectively choose an active set manually.

NOTICE

The description of the parameters can be found within chapter System Parameters.

Copying Setting Groups (Parameter Sets) Via Smart View

NOTICE

Setting groups can only be copied if there are no implausibilities (no red question mark).

It is not necessary to set up two setting groups that only differ in few parameters.

With the help of "Smart View" you can simply copy an existing setting group to another (not yet configured) one. You only need to change those parameters where the two setting groups are different.

To efficiently establish a second parameter set where only few parameters are different, proceed as follows:

- In case Smart View is not running please start it.
- Open an (offline) parameter file of a device or load data of a connected device.
- By way of precaution, save (the relevant) device parameters [File\Save as].
- Select »Copy Parameter Sets« out of the menu "Edit".
- Then define both, source and destination of the parameter sets to be copied (source = copy from; destination: copy to).
- Mouse click on »OK« to start copy procedure.
- The copied parameter set is now cached (not yet saved!).
- Then, modify the copied parameter set(s), if applicable.
- Assign a new file name to the revised device parameter file and save it on your hard disk (backup copy).
- To transfer the modified parameters back to the device, click on menu item »Device« and select »Transfer All Parameters into the Device«.

Comparing Setting Groups Via Smart View

- In case Smart View is not running please start it.
- Click on menu item »Edit« and select »Compare Parameter Sets«.
- Select the two parameter sets from the (two) drop down menus you would like to have compared with each other.
- Press the push-button »compare«.
- The values that are different from the set parameters will be listed in tabular form.

Page 254 EN MRM4 12/11

Comparing Parameter Files Via Smart View

With the help of "Smart View" you can simply compare/diff the currently open parameter/device file against a file on your hard disk. The precondition is that the versions and type of devices match. Please proceed as follows:

- Click on »Compare with a Parameter File« within the menu »Device«.
- Click on the Folder icon in order to select a file on your hard disk.
- The differences will be shown in tabular form.

Converting Parameter Files Via Smart View

Parameter files of the same type can be up- or downgraded (converted). As many parameters as possible will be taken over.

- Parameters, that are newly added, will be set to default.
- Parameters, that are not included in the target file version, will be deleted.

In order to convert a parameter file please proceed as follows:

- In case *Smart View* is not in operation start this software.
- Open a parameter file or load the parameters from a device that should be converted.
- Make a backup of this file at a fail-safe place.
- Choose »Save as« from menu »File«
- Enter a new file name (in order to prevent overwriting the original file)
- Choose the new file type from drop down menu »File Type«.
- Confirm the security check by clicking on »yes« if and only you are sure that the file conversion should be executed.
- In tabular form the modifications will be shown as follows.

Added parameter:	梦
Deleted parameter:	Ž.

Program Mode

By means of the <u>Program Mode</u>, parameter settings can be locked against any changes as long as one or all of the breaker(s) are in the »*CLOSED«* position. The <u>Program Mode</u> can be activated within the [Protection Parameters/Program Mode]

- »Open« Parameter changes require all breakers to be in the »OPEN« state.
- »Either« Parameter changes require minimum one breaker to be in the »OPEN« state.

Unlike, if the *Program Mode* is used for Motor Protection devices, the User has the following options:

»Motor Stop« - Lock parameter changes, if the motor is not in the »STOP« state.
 This ensures, that parameter changes can only be done while the motor stands still (zero speed).

Page 256 EN MRM4 12/11

Device Parameters

Sys

Date and Time

In menu »Device parameters/Date/Time« you can set date and time.

Synchronize Date and Time Via Smart View

- In case Smart View is not running please start it.
- If device data has not been loaded yet click »Receive Data From The Device« in menu »Device«
- Double click the »Device parameters« icon in the navigation tree.
- Double click the »Date/time-icon« within the operational data.
- Out of the working window you can now synchronize date and time of the device with your PC i.e. That means, that the device takes over date and time from your PC.

Version

Within this menu »Device parameters/Version« you can obtain information on the soft- and hardware version.

Version Via Smart View

Within this menu »File/Properties« you can obtain detailed information on the currently opened file like e.g. softand hardware version....



In order to be able to transmit a parameter file (e.g. offline created) into the device the following issues must comply:

- Type Code (written on the top of the device/type label) and
- Version of the device model (can be found in menu [Device Parameters\Version].

TCP/IP Settings

Within menu »Device Para / TCP/IP« the TCP/IP settings have to be set.

The first-time setting of the TCP/IP Parameters can be done at the panel (HMI) only.



Establishing a connection via TCP/IP to the device is only possible if your device is equipped with an Ethernet Interface (RJ45).

Contact your IT administrator in order to establish the network connection.

Set the TCP/IP Parameters

Call up »Device parameter/TCP/IP« at the HMI (panel) and set the following parameters:

- TCP/IP address
- Subnetmask
- Gateway

Direct Commands of the System Module

Parameter	Description	Setting range	Default	Menu path
Ack LED	All acknowledgeable LEDs will be acknowledged.	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/Acknowledge]
Ack BO	All acknowledgeable binary relay output contacts	inactive,	inactive	[Operation
	will be acknowledged.	active		/Reset/Acknowledge
				/Acknowledge]
Ack Scada	SCADA will be acknowledged.	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/Acknowledge]
Ack BO LED	Reset the binary relay output contacts, LEDs,	inactive,	inactive	[Operation
Scd TCmd	SCADA and the Trip Command.	active		/Reset/Acknowledge
				/Acknowledge]
Res	Reset all counters in history group operations	inactive,	inactive	[Operation
OperationsCr		active		/Reset/Acknowledge
				/History]
Res AlarmCr	Reset all counters in history group alarms	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/History]
Res TripCr	Reset all counters in history group trips	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/History]
Res TotalCr	Reset all counters in history group total	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/History]
Res All	Reset of all Counters	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/History]
Reboot	Rebooting the device.	no,	no	[Service
		yes		/General]

Page 258 EN MRM4 12/11

CAUTION

CAUTION, rebooting the device manually will release the Supervision Contact.

Global Protection Parameters of the System

Parameter	Description	Setting range	Default	Menu path
PSet-Switch	Switching Parameter Set	PS1,	PS1	[Protection Para
		PS2,		/PSet-Switch]
		PS3,		
		PS4,		
		PSS via Inp fct,		
		PSS via Scada		
PS1: activated by	This Setting Group will be the active one if: The Parameter Setting Group Switch is set to "Switch via Input" and the other three input functions are inactive at the same time. In case that there is more than one input function active, no Parameter Setting Group Switch will be executed. In case all input functions are inactive, the device will keep working with the Setting Group that was activated lastly.	1n, PSS		[Protection Para /PSet-Switch]
	Only available if: PSet-Switch = PSS via Inp fct			
PS2: activated by	This Setting Group will be the active one if: The Parameter Setting Group Switch is set to "Switch via Input" and the other three input functions are inactive at the same time. In case that there is more than one input function active, no Parameter Setting Group Switch will be executed. In case all input functions are inactive, the device will keep working with the Setting Group that was activated lastly.	1n, PSS		[Protection Para /PSet-Switch]
	Only available if: PSet-Switch = PSS via Inp fct			
PS3: activated by	This Setting Group will be the active one if: The Parameter Setting Group Switch is set to "Switch via Input" and the other three input functions are inactive at the same time. In case that there is more than one input function active, no Parameter Setting Group Switch will be executed. In case all input functions are inactive, the device will keep working with the Setting Group that was activated lastly.	1n, PSS		[Protection Para /PSet-Switch]
	Only available if: PSet-Switch = PSS via Inp fct			

Parameter	Description	Setting range	Default	Menu path
PS4: activated by	This Setting Group will be the active one if: The Parameter Setting Group Switch is set to "Switch via Input" and the other three input functions are inactive at the same time. In case that there is more than one input function active, no Parameter Setting Group Switch will be executed. In case all input functions are inactive, the device will keep working with the Setting Group that was activated lastly. Only available if: PSet-Switch = PSS via Inp fct	1n, PSS		[Protection Para /PSet-Switch]
Ack LED	All acknowledgeable LEDs will be acknowledged if the state of the assigned signal becomes true.	1n, Assignment List		[Device Para /Ex Acknowledge]
Ack BO	All acknowledgeable binary relay output contacts will be acknowledged if the state of the assigned signal becomes true.	1n, Assignment List		[Device Para /Ex Acknowledge]
Ack Scada	SCADA will be acknowledged if the state of the assigned signal becomes true.	1n, Assignment List		[Device Para /Ex Acknowledge]
Scaling	Display of the measured values as primary, secondary or per unit values	Per unit values, Primary values, Secondary values	Per unit values	[Device Para /Measurem Display]

System Module Input States

Name	Description	Assignment via
Ack LED-I	Module input state: LEDs acknowledgment	[Device Para
	by digital input	/Ex Acknowledge]
Ack BO-I	Module input state: Acknowledgment of the	[Device Para
	binary Relay Output Contacts	/Ex Acknowledge]
Ack Scada-I	Module input state: Acknowledge Scada via	[Device Para
	digital input. The replica that SCADA has got from the device is to be reset.	/Ex Acknowledge]
PS1-I	signal, that should activate this Parameter	[Protection Para
		/PSet-Switch]
PS2-I	State of the module input respectively of the	[Protection Para
	signal, that should activate this Parameter Setting Group.	
PS3-I	State of the module input respectively of the	[Protection Para
	signal, that should activate this Parameter Setting Group.	/PSet-Switch]
PS4-I	State of the module input respectively of the	[Protection Para
	signal, that should activate this Parameter Setting Group.	/PSet-Switch]

Page 260 EN MRM4 12/11

System Module Signals

Name	Description
Reboot	Signal: Rebooting the device: 1=Restart initiated by power supply; 2=Restart initiated by the User; 3=Set on defaults (Super Reset); 4=Restart by the debugger; 5=Restart because of configuration change; 6=General failure; 7=Restart initiated by System Abort (host side); 8=Restart initiated by watchdog timeout (host side); 9=Restart initiated by System Abort (dspside); 10=Restart initiated by watchdog timeout (dspside); 11=Power supply failure (short term interruption) or power supply voltage to low; 12=illegal memory access.
Act Set	Signal: Active Parameter Set
PS 1	Signal: Parameter Set 1
PS 2	Signal: Parameter Set 2
PS 3	Signal: Parameter Set 3
PS 4	Signal: Parameter Set 4
PSS manual	Signal: Manual Switch over of a Parameter Set
PSS via Scada	Signal: Parameter Set Switch via Scada
PSS via Inp fct	Signal: Parameter Set Switch via input function
min 1 param changed	Signal: At least one parameter has been changed
Param to be saved	Number of parameters to be saved. 0 means that all parameter changes are overtaken.
Ack LED	Signal: LEDs acknowledgment
Ack BO	Signal: Acknowledgment of the Binary Outputs
Ack Counter	Signal: Reset of all Counters
Ack Scada	Signal: Acknowledge Scada
Ack TripCmd	Signal: Reset Trip Command
Ack LED-HMI	Signal: LEDs acknowledgment :HMI
Ack BO-HMI	Signal: Acknowledgment of the Binary Outputs :HMI
Ack Counter-HMI	Signal: Reset of all Counters :HMI
Ack Scada-HMI	Signal: Acknowledge Scada :HMI
Ack TripCmd-HMI	Signal: Reset Trip Command :HMI
Ack LED-Sca	Signal: LEDs acknowledgment :SCADA
Ack BO-Sca	Signal: Acknowledgment of the Binary Outputs :SCADA
Ack Counter-Sca	Signal: Reset of all Counters :SCADA
Ack Scada-Sca	Signal: Acknowledge Scada :SCADA
Ack TripCmd-Sca	Signal: Reset Trip Command :SCADA
Res OperationsCr	Signal:: Res OperationsCr
Res AlarmCr	Signal:: Res AlarmCr
Res TripCr	Signal:: Res TripCr
Res TotalCr	Signal:: Res TotalCr

Special Values of the System Module

Value	Description	Menu path
Build	Build	[Device Para
		/Version]
Version	Version	[Device Para
		/Version]
Operating hours Cr	Operating hours counter of the protective	[Operation
	device	/Count and RevData
		/Sys]
Hours Counter	Hours Counter	[Operation
		/History
		/TotalCr]

Page 262 EN MRM4 12/11

System Parameters

Field Para

Within the system parameters you can set all parameters, that are relevant for the primary side and the mains operational method like frequency, primary and secondary values and the star point treatment.

General System Parameters

Parameter	Description	Setting range	Default	Menu path
Phase	Phase Sequence direction	ABC,	ABC	[Field Para
Sequence		ACB		/General settings]
f	Nominal frequency	50Hz,	50Hz	[Field Para
		60Hz		/General settings]

System Parameters – Current Related

Parameter	Description	Setting range	Default	Menu path
CT pri	Nominal current of the primary side of the current transformers.	1 – 50000 A	10 A	[Field Para /Current transf]
CT sec	Nominal current of the secondary side of the current transformers.	1 A, 5 A	1 A	[Field Para /Current transf]
CT dir	Protection functions with directional feature can only work properly if the connection of the current transformers is free of wiring errors. If all current transformers are connected to the device with an incorrect polarity, the wiring error can be compensated by this parameter. This parameter turns the current vectors by 180 degrees.	0°, 180°	0°	[Field Para /Current transf]
ECT pri	This parameter defines the primary nominal current of the connected earth current transformer. If the earth current is measured via the Holmgreen connection, the primary value of the phase current transformer must be entered here.	1 – 50000 A	50 A	[Field Para /Current transf]
ECT sec	This parameter defines the secondary nominal current of the connected earth current transformer. If the earth current is done via the Holmgreen connection, the primary value of the phase current transformer must be entered here.	1 A, 5 A	1 A	[Field Para /Current transf]
ECT dir	Earth fault protection with directional feature depends also on the correct wiring of the earth current transformer. An incorrect polarity/wiring can be corrected by means of the settings "0°" or "180°". The operator has the possibility of turning the current vector by 180 degrees (change of sign) without modification of the wiring. This means, that – in terms of figures - the determined current indicator was turned by 180° by the device.	0°, 180°	0°	[Field Para /Current transf]

Parameter	Description	Setting range	Default	Menu path
IL1, IL2, IL3 Cutoff Level	The Current shown in the Display or within the PC Software will be displayed as zero, if the Current falls below this Cutoff Level. This parameter has no impact on recorders.	0.0 – 0.100 In	0.005 In	[Device Para /Measurem Display]
IG meas Cutoff Level	The measured Earth Current shown in the Display or within the PC Software will be displayed as zero, if the measured Earth Current falls below this Cutoff Level. This parameter has no impact on recorders.	0.0 – 0.100 ln	0.005 In	[Device Para /Measurem Display]
IG calc Cutoff Level	The calculated Earth Current shown in the Display or within the PC Software will be displayed as zero, if the calculated Earth Current falls below this Cutoff Level. This parameter has no impact on recorders.	0.0 – 0.100 ln	0.005 In	[Device Para /Measurem Display]
I012 Cutoff Level	The Symmetrical Component shown in the Display or within the PC Software will be displayed as zero, if the Symmetrical Component falls below this Cutoff Level. This parameter has no impact on recorders.	0.0 – 0.100 ln	0.005 In	[Device Para /Measurem Display]

Page 264 EN MRM4 12/11

Blockings

The device provides a function for temporary and permanent blocking of the complete protection functionality or of single protection stages.



Make absolutely sure that no illogical or even life-threatening blockings are allocated.

Make sure that you do not carelessly deactivate protection functions which have to be available according to the protection concept.

Permanent Blocking

Switching ON or OFF the complete protection functionality

In module <u>»Protection«</u> the complete protection of the device can be switched on or off. Set the parameter Function to <u>»active«</u> or <u>»inactive«</u> in module <u>»Prot«</u>.



Only if in module »Prot« the parameter *Function* is = *»active*«, the protection is activated; i.e. with *»Function« = »inactive«*, no protection function is operating. Then the device cannot protect any components.

Switching modules ON or OFF

Each of the modules can be switched on or off (permanently). This is achieved when the parameter »Function« is set to »active« or »inactive« in the respective module.

Activating or deactivating the tripping command of a protection stage permanently

In each of the protection stages the tripping command to the Bkr. can be permanently blocked. For this purpose the parameter "TripCmd Blo" has to be set to "active".

Temporary Blocking

To block the complete protection of the device temporarily by a signal

In module <u>»Prot«</u> the complete protection of the device can be blocked temporarily by a signal. On condition that a module-external blocking is permitted <u>»ExBlo Fc=active«</u>. In addition to this, a related blocking signal from the <u>**</u> »assignment list« must have been assigned. For the time the allocated blocking signal is active, the module is blocked.



If the module <u>»Prot«</u> is blocked, the complete protection function does not work. As long as the blocking signal is active, the device cannot protect any components.

To block a complete protection module temporarily by an active assignment

- In order to establish a temporary blockage of a protection module, the parameter »ExBlo Fc« of the module has to be set to »active«. This gives the permission: »This module can be blocked«.
- Within the general protection parameters a signal has to be additionally chosen from the »ASSIGNMENT LIST«. The blocking only becomes active when the assigned signal is active.

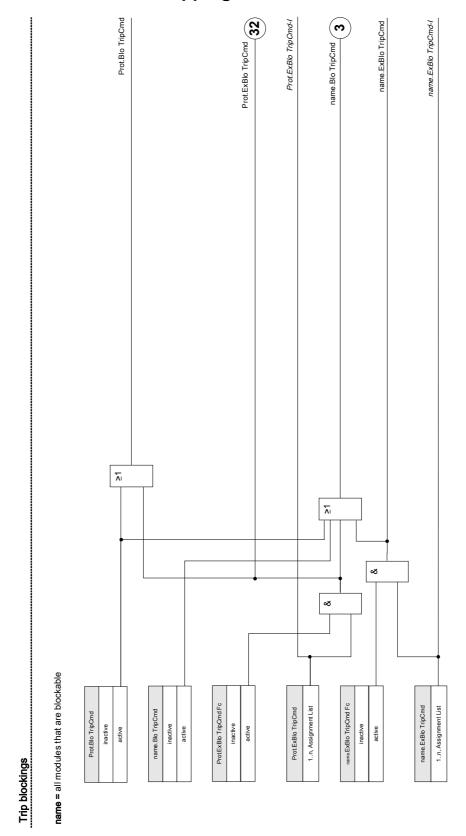
To block the tripping command of a protection stage temporarily by an active assignment.

The tripping command of any of the protection modules can be blocked from external. In this case, external does not only mean from outside the device, but also from outside the module. Not only real external signals are permitted to be used as blocking signals, as for example, the state of a digital input, but you can also choose any other signal from the **assignment list**.

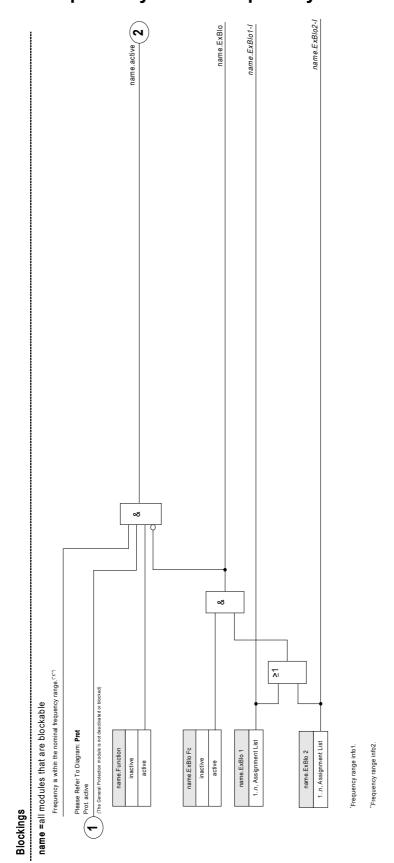
- In order to establish a temporary blockage of a protection stage, the parameter »ExBlo TripCmd Fc« of the module has to be set to »active«. This gives the permission: »The tripping command of this stage can be blocked«.
- Within the general protection parameters, a signal has to be chosen additionally and assigned to the parameter »ExBlo« from the »assignment list«. If the selected signal is activated, the temporary blockage becomes effective.

Page 266 EN MRM4 12/11

To Activate or Deactivate the Tripping Command of a Protection Module



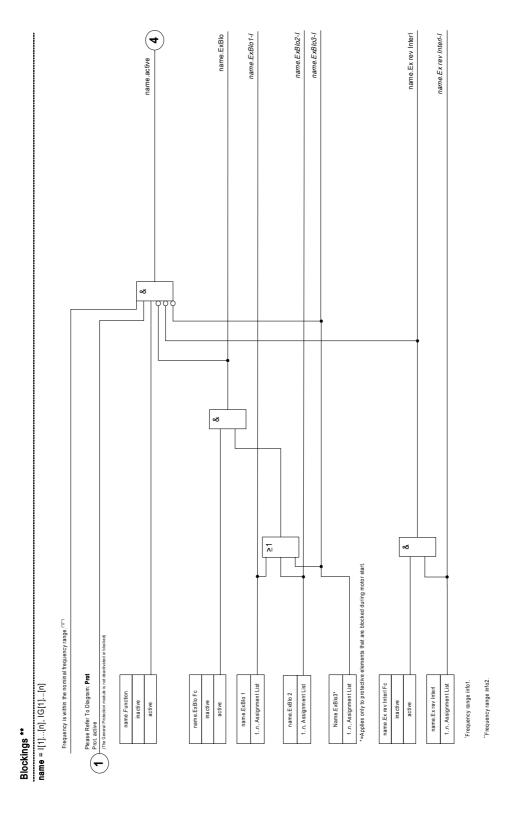
Activate, Deactivate respectively Block Temporarily Protection Functions



Page 268 EN MRM4 12/11

Current protective functions cannot only be blocked permanently (*function = inactive*) or temporarily by any blocking signal from the *assignment list*, but also by *reverse Interlocking*.

All other protection functions can be activated, deactivated or blocked in the same manner.



Module: Protection (Prot)

Prot

The module <u>»Protection«</u> serves as outer frame for all other protection modules, i.e. they are all enclosed by the module <u>»Protection«</u>. All alarms and tripping commands are combined in module <u>»Protection«</u> by an OR-logic.



If in module <u>»Protection«</u> the parameter »Function« is set on »inactive« or in case the module is blocked, then the complete protective function of the device does not work anymore.

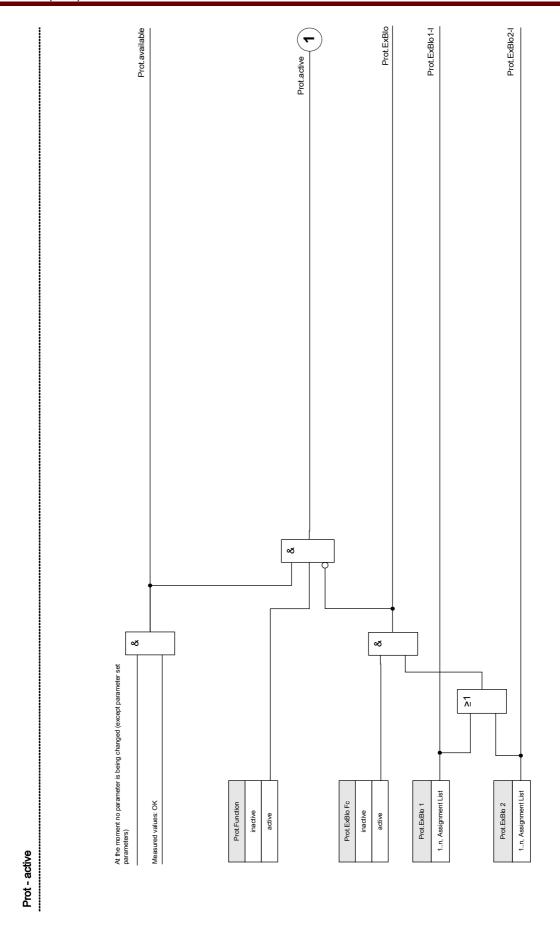
Protection inactive

If the master module <u>»Protection«</u> was permanently deactivated or if a temporary blockage of this module has occurred and the allocated blocking signal is still active, then the complete functionality (protection) of the device is zero. In such a case the protective function is »inactive«.

Protection active

If the master module <u>»Protection«</u> was activated and a blockade for this module was not activated respectively the assigned blocking signal is inactive at that moment, then the <u>»Protection«</u> is <u>»active</u>«.

Page 270 EN MRM4 12/11



Each protection stage can decide automatically about a trip. The trip decision is passed on to module <u>»Prot«</u> and The tripping commands of all protection stages are combined in module <u>»Prot«</u> by an OR logic (Collective signals, direction decisions, information about phases). The tripping commands are executed by the module <u>»TripControl«</u>.

AWARNING

The tripping commands are executed by the module <u>»TripControl«</u>

If a protection module is activated respectively issues a trip command to the Bkr. two alarm signals will be created:

- 1. The module or the protection stage issues an alarm e.g. »I[1]. Alarm« or »I[1]. Trip«.
- 2.The master module <u>»Prot«</u> collects/summarizes the signals and issues an alarm or a trip signal »Prot.Alarm« »Prot.Trip«.

Page 272 EN MRM4 12/11

Prot.Trip name = Each trip of an active, trip authorized protection module will lead to a general trip. Σ١

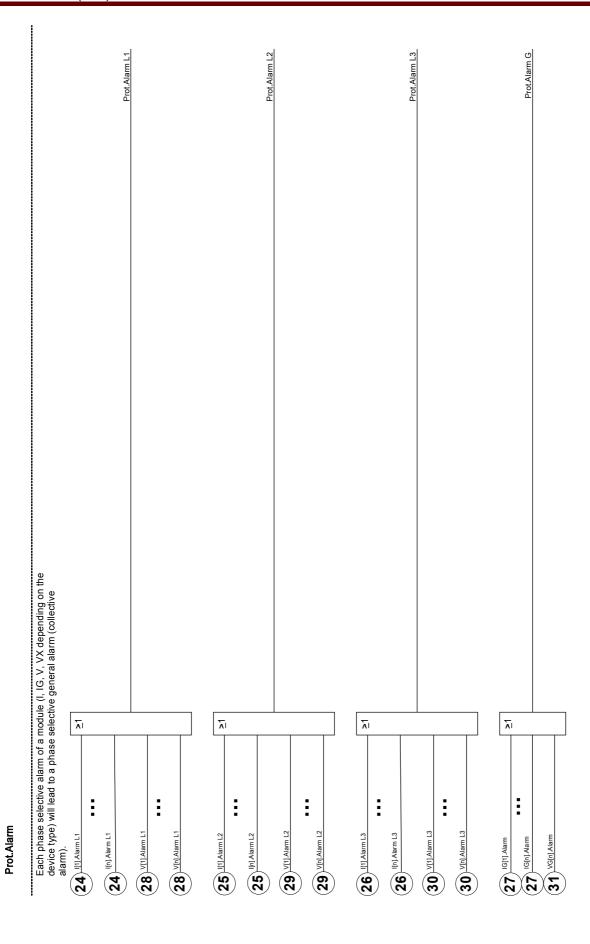
name = Each alarm of a module (except from supervision modules but including CBF) will lead to a general alarm (collective alarm). Σı

Prot.Alarm

Page 274 EN MRM4 12/11

Prot.Trip

Prot.Trip L2 Prot.Trip G Prot.Trip L1 Each phase selective trip of a trip authorized module (I, IG, V, VX depending on the device type) will lead to a phase selective general trip. 7 ۲ 71 Σ (19) IG[n].Trip Cmd (22) V[1].Trip L3 (20) V[1].Trip L1 20 V[n].Trip L1 **21** V(1).Trip L2 (18) I[n].Trip L3 (22) V[n].Trip L3 (18) I[1].Trip L3 17) I[1].Trip L2 (17) I[n].Trip L2 (16) I[1].Trip L1 (16) I[n].Trip L1 (19) IG[1].Trip



Page 276 EN MRM4 12/11

Direct Commands of the Protection Module

Parameter	Description	Setting range	Default	Menu path
	3	inactive,	inactive	[Operation
Mains No	faults.	active		/Reset/Acknowledge
				/Reset]

Global Protection Parameters of the Protection Module

Parameter	Description	Setting range	Default	Menu path
Function	module/stage	inactive,	active	[Protection Para
		active		/Global Prot Para
				/Prot]
ExBlo Fc	Activate (allow) the external blocking of the global	inactive,	inactive	[Protection Para
	protection functionality of the device.	active		/Global Prot Para
				/Prot]
ExBlo1	If external blocking of this module is activated (allowed), the global protection functionality of the device will be blocked if the state of the assigned signal becomes true.	1n, Assignment List		[Protection Para
				/Global Prot Para
				/Prot]
ExBlo2	If external blocking of this module is activated (allowed), the global protection functionality of the device will be blocked if the state of the assigned signal becomes true.	1n, Assignment List		[Protection Para
				/Global Prot Para
				/Prot]
Blo TripCmd	Permanent blocking of the Trip Command of the entire Protection.	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/Prot]
ExBlo	Activate (allow) the external blocking of the trip	inactive,	inactive	[Protection Para
TripCmd Fc	command of the entire device.	active		/Global Prot Para
				/Prot]
ExBlo	If external blocking of the tripping command is	1n, Assignment List	-1-	[Protection Para
TripCmd	activated (allowed), the tripping command of the entire device will be blocked if the state of the assigned signal becomes true.			/Global Prot Para
				/Prot]

Protection Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/Prot]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/Prot]

Protection Module Signals (Output States)

Name	Description
available	Signal: Protection is available
active	Signal: active
ExBlo	Signal: External Blocking
Alarm L1	Signal: General-Alarm L1
Alarm L2	Signal: General-Alarm L2
Alarm L3	Signal: General-Alarm L3
Alarm G	Signal: General-Alarm - Earth fault
Alarm	Signal: General Alarm
Trip L1	Signal: General Trip L1
Trip L2	Signal: General Trip L2
Trip L3	Signal: General Trip L3
Trip G	Signal: General Trip Ground fault
Trip	Signal: General Trip
Res Fault a Mains No	Signal: Resetting of fault number and number of grid faults.

Protection Module Values

Name	Description	Assignment via
FaultNo	Disturbance No	0
No of grid faults	Number of grid faults: A grid fault, e.g. a short circuit, might cause several faults with trip and autoreclosing, each fault being identified by an increased fault number. In this case, the grid fault number remains the same.	

Page 278 EN MRM4 12/11

Switchgear/Breaker - Manager

SG



WARNING Misconfiguration of the switchgear can result in death or serious injury.

Beside protection functions, protective relays more and more will take care about controlling switchgear, like circuit breakers, load break switches, disconnectors and ground connectors.

The Switchgear/Breaker-Manager of this protective device is designed to manage one switchgear.

The correct configuration is an indispensable precondition for the proper functioning of the protective device. This also is the case, when the switchgear is not controlled, but supervised only.

Single Line Diagram

The single line diagram includes the graphically description of the switchgear and its designation (name) as well as its features (short circuit proof or not ...). For displaying in the devices software, the switchgear' designations (e. g. QA1, QA2, instead of SG[x]) will be taken from the single line diagram (configuration file).

The configuration file includes the single line diagram and the switchgear properties. Switchgear properties and single line diagram are coupled via the configuration file.

Switchgear Configuration

Wiring

At first the switchgear's positioning indicators have to be connected to the digital inputs of the protection device. One of the position indicators (either the »Aux ON« or the »Aux OFF«) contact has to be connected necessarily. It is recommended to connect both contacts.

Thereafter the command outputs (relay output contacts) have to be connected with the switchgear.



Please observe the following option: In the general settings of a breaker, the ON/OFF commands of a protection element can be issued to the same relay output contacts, where the other control commands are issued.

If the commands are issued to different relays relay output contacts the amount of wiring increases.

Assignment of Position Indications

The position indication is needed by the device to get (evaluate) the information about the current status /position of the breaker. The switchgear's position is shown in the devices display. Each position change results in a change of the switchgear symbol.

NOTICE

For the detection of a switchgear's position always two separate Aux contacts are recommended! If only one Aux contact is used, no intermediate or disturbed positions can be detected.

A reduced transition supervision (time between issue of the command and position feedback indication of the switchgear) is also possible by one Aux contact.

In the menu [Control/Bkr/Pos Indicators wiring] the assignments for the position indications have to be set.

Detection of switchgear position with two Aux contacts – Aux ON and Aux OFF (recommended!)

For detection of position the switchgear is provided with Aux contacts (Aux ON and Aux OFF). It is recommended to use both contacts to detect intermediate and disturbed positions too.

The protection device continuously supervises the status of the inputs **Aux ON-I** and **Aux OFF-I**. These signals are validated based on the supervision timers **t-Move ON** and **t-Move OFF** validation functions. As a result, the switchgear position will be detected by the following signals:

- Pos ON
- Pos OFF
- Pos Indeterm
- Pos Disturb.
- Pos (State=0,1,.2 or 3)

Supervision of the ON command

When an ON command is initiated, the *»t-Move ON«* timer will be started. While the timer is running, the *»POS INDETERM«* State will become true. If the command is executed and properly fed back from the switchgear before the timer has run down, *»POS ON«* will become true. Otherwise, if the timer has expired *»POS DISTURB«* will become true.

Supervision of the OFF command

When an OFF command is initiated, the *»t-Move OFF«* timer will be started. While the timer is running, the *»POS INDETERM«* State will become true. If the command is executed and properly fed back before the timer has run down, *»POS OFF«* will become true. Otherwise, if the timer has expired *»POS DISTURB«* will become true.

The following table shows how switchgear positions are validated:

States of the	Digital Inputs	Validated Switchgear Positions				
Aux ON-I	Aux OFF-I	POS ON	POS OFF	POS Indeterm	POS Disturb	POS State
0	0	0	0	1 (while a Moving timer is running)	0 (while a Moving timer is running)	0 Intermediate
1	1	0	0	1 (while a Moving timer is running)	0 (while a Moving timer is running)	0 Intermediate
0	1	0	1	0	0	1 OFF
1	0	1	0	0	0	2 ON

Page 280 EN MRM4 12/11

States of the Digital Inputs			Validated	l Switchgear Po	sitions	
0	0	0	0	0 (Moving timer elapsed)	1 (Moving timer elapsed)	3 Disturbed
1	1	0	0	0 (Moving timer elapsed)	1 (Moving timer elapsed)	3 Disturbed

Single Position Indication Aux ON or Aux OFF

If the single pole indication is used, the »SI Single ContactInd« will become true.

The moving time supervision works only in one direction. If the Aux OFF signal is connected to the device, only the "OFF command" can be supervised and if the Aux ON signal is connected to the device, only the "ON command" can be supervised.

Single Position Indication - Aux ON

If only the Aux ON signal is used for the Status Indication of an "ON command", the switch command will also start the moving time, the position indication indicates an INTERMEDIATE position during this time interval. When the switchgear reaches the end position indicated by the signals »Pos ON« and »CES success« before the moving time has elapsed the signal Pos Indeterm disappears.

If the moving time elapsed before the switchgear has reached the end position, the switching operation was not successful and the Position Indication will change to POS Disturb and the signal Pos Indeterm disappears.

The following table shows how breaker positions are validated based on Aux ON:

States of the	e Digital Input	Validated Switchgear Positions				
Aux ON-I	Aux OFF-I	POS ON	POS OFF	POS Indeterm	POS Disturb	POS State
0	Not wired	0	0	1 (while t-Move ON is running)	0 (while t-Move ON is running)	0 Intermediate
0	Not wired	0	1	0	0	1 OFF
1	Not wired	1	0	0	0	2 ON

If there is no digital input assigned to the »Aux On« contact, the position indication will have the value 3 (disturbed).

Single Position Indication – Aux OFF

If only the Aux OFF signal is used for the monitoring of the "OFF command", the switch command will start the moving timer. The Position Indication will indicate an INTERMEDIATE position. When the the switchgear reaches its end position before the moving timer elapses, and "CES success" will be indicated. At the same time the signal "Pos Indeterm" disappears.

If the moving time elapsed before the switchgear has reached the OFF position, the switching operation was not successful and the Position Indication will change to »Pos Disturb« and the signal »Pos Indeterm« disappears.

The following table shows how breaker positions are validated based on Aux OFF:

States of th	e Digital Input	Validated Switchgear Positions				
Aux ON-I	Aux OFF-I	POS ON	POS OFF	POS Indeterm	POS Disturb	POS State
Not wired	0	0	0	1 (while t-Move OFF is running)	0 (while t-Move OFF is running)	0 Intermediate
Not wired	0	0	1	0	0	1 OFF
Not wired	1	1	0	0	0	2 ON

If there is no digital input assigned to the »Aux OFF« contact, the position indication will have the value 3 (disturbed).

Setting of Supervision Times

In the menu [Control/Bkr/General Settings] the supervision times of the individual switchgear have to be set. Dependent on the type of switchgear it can be necessary to set further parameters.

Interlockings

To avoid faulty operations, interlockings have to be provided. This can be realised mechanically, or electrically within the menu [Control/Bkr/General Settings] .

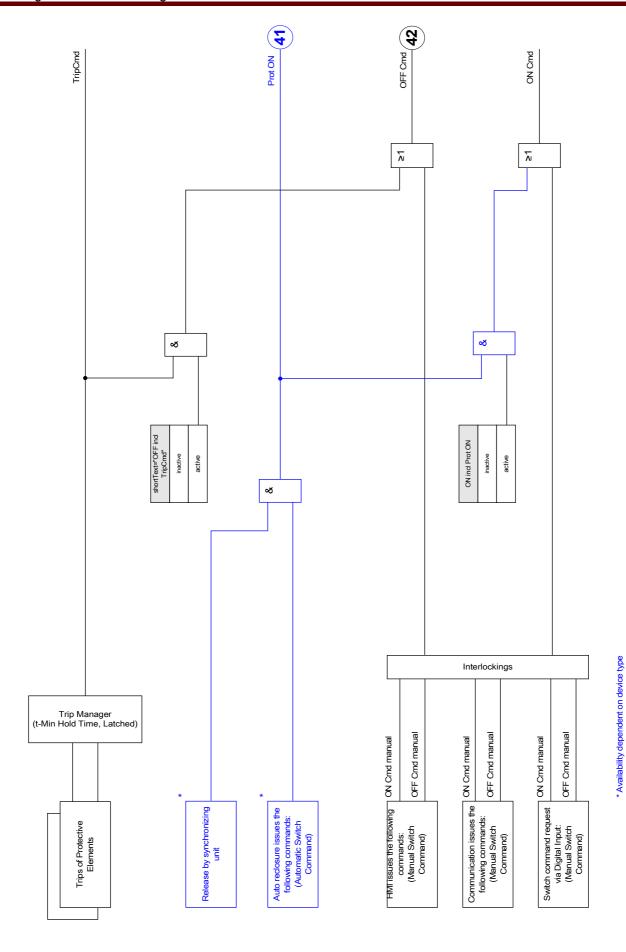
For a controllable switchgear up to three interlockings can be assigned in both switching directions (ON/OFF). These interlockings prevent switching in the corresponding direction.

The protection OFF command and the reclosing command of the AR* module are always executed without interlockings. For the case, that a protection OFF command must not be issued, this must be blocked separately.

Further interlockings can be realised by means of the Logic module.

Page 282 EN MRM4 12/11

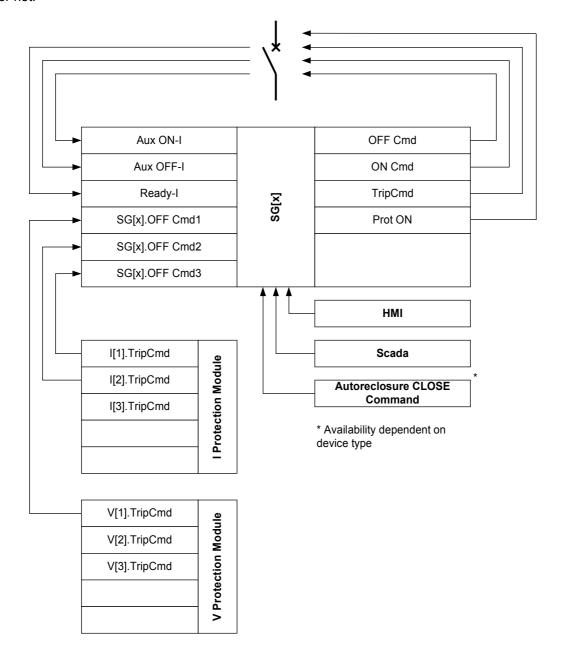
^{*=}availability depends on ordered device.



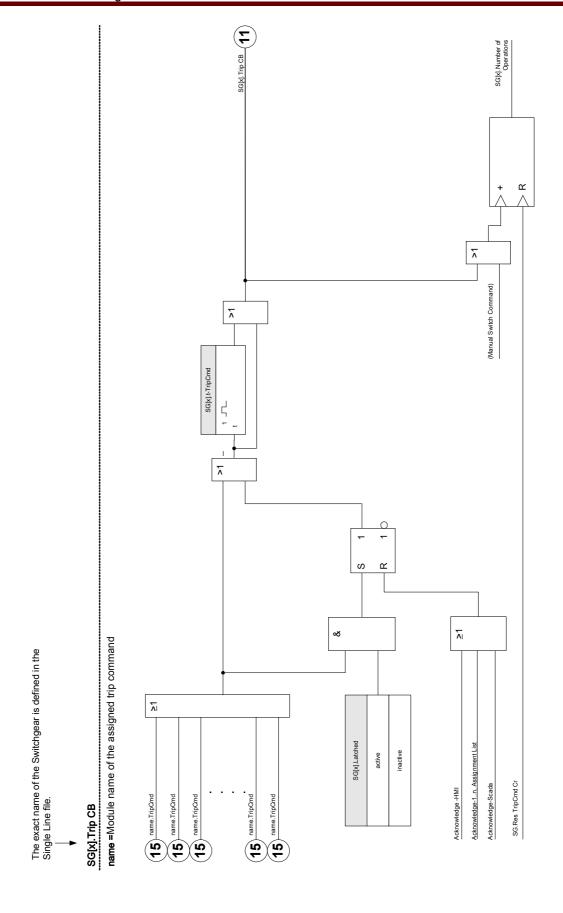
Trip Manager - Assignment of Commands

The trip commands of the protection elements have to be assigned within menu [Control/Bkr/Trip Manager] to the switchgear (presumed, that the switchgear is make/break capable) . In the Trip Manger all tripping commands are combined by an "OR" logic. The actual tripping command to the

In the Trip Manger all tripping commands are combined by an "OR" logic. The actual tripping command to the switchgear is exclusively given by the Trip Manager. This means, that only tripping commands which are assigned in the Trip Manager lead to an operation of the switchgear. In addition to that, the User can set the minimum hold time of the tripping command within this module and define whether the tripping command is latched or not.



Page 284 EN MRM4 12/11



Ex ON/OFF

If the switchgear should be opened or closed by an external signal, the User can assign one signal that will trigger the ON and one signal that will trigger the OFF command (e.g. digital inputs or output signals of the Logic) within menu [Control/Bkr/Ex ON/OFF Cmd] . An OFF command has priority. ON commands are slope oriented, OFF commands are level oriented

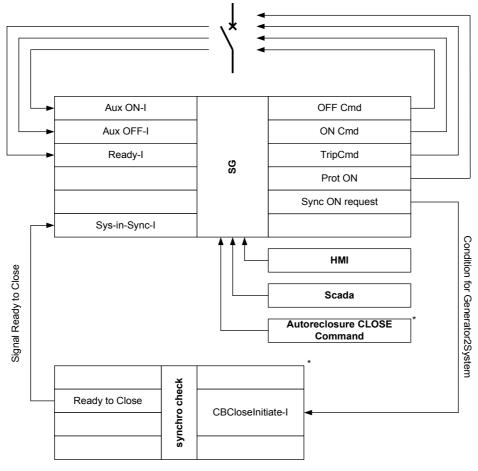
Synchronised Switching*

Before a switchgear may connect two mains sections, synchronism of these sections must be assured. In the sub-menu [Synchronous Switching] the parameter »Synchronism« defines which signal indicates synchronism.

If the synchronism condition shall be evaluated by the internal Synch-Check module the signal »Sync. Ready to Close« (release by synch-check module) has to be assigned. Alternatively a digital input or a logic output can be assigned.

In the synchronisation mode "Generator-to-System" additionally the synchronism request has to be assigned to the Sync-check function in the menu [Protection Para\Global Prot Para\Sync].

If a synchronism signal is assigned, the switching command will only be executed, when the synchronism signal will become true within the maximum supervision time »*t-MaxSyncSuperv*«. This supervision time will be started with the issued ON command. If no synchronism signal has been assigned, the synchronism release is permanently.



* Availability dependent on device type

Page 286 EN MRM4 12/11

^{*=}availability depends on ordered device type

Switching Authority

For the Switching Authority [Control\General Settings], the following general settings are possible:

NONE: No control function;

LOCAL: Control only via push buttons at the panel;

REMOTE: Control only via SCADA, digital inputs, or internal signals; and LOCAL&REMOTE: Control via push buttons, SCADA, digital inputs, or internal signals.

Non-interlocked Switching

For test purposes, during commissioning and temporarily operations, interlockings can be disabled.



WARNING: Non interlocked Switching can lead to serious injuries or death!

For non interlocked switching the menu [Control\General Settings] provides the following options:

- Non interlocked switching for one single command
- Permanent
- Non interlocked switching for a certain time
- Non interlocked switching, activated by an assigned signal

The set time for non interlocked switching applies also for the "single Operation" mode.

Manual Manipulation of the Switchgear Position

In case of faulty position indication contacts (Aux contacts) or broken wires, the position indication resulted from the assigned signals can be manipulated (overwritten) manually, to keep the ability to switch the affected switchgear. A manipulated switchgear position will be indicated on the display by an exclamation mark "!" beside the switchgear symbol.



WARNING: Manipulation of the Switchgear Position can lead to serious injuries or death!

Double Operation Locking

All control commands to any switchgear in a bay have to be processed sequentially. During a running control command no other command will be handled.

Switch Direction Control

Switching command are validated before execution. When the switchgear is already in the desired position, the switch command will not be issued again. An opened breaker cannot be opened again. This also applies for switching command at the HMI or via SCADA.

Anti Pumping

By pressing the ON command softkey only a single switching ON impulse will be issued independent, how low the softkey is actuated. The switchgear will close only once per close command.

Direct Commands of the Control

Parameter	Description	Setting range	Default	Menu path
Manipulate	Manipulation	inactive,	inactive	[Control
Position		Pos OFF,		/SG
		Pos ON		/General settings]
Res SGwear		inactive,	inactive	[Operation
SI SG		active		/Reset/Acknowledge
				/Reset]
Ack TripCmd	Acknowledge Trip Command	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/Acknowledge]

Global Protection Parameters of the Control

Parameter	Description	Setting range	Default	Menu path
Aux ON	assigned signal is true (52a).	,		[Control
		DI Slot X1.DI 1,		/SG
		DI Slot X1.DI 2,		/Pos Indicators wiring]
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		

Page 288 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Aux OFF	The Bkr. is in OFF-position if the state of the assigned signal is true (52b).	7-7,		[Control
		DI Slot X1.DI 1,		/SG
		DI Slot X1.DI 2,		/Pos Indicators
		DI Slot X1.DI 3,		wiring]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
Ready	Breaker is ready for operation if the state of the	7-7,		[Control
	assigned signal is true. This digital input can be used by some protective elements (if they are available within the device) like Auto Reclosure (AR), e.g. as a trigger signal.	DI Slot X1.DI 1,		/SG
		DI Slot X1.DI 2,		/Pos Indicators
		DI Slot X1.DI 3,		wiring]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
Interl ON1	Interlocking of the ON command	1n, Assignment List		[Control
				/SG
				/Interlockings]
Interl ON2	Interlocking of the ON command	1n, Assignment List		[Control
				/SG
				/Interlockings]
Interl ON3	Interlocking of the ON command	1n, Assignment List		[Control
				/SG
				/Interlockings]
Interl OFF1	Interlocking of the OFF command	1n, Assignment List		[Control
				/SG
				/Interlockings]

Parameter	Description	Setting range	Default	Menu path
Interl OFF2	Interlocking of the OFF command	1n, Assignment List		[Control
				/SG
				/Interlockings]
Interl OFF3	Interlocking of the OFF command	1n, Assignment List		[Control
				/SG
				/Interlockings]
SCmd ON	Switching ON Command, e.g. the state of the	1n, DI-LogicList		[Control
	Logic or the state of the digital input			/SG
				/Ex ON/OFF Cmd]
SCmd OFF	Switching OFF Command, e.g. the state of the	1n, DI-LogicList		[Control
	Logic or the state of the digital input			/SG
				/Ex ON/OFF Cmd]
t-TripCmd	Minimum hold time of the OFF-command	0 – 300.00 s	0.2 s	[Control
	(breaker, load break switch)			/SG
				/Trip Manager]
Latched	Defines whether the Binary Relay Output Contact	inactive,	inactive	[Control
	will be Latched when it picks up.	active		/SG
				/Trip Manager]
Ack TripCmd	Ack TripCmd	1n, Assignment List		[Control
				/SG
				/Trip Manager]
Off Cmd1	Off Command to the Breaker if the state of the	1n, Trip Cmds	MStart.TripCmd	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd2	Off Command to the Breaker if the state of the	1n, Trip Cmds	I[1].TripCmd	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd3	Off Command to the Breaker if the state of the	1n, Trip Cmds	I[2].TripCmd	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd4	Off Command to the Breaker if the state of the	1n, Trip Cmds	I2>[1].TripCmd	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd5	Off Command to the Breaker if the state of the	1n, Trip Cmds	ThR.TripCmd	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd6	Off Command to the Breaker if the state of the	1n, Trip Cmds	Jam[1].TripCmd	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]

Page 290 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Off Cmd7	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds	I<[1].TripCmd	[Control /SG
				/Trip Manager]
Off Cmd8	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd9	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd10	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd11	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd12	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd13	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds		[Control
				/SG
				/Trip Manager]
Off Cmd14	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd15	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd16	Off Command to the Breaker if the state of the	1n, Trip Cmds	-:-	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd17	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd18	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd19	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]

Parameter	Description	Setting range	Default	Menu path
Off Cmd20	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds		[Control
				/SG
				/Trip Manager]
Off Cmd21	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd22	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd23	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd24	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd25	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds		[Control
				/SG
				/Trip Manager]
Off Cmd26	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd27	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd28	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd29	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd30	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.	. '		/SG
				/Trip Manager]
Off Cmd31	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd32	Off Command to the Breaker if the state of the	1n, Trip Cmds	-,-	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]

Page 292 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Off Cmd33	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds		[Control
	assigned signal becomes tide.			/SG
				/Trip Manager]
Off Cmd34	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd35	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd36	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd37	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd38	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds		[Control
		·		/SG
				/Trip Manager]
Off Cmd39	Off Command to the Breaker if the state of the assigned signal becomes true.	1n, Trip Cmds		[Control
				/SG
				/Trip Manager]
Off Cmd40	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd41	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd42	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd43	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd44	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.	,		/SG
				/Trip Manager]
Off Cmd45	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.	,		/SG
				/Trip Manager]

Parameter	Description	Setting range	Default	Menu path
Off Cmd46	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd47	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd48	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd49	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd50	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd51	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd52	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd53	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd54	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd55	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.	·		/SG
				/Trip Manager]
Off Cmd56	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd57	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.	,		/SG
				/Trip Manager]
Off Cmd58	Off Command to the Breaker if the state of the	1n, Trip Cmds	-,-	[Control
	assigned signal becomes true.			/SG
				/Trip Manager]

Page 294 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Off Cmd59	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd60	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd61	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd62	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd63	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd64	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd65	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd66	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd67	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd68	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd69	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.			/SG
				/Trip Manager]
Off Cmd70	Off Command to the Breaker if the state of the	1n, Trip Cmds		[Control
	assigned signal becomes true.	·		/SG
				/Trip Manager]
OFF incl	The OFF Command includes the OFF Command	inactive,	inactive	[Control
TripCmd	issued by the Protection module.	active		/SG
				/General settings]

Parameter	Description	Setting range	Default	Menu path
t-Move ON	Time to move to the ON Position	0.01 – 100.00 s	0.1 s	[Control
				/SG
				/General settings]
t-Move OFF	Time to move to the OFF Position	0.01 – 100.00 s	0.1 s	[Control
				/SG
				/General settings]
t-Dwell	Dwell time	0 – 100.00 s	0 s	[Control
				/SG
				/General settings]

Switchgear/Breaker Input States

Name	Description	Assignment via
Aux ON-I	indicator/shock back signal of the Pkr (52a)	[Control
		/SG
		/Pos Indicators wiring]
Aux OFF-I	Module input state: Position	[Control
	indicator/check-back signal of the Bkr. (52b)	/SG
		/Pos Indicators wiring]
Ready-I	Module input state: Bkr. ready	[Control
		/SG
		/Pos Indicators wiring]
Ack TripCmd-I	Signal (only for automatic acknowledgment) Module input signal	[Control
		/SG
		/Trip Manager]
Interl ON1-l	the ON command	[Control
		/SG
		/Interlockings]
Interl ON2-I		[Control
	the ON command	/SG
		/Interlockings]
Interl ON3-I	State of the module input: Interlocking of	[Control
	the ON command	/SG
		/Interlockings]
Interl OFF1-I	State of the module input: Interlocking of	[Control
	the OFF command	/SG
		/Interlockings]

Page 296 EN MRM4 12/11

Name	Description	Assignment via
Interl OFF2-I	State of the module input: Interlocking of	[Control
	the OFF command	/SG
		/Interlockings]
Interl OFF3-I	State of the module input: Interlocking of	[Control
	the OFF command	/SG
		/Interlockings]
Command e.g. the state of the Logic or the		[Control
	/SG	
,		/Ex ON/OFF Cmd]
SCmd OFF-I	SCmd OFF-I State of the module input: Switching OFF	[Control
	Command, e.g. the state of the Logic or the state of the digital input	/SG
	otato of the digital input	/Ex ON/OFF Cmd]

Switchgear/Breaker Signals (Outputs States)

Name	Description
SI SingleContactInd	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.
Pos not ON	Signal: Pos not ON
Pos ON	Signal: Breaker is in ON-Position
Pos OFF	Signal: Breaker is in OFF-Position
Pos Indeterm	Signal: Breaker is in Indeterminate Position
Pos Disturb	Signal: Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
Pos	Signal: Breaker Position (0 = Indeterminate, 1 = OFF, 2 = ON, 3 = Disturbed)
Ready	Signal: Breaker is ready for operation.
Interl ON	Signal: One or more IL_On inputs are active.
Interl OFF	Signal: One or more IL_Off inputs are active.
CES succesf	Command Execution Supervision: Switching command executed successfully.
CES Disturbed	Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.
CES Fail TripCmd	Command Execution Supervision: Command execution failed because trip command is pending.
CES SwitchgDir	Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.

Name	Description
CES ON d OFF	Command Execution Supervision: On Command during a pending OFF Command.
CES SG not ready	Command Execution Supervision: Switchgear not ready
CES Fiel Interl	Command Execution Supervision: Switching Command not executed because of field interlocking.
CES SyncTimeout	Command Execution Supervision: Switching Command not executed No Synchronization signal while t-sync was running.
TripCmd	Signal: Trip Command
Ack TripCmd	Signal: Acknowledge Trip Command
SGwear Slow SG	Signal: Alarm, the breaker (load-break switch) becomes slower
Res SGwear SI SG	Signal: Resetting the slow Switchgear Alarm
ON Cmd	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
OFF Cmd	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
ON Cmd manual	Signal: ON Cmd manual
OFF Cmd manual	Signal: OFF Cmd manual

Counters of the Switching Authority

Name	Description	Assignment via
CES SAuthority	Command Execution Supervision: Switching Command not executed. No switching authority.	
CES DoubleOperating	Command Execution Supervision: A second switch command is in conflict with a pending one.	
No. of rej. com. because Locked by ParaSystem	No. of rej. com. because Locked by ParaSystem	

Switchgear Wear

Switchgear Wear Features

The sum of the accumulated interrupted currents.

A »SGwear Slow Switchgear« might indicate malfunction at an early stage.

The protective relay will calculate the »SG OPEN Capacity« continuously. 100% means, that switchgear maintenance is mandatory now.

The protective relay will make a alarm decision based on the curve that the User provides.

The relay will monitor the frequency of ON/OFF cycles. The User can set thresholds for the maximum allowed sum of interrupt currents and the maximum allowed sum of interrupt currents per hour. By means of this alarm, excessive switchgear operations can be detected at an early stage.

Page 298 EN MRM4 12/11

Slow Switchgear Alarm

An increase of the close or opening time of the switchgear is an indication for the maintenance need. If the measured time exceeds the time *»t-Move OFF«* or *»t-Move ON«*, the signal *»*SGwear Slow Switchgear« will be activated.

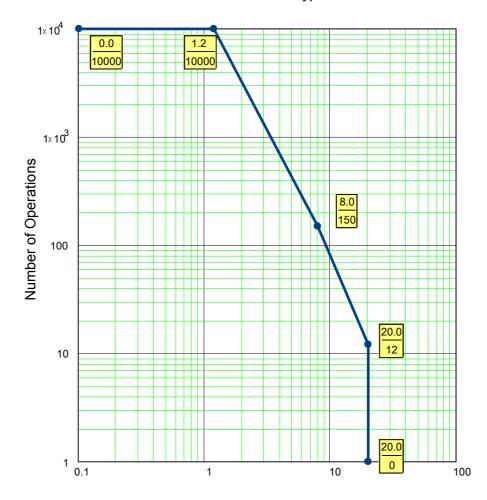
Switchgear Wear Curve

In order to keep the switchgear in good working condition, the switchgear needs to be monitored. The switchgear health (operation life) depends above all on:

- The number of CLOSE/OPEN cycles.
- The amplitudes of the interrupting currents.
- The frequency that the switchgear operates (Operations per hour).

The User has to maintain the switchgear accordingly to the maintenance schedule that is to be provided by the manufacturer (switchgear operation statistics). By means of up to ten points that the User can replicate the switchgear wear curve within menu [Control/SG/SG[x]/SGW] . Each point has two settings: the interrupt current in kilo amperes and the allowed operation counts. No matter how many points are used, the operation counts the last point as zero. The protective relay will interpolate the allowed operations based on the switchgear wear curve. When the interrupted current is greater than the interrupt current at the last point, the protective relay will assume zero operation counts.

Breaker Maintenance Curve for a typical 25kV Breaker



Interrupted Current in kA per operation

Global Protection Parameters of the Breaker Wear Module

Parameter	Description	Setting range	Default	Menu path
ReadIKTime Delay DeveloperOn ly	ReadIKTimeDelay DeveloperOnly	0.00 – 1.00 s	0.00 s	[Control /SG /SG Wear]
Operations Alarm	Service Alarm, too many Operations	1 - 100000	9999	[Control /SG /SG Wear]
Isum Intr Alarm	Alarm, the Sum (Limit) of interrupting currents has been exceeded.	0.00 – 2000.00 kA	100.00 kA	[Control /SG /SG Wear]
Isum Intr ph Alm	Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.	0.00 – 2000.00 kA	100.00 kA	[Control /SG /SG Wear]
SGwear Curve Fc	The Breaker (load-break switch) Wear Curve defines the maximum allowed CLOSE/OPEN cycles depending on the brake currents. If the breaker maintenance curve is exceeded, an alarm will be issued. The breaker maintenance curve is to be taken from the technical data sheet of the breaker manufacturer. By means of the available points this curve is to be replicated.	inactive, active	inactive	[Control /SG /SG Wear]
WearLevel Alarm	Threshold for the Alarm Only available if:SGwear Curve Fc = active	0.00 - 100.00%	80.00%	[Control /SG /SG Wear]
WearLevel Lockout	Threshold for the Lockout Level Only available if:SGwear Curve Fc = active	0.00 - 100.00%	95.00%	[Control /SG /SG Wear]
Current1	Interrupted Current Level #1 Only available if:SGwear Curve Fc = active	0.00 – 2000.00 kA	0.00 kA	[Control /SG /SG Wear]
Count1	Open Counts Allowed #1 Only available if:SGwear Curve Fc = active	1 - 32000	10000	[Control /SG /SG Wear]
Current2	Interrupted Current Level #2 Only available if:SGwear Curve Fc = active	0.00 – 2000.00 kA	1.20 kA	[Control /SG /SG Wear]
Count2	Open Counts Allowed #2 Only available if:SGwear Curve Fc = active	1 - 32000	10000	[Control /SG /SG Wear]

Page 300 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Current3	Interrupted Current Level #3	0.00 – 2000.00 kA	8.00 kA	[Control
	Only available if:SGwear Curve Fc = active			/SG
	,			/SG Wear]
Count3	Open Counts Allowed #3	1 - 32000	150	[Control
	Only available if:SGwear Curve Fc = active			/SG
	•			/SG Wear]
Current4	Interrupted Current Level #4	0.00 – 2000.00 kA	20.00 kA	[Control
	Only available if:SGwear Curve Fc = active			/SG
				/SG Wear]
Count4	Open Counts Allowed #4	1 - 32000	12	[Control
	Only available if:SGwear Curve Fc = active			/SG
				/SG Wear]
Current5	Interrupted Current Level #5	0.00 – 2000.00 kA	20.00 kA	[Control
	Only available if:SGwear Curve Fc = active			/SG
				/SG Wear]
Count5	Open Counts Allowed #5	1 - 32000	1	[Control
	Only available if:SGwear Curve Fc = active			/SG
				/SG Wear]
Current6	Interrupted Current Level #6	0.00 – 2000.00 kA	20.00 kA	[Control
	Only available if:SGwear Curve Fc = active			/SG
0	00	4 20000	4	/SG Wear]
Count6	Open Counts Allowed #6	1 - 32000	1	[Control
	Only available if:SGwear Curve Fc = active			/SG
Current7	Interrupted Current Level #7	0.00 – 2000.00 kA	20.00 kA	/SG Wear]
Current	interrupted Current Level #1	0.00 – 2000.00 KA	20.00 KA	[Control /SG
	Only available if:SGwear Curve Fc = active			/SG Wear]
Count7	Open Counts Allowed #7	1 - 32000	1	[Control
Countr		1 02000		/SG
	Only available if:SGwear Curve Fc = active			/SG Wear]
Current8	Interrupted Current Level #8	0.00 – 2000.00 kA	20.00 kA	[Control
				/SG
	Only available if:SGwear Curve Fc = active			/SG Wear]
Count8	Open Counts Allowed #8	1 - 32000	1	[Control
	Only available if:SGwear Curve Fc = active			/SG
	Orny available in Sowear ourver to - active			/SG Wear]
Current9	Interrupted Current Level #9	0.00 – 2000.00 kA	20.00 kA	[Control
	Only available if:SGwear Curve Fc = active			/SG
				/SG Wear]

Parameter	Description	Setting range	Default	Menu path
Count9	Open Counts Allowed #9	1 - 32000	1	[Control
	Only available if:SGwear Curve Fc = active			/SG
	only available in served server search			/SG Wear]
Current10	Interrupted Current Level #10	0.00 – 2000.00 kA	20.00 kA	[Control
	Only available if:SGwear Curve Fc = active			/SG
	only available incorrect converte assist			/SG Wear]
Count10	Open Counts Allowed #10	1 - 32000	1	[Control
	Only available if:SGwear Curve Fc = active			/SG
	only available in control out ve to addive			/SG Wear]

Breaker Wear Signals (Output States)

Name	Description
Operations Alarm	Signal: Service Alarm, too many Operations
Isum Intr trip: IL1	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1
Isum Intr trip: IL2	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2
Isum Intr trip: IL3	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3
Isum Intr trip	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.
Res TripCmd Cr	Signal: Resetting of the Counter: total number of trip commands
Res Sum trip	Signal: Reset summation of the tripping currents
WearLevel Alarm	Signal: Threshold for the Alarm
WearLevel Lockout	Signal: Threshold for the Lockout Level
Res SGwear Curve	Signal: Res SGwear Curve
Isum Intr ph Alm	Signal: Isum Intr ph Alm
Res Isum Intr ph Alm	Signal: Res Isum Intr ph Alm

Breaker Wear Counter Values

RevData
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Page 302 EN MRM4 12/11

Breaker Wear Values

Value	Description	Default	Size	Menu path
Sum trip IL1	Summation of the tripping currents phase	0.00 A	0.00 – 1000.00 A	[Operation
				/Count and RevData
				/Ctrl
				/SG]
Sum trip IL2	Summation of the tripping currents phase	0.00 A	0.00 – 1000.00 A	[Operation
				/Count and RevData
				/Ctrl
				/SG]
Sum trip IL3	Summation of the tripping currents phase	0.00 A	0.00 – 1000.00 A	[Operation
				/Count and RevData
				/Ctrl
				/SG]
Isum Intr per hour	Sum per hour of interrupting currents.	0.00 kA	0.00 – 1000.00 kA	[Operation
				/Count and RevData
				/Ctrl
				/SG]
SG OPEN capacity	Used capacity. 100% means, that the	0.0%	0.0 - 100.0%	[Operation
	switchgear is to be maintenanced.			/Count and RevData
				/Ctrl
				/SG]

Direct Commands of the Breaker Wear Module

Parameter	Description	Setting range	Default	Menu path
Res TripCmd		inactive,	inactive	[Operation
Cr	commands	active		/Reset/Acknowledge
				/Reset]
Res Sum trip	Reset summation of the tripping currents	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/Reset]
Res Isum Intr	Reset of the Sum per hour of interrupting currents.	inactive,	inactive	[Operation
per hour		active		/Reset/Acknowledge
				/Reset]
Res Bkr.	Resetting of the Bkr. OPEN capacity. 100%	inactive,	inactive	[Operation
OPEN capacity	means, that the breaker is to be maintenanced.	active		/Reset/Acknowledge
35,537,7				/Reset]

Control Parameters

<u>Ctrl</u>

Direct Commands of the Control Module

Parameter	Description	Setting range	Default	Menu path
Switching	Switching Authority	None,	Local	[Control
Authority		Local,		/General settings]
		Remote,		
		Local and Remote		
NonInterl	DC for Non-Interlocking	inactive,	inactive	[Control
		active		/General settings]

Global Protection Parameters of the Control Module

Parameter	Description	Setting range	Default	Menu path
Res NonIL	Resetmode Non-Interlocking	single Operation,	single Operation	[Control
		Timeout,		/General settings]
		permanent		
Timeout	Timeout Non-Interlocking	2 – 3600 s	60 s	[Control
NonIL	Only available if: Res NonIL = permanent			/General settings]
NonIL Assign	Assignment Non-Interlocking	1n, Assignment List		[Control
				/General settings]

Signals of the Control Module

Name	Description
Local	Switching Authority: Local
Remote	Switching Authority: Remote

Assignable Trip Commands (Trip Manager)

Name	Description
7-7	No assignment
MStart.TripCmd	Signal: Trip Command
I[1].TripCmd	Signal: Trip Command
I[2].TripCmd	Signal: Trip Command
I[3].TripCmd	Signal: Trip Command
I[4].TripCmd	Signal: Trip Command
I[5].TripCmd	Signal: Trip Command
I[6].TripCmd	Signal: Trip Command

Page 304 EN MRM4 12/11

Name	Description
IG[1].TripCmd	Signal: Trip Command
IG[2].TripCmd	Signal: Trip Command
IG[3].TripCmd	Signal: Trip Command
IG[4].TripCmd	Signal: Trip Command
12>[1].TripCmd	Signal: Trip Command
12>[2].TripCmd	Signal: Trip Command
ThR.TripCmd	Signal: Trip Command
Jam[1].TripCmd	Signal: Trip Command
Jam[2].TripCmd	Signal: Trip Command
I<[1].TripCmd	Signal: Trip Command
I<[2].TripCmd	Signal: Trip Command
I<[3].TripCmd	Signal: Trip Command
RTD.TripCmd	Signal: Trip Command
ExP[1].TripCmd	Signal: Trip Command
ExP[2].TripCmd	Signal: Trip Command
ExP[3].TripCmd	Signal: Trip Command
ExP[4].TripCmd	Signal: Trip Command

Trigger Signals for Sync-check



NOTICE: The availability depends on the ordered device.

Control - Example: Switching of a Breaker

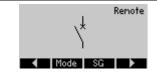
The following example shows how to switch a breaker via the HMI at the device.



Change into the menu »Control« or alternatively push the »CTRL« button at the device front.

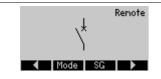


Change to the control page by pushing the »right arrow« softkey.



Information only: On the control page the current switchgear positions is displayed. By means of the softkey »Mode« it can be switched to the menu »General Settings«. In this menu switching authority and interlockings can be set

By means of the softkey »SG« it can be switched to the menu »SG«. In this menu specific settings for the switch gear can be done.



To execute a switching operation, change into the switching menu by pushing the right arrow softkey button.



Executing a switching command via the devices HMI is only possible when the switching authority is set to »Local«. If no switching authority is given, this has to be set first to »Local« or »Local and Remote«.

With the softkey »OK« it can be switched back to the single line diagram page.



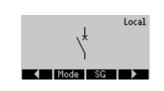
Pushing the softkey »Mode« leads to the menu »General Settings«.



In this menu the switching authority can be changed.



Select between »Local« or »Local and Remote«.



Now it is possible to execute switching commands at the HMI.



Push the »right arrow« softkey to get to the control page.

Page 306 EN MRM4 12/11



The breaker is opened, therefore it can be closed only.

After pushing the softkey »CLOSE« a confirmation window appears.



When you are sure to proceed with the switching operation, press the softkey »YES«.



The switching command will be given to the breaker. The display shows the intermediate position of the switchgear.



It will be shown on the display when the switchgear reaches the new end position. Further possible switching operations (OPEN) will be displayed by softkeys.



Notice: For the case, the switchgear does not reach the new end position within the set supervision time the following Warning appears on the display.

Protective Elements

MStart - Motor Starting and Control [48,66]

Available elements: MStart

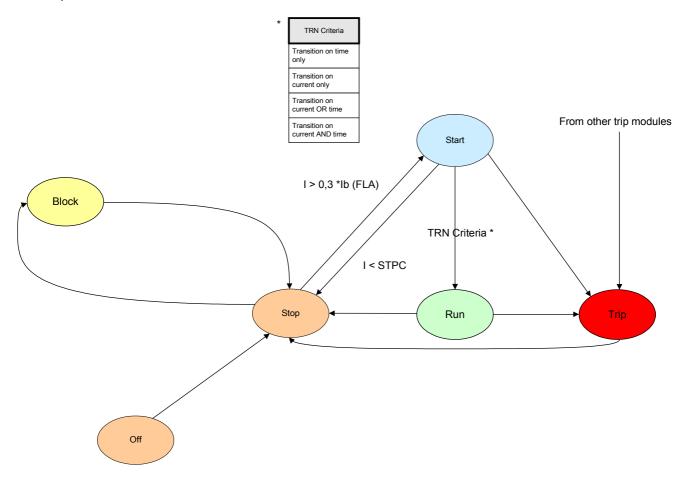
General - Principle Use

The motor start control logic is the core control and protective function for a motor protection device. The logic comprises:

- Motor Operation States,
- Motor Start Control
- Motor Start Blockings
- Motor Start / Transition Trips
- Motor Cold Warm Detection
- Emergency Override.

Motor Operation States

Motor Operation States



The basic motor operation states can be classified as four states that include:

1.Start cycle;

Page 308 EN MRM4 12/11

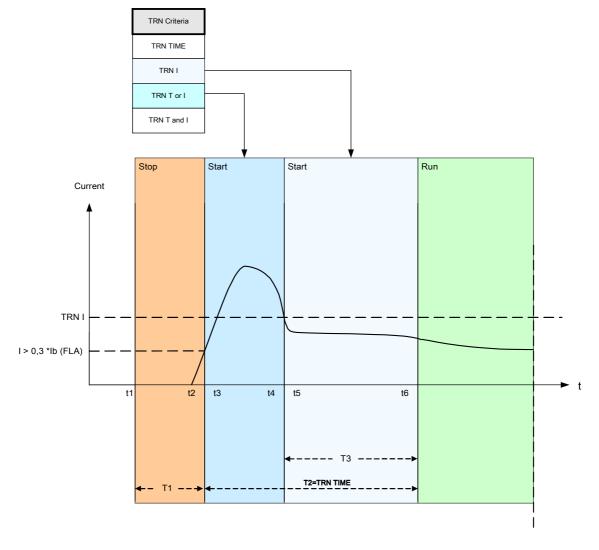
- 2.Run cycle;
- 3.Stop cycle; and
- 4. Trip state.

Under normal conditions, the motor operations should go through »stop«, »start«, »run«, and »stop« cycles that are referred to as a complete operation sequence; while under certain abnormal conditions, the motor could go from »start« to »stop«, or »start« to »trip«, or »run« to »trip«.

If other protection trips occur at either the »start« or »run« cycle, the motor will be forced to go to »trip« mode. After motor currents are terminated, the motor will go into the »stop« cycle.

Start Control

The parameters for the Start Control have to be set within menu [Protection Para\MStart\StartControl]



T1: Stop Cycle

t4-t3: Start Cycle if TRNC is selected t6-t3: Start Cycle if TRNT is selected

The Start Control Module drawing shows an example of how the protective device reacts to a normal operating-cycle current profile. Initially, the motor is stopped and the current is zero. As long as the protective device is not in a »trip« state, it permits contactor energization by closing its trip contact in series with the contactor. The contactor is energized by the operator or process control system through a normal two-wire or three-wire motor control scheme, external to the protective device. The protective device declares a motor start when it senses a motor current that exceeds 30% of the »*Ib«* (FLA) setting. Meanwhile, the transition timer »*TRNT«* begins to run. The protective device also monitors the large starting current, noting when the current falls below the transition level »*TRNC«*.

The Start to Run transition is based on the setting »TRN Criteria«, which has four transition behaviors for the User to select:

- TRN T Transition to RUN after time setting TRNT only. Current is ignored.
- TRN C Transition when starting current drops below the setting only. If the time set in TRNT expires before the current transition, the motor trips.
- TRN T or C Transition on time or current, whichever comes first.
- TRN T and C- Transition on time and current. Both must occur, and the current must drop below the setting before the time delay expires. If the timer expires before the current falls below the set transition level, the motor trips.

If there is no transition trip, the protective device relay declares a successful transition to »RUN« cycle and the corresponding transition signal(s) (current or time, or both, depending on the settings and motor current) is set. The transition signal(s) is the part of the global output list, which can be assigned to any module input or relay output. If it is assigned to a relay output, it can control a reduced-voltage starter, switching to full running voltage.

Even if the transition control output contact is not used, the transition function can provide clear indications of the actual state of the motor (»START« versus »RUN«) on the front panel display and via data communications. A good way to do this is to use the settings of TRN Criteria = TRN T or C and TRNC = 130% of »*lb*« (FLA). Modify the latter, if needed, to lie at a transition value between the starting current and post-start maximum load current. Set the transition timer well beyond the normal start time to avoid a transition trip.

Start Delays

The parameters for the Start Delays have to be set within menu [Protection Para\MStart\Start Delay Timer]

When the protective device declares a »START«, all start timers of the enabled functions begin to time. Each of these timers blocks the respective function until the set delay expires. These start timers are affected by transitions - they run for the set time, which may be less than or greater than the time of transition. These start delay timers include:

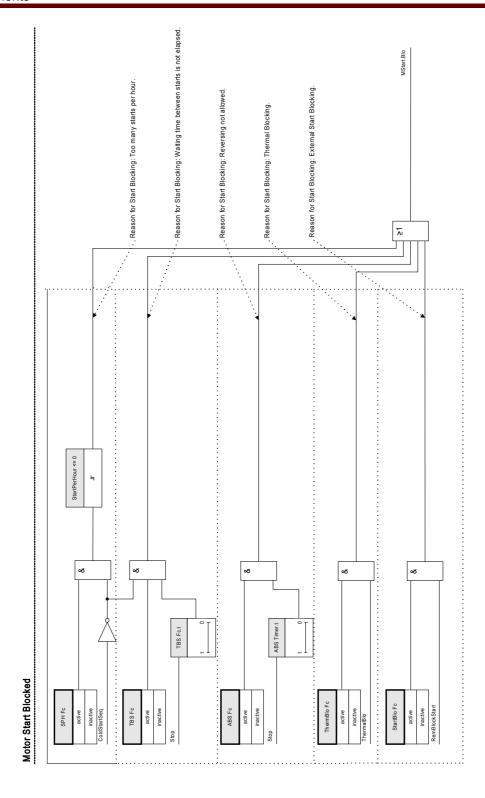
- IOC (Instantaneous overcurrent start delay);
- GOC (Ground fault start delay);
- UnderLoad (Under load trip and alarm start delay);
- IUnbalance (Current unbalance trip and alarm start delay);
- JAM (Jam trip and alarm start delay); and
- Generic1 to Generic5 (Generic start delay).

Note that the generic start delays are not tied to anything, and they can be used to block anything at the User's choice.

Motor Start Blocked

A Motor Start can be blocked by certain events, if any of the following conditions are noted - motor starts limit, starting frequency, thermal and mechanical constraints. The User may choose to use the states to block the motor from starting or use it as an alarm or indication.

Page 310 EN MRM4 12/11



Blocking Conditions

The reasons for a Motor Start Blocking are as follows:

The Motor Start will be blocked due to:

- There are too many starts per hour (if configured).
- The waiting time between starts is not elapsed (if configured).
- If the Anti Backspin protection detects a reversing of the motor (reversing not allowed, if configured).
- The thermal model blocks the motor (if configured).
- External Blocking becomes active (if configured).

When any of Anti-Backspin, thermal, and external blocks are on, the »MStart.Blo« signal will be set. The »TBS« and »SPH« can turn on the »MStart.Blo« signal only if the motor is not in a cold start sequence; »NOCS« block can not cause the »MStart.Blo« signal to be set.

Start Limits

Because motor starting consumes a considerable amount of thermal energy compared to its normal load conditions, the number of starts in a given time period must be monitored and controlled. The protective device has three functions that contribute to the start limits monitoring. These are:

- TBS (Time between Starts);
- SPH (Starts per Hour); and
- NOCS (Number of Cold Starts).

Most motors can tolerate some number of consecutive cold starts before the time between starts is enforced. The protective device treats a start as the first in a sequence of cold starts if the motor has been stopped for at least the time period that is the greatest of *»one hour«* and *»TBS«*. Subsequent starts are treated as additional cold starts in the same sequence, only if they run no more than ten minutes, until the set number of cold starts is reached. Once the motor is in the cold starting sequence, it will ignore *»TBS«* and *»SPH«* limits. The cold start sequence will be terminated if the motor has run for more than ten minutes for a cold start before it exhausts *»NOCS«*, then starts after this are subject to time and count limits imposed by *»TBS«* and *»SPH«*. If the motor reaches the *»NOCS«* limit in a cold start sequence, *»NOCS«* block signal will be set and *»TBS«* will start to time. When *»TBS«* reaches its limit while the *»NOCS«* block signal is still set, the cold start sequence will be terminated and the *»NOCS«* block will be released. Meanwhile, the *»SPH«* will start to count at the last start in the complete cold start sequence.

Stop Cycle

The run cycle continues until the motor current level falls below the Stop Current Threshold setting current on all three phases. Then a stop is declared. The start limits (also referred as Jogging start limits) and the anti-backspin time delay (ABS) are checked. If blocking conditions exist, the protective device can be configured to block a motor from starting. Remaining jogging block times are displayed and counted down, indicating how long to wait. If there are no such starting block conditions in effect, the protective device is ready for a new start.

Anti-Backspin Delay Time (ABS)

»ABS« sets the time in seconds before a motor restart is permitted after a trip or stop condition. This function can be set to »inactive«.

This function is used with a motor driving a pump working into a head, or any other load that tends to spin in a reverse direction (backspin) when the motor is de-energized. It blocks starting during the time when the motor might be rotating in reverse following a trip. Also, this function may be used simply to set idle time (time between stop and start) before a restart is permitted.

Page 312 EN MRM4 12/11

External Start Blocking

A motor can be blocked through a digital input. If this feature is enabled, the User must make sure that both the Motor Start and Digital Input modules are configured properly.

Thermal Block

Besides the previously mentioned start monitoring and controlling means, the motor can be blocked if the thermal capacity used exceeds the alarm level. It is the User's choice to turn on or off this feature and set an appropriate alarm level in the thermal model module.

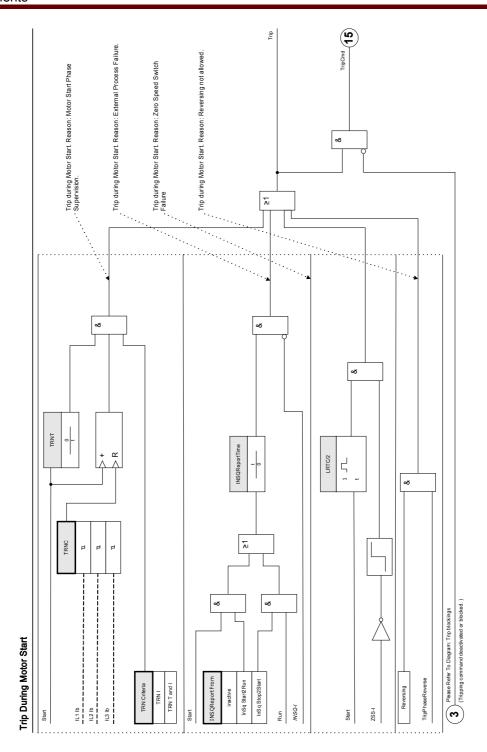
Forced Starting

It is recommended that the User wires the »MSTART.BLO« output to the motor trip circuit for preventing the motor from starting under these blocked conditions. If the User chooses not to do this for their applications, a Forced Starting signal will be set when the motor is started with the blocked conditions. This signal can only be reset manually though *Smart View* or from the front panel (please refer to section Emergency Override).

Motor Start / Transition Trips

The Motor will be tripped during the start phase, in case that:

- The Start Control detects an unsuccessful Start. (Please see section Start Control Module)
- There is an Incomplete Start Sequence. The device detects via an digital input, that the external process is not properly started.
- If a reverse direction is detected but reversing is not allowed.
- If case of a Zero Speed Switch failure.



Incomplete Sequence Report Back Time (INSQ)

The incomplete sequence function requires a report back contact (via digital input) from the process that the motor runs - any indication that the process has started to operate as expected some time after the motor start. If the process does not start up correctly, the contact does not close within the expected time. If a problem develops later on, the report back contact opens. In either case, the open contact state indicates that the motor should be tripped.

To use this function, set a time limit for report back here and define the start of report back timing. Connect the report-back contact to one of the protective device Digital Inputs. If this input is not energized before the set time expires, the relay will trip for incomplete sequence.

Note that the input must be energized continuously after the time delay has expired to hold off this trip.

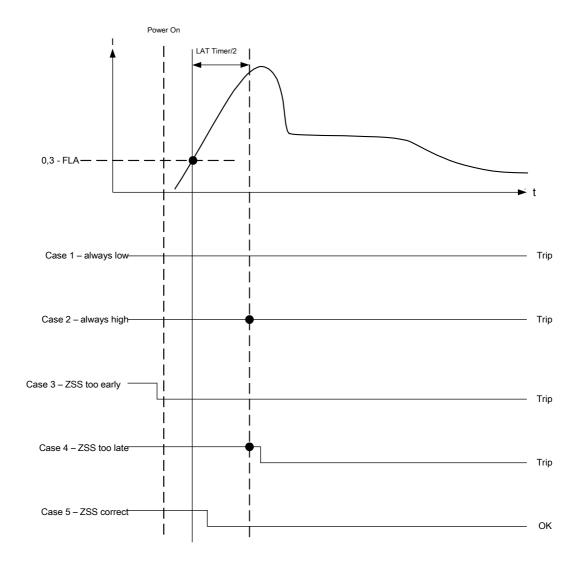
Page 314 EN MRM4 12/11

Zero Speed Switch (ZSS ON or OFF)

ZSS enables the function that verifies if the motor begins to physically spin after a start. It requires a zero-speed switch (digital switch) on the motor, which is closed at rest and opens as the rotor reaches (5%-10%) its normal speed. Connect the zero-speed switch contact to one of the protective device Digital Inputs. If the contact fails to open within »LRT/2« (one-half of locked-rotor time) after a start, the relay trips with a zero-speed switch trip message.

This protection is always useful, but is essential if the Long Acceleration Time (LAT) function setting is used.

With ZSS being enabled and being mapped to one of the digital inputs, the protective device checks the ZSS input status at the very moment it sees a start - it wants to sense the initially closed zero-speed switch, which opens shortly thereafter as the motor spins. If it fails to find the closed contact, it trips immediately. Check the wiring and contact for problems.



Long Acceleration Time (LAT)

When the LAT function is enabled, the »LAT« timer is used to set a time interval during which the motor is permitted to accelerate a high-inertia load, which is longer than the locked-rotor time. This function can be (and usually should be) set to »inactive«. If the thermal-model accumulator bucket fills to 100% during the long acceleration time, it is limited to that value and the thermal trip is held off until the LAT timer expires. By then, the thermal bucket level must have decreased (thermal model cooled) below 100% or the motor trips.

The LAT function should be used but not limited only on motors with a zero-speed switch (a normally-closed contact that opens when the motor actually begins to spin). Connect the zero-speed switch contact to one of the protective device Digital Inputs. The Zero-Speed Switch function must be enabled (ZSS ON). The protective device requires the zero-speed switch to open within LRT/2 (one-half of locked-rotor time) after a start, or the motor is tripped by the ZSS function. This protects a completely stalled motor from being damaged when the LAT timer blocks the locked-rotor thermal trip.

CAUTION

The long acceleration time (LAT) function can block the critical LRC-LRT rotor thermal protection during a start and destroy the motor. Turn LAT OFF unless absolutely needed and the motor's suitability for this starting duty has been confirmed. Use only with zero speed switch function ZSS ON and switch input connected to protect a stalled motor.

The User can temporarily defeat the I2t thermal protection limit after a start by setting a Long Acceleration Time delay. This can be a dangerous setting that blocks thermal tripping and holds the bucket at a 100% level if the load takes a long time to reach running speed. An example is a motor spinning a large centrifuge. In using LAT, the User can take advantage of the partial cooling from airflow produced by the motor spinning at below-normal speed, as compared to unfanned heating of a locked rotor. The motor must be rated for this severe starting duty. Also, the User must ensure that the motor actually has begun to spin well before the locked-rotor time has expired. This is accomplished by connecting a zero-speed switch to a Digital Input and turning on ZSS function. The zero-speed switch is a contact that is closed when the motor is at rest, and opens as the motor begins to spin, usually at 5-10% of running speed. If ZSS is set to ON and the protective device relay does not sense the contact open in one-half the locked-rotor time setting, it trips the motor.



Turn OFF LAT unless the application specifically demands it. Use a zero speed switch with LAT. Using an LAT setting greater than locked rotor time without a zero speed switch temporarily defeats thermal protection and damages the motor if the rotor actually is locked.

If »LAT« is used, check the settings of transition time »TRNT« and jam start delay to be sure they are coordinated with the prolonged starting cycle.

Anti-Backspin Delay Time (ABS)

»ABS« sets the time in seconds before a motor restart is permitted after a trip or stop condition. This function can be set to »inactive«.

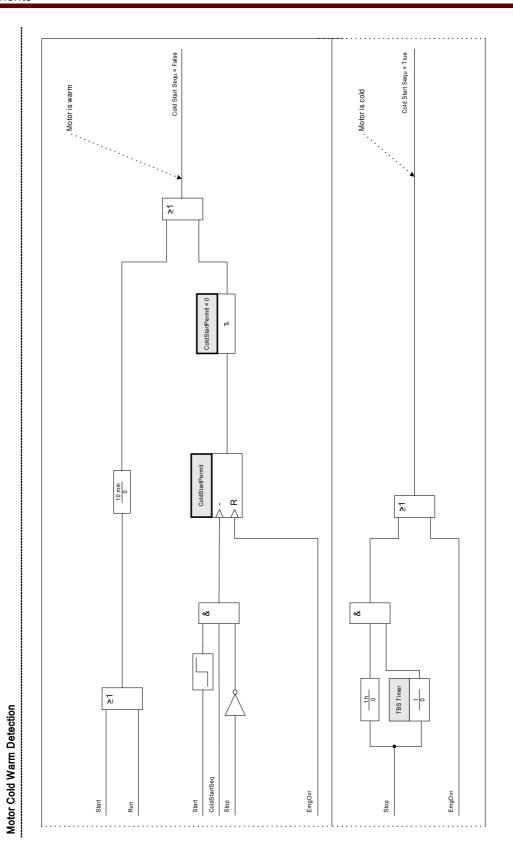
This function is used with a motor driving a pump working into a head, or any other load that tends to spin in a reverse direction (backspin) when the motor is de-energized. It blocks starting during the time when the motor might be rotating in reverse following a trip. Also, this function may be used simply to set idle time (time between stop and start) before a restart is permitted.

Motor Cold Warm Detection

The motor will be considered as cold (»Cold sequ = True«) after being in the »stop« mode for more than one hour if the time between starts timer is set to a lower value than 1 hour.

Else, the motor will fall back into the »cold« state if the time between starts timer is elapsed. By means of the Emergency Override function, the motor can be forced to switch to the cold state.

Page 316 EN MRM4 12/11



Emergency Override

The Emergency Override function can be enabled or disabled in the following menu [Protection Para\Global Prot Para\MStart\Start Control\EMGOVR]. Also it can be determined whether this function can be executed by a DI or by a softkey at the HMI or both.

If enabled, an emergency override can be executed by pushing the »*Emrg Override«* Softkey at the front panel. In any case, an emergency override can be performed by a remote contact connected to any one of the digital inputs programmed as »*EMG OVR«*, or via front panel under [Operations\Reset\EMGOVR] menu. The asshipped setting is disabled.

Emergency override allows a panic restart of a tripped motor without completely disabling protection. When the override request is received, the thermal-model accumulator bucket is drained to its initial level of 40°C (104°F). Cold starts are fully restored.

The motor protection is now in the state it would be in if the motor had been standing for a long time prior to the moment of the override. This allows an immediate restart of the motor. The override can also delay an impending thermal trip of a running motor. The emergency override action is counted in the history record, and noted with its time tag in the logbook record.

CAUTION

The emergency override function clears and restarts all protective functions of the protective device. Using this function can damage the motor. Use it only for true emergencies, when it is known what caused the trip. Override permits the risk of motor damage to avoid an even more dangerous process situation caused by the tripping of the motor.

Global Protection Parameters of the Motor Start Module

Parameter	Description	Setting range	Default	Menu path
Reversing		inactive,	inactive	[Field Para
	affect the sequence current calculations.	active		/Motor]
lb	Full load current (amperes). Set to maximum stator continuous RMS current primary (actual motor winding) amperes in each phase. Use motor nameplate or manufacturers data. Note that the ratio lb/CT prim must lie between 0.25 and 1.5 in order to have reliable motor protection.	10 – 6000 A	10 A	[Field Para /Motor]
LRC	Set to the locked-rotor current (the current the motor draws when stalled), in times of lb. Use motor nameplate or manufacturers data.	3.00 – 12.00 lb	3.00 lb	[Field Para /Motor]
LRTC	Specifies how long a locked-rotor or stall condition can be maintained before the motor is damaged, in seconds, for a cold start. Use motor nameplate or manufacturers data.	1 – 120 s	1 s	[Field Para /Motor]
STPC	Stop current threshold, in percent of lb, if the actual current is below the threshold for at least 300 milliseconds. If a stop state occurs, the jogging functions Starts per Hour Allowed (SPH), Time Between Starts (TBS) and Anti-Backspin (ABS) are enforced. All phases of the current must be below this level before a stop will be declared.	0.02 – 0.20 lb	0.02 lb	[Field Para /Motor]
k-Factor	The k-Factor is to be calculated by the maximum allowed continuous current over the rated current transformer current (e.g. 1.2 times rated motor current over rated transformer current).	0.25 - 1.50	0.85	[Field Para /Motor]

Page 318 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
StartBlo Fc	StartBlo Fc	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/MStart
				/Start Control]
ThermBlo Fc	ThermBlo Fc	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/MStart
				/Start Control]
TRN Criteria	Start transition criterion	TRN I,	TRN T and I	[Protection Para
		TRN TIME,		/Global Prot Para
		TRN T and I,		/MStart
		TRN T or I		/Start Control]
TRNT	Motor start transition time limit	0 – 1200 s	10 s	[Protection Para
	Only available if: TRN Criteria = TRN T and I Or			/Global Prot Para
	TRN Criteria = TRN TIME			/MStart
				/Start Control]
TRNC	Motor start transitions current level in lb%	0.10 – 3.00 lb	1.30 lb	[Protection Para
	Only available if: TRN Criteria = TRN T and I Or			/Global Prot Para
	TRN Criteria = TRN I			/MStart
				/Start Control]
NOCS	Number of cold starts limit	1 - 5	1	[Protection Para
				/Global Prot Para
				/MStart
				/Start Control]
TBS Fc	Time Between Starts on/off	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/MStart
				/Start Control]
TBS Timer	Time Between Starts Limit	1 – 240 min	60 min	[Protection Para
	Only available if: TBS Fc = active			/Global Prot Para
				/MStart
				/Start Control]
SPH Fc	Starts Per Hour	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/MStart
				/Start Control]

Parameter	Description	Setting range	Default	Menu path
SPH	SPH	1 - 10	1	[Protection Para
	Only available if: SPH Fc = active			/Global Prot Para
	J., a.aa.a			/MStart
				/Start Control]
INSQReport	INcomplete SeQuence report time starting point	inactive,	inactive	[Protection Para
From		InSq Start2Run,		/Global Prot Para
		InSq Stop2Start		/MStart
				/Start Control]
INSQReport	INSQ Report back time	1 – 240 s	1 s	[Protection Para
Time	Only available if: INSQReportFrom = active			/Global Prot Para
	only available ii. iivoqroporti forii addive			/MStart
				/Start Control]
LAT Fc	Long Time Acceleration Timer	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/MStart
				/Start Control]
LAT Timer	Large motors with a high inertia may experience starting currents that exceed the locked rotor current and time. The protective relay has logic and provisions for a zero speed switch input to	1 – 1200 s	1200 s	[Protection Para
				/Global Prot Para
				/MStart
	differentiate between a stall and start condition. If the motor is spinning then the relay will not trip on			/Start Control]
	the normal locked rotor time allowing the motor to			
	start.			
	Only available if: LAT Fc = active			
ABS Fc	For certain applications, such as pumping a fluid	inactive,	inactive	[Protection Para
	up a pipe, the motor may be driven backward for a period of time after it stops. The protective relay	active		/Global Prot Para
	provides an anti-backspin timer to prevent starting			/MStart
	the motor while it is spinning in the reverse direction. The timer begins counting from the			/Start Control]
	moment a stop is declared by the relay.			
ABS Timer	For certain applications, such as pumping a fluid	1 – 3600 s	3600 s	[Protection Para
	up a pipe, the motor may be driven backward for a period of time after it stops. The protective relay			/Global Prot Para
	provides an anti-backspin timer to prevent starting			/MStart
	the motor while it is spinning in the reverse direction. The timer begins counting from the			/Start Control]
	moment a stop is declared by the relay.			
	Only available if: ABS Fc = active			
ZSS	Zero Speed Switch	inactive,	inactive	[Protection Para
		active		/Global Prot Para
				/MStart
				/Start Control]

Page 320 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
EMGOVR	active in order to release the thermal capacity of the motor. Please notice that by doing this you run the risk of damaging the motor. "EMGOVR" has to	inactive,	inactive	[Protection Para
		DI,		/Global Prot Para
		HMI,		/MStart
	be set to "DI" or "DI or UI" for this input to take effect.	DI or HMI		/Start Control]
Start Signal	Motor Start Signal. User can tie a digital input to	,		[Protection Para
	this Input. If "Start-I" becomes true, "StartMotorCommand" becomes true for at least	DI Slot X1.DI 1,		/Global Prot Para
ı	500ms.	DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
ı		DI Slot X1.DI 4,		
ı		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
Stop	Stop Motor Signal	,		[Protection Para
		DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
1		DI Slot X1.DI 6,		
1		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		

Parameter	Description	Setting range	Default	Menu path
StartBlock	Start Motor Signal	,		[Protection Para
	Only available if: StartBio FC = active	DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
EmgOvr	order to release the thermal capacity of the motor. Please notice that by doing this you run the risk of damaging the motor. "EMGOVR" has to be set to "DI" or "DI or III" for this input to take effect	,		[Protection Para
		DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		

Page 322 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
INSQ	INcomplete SeQuence	,		[Protection Para
		DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
ThermSwitch	Therm Switch	,		[Protection Para
		DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		

Parameter	Description	Setting range	Default	Menu path
ZSS	Zero Speed Switch	,		[Protection Para
	Only available if: ZSS = active	DI Slot X1.DI 1,		/Global Prot Para
	3, a.a	DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
STPC Blo	With this setting a Digital Input keeps the Motor in	,		[Protection Para
	the RUN mode, even when the motor current drops below STPC (motor stop current).	DI Slot X1.DI 1,		/Global Prot Para
	urops below 311 C (motor stop current).	DI Slot X1.DI 2,		/MStart
		DI Slot X1.DI 3,		/Motor Inputs]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
t-Blo-IOC	Phase Overcurrent Start Delay. Phase	0.03 – 1.00 s	0.05 s	[Protection Para
	Overcurrent elements are blocked for the time programmed under this parameter, while the			/Global Prot Para
	motor is starting.			/MStart
				/Start Delay Timer]
t-Blo-GOC	Ground Overcurrent Start Delay. Ground	0.03 – 1.00 s	0.08 s	[Protection Para
I-BIO-GOC	Overcurrent elements are blocked for the time programmed under this parameter, while the motor is starting	1.00 0	5.00 0	/Global Prot Para
				/MStart
	motor is starting			/Start Delay Timer]
t-Blo-I<	Under load Start Delay. 37[x] elements are	0 – 1200 s	60 s	[Protection Para
ר-טוט-ו	blocked for the time programmed under this	0 - 1200 3	00 3	/Global Prot Para
	parameter, while the motor is starting			/MStart
				/Start Delay Timer]

Page 324 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
t-Blo-	Current Unbalance Start Delay. 46[x] elements	0.03 – 1200.00 s	10.00 s	[Protection Para
IUnbalance	are blocked for the time programmed under this parameter, while the motor is starting			/Global Prot Para
	parameter, willow the motor to starting			/MStart
				/Start Delay Timer]
t-Blo-JAM	Jam Start Delay. 50J[x] elements are blocked for	0.03 – 1200.00 s	60.00 s	[Protection Para
	the time programmed under this parameter, while the motor is starting			/Global Prot Para
	and motion to starting			/MStart
				/Start Delay Timer]
t-Blo-	t-Blo-Generic1	0 – 1200 s	0 s	[Protection Para
Generic1				/Global Prot Para
				/MStart
				/Start Delay Timer]
t-Blo-	t-Blo-Generic2	0 – 1200 s	0 s	[Protection Para
Generic2				/Global Prot Para
				/MStart
				/Start Delay Timer]
t-Blo-	t-Blo-Generic3	0 – 1200 s	0 s	[Protection Para
Generic3				/Global Prot Para
				/MStart
				/Start Delay Timer]
t-Blo-	t-Blo-Generic4	0 – 1200 s	0 s	[Protection Para
Generic4				/Global Prot Para
				/MStart
				/Start Delay Timer]
t-Blo-	t-Blo-Generic5	0 – 1200 s	0 s	[Protection Para
Generic5				/Global Prot Para
				/MStart
				/Start Delay Timer]

Motor Start Module Input States

Name	Description	Assignment via
ThermalBlo-I	State of the module input: ThermalBlo	
Start Signal-I	State of the module input: Motor Start	[Protection Para
	Signal. User can tie a digital input to this Input. If "Start-I" becomes true,	/Global Prot Para
	"StartMotorCommand" becomes true for at	/MStart
	least 500ms.	/Motor Inputs]

Name	Description	Assignment via
Stop-I	State of the module input: Stop Motor	[Protection Para
	Signal	/Global Prot Para
		/MStart
		/Motor Inputs]
StartBlock-I	State of the module input: Start Motor	[Protection Para
	Signal	/Global Prot Para
		/MStart
		/Motor Inputs]
EmgOvr-I	State of the module input: Emergency	[Protection Para
	Override. Signal has to be active in order to release the thermal capacity of the motor.	/Global Prot Para
	Please notice that by doing this you run the	/MStart
	risk of damaging the motor. "EMGOVR" has to be set to "DI" or "DI or UI" for this input to take effect	/Motor Inputs]
INSQ-I	SeQuence	[Protection Para
		/Global Prot Para
		/MStart
		/Motor Inputs]
ThermSwitch-I	State of the module input: Therm Switch	[Protection Para
		/Global Prot Para
		/MStart
		/Motor Inputs]
ZSS-I	State of the module input: Zero Speed	[Protection Para
	Switch	/Global Prot Para
		/MStart
		/Motor Inputs]
STPC Blo-I	State of the module input: With this setting	[Protection Para
	mode, even when the motor current drops	/Global Prot Para
		/MStart
		/Motor Inputs]

Motor Start Module Signals (Output States)

Name	Description
active	Signal: active
Blo TripCmd	Signal: Trip Command blocked
Trip	Signal: Trip
TripCmd	Signal: Trip Command
Start	Signal: Motor is in start mode
Run	Signal: Motor is in run mode

Page 326 EN MRM4 12/11

Name	Description
Stop	Signal: Motor is in stop mode
Blo	Signal: Motor is blocked for starting or transition to Run mode
NOCSBlocked	Signal: Motor is prohibited to start due to number of cold start limits
SPHBlocked	Signal: Motor is prohibited to start due to starts per hour limits
SPHBlockAlarm	Signal: Motor is prohibited to start due to starts per hour limits, would come active in the next stop
TBSBlocked	Signal: Motor is prohibited to start due to time between starts limits
ThermalBlo	Signal: Thermal block
RemBlockStart	Signal: Motor is prohibited to start due to external blocking through digital input DI
TransitionTrip	Signal: Start transition fail trip
ZSSTrip	Signal: Zero speed trip (possible locked rotor)
INSQSP2STFaill	Signal: Fail to transit from stop to start based on reported back time
INSQSt2RunFail	Signal: Fail to transit from start to run based on reported back time
LATBlock	Signal: Long acceleration timer enforced
ColdStartSeq	Signal: Motor cold start sequence flag
ForcedStart	Signal: Motor being forced to start
TripPhaseReverse	Signal: Relay tripped because of phase reverse detection
EmergOverrideDI	Signal: Emergency override start blocking through digital input DI
EmergOverrideUI	Signal: Emergency override start blocking through front panel
ABSActive	Signal: Anti-backspin is active. For certain applications, such as pumping a fluid up a pipe, the motor may be driven backward for a period of time after it stops. The anti-backspin timer prevents starting the motor while it is spinning in the reverse direction.
Blo-GOCStart	Signal: Ground Instantaneous Overcurrent Start Delay. GOC (Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
Blo-IOCStart	Signal: Phase Instantaneous Overcurrent Start Delay. IOC (Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
Blo-I <start< td=""><td>Signal: Under load Start Delay. Under load(Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter</td></start<>	Signal: Under load Start Delay. Under load(Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
Blo-JamStart	Signal: JAM Start Delay. JAM(Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
Blo-UnbalStart	Signal: Motor start block current unbalance signal
Blo-Generic1	Generic Start Delay. This value can be used to block any protective element.1
Blo-Generic2	Generic Start Delay. This value can be used to block any protective element.2
Blo-Generic3	Generic Start Delay. This value can be used to block any protective element.3

Name	Description
Blo-Generic4	Generic Start Delay. This value can be used to block any protective element.4
Blo-Generic5	Generic Start Delay. This value can be used to block any protective element.5
I_Transit	Signal: Current transition signal
T_Transit	Signal: Time transition signal
StartMotorCmd	Signal: Start motor command
MotorStopBlo	Signal: Motor stop block other protection functions
Rotating forward	Signal: Rotation Direction forward
Rotating backward	Signal: Rotation Direction reverse

Direct Commands of the Motor Start Module

Parameter	Description	Setting range	Default	Menu path
EmergOverH	Emergency override through front display	inactive,	inactive	[Operation
MI	Only available if: EMGOVR = active	active		/Reset/Acknowledge
	asing aramasis in Emberry asing			/EMGOVR]
RstForcedSt	Reset Forced Start flag	inactive,	inactive	[Operation
art		active		/Reset/Acknowledge
				/Reset]

Motor Start Module Counter Values

Value	Description	Default	Size	Menu path
WaitTimeStarts	Wait time between starts remained	0 s	0 – 9999999999 s	[Operation
				/Measured values
				/Motor]
ColdStartPermit	Number of cold starts remaining	0	0 - 999999999	[Operation
				/Measured values
				/Motor]
StartPerHour	StartPerHour	0	0 - 999999999	[Operation
				/Measured values
				/Motor]
SPH Release	In case that the Motor is blocked by a SPH	0 min	0 – 999999999 min	[Operation
	blocking, this timer needs to be expired before the blocking is released and the next			/Measured values
	motor start is permitted. The next Motor Start will increment the SPH counter again.			/Motor]
AntiBackSpin	Anti-BackspinTimer	0 s	0 – 9999999999 s	[Operation
				/Measured values
				/Motor]

Page 328 EN MRM4 12/11

Value	Description	Default	Size	Menu path
IL1 lb	Measured value: Phase current as	0 lb	0 – 1000 lb	[Operation
	percentage of lb			/Measured values
				/Current RMS]
IL2 lb	Measured value: Phase current as	0 lb	0 – 1000 lb	[Operation
	percentage of lb			/Measured values
				/Current RMS]
IL3 lb	Measured value: Phase current as	0 lb	0 – 1000 lb	[Operation
	percentage of lb			/Measured values
				/Current RMS]
13 P (%lb) avg	Average RMS current of all 3 phases as	0 lb	0 – 1000 lb	[Operation
	percentages of lb			/Measured values
				/Current RMS]
OCNT	Motor Operation count since last reset.	0	0 - 65535	[Operation
				/History
				/OperationsCr]
HighestStartI	Highest starting phase current. The time	0 A	0 – 99999999 A	[Operation
	stamp indicates the point in time when the maximum current has occurred.			/History
				/OperationsCr]
HighestRunl	Highest running phase current. The time	0 A	0 – 999999 A	[Operation
	stamp indicates the point in time when the maximum current has occurred.			/History
				/OperationsCr]
nEmrgOvr	Number of emergency overrides since last	0	0 - 65535	[Operation
	reset.			/History
				/OperationsCr]
nISQT	Number of incomplete sequence trips since	0	0 - 65535	[Operation
	last reset.			/History
				/TripCr]
nSPHBlocks	Number of start per hour blocks since last	0	0 - 65535	[Operation
	reset.			/History
				/TripCr]
nTBSBlocks	Number of time between start blocks since	0	0 - 65535	[Operation
	last reset.			/History
				/TripCr]
nTRNTrips	Number of transition trips since last reset.	0	0 - 65535	[Operation
				/History
				/TripCr]

Value	Description	Default	Size	Menu path
nZSWTrips	Number of zero speed switch trips since last	0	0 - 65535	[Operation
	reset.			/History
				/TripCr]
nRevTrips	Number of reverse spinning trips since last	0	0 - 65535	[Operation
	reset.			/History
				/TripCr]
TOCS	Total Motor Operation count since last reset.	0	0 - 65535	[Operation
				/History
				/TotalCr]

Motor Start Module Values

Value	Description	Menu path
I3 PRMS avg	Average RMS current of all 3 phases	[Operation
		/Measured values
		/Current RMS]
RunTime	Motor Operation time since last reset.	[Operation
		/History
		/OperationsCr]
Highest%I2/I1	Highest %I2/I1 value since last reset. The	[Operation
time stamp indicates the point in time when the maximum unbalanced load has		/History
	occurred.	/OperationsCr]
TRunTime Motor Operation (Motor run time) time since last reset.		[Operation
		/History
		/TotalCr]

Motor Start Module Statistics

Value	Description	Menu path
IL1 max lb	IL1 maximum value as percentage of lb	[Operation
		/Statistics
		/Max
		/Current]
IL1 avg lb	IL1 average value as percentage of lb	[Operation
		/Statistics
		/Demand
		/Current Demand]

Page 330 EN MRM4 12/11

Value	Description	Menu path
IL1 min lb	IL1 minimum value as percentage of lb	[Operation
		/Statistics
		/Min
		/Current]
IL2 max lb	IL2 maximum value as percentage of lb	[Operation
		/Statistics
		/Max
		/Current]
IL2 avg lb	IL2 average value as percentage of lb	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL2 min Ib	IL2 minimum value as percentage of lb	[Operation
		/Statistics
		/Min
		/Current]
IL3 max lb	IL3 maximum value as percentage of lb	[Operation
		/Statistics
		/Max
		/Current]
IL3 avg lb	IL3 average value as percentage of Ib	[Operation
		/Statistics
		/Demand
		/Current Demand]
IL3 min Ib	IL3 minimum value as percentage of lb	[Operation
		/Statistics
		/Min
		/Current]
I3P Fla Demand	RMS current of all 3 phases calculated in a	[Operation
	fixed demand window as percentages of lb	/Statistics
		/Demand
		/Current Demand]

I>> - IOC Function

The instantaneous overcurrent function (IOC) or 50P is intended to protect in the event of a high-current fault. The example IOC setting used in the Motor Protection Curve (see the Motor Protection Curve Examples in the Ultimate Trip Current Section) is 12 times (1,200%) of FLA. In general, the instantaneous IOC should be at least 1.5 times LRC (Locked Rotor Current), well above the locked rotor current normally seen at the moment of a start.

IOC should trip fast and therefore no run or pickup delay is provided. A start delay is set at a minimum of 0.03 sec. or more if needed to block IOC tripping on magnetizing inrush when the motor is first energized. An additional IOC tripping delay setting is set at a default of zero seconds.

IOC Trip Level

The IOC sets the instantaneous overcurrent trip limit in percentage of »*lb«* (FLA) above at which the relay trips. This trip type can be set to Inactive to deactivate this protective device element. For currents clearly above the setting, the IOC function picks up in 1.5 power cycles or less (at 50 Hz).

IOC Start Delay (IOCSD)

This setting sets the number of power cycles after a start is recognized until the IOC trip and alarm functions are enabled. Use this delay to inhibit IOC tripping on a current peak caused by magnetic inrush when the motor is first energized (usually two to three cycles).

I - Overcurrent Protection [50, 51,51Q, 51V]



If you are using inrush blockings the tripping delay of the current protection functions must be at least 30ms or more in order to prevent faulty trippings.



All overcurrent protective elements are identically structured.



This module offers Adaptive Parameter Sets.

Parameters can be modified within parameter sets dynamically by means of Adaptive Parameter Sets. Please refer to chapter Parameter / Adaptive Parameter Sets.

The following table shows the application options of the Overcurrent Protection element

Applications of the I-Protection Module	Setting in	Option
ANSI 50 – Overcurrent protection, non-directional	Device Planning menu	Measuring Mode: Fundamental/TrueRMS/negative phase sequence current (I2)
ANSI 51 – Short circuit protection, non-directional	Device Planning menu	Measuring Mode: Fundamental/TrueRMS/negative phase sequence current (I2)

Page 332 EN MRM4 12/11

Applications of the I-Protection Module	Setting in	Option
ANSI 51V – Voltage restraint overcurrent protection	Parameter Set: VRestraint = active	Measuring Mode: Fundamental/TrueRMS/negative phase sequence current (I2)
		Measuring Channel: Phase to Phase/Phase to Neutral
ANSI 51Q Negative Phase Sequence Overcurrent Protection	Parameter Set: Measuring Method =I2 (Negative Sequence Current)	
51R Voltage controlled overcurrent protection	Adaptive Parameters	Measuring Mode: Fundamental/TrueRMS/negative phase sequence current (I2)
(Please refer to the chapter Parameter/Adaptive Parameter)		Measuring Channel: (in voltage protection module) Phase to Phase/Phase to Neutral

Measuring Mode

For all protection elements it can be determined, whether the measurement is done on basis of the *»Fundamental«* or if *»TrueRMS«* measurement is used.

Alternatively the »Measuring Mode« can be set to »I2«. In this case the negative phase sequence current will be measured. This is to detect unbalanced faults.

Voltage restraint overcurrent protection 51V

When the Parameter »*VRestraint«* is set to active the overcurrent protection element works voltage restraint. That means, the overcurrent pickup threshold will be lowered during voltage drops. This results in a more sensitive overcurrent protection. For the voltage threshold »*VRestraint max«* additionally the »*Measuring Channel«* can be determined.

Measuring Channel

With the parameter »Measuring Channel« it can be determined, whether the »Phase to Phase« voltage or the »Phase to Neutral« voltage is measured.

For each element the following characteristics are available:

- DEFT (UMZ)
- NINV (IEC/AMZ)
- VINV (IEC/AMZ)
- LINV (IEC/AMZ)
- EINV (IEC/AMZ)
- MINV (ANSI/AMZ)
- VINV (ANSI/AMZ)
- EINV (ANSI/AMZ)
- Thermal Flat
- IT
- I2T
- I4T

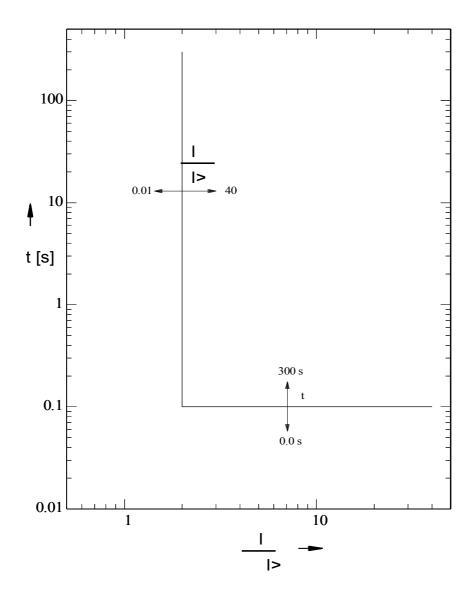
Explanation:

t = Tripping delay

t-char = Time multiplier/tripping characteristic factor. The setting range depends on the selected tripping curve. I = Fault current

I> = If the pickup value is exceeded, the module/element starts to time out to trip.

DEFT



Page 334 EN MRM4 12/11

IEC NINV



Notice!

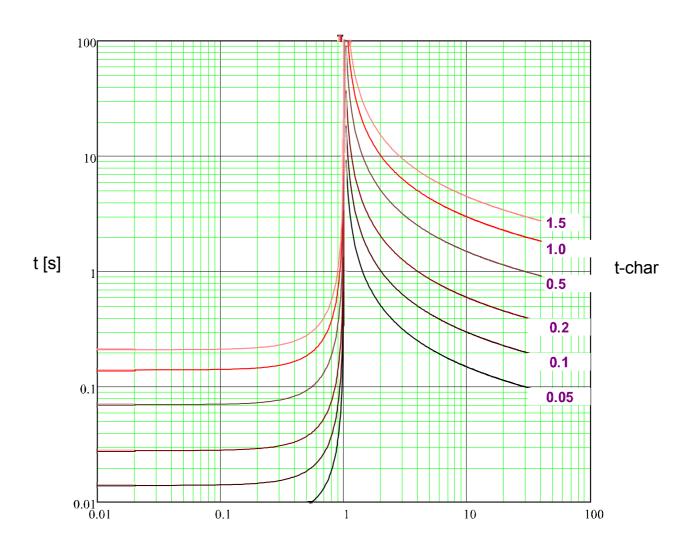
Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{0.14}{\left(\frac{1}{||}\right)^2 - 1} \right| * t-char [s]$$

$$t = \frac{0.14}{\left(\frac{1}{||}\right)^{0.02} - 1} * t-char [s]$$

$$t = \frac{0.14}{\left(\frac{1}{1>}\right)^{0.02}} *t-char[s]$$



x * I> (multiples of pickup)

IEC VINV



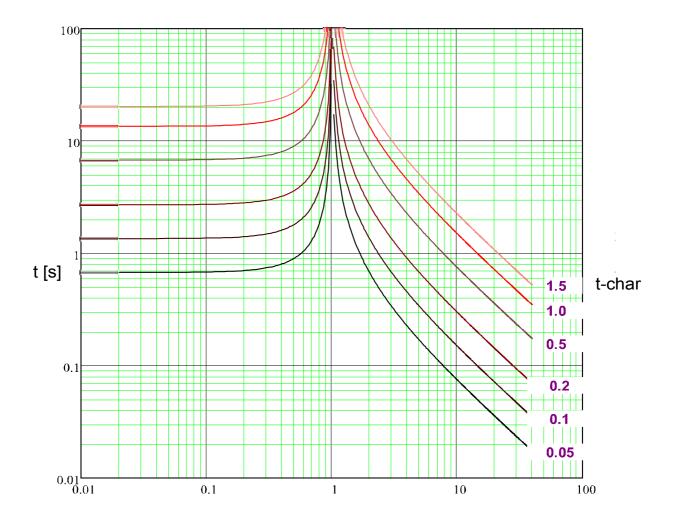
Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{13.5}{\left(\frac{l}{l>}\right)^2 - 1} \right| \text{*t-char [s]} \qquad \qquad t = \frac{13.5}{\left(\frac{l}{l>}\right) - 1} \text{*t-char [s]}$$

$$t = \frac{13.5}{\left(\frac{I}{I>}\right)-1} *t-char [s]$$



x * I> (multiples of pickup)

Page 336 EN MRM4 12/11

IEC LINV



Notice!

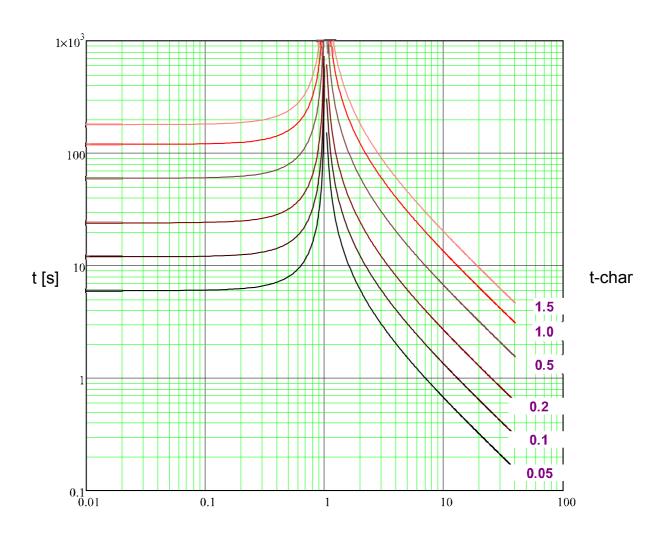
Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{120}{\left(\frac{l}{l}\right)^2 - 1} \right| *t-char [s]$$

$$t = \frac{120}{\left(\frac{l}{l}\right) - 1} *t-char [s]$$

$$t = \frac{120}{\left(\frac{l}{l}\right)-1} *t-char [s]$$



x * I> (multiples of pickup)

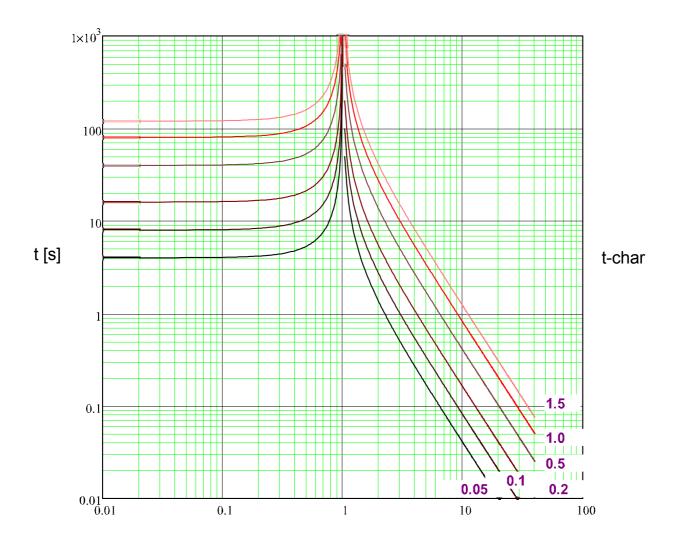
IEC EINV



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip $t = \left| \frac{80}{\left(\frac{1}{|s|}\right)^2 - 1} \right| \text{*t-char [s]} \qquad t = \frac{80}{\left(\frac{1}{|s|}\right)^2 - 1} \text{*t-char [s]}$



x * I> (multiples of pickup)

Page 338 EN MRM4 12/11

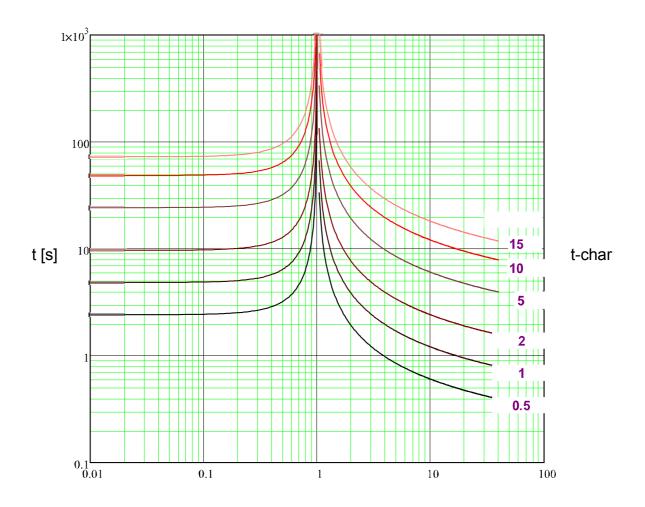
ANSI MINV



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip $t = \left| \frac{4.85}{\left(\frac{1}{||}\right)^2 - 1} \right| * t-char [s] \qquad t = \left(\frac{0.0515}{\left(\frac{1}{||}\right)^{0.02} - 1} + 0.1140 \right) * t-char [s]$



x * I> (multiples of pickup)

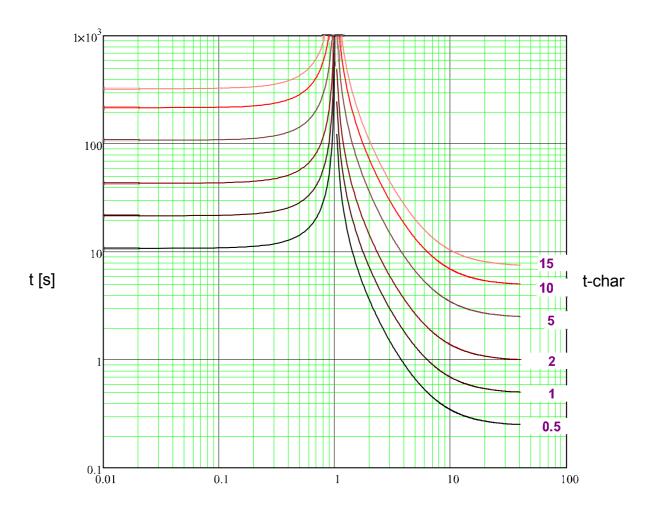
ANSI VINV



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip
$$t = \left| \frac{21.6}{\left(\frac{1}{||}\right)^2 - 1} \right| * t-char [s] \qquad t = \left(\frac{19.61}{\left(\frac{1}{||}\right)^2 - 1} + 0.491\right) * t-char [s]$$



x * I> (multiples of pickup)

Page 340 EN MRM4 12/11

ANSI EINV



Notice!

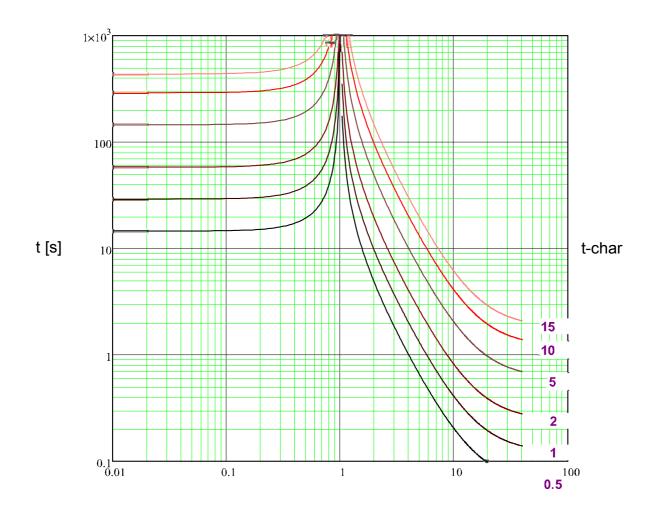
Various reset modes are available. Resetting via characteristic, delayed and

Reset

$$t = \left| \frac{29.1}{\left(\frac{l}{l>}\right)^2 - 1} \right| *t-char [s]$$

$$t = \left| \frac{29.1}{\left(\frac{1}{||s|}\right)^2 - 1} \right| * t-char [s]$$

$$t = \left(\frac{28.2}{\left(\frac{1}{||s|}\right)^2 - 1} + 0.1217 \right) * t-char [s]$$



x * I> (multiples of pickup)

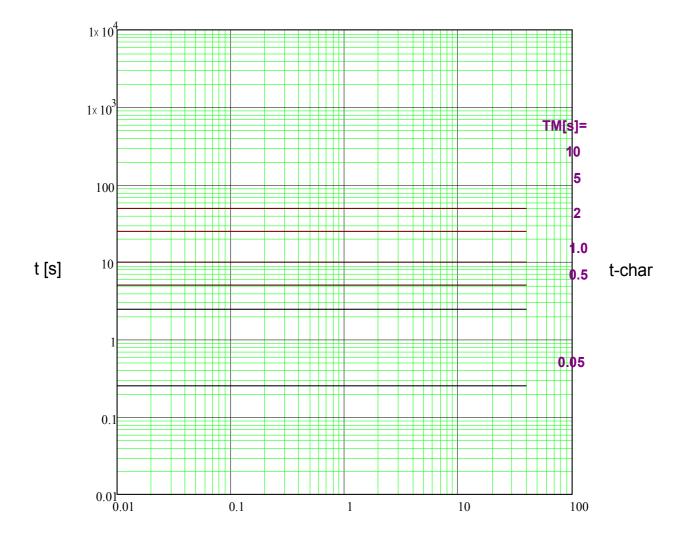
Therm Flat



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip
$$t = \left| \frac{5*3^2}{\left(\frac{l}{\ln}\right)^0} \right| \text{*t-char [s]} \qquad t = \frac{5*1^2}{\left(\frac{l}{\ln}\right)} \text{*t-char [s]}$$



x * I> (multiples of pickup)

Page 342 EN MRM4 12/11



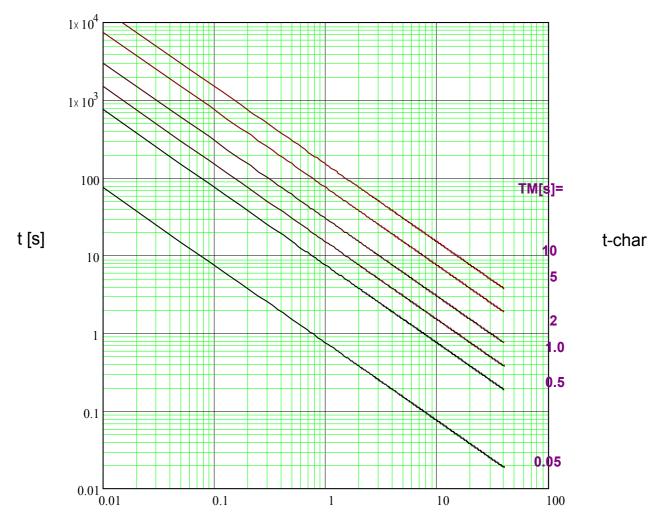
Notice!

Various reset modes are available. Resetting via characteristic, delayed and

Reset

$$t = \left| \frac{5*3^2}{\left(\frac{l}{\ln}\right)^0} \right| \text{*t-char [s]} \qquad t = \frac{5*3^1}{\left(\frac{l}{\ln}\right)^1} \text{*t-char [s]}$$

$$t = \frac{5*3^1}{\left(\frac{l}{\ln l}\right)^1} *t-char [s]$$



x * I> (multiples of pickup)

12T



Notice!

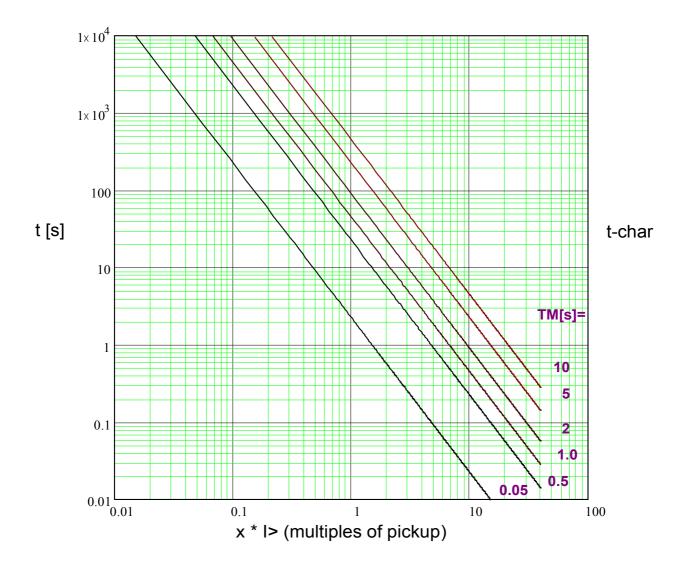
Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

Trip

$$t = \left| \frac{5*3^2}{\left(\frac{l}{\ln n}\right)^0} \right| * t-char [s] \qquad t = \frac{5*3^2}{\left(\frac{l}{\ln n}\right)^2} * t-char [s]$$

$$t = \frac{5*3^2}{\left(\frac{1}{\ln n}\right)^2} *t-char [s]$$



Page 344 EN MRM4 12/11

14T



Notice!

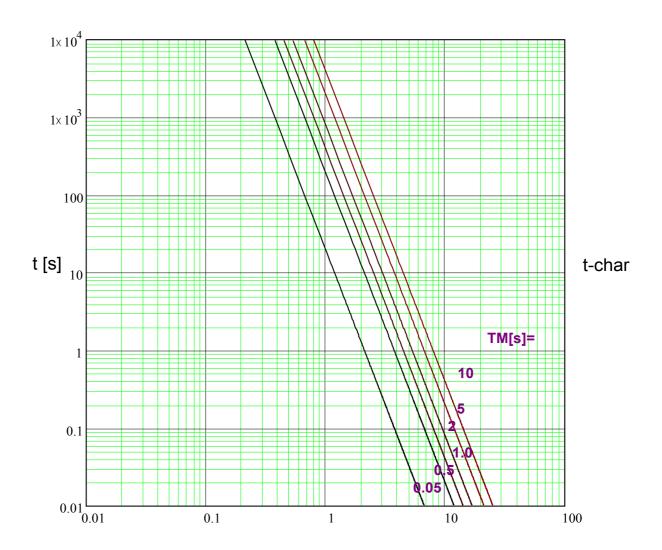
Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{5*3^2}{\left(\frac{l}{\ln 0}\right)^0} \right| *t-char[s] \qquad t = \frac{5*3^4}{\left(\frac{l}{\ln 0}\right)^4} *t-char[s]$$

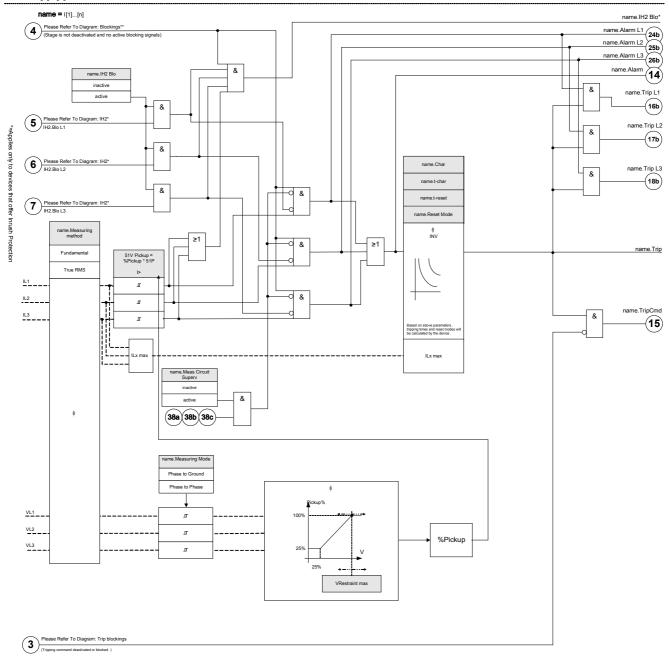
$$t = \frac{5*3^4}{\left(\frac{l}{\ln l}\right)^4} *t-char[s]$$

Trip



x * I> (multiples of pickup)

l[1]...[n]



Page 346 EN MRM4 12/11

Device Planning Parameters of the I Module

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	I[1]: non	[Device planning]
		non directional	directional	
			I[2]: non directional	
			I[3]: do not use	
			I[4]: do not use	
			I[5]: do not use	
			I[6]: do not use	

Global Protection Parameters of the I Module

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
	and state of the designed signal to trace			/I-Prot
				/I[1]]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
				/I-Prot
				/I[1]]
ExBlo3	External blocking of the module, if blocking is	,	MStart.Blo-	[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.	MStart.Blo-GOCStart,	IOCStart	/Global Prot Para
	and state of the designed signal to due.	MStart.Blo-IOCStart,		/I-Prot
		MStart.Blo-I <start,< td=""><td></td><td>/I[1]]</td></start,<>		/I[1]]
		MStart.Blo-JamStart,		
		MStart.Blo-UnbalStart,		
		MStart.Blo-Generic1,		
		MStart.Blo-Generic2,		
		MStart.Blo-Generic3,		
		MStart.Blo-Generic4,		
		MStart.Blo-Generic5		
ExBlo	External blocking of the Trip Command of the	1n, Assignment List		[Protection Para
TripCmd	module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
				/I-Prot
				/I[1]]

Parameter	Description	Setting range	Default	Menu path
Ex rev Interl	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of	1n, Assignment List		[Protection Para
				/Global Prot Para
	the assigned signal is true.			/I-Prot
				/I[1]]
AdaptSet 1	Assignment Adaptive Parameter 1	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/I[1]]
AdaptSet 2	Assignment Adaptive Parameter 2	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/I[1]]
AdaptSet 3	Assignment Adaptive Parameter 3	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/I[1]]
AdaptSet 4	Assignment Adaptive Parameter 4	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/I[1]]

Setting Group Parameters of the I Module

Parameter	Description	Setting range	Default	Menu path
Function		inactive,	I[1]: active	[Protection Para
	module/stage.	active	I[2]: active	/<14>
			I[3]: inactive	/I-Prot
			I[4]: inactive	/I[1]]
			I[5]: inactive	
			I[6]: inactive	
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".	inactive, active	inactive	[Protection Para /<14> /I-Prot /I[1]]
Ex rev Interl Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "Ex rev Interl Fc = active".	inactive, active	inactive	[Protection Para /<14> /I-Prot /I[1]]

Page 348 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Blo TripCmd	Permanent blocking of the Trip Command of the	inactive,	inactive	[Protection Para
n	module/stage.	active		/<14>
				/I-Prot
				/I[1]]
ExBlo	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
TripCmd Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/I-Prot
	those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".			/I[1]]
Measuring	Measuring method: fundamental or RMS	Fundamental,	Fundamental	[Protection Para
method		True RMS,		/<14>
		12		/I-Prot
1				/I[1]]
>	If the pickup value is exceeded, the	0.02 – 40.00 In	I[1]: 2.00 In	[Protection Para
	module/element starts to time out to trip.		1.00 In	/<14>
	Only available if: Characteristic = DEFT Or		1.00 In	/I-Prot
	Characteristic = INV Minimum of the setting range If: VRestraint = active Minimum of the		1.00 In	/I[1]]
	setting range If: VRestraint = inactive		1.00 In	
			I[2]: 5.00 In	
			1.00 In	
			1.00 In	
			1.00 In	
			1.00 In	
			I[3]: 1.00 In	
			I[4]: 1.00 In	
			I[5]: 1.00 In	
			I[6]: 1.00 In	
Char	Characteristic	DEFT,	DEFT	[Protection Para
		IEC NINV,		/<14>
		IEC VINV,		/I-Prot
		IEC EINV,		/I[1]]
		IEC LINV,		
		ANSI MINV,		
		ANSI VINV,		
		ANSI EINV,		
		Therm Flat,		
		IT,		
		I2T,		
		I4T		

	Description	Setting range	Default	Menu path
t	Tripping delay	0.00 – 300.00 s	I[1]: 0.50 s	[Protection Para
ļ	Only available if: Characteristic = DEFT		1.00 s	/<14>
			1.00 s	/I-Prot
			1.00 s	/I[1]]
			1.00 s	
ļ			I[2]: 0.0 s	
ļ			1.00 s	
ļ			1.00 s	
ļ			1.00 s	
ļ			1.00 s	
ļ			I[3]: 1.00 s	
ļ			I[4]: 1.00 s	
ļ			I[5]: 1.00 s	
ļ			I[6]: 1.00 s	
t-char	Time multiplier/tripping characteristic factor. The setting range depends on the selected tripping curve.	0.02 - 20.00	1	[Protection Para
ļ				/<14>
				/I-Prot
	Only available if: Characteristic = INV Or Characteristic = Therm Flat Or Characteristic = IT Or Characteristic = I2T Or Characteristic = I4T			/[[1]]
Reset Mode	Reset Mode	instantaneous,	instantaneous	[Protection Para
ļ	Only available if: Characteristic = INV Or	t-delay,		/<14>
	Characteristic = Therm Flat Or Characteristic =	calculated		/I-Prot
	IT Or Characteristic = I2T Or Characteristic = I4T			/1[1]]
t-reset	Reset time for intermittent phase failures (INV	0.00 - 60.00 s	0 s	[Protection Para
	characteristics only)			/<14>
	Available if:Reset Mode = t-delay			/I-Prot
				/I[1]]

I Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/I-Prot
		/1[1]]

Page 350 EN MRM4 12/11

Name	Description	Assignment via
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/I-Prot
		/[[1]]
ExBlo TripCmd-I	Module input state: External Blocking of the	[Protection Para
	Trip Command	/Global Prot Para
		/I-Prot
		/[[1]]
Ex rev Interl-I	Module input state: External reverse	[Protection Para
	interlocking	/Global Prot Para
		/I-Prot
		Л[1]]
AdaptSet1-I	Module input state: Adaptive Parameter1	[Protection Para
		/Global Prot Para
		/I-Prot
		/[[1]]
AdaptSet2-I	Module input state: Adaptive Parameter2	[Protection Para
		/Global Prot Para
		/I-Prot
		/[[1]]
AdaptSet3-I	Module input state: Adaptive Parameter3	[Protection Para
		/Global Prot Para
		/I-Prot
		/[[1]]
AdaptSet4-I	Module input state: Adaptive Parameter4	[Protection Para
		/Global Prot Para
		/I-Prot
		/[[1]]

I Module Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Ex rev Interl	Signal: External reverse Interlocking
Blo TripCmd	Signal: Trip Command blocked
ExBlo TripCmd	Signal: External Blocking of the Trip Command
Alarm L1	Signal: Alarm L1
Alarm L2	Signal: Alarm L2

Name	Description
Alarm L3	Signal: Alarm L3
Alarm	Signal: Alarm
Trip L1	Signal: General Trip Phase L1
Trip L2	Signal: General Trip Phase L2
Trip L3	Signal: General Trip Phase L3
Trip	Signal: Trip
TripCmd	Signal: Trip Command
Active AdaptSet	Active Adaptive Parameter
DefaultSet	Signal: Default Parameter Set
AdaptSet 1	Signal: Adaptive Parameter 1
AdaptSet 2	Signal: Adaptive Parameter 2
AdaptSet 3	Signal: Adaptive Parameter 3
AdaptSet 4	Signal: Adaptive Parameter 4

Counter Values of the I Module

Value	Description	Default	Size	Menu path
NumberOfAlarms	Number of alarms since last reset.	0	0 - 9999999999	[Operation
				/History
				/AlarmCr]
NumberOfTrips	Number of trips since last reset	0	0 - 9999999999	[Operation
				/History
				/TripCr]

Commissioning: Overcurrent Protection, non-directional [50, 51]

Object to be tested

• Signals to be measured for each current protection element, the threshold values, total tripping time (recommended), or alternatively tripping delays and the drop-out ratios; each time 3 x single-phase and 1 x three-phase.



Especially in Holmgreen connections, wiring errors can easily happen, and these are then detected safely. Measuring the total tripping time can ensure that the secondary wiring is O.K. (from the terminal on, up to the trip coil of the Bkr.).

NOTICE

It is recommended to measure the total tripping time instead of the tripping delay. The tripping delay should be specified by the customer. The total tripping time is measured at the position signalling contact of the Bkr. (not at the relay output!).

Total tripping time = tripping delay (please refer to the tolerances of the protection stages) + Bkr. operating time (about 50 ms)

Please take the Bkr. operating times from the technical data specified in the relevant documentation provided by the Bkr. manufacturer.

Page 352 EN MRM4 12/11

Necessary means

- Current source
- May be: ampere meters
- Timer

Procedure

Testing the threshold values (3 x single-phase and 1 x three-phase)

Each time feed a current which is about 3-5% above the threshold value for activation/tripping. Then check the threshold values.

Testing the total tripping delay (recommendation)

Measure the total tripping times at the auxiliary contacts of the Bkr. (Bkr. tripping).

Testing the tripping delay (measuring at the relay output)

Measure the tripping times at the relay output.

Testing the drop-out ratio

Reduce the current to 97% below the trip value and check the drop-out ratio.

Successful test result

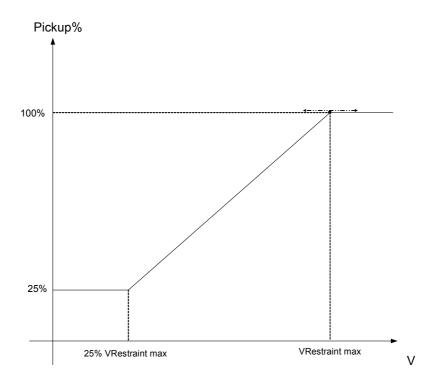
The measured total tripping delays or individual tripping delays, threshold values and drop-out ratios correspond with those values, specified in the adjustment list. Permissible deviations/tolerances can be found under Technical Data.

51V - Voltage Restraint Overcurrent

For activating this function, the parameter » VRestraint« has to be set to active in the parameter set of the corresponding overcurrent element I[x].

The <u>51V</u> protection function restrains operation which reduces pickup levels. This allows the User to lower the pickup value of the <u>51V</u> protection function with the corresponding phase input voltage (phase-to-phase or phase-to-ground, depending on the setting of »*Measuring Channel«* within the current protection module). When the minimum fault phase current is close to the load current, it may make the phase time overcurrent protection coordination difficult. In this case, an undervoltage function may be used to alleviate this situation. When the voltage is low, the phase time overcurrent pickup threshold may be set low accordingly, so that the phase time overcurrent protection may achieve adequate sensitivity and better coordination. The device uses a simple linear model to determine the effective pickup by characterizing the relationship between the voltage and the phase time overcurrent pickup threshold.

Once the voltage restraint protection function is activated, the effective phase time overcurrent pickup threshold will be the calculated Pickup% times the phase time overcurrent pickup setting. The effective pickup threshold must be within the setting range allowed and, if it is less, the minimum pickup value will be used.



That means:

Vmin = 0.25*Vmax;

Pickup%min = 25%;

• Pickup% = 25%, if V <= Vmin;

Pickup% = 1/Vmax*(V - Vmin) + 25%, if Vmin < V < Vmax;

Pickup% = 100%, if V >= Vmax;

The tripping curves (characteristic) will not be influenced by the voltage restraint function. If the voltage transformer supervision is activated, the voltage restraint overcurrent protection element is blocked in case of m.c.b. trip to avoid false trippings.

NOTICE

Definition of Vn:

Vn is dependent on the *»Measuring Channel«* setting in the current protection modules.

In case that this parameter is set to "Phase to Phase":

$$Vn = Main VT sec$$

In case that this parameter is set to "Phase to Neutral":

$$Vn = \frac{Main\,VT\,sec}{\sqrt{3}}$$

If the parameter »VT con« within the system parameters is set to »Phase to Phase« the setting »Phase to Neutral« in the current modules is effectless.

Page 354 EN MRM4 12/11

Commissioning: Overcurrent Protection, Non-directional [ANSI 51V]

Object to be tested:

Signals to be measured for Voltage Restraint protection function: the threshold values, total tripping time (recommended), or alternatively tripping delays and the dropout ratios; each time 3 x single-phase and 1 x three-phase.

NOTICE

It is recommended to measure the total tripping time instead of the tripping time. The tripping delay should be specified by the customer. The total tripping time is measured at the position signalling contacts of the Bkrs. (not at the relay output!).

Total tripping time: = tripping delay (please refer to the tolerances of the protection stages) + Bkr. operating time (about 50 ms)

Please take the Bkr. switching times from the technical data, specified in the relevant documentation, provided by the Bkr. manufacturer.

Necessary means:

- Current source;
- Voltage Source;
- Current and Voltage meters; and
- Timer.

Procedure:

Testing the threshold values (3 x single-phase and 1 x three-phase)

Feed %Pickup voltage. For each test performed, feed a current that is about 3-5% above the threshold value for activation/tripping. Then check if the pickup values are %Pickup of the value according to the standard overcurrent protection.

Testing the total tripping delay (recommendation)

Measure the total tripping times at the auxiliary contacts of the breakers (breaker tripping).

Testing the tripping delay (measuring at the relay output contact)

Measure the tripping times at the relay output contact.

Testing the dropout ratio

Reduce the current to 97% below the trip value and check the dropout ratio.

Successful test result

The measured total tripping delays or individual tripping delays, threshold values, and dropout ratios correspond with those values specified in the adjustment list. Permissible deviations/tolerances can be found under Technical Data.

I2> - Negative-Sequence Overcurrent [51Q]

Elements

For activating this function, the parameter *»Measuring Mode«* has to be set to *»I2«* in the parameter set of the corresponding overcurrent element I[x].

The negative-sequence overcurrent protection function ($\underline{I2}$) is to be seen as an equivalent to the phase overcurrent protection with the exception that it uses negative-sequence current ($\underline{I2}$) as measured quantities instead of the three phase currents used by phase overcurrent protection function. The negative-sequence current used by $\underline{I2}$ is derived from the following well-known symmetrical component transformation:

$$I_2 = \frac{1}{3} (I_{LI} + a^2 I_{L2} + a I_{L3})$$

The pickup set value of a <u>12></u> protection function should be set in accordance of the negative-sequence current occurrence in the protected object.

Besides that, the negative-sequence overcurrent protection function ($\underline{I2}$) uses the same setting parameters as the phase overcurrent protection function, like trip and reset characteristics from both IEC/ANSI standards, time multiplier, etc.

The negative-sequence overcurrent protection function ($\underline{I2}$) can be used for line, generator, transformer and motor protection to protect the system from unbalanced faults. Because the $\underline{I2}$ -protection function operates on the negative-sequence current component which is normally absent during load conditions, the $\underline{I2}$ - can, therefore, be set more sensitive than the phase overcurrent protection functions. On the other hand, coordination of negative-sequence overcurrent protection function in a radial system does not mean automatically very long fault clearing time for the furthest upstream protection devices, because the tripping time of concerned negative-sequence overcurrent protection function needs only be coordinate with the next downstream device with the negative-sequence overcurrent protection function. This makes the $\underline{I2}$ - in many cases as an advantageous protection concept in addition to the phase overcurrent protection function.

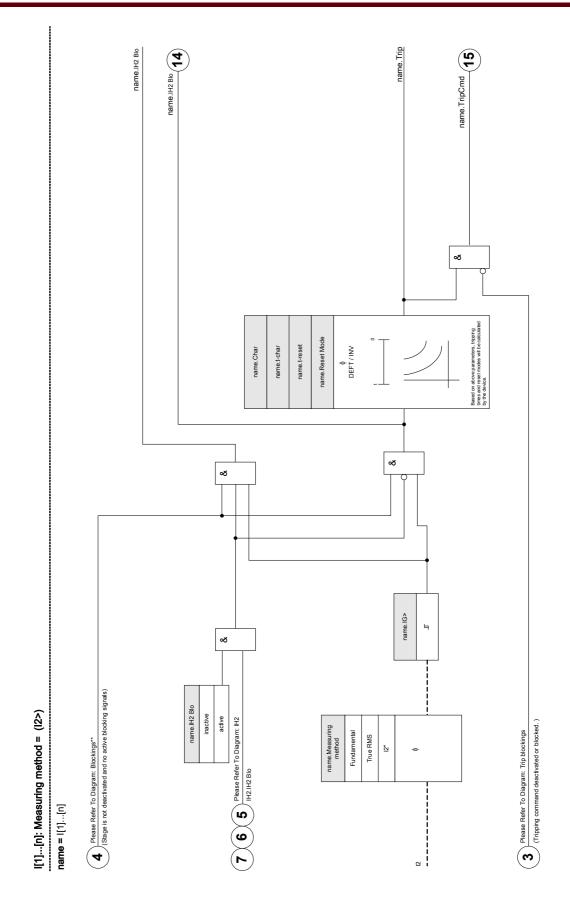


If you are using inrush blockings, the tripping delay of the current protection functions must be at least 30 ms or more in order to prevent faulty trippings.



At the moment of breaker closure, negative-sequence current might be the result of transients.

Page 356 EN MRM4 12/11



Commissioning: Negative Sequence Overcurrent

Object to be tested

Signals to be measured for each current protection function: the threshold values, total tripping time (recommended), or alternatively tripping delays and the dropout ratios.

NOTICE

It is recommended to measure the total tripping time instead of the tripping time. The tripping delay should be specified by the customer. The total tripping time is measured at the position signalling contacts of the Bkrs. (not at the relay output!).

Total tripping time: = tripping delay (please refer to the tolerances of the protection stages) + Bkr. operating time (about 50 ms)

Please take the Bkr. switching times from the technical data, specified in the relevant documentation, provided by the Bkr. manufacturer.

Necessary means:

- Current source
- Current meters
- Timer

Procedure:

Testing the threshold values

In order to get a negative-sequence current, please change the phase sequence at the terminals of the current source (in case of ABC sequence to ACB – in case of a ACB sequence to ABC).

For each test performed, feed a current that is about 3-5% above the threshold value for activation/tripping. Then check the threshold values.

Testing the total tripping delay (recommendation)

Measure the total tripping times at the auxiliary contacts of the breakers (breaker tripping).

Testing the tripping delay (measuring at the relay output contact)

Measure the tripping times at the relay output contact.

Testing the dropout ratio

Reduce the current to 97% below the trip value and check the dropout ratio.

Successful test result

The measured total tripping delays or individual tripping delays, threshold values, and dropout ratios correspond with those values specified in the adjustment list. Permissible deviations/tolerances can be found under Technical Data.

Page 358 EN MRM4 12/11

I2 and %I2/I1> - Unbalanced Load [46]

Elements: 12>[1],12>[2]

The $\underline{I2}$ Current Unbalance element works similar to the \underline{V} 012 Voltage Unbalance element. The positive and negative sequence currents are calculated from the 3-phase currents. The Threshold setting defines a minimum operating current magnitude of I2 for the 46 function to operate, which insures that the relay has a solid basis for initiating a current unbalance trip. The $\frac{3}{12}$ (option) setting is the unbalance trip pickup setting. It is defined by the ratio of negative sequence current to positive sequence current $\frac{3}{12}$ (12/11)«.

This function requires positive or negative sequence current magnitude above the threshold setting and the percentage current unbalance above the »%(I2/I1)« setting before allowing a current unbalance trip. Therefore, both the threshold and percent settings must be met for the specified Delay time setting before the relay initiates a trip for current unbalance.

NOTICE

All elements are identically structured.

Rating value I2> is the permitted continuous unbalanced load current. For both steps trip characteristics are provided, namely a definite time characteristic (DEFT) and an inverse characteristic (INV).

The characteristic of the inverse curve is as follows:

$$t[s] \le \frac{K * ln^2}{l2^2 - l2 > 2}$$

Legend:

In [A] = Nominal current

t [s] = Tripping delay

K [s] = Indicates the thermal load capability of the engine while running with 100% unbalanced load current.

I2> [A] = The Threshold setting defines a minimum operating current magnitude of I2 for the 46 function to operate, which ensures that the relay has a solid basis for initiating a current unbalance trip. This is a supervisory function and not a trip level

I2 [A] = Measured value (calculated): Unbalanced load current

In the equation shown above the heating-up process is assumed by integration of the counter system current I2. When I2> is undershoot, the built-up heat amount will be reduced in line with the adjusted cooling-down constant "tau-cool".

Theta(t) = Theta₀ *
$$e^{-\frac{t}{t-cool}}$$

Legend:

t = Tripping delay

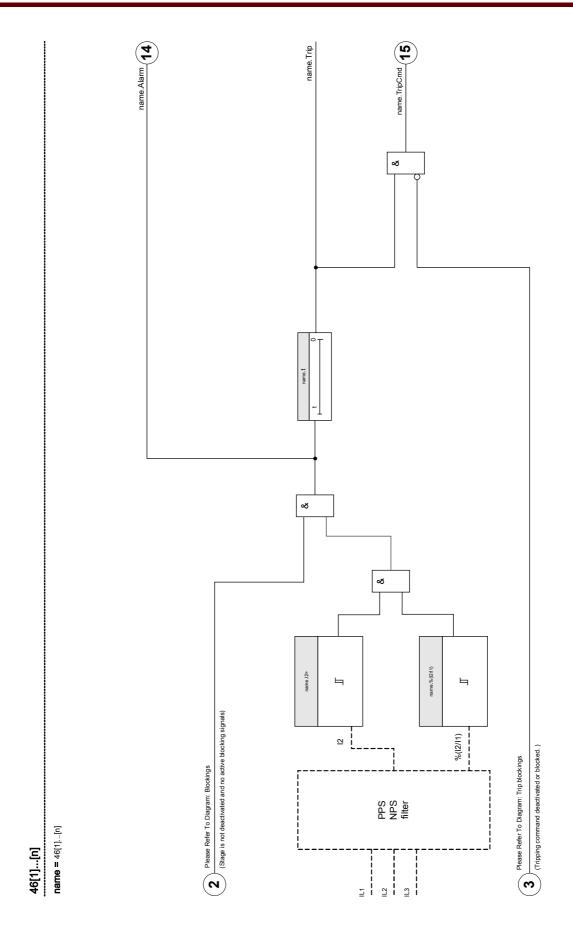
t-cool = Cooling time constant

Theta(t) = Momentary heat (thermal) energy

Theta $_0$ = Heat (thermal) energy before the cooling down has started

If the heat amount is not reduced when the permitted unbalanced load current is overshoot again, the remaining heat amount will cause an earlier tripping.

Page 360 EN MRM4 12/11



Device Planning Parameters of the Current Unbalance Module

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	I2>[1]: use	[Device planning]
		use	12>[2]: do not use	

Global Protection Parameters of the Current Unbalance Module

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and date of the deergree eighten to trace.			/I-Prot
				/12>[1]]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List	-,-	[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
				/I-Prot
				/12>[1]]
ExBlo3	activated (allowed) within a parameter set and if the state of the assigned signal is true.	,	MStart.Blo-	[Protection Para
		MStart.Blo-GOCStart,	UnbalStart	/Global Prot Para
		MStart.Blo-IOCStart,		/I-Prot
		MStart.Blo-I <start,< td=""><td></td><td>/12>[1]]</td></start,<>		/12>[1]]
		MStart.Blo-JamStart,		
		MStart.Blo-UnbalStart,		
		MStart.Blo-Generic1,		
		MStart.Blo-Generic2,		
		MStart.Blo-Generic3,		
		MStart.Blo-Generic4,		
		MStart.Blo-Generic5		
ExBlo	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the	1n, Assignment List		[Protection Para
TripCmd				/Global Prot Para
	assigned signal is true.			/I-Prot
				/12>[1]]

Setting Group Parameters of the Current Unbalance Module

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/I-Prot
				/l2>[1]]

Page 362 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".	inactive, active	inactive	[Protection Para /<14> /I-Prot //2>[1]]
Blo TripCmd	Permanent blocking of the Trip Command of the module/stage.	inactive, active	inactive	[Protection Para /<14> /I-Prot //2>[1]]
ExBlo TripCmd Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".	inactive, active	inactive	[Protection Para /<14> /I-Prot //2>[1]]
12>	The Threshold setting defines a minimum operating current magnitude of I2 for the 46 function to operate, which ensures that the relay has a solid basis for initiating a current unbalance trip. This is a supervisory function and not a trip level. Only available if:CurrentBase = Protected Object Rating	0.01 – 4.00 In	I2>[1]: 0.08 In I2>[2]: 0.01 In	[Protection Para /<14> /I-Prot /I2>[1]]
%(I2/I1)	The %(I2/I1) setting is the unbalance trip pickup setting. It is defined by the ratio of negative sequence current to positive sequence current (% Unbalance=I2/I1). Phase sequence will be taken into account automatically.	inactive, active	inactive	[Protection Para /<14> /I-Prot //2>[1]]
%(I2/I1)	The %(I2/I1) setting is the unbalance trip pickup setting. It is defined by the ratio of negative sequence current to positive sequence current (% Unbalance=I2/I1). Phase sequence will be taken into account automatically. Only available if: %(I2/I1) = use	2 - 40%	20%	[Protection Para /<14> /I-Prot //2>[1]]
Char	Characteristic	DEFT, INV, AANSI INV	DEFT	[Protection Para /<14> /I-Prot //2>[1]]
t	Tripping delay Only available if: Characteristic = DEFT	0.00 – 300.00 s	0.00 s	[Protection Para /<14> /I-Prot //2>[1]]

Parameter	Description	Setting range	Default	Menu path
K	This setting is the negative sequence capability	1.00 – 200.00 s	10.0 s	[Protection Para
	constant. This value is normally provided by the generator manufacturer.			/<14>
				/I-Prot
	Only available if: Characteristic = INV			/12>[1]]
т-сооІ	If the unbalanced load current falls below the	0.0 - 60000.0 s	0.0 s	[Protection Para
	pickup value, the cooling-off time is taken into account. If the unbalanced load exceeds the			/<14>
	pickup value again, than the saved heat within the			/I-Prot
	electrical equipment will lead to an accelerated trip.			/12>[1]]
	Only available if: Characteristic = INV			

Current Unbalance Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/I-Prot
		/12>[1]]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/I-Prot
		/12>[1]]
ExBlo TripCmd-I	Module input state: External Blocking of the	[Protection Para
	Trip Command	/Global Prot Para
		/I-Prot
		/12>[1]]

Current Unbalance Module Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Blo TripCmd	Signal: Trip Command blocked
ExBlo TripCmd	Signal: External Blocking of the Trip Command
Alarm	Signal: Alarm Negative Sequence
Trip	Signal: Trip
TripCmd	Signal: Trip Command

Page 364 EN MRM4 12/11

Current Unbalance Module Counter Values

Value	Description	Default	Size	Menu path
NumberOfAlarms	Number of alarms since last reset.	0	0 - 9999999999	[Operation
				/History
				/AlarmCr]
NumberOfTrips	Number of trips since last reset	0	0 - 9999999999	[Operation
				/History
				/TripCr]

Commissioning: Current Unbalance Module

Object to be tested:

Test of the unbalanced load protection function.

Necessary means:

- Three-phase current source with adjustable current unbalance; and
- Timer.

Procedure:

Check the phase sequence:

- Ensure that the phase sequence is the same as that set in the system parameters.
- Feed-in a three-phase nominal current.
- Change to the »Measuring Values« menu.
- Check the measuring value for the unbalanced current »12«. The measuring value displayed for »12« should be zero (within the physical measuring accuracy).



If the displayed magnitude for I2 is the same as that for the symmetrical nominal currents fed to the relay, it implies that the phase sequence of the currents seen by the relay is reversed.

Having tripped the relay in the previous test, now decrease the phase A current. The drop-out ratio must not be higher than 0.97 times the threshold value.

Testing %I2/I1

- Configure minimum threshold value »*Threshold*« (I2) (0.01 x In) and set »*%I2/I1*« greater or equal to 10%.
- Apply a symmetrical three-phase current system (nominal currents). The measuring value of »%/12/11« should be 0%.

- Now increase the phase L1 current. With this configuration, the threshold value » Threshold« (I2) should be reached before the value » % I2/I1« reaches the set » % I2/I1« ratio threshold.
- Continue increasing the phase 1 current until the relay is activated.

Testing the drop-out ratio of %I2/I1

Having tripped the relay in the previous test, now decrease the phase L1 current. The drop-out of »%/12/11« has to be 1% below the »%/12/11 «setting.

Successful test result:

The measured trip delays, threshold values, and drop-out ratios are within the permitted deviations/tolerances, specified under Technical Data.

Special Notes on Earth Fault Current Transformers

Elements: Earth Fault Protection

Functional Description

Earth current measurement is usually been done with a summation current transformer (core-balance transformer). This CT has a large primary window through which all three-phase conductors can pass.

Note that the earth fault current settings are based on the earth CT rated **primary current (In)**, not on FLA or the phase CT ratio. For example, a pickup setting of 0.10 gives a trip or alarm for an actual earth leakage current of 5 A on the primary side of the sensor with a 50:5 CT.

This function is only useful for a earthed power system. The earth return is normally made from the neutral of the secondary wire winding of the supply power transformer. Resistance earthing is acceptable as long as the resulting fault current is at a level the protective device can be set to detect.

The earth CT, which provides sensitive protection for high-resistance earth faults, may saturate for a robust heavy-current earth fault in a solidly-earthed system. Minimize the saturation problem by minimizing the burden. Use the shortest and heaviest leads possible between the earth CT and the relay. The relay itself has very low burden, usually much lower than the connecting wiring.

A residual connection – the wired summation of the phase CT circuits through the earth CT input – requires a much higher earth fault time setting to avoid false tripping. Thus, sensitivity is not nearly as good as with a separate flux-canceling CT.

If the relay is installed where a residual connection is used, XCT should be set to the same value as CT Pri. The User must then set the earth fault trip level at a high value to avoid nuisance tripping from CT ratio errors, third harmonic and certain higher harmonics, or other measurement errors producing false residual currents. Monitor the metered earth current during various loading conditions to ensure a good margin between these error currents and the earth fault trip current setting 50R [x]. Also, watch out for phase CTs that saturate during motor starting. The saturation produces a large residual current and a earth fault trip.

IG> - Earth Fault [50N/G, 51N/G]

Available elements: IG[1], IG[3], IG[4]



If you are using inrush blockings the tripping delay of the earth current protection functions must be at least 30 ms or more in order to prevent faulty trippings.

Page 366 EN MRM4 12/11

NOTICE

All earth current elements are identically structured.

NOTICE

This module offers Adaptive Parameter Sets.

Parameters can be modified within parameter sets dynamically by means of Adaptive Parameter Sets. Please refer to chapter Parameter / Adaptive Parameter Sets.

The following table shows the application options of the earth overcurrent protection element

Applications of the IE-Protection Module	Setting in	Option
ANSI 50N/G – Earth overcurrent protection, non directional	Device Planning menu Setting: non directional	Measuring Mode: Fundamental/TrueRMS
ANSI 51N/G – Earth short circuit protection, non directional	Device Planning menu Setting: non directional	Measuring Mode: Fundamental/TrueRMS

Measuring Mode

For all protection elements it can be determined, whether the measurement is done on basis of the »Fundamental« or if »TrueRMS« measurement is used.

For each element the following characteristics are available:

- DEFT
- NINV (IEC)
- VINV (IEC)
- LINV (IEC)
- EINV (IEC)
- MINV (ANSI)
- VINV (ANSI)
- EINV (ANSI)
- RXIDG
- Thermal Flat
- IT
- I2T
- I4T

Explanation:

t = Tripping delay

t-char = Time multiplier/tripping characteristic factor. The setting range depends on the selected tripping curve. IG = Fault current

IG> = If the pickup value is exceeded, the module/element starts to time out to trip.

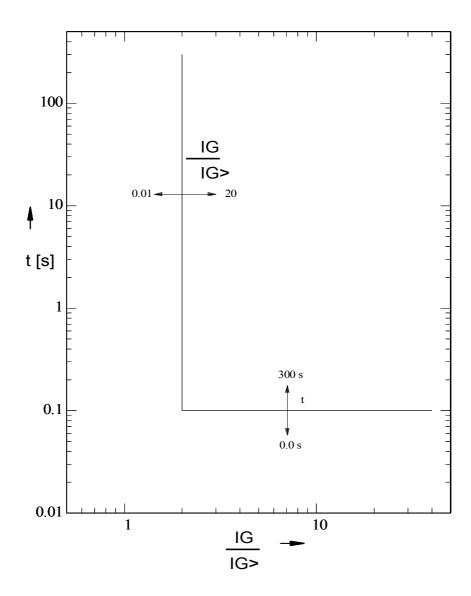
The directional decision depends on the layout of the mains star-point or the angle between residual voltage and ground current. The residual voltage can be measured via suitable transformers (da-dn winding – formerly: e-n) or can be calculated, provided the VTs are in star-connection.

The earth current can be measured either directly via a cable-type transformer or detected by a Holmgreen connection. The earth current can alternatively be calculated from the phase currents; but this is only possible if the phase currents are not ascertained by a V-connection.

The device can optionally be procured with a sensitive earth current measuring input (in preparation).

Page 368 EN MRM4 12/11

DEFT



IEC NINV



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

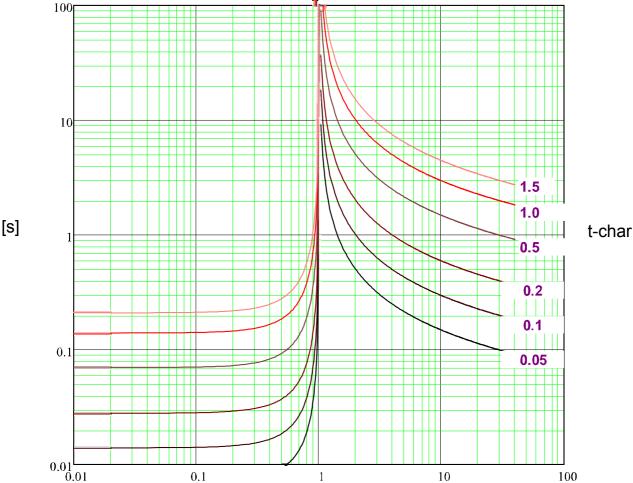
Reset

Trip

$$t = \left| \frac{0.14}{\left(\frac{|G|}{|G|}\right)^2 - 1} \right| * t-char [s]$$

$$t = \frac{0.14}{\left(\frac{|G|}{|G|}\right)^{0.02} - 1} * t-char [s]$$

$$t = \frac{0.14}{\left(\frac{IG}{IG}\right)^{0.02}} *t-char[s]$$



x * IG> (multiples of pickup)

Page 370 EN MRM4 12/11

t [s]

IEC VINV



Notice!

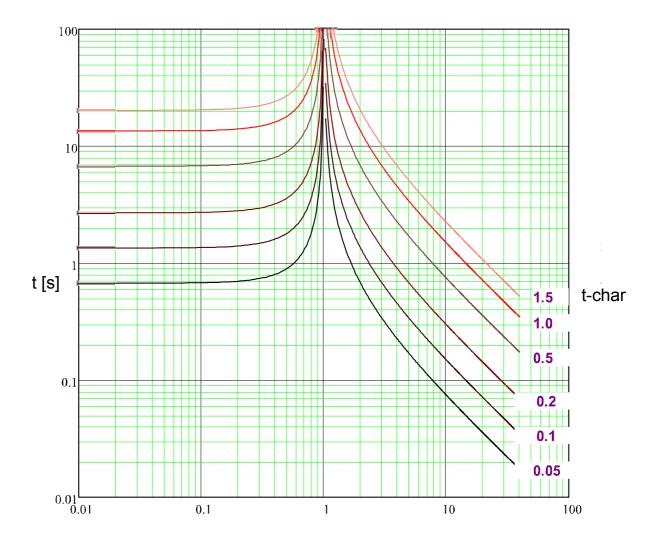
Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{13.5}{\left(\frac{IG}{IG}\right)^2 - 1} \right| * t-char [s]$$

$$t = \frac{13.5}{\left(\frac{IG}{IG}\right) - 1} * t-char [s]$$

$$t = \frac{13.5}{\left(\frac{IG}{IG}\right)-1} *t-char [s]$$



x * IG> (multiples of pickup)

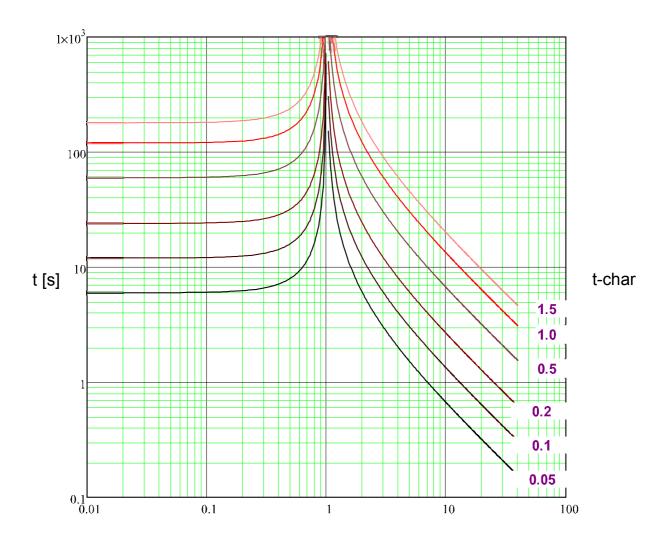
IEC LINV



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip $t = \left| \frac{120}{\left(\frac{|G|}{|G>}\right)^2 - 1} \right| * t-char [s]$ $t = \frac{120}{\left(\frac{|G|}{|G>}\right) - 1} * t-char [s]$



x * IG> (multiples of pickup)

Page 372 EN MRM4 12/11

IEC EINV



Notice!

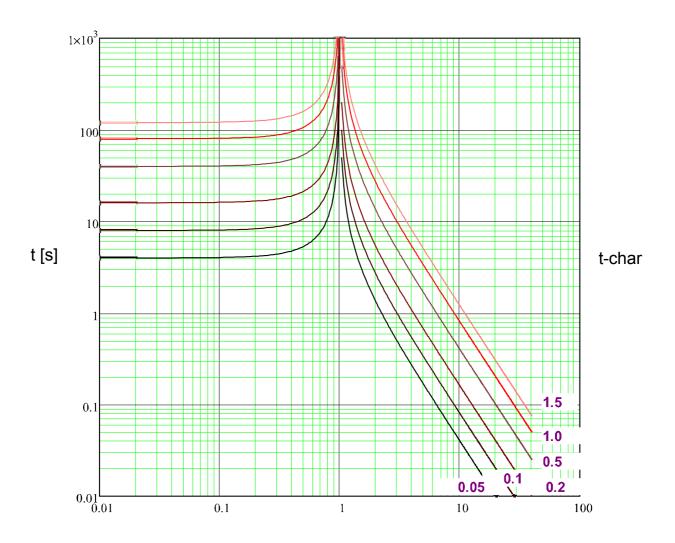
Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{80}{\left(\frac{|G|}{|G|}\right)^2 - 1} \right| * t-char [s]$$

$$t = \frac{80}{\left(\frac{|G|}{|G|}\right)^2 - 1} * t-char [s]$$

$$t = \frac{80}{\left(\frac{|G|}{|G|}\right)^2 - 1} *t-char [s]$$



x * IG> (multiples of pickup)

ANSI MINV



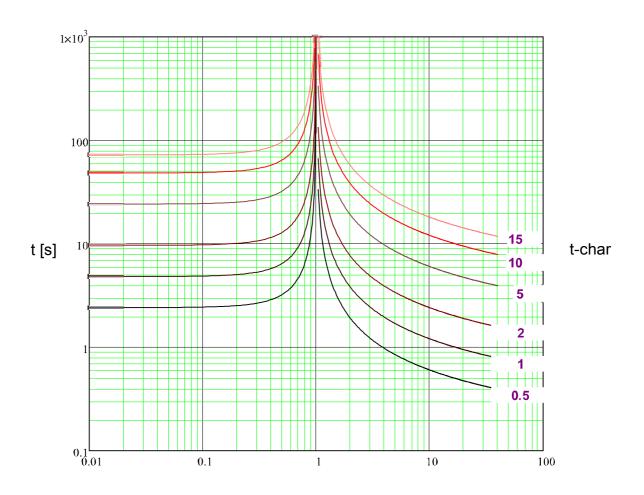
Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{4.85}{\left(\frac{|G|}{|S|}\right)^2 - 1} \right| *t-char [s]$$

$$t = \left| \frac{4.85}{\left(\frac{|G|}{|S|}\right)^2 - 1} \right| *t-char[s] \qquad t = \left(\frac{0.0515}{\left(\frac{|G|}{|G|}\right)^{0.02} + 0.1140} \right) *t-char[s]$$



x * IG> (multiples of pickup)

Page 374 EN MRM4 12/11

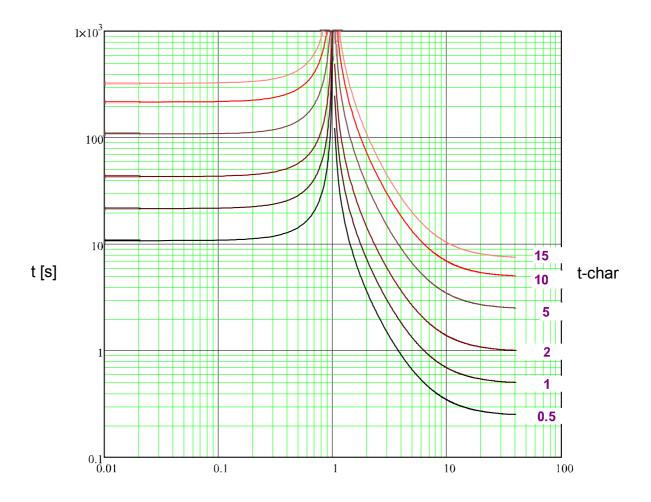
ANSI VINV



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip
$$t = \left| \frac{21.6}{\left(\frac{|G|}{|G|}\right)^2 - 1} \right| *t-char[s] \qquad t = \left(\frac{19.61}{\left(\frac{|G|}{|G|}\right)^2 - 1} + 0.491\right) *t-char[s]$$



x * IG> (multiples of pickup)

ANSI EINV



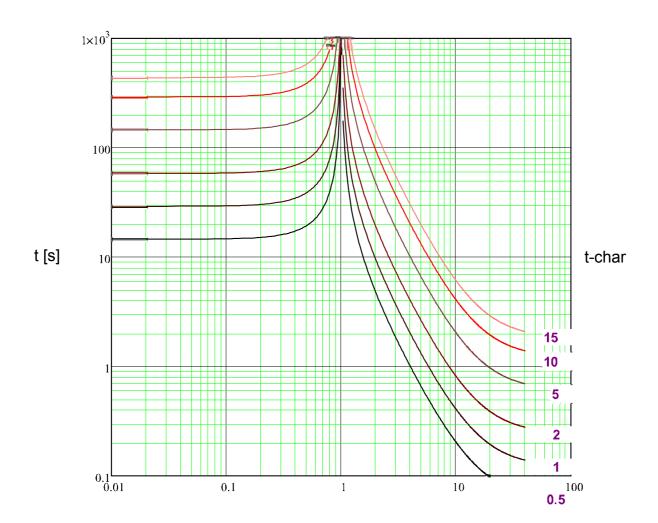
Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset

$$t = \left| \frac{29.1}{\left(\frac{|G|}{|G|}\right)^2 - 1} \right| *t-char [s]$$

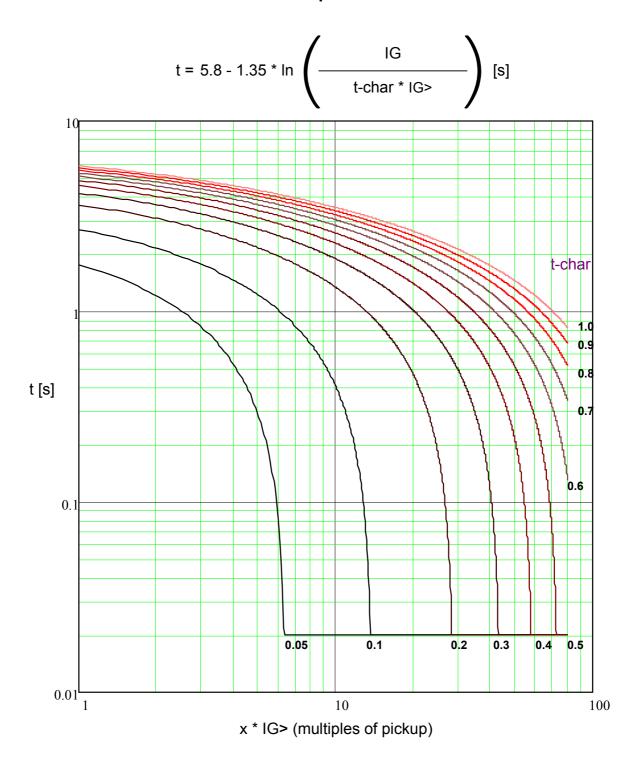
$$t = \left| \frac{29.1}{\left(\frac{IG}{IG}\right)^2 - 1} \right| *t-char[s] \qquad t = \left(\frac{28.2}{\left(\frac{IG}{IG}\right)^2 - 1} + 0.1217 \right) *t-char[s]$$



x * IG> (multiples of pickup)

Page 376 EN MRM4 12/11

Trip



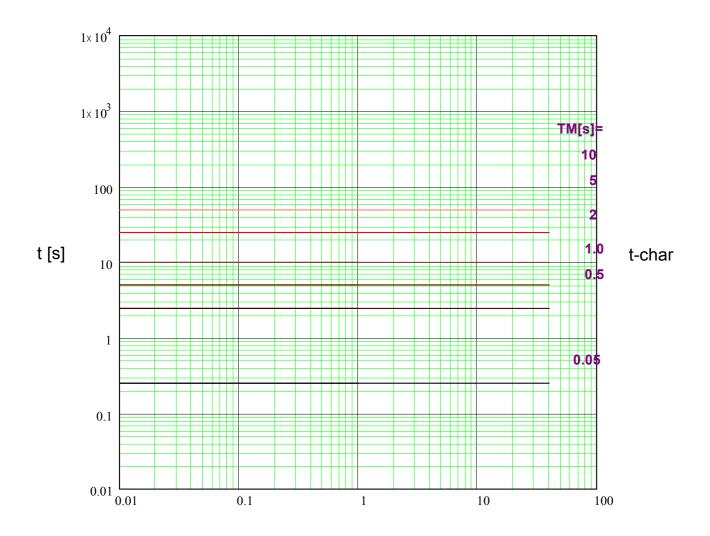
Therm Flat



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip
$$t = \left| \frac{5*1^{2}}{\left(\frac{IG}{IGnom}\right)^{0}} \right| *t-char [s] \qquad t = \frac{5}{\left(\frac{IG}{IGnom}\right)^{0}} *t-char [s]$$



x * IG> (multiples of pickup)

Page 378 EN MRM4 12/11

IT



Notice!

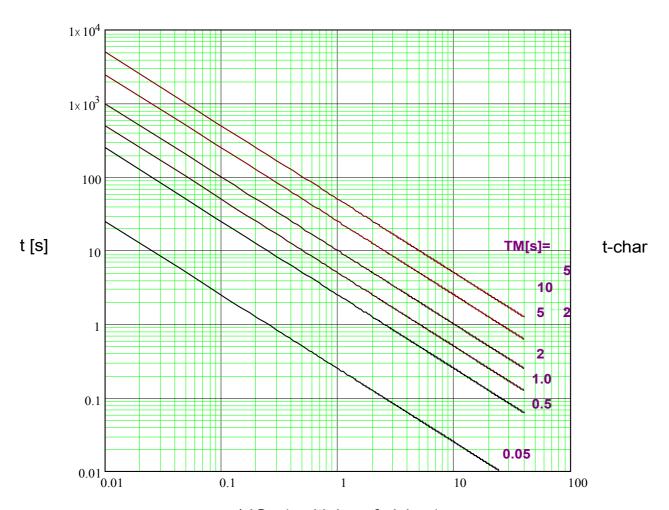
Various reset modes are available. Resetting via characteristic, delayed and

Reset

$$t = \left| \frac{5*1^2}{\left(\frac{IG}{IGnom}\right)^0} \right| * t-char [s]$$

$$t = \frac{5*1^1}{\left(\frac{IG}{IGnom}\right)^1} * t-char [s]$$

$$t = \frac{5*1^{1}}{\left(\frac{IG}{IGnom}\right)^{1}} *t-char [s]$$



x * IG> (multiples of pickup)

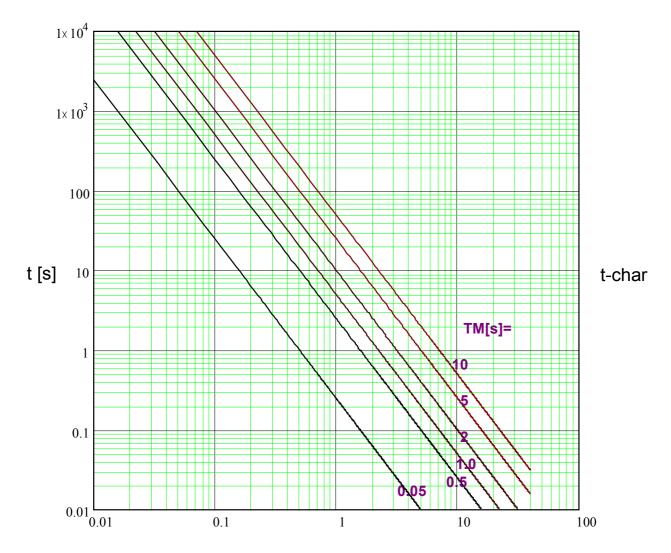
12T



Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip $t = \left| \frac{5^*1^2}{\left(\frac{IG}{IGnom}\right)^0} \right| * t\text{-char [s]} \qquad t = \frac{5^*1^2}{\left(\frac{IG}{IGnom}\right)^2} * t\text{-char [s]}$



x * IG> (multiples of pickup)

Page 380 EN MRM4 12/11

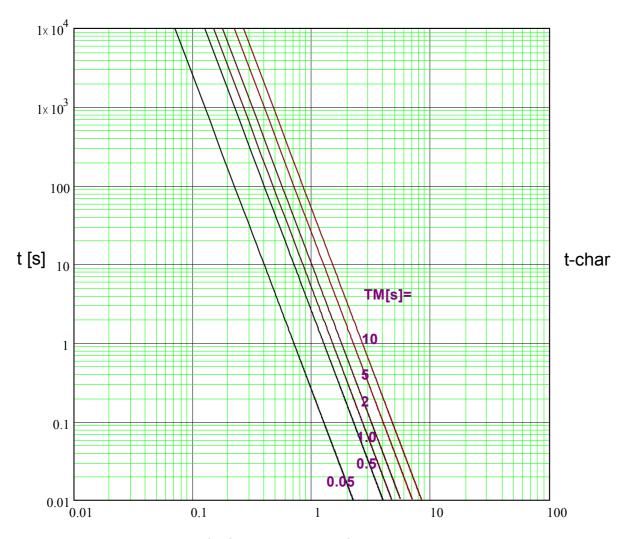
14T



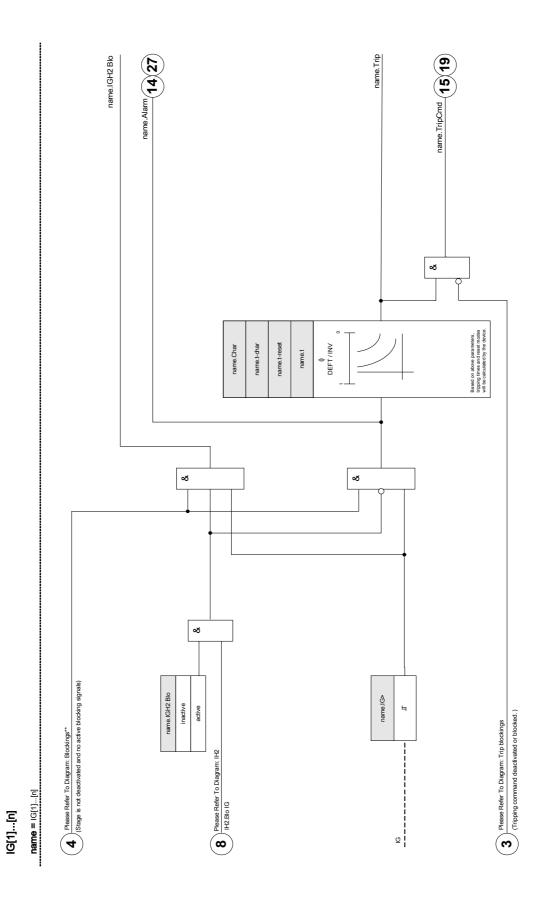
Notice!

Various reset modes are available. Resetting via characteristic, delayed and instantaneous.

Reset Trip
$$t = \left| \frac{5^*1^2}{\left(\frac{IG}{IGnom}\right)^0} \right| *t-char[s] \qquad t = \frac{5^*1^4}{\left(\frac{IG}{IGnom}\right)^4} *t-char[s]$$



x * IG> (multiples of pickup)



Page 382 EN MRM4 12/11

Device Planning Parameters of the Ground Fault Protection

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	do not use	[Device planning]
		non directional		

Global Protection Parameters of the Ground Fault Protection

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para /Global Prot Para /I-Prot /IG[1]]
ExBlo2	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para /Global Prot Para /I-Prot /IG[1]]
ExBlo3	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	, MStart.Blo-GOCStart, MStart.Blo-IOCStart, MStart.Blo-I <start, mstart.blo-generic1,="" mstart.blo-generic2,="" mstart.blo-generic3,="" mstart.blo-generic4,="" mstart.blo-generic5<="" mstart.blo-jamstart,="" mstart.blo-unbalstart,="" td=""><td>MStart.Blo-GOCStart</td><td>[Protection Para /Global Prot Para /I-Prot /IG[1]]</td></start,>	MStart.Blo-GOCStart	[Protection Para /Global Prot Para /I-Prot /IG[1]]
ExBlo TripCmd	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para /Global Prot Para /I-Prot /IG[1]]
Ex rev Interl	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para /Global Prot Para /I-Prot /IG[1]]
AdaptSet 1	Assignment Adaptive Parameter 1	AdaptSet	-:-	[Protection Para /Global Prot Para /I-Prot /IG[1]]

Parameter	Description	Setting range	Default	Menu path
AdaptSet 2	Assignment Adaptive Parameter 2	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/IG[1]]
AdaptSet 3	Assignment Adaptive Parameter 3	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/IG[1]]
AdaptSet 4	Assignment Adaptive Parameter 4	AdaptSet		[Protection Para
				/Global Prot Para
				/I-Prot
				/IG[1]]

Setting Group Parameters of the Ground Fault Protection

Parameter	Description	Setting range	Default	Menu path
Function	module/stage	inactive,	inactive	[Protection Para
		active		/<14>
				/I-Prot
				/IG[1]]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/I-Prot
	those modules/stages are blocked that are configured "ExBlo Fc=active".			/IG[1]]
Ex rev Interl	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true,	active		/<14>
				/I-Prot
	those modules/stages are blocked that are configured "Ex rev Interl Fc = active".			/IG[1]]
Blo TripCmd	Permanent blocking of the Trip Command of the	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/I-Prot
				/IG[1]]
ExBlo	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
TripCmd Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/I-Prot
	those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".			/IG[1]]

Page 384 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
IG Source	Selection if measured or calculated ground current	calculated,	calculated	[Protection Para
	should be used.	measured		/<14>
				/I-Prot
				/IG[1]]
Measuring	Measuring method: fundamental or RMS	Fundamental,	Fundamental	[Protection Para
method		True RMS		/<14>
				/I-Prot
				/IG[1]]
Meas Circuit	Measuring Circuit Supervision	inactive	inactive	[Protection Para
Superv	Only available if the device is equipped with a			/<14>
	Measuring Circuit Supervision.			/I-Prot
				/IG[1]]
IG>	If the pickup value is exceeded, the module/stage	0.02 – 20.00 In	IG[1]: 1 In	[Protection Para
	will be started.		IG[2]: 2 In	/<14>
	Only available if:Default Current Measuring Card		IG[3]: 0.02 In	/I-Prot
	Only available if:Default Current Measuring Card		IG[4]: 0.02 In	/IG[1]]
IGs>	If the pickup value is exceeded, the module/stage	0.002 – 2.000 In	0.02 In	[Protection Para
	will be started.			/<14>
				/I-Prot
				/IG[1]]
Char	Characteristic	DEFT,	DEFT	[Protection Para
		IEC NINV,		/<14>
		IEC VINV,		/I-Prot
		IEC EINV,		/IG[1]]
		IEC LINV,		
		ANSI MINV,		
		ANSI VINV,		
		ANSI EINV,		
		Therm Flat,		
		IT,		
		I2T,		
		I4T		
<u> </u>	Tripping delay	0.00 – 300.00 s	IG[1]: 0.5 s	[Protection Para
			IG[2]: 0.5 s	/<14>
	Only available if: Characteristic = DEFT		IG[3]: 0.00 s	/I-Prot
			IG[4]: 0.00 s	

Parameter	Description	Setting range	Default	Menu path
t-char	Time multiplier/tripping characteristic factor. The setting range depends on the selected tripping curve. Only available if: Characteristic = INV Or Characteristic = Therm Flat Or Characteristic = IT Or Characteristic = I4T	0.02 - 20.00	1	[Protection Para /<14> /I-Prot /IG[1]]
Reset Mode	Reset Mode Only available if: Characteristic = INV Or Characteristic = Therm Flat Or Characteristic = IT Or Characteristic = I2T Or Characteristic = I4T	instantaneous, t-delay, calculated	instantaneous	[Protection Para /<14> /I-Prot /IG[1]]
t-reset	Reset time for intermittent phase failures (INV characteristics only) Only available if: Characteristic = INV Or Characteristic = Therm Flat Or Characteristic = IT Or Characteristic = I2T Or Characteristic = I4T Only available if:Reset Mode = t-delay	0.00 - 60.00 s	0.00 s	[Protection Para /<14> /I-Prot /IG[1]]

Ground Fault Protection Input States

Name	Description	Assignment via
ExBlo1-l	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]
ExBlo TripCmd-I	Trip Command	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]
Ex rev Interl-I	interlocking	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]
AdaptSet1-I	Module input state: Adaptive Parameter1	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]

Page 386 EN MRM4 12/11

Name	Description	Assignment via
AdaptSet2-I	Module input state: Adaptive Parameter2	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]
AdaptSet3-I	Module input state: Adaptive Parameter3	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]
AdaptSet4-I	Module input state: Adaptive Parameter4	[Protection Para
		/Global Prot Para
		/I-Prot
		/IG[1]]

Ground Fault Protection Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Ex rev Interl	Signal: External reverse Interlocking
Blo TripCmd	Signal: Trip Command blocked
ExBlo TripCmd	Signal: External Blocking of the Trip Command
Alarm	Signal: Alarm IG
Trip	Signal: Trip
TripCmd	Signal: Trip Command
Active AdaptSet	Active Adaptive Parameter
DefaultSet	Signal: Default Parameter Set
AdaptSet 1	Signal: Adaptive Parameter 1
AdaptSet 2	Signal: Adaptive Parameter 2
AdaptSet 3	Signal: Adaptive Parameter 3
AdaptSet 4	Signal: Adaptive Parameter 4

Ground Fault Protection Counter Values

Value	Description	Default	Size	Menu path
NumberOfAlarms	Number of alarms since last reset.	0	0 - 9999999999	[Operation
				/History
				/AlarmCr]
NumberOfTrips	Number of trips since last reset	0	0 - 9999999999	[Operation
				/History
				/TripCr]

Commissioning: Ground Fault Protection – Non-directional [50N/G, 51N/G]

Please test the non-directional earth overcurrent analog to the non-directional phase overcurrent protection.

I< - Undercurrent [37]

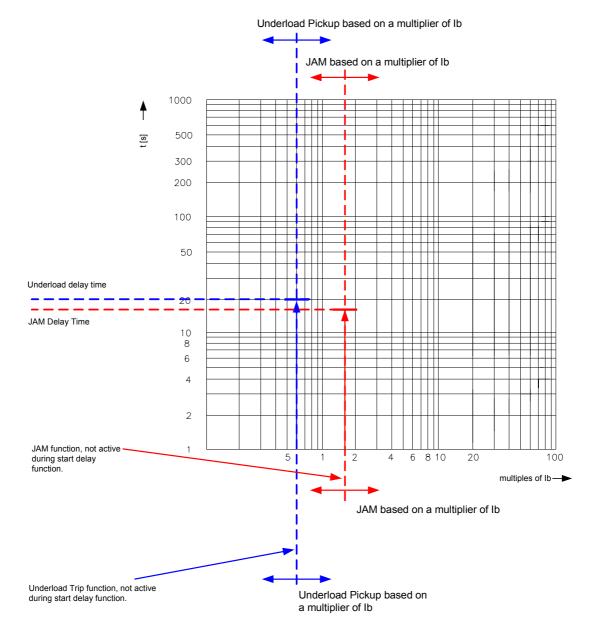
Available Elements: <a href="I<[1]">I<[1], <a href="I<[2], <a href="I<[3]

Functional Description

When the motor is running, a current reduction might indicate a malfunction in the load. <u>Underload</u> protection recognizes mechanical problems, such as a blocked flow or loss of back pressure in a pump, or a broken drive belt or drive shaft.

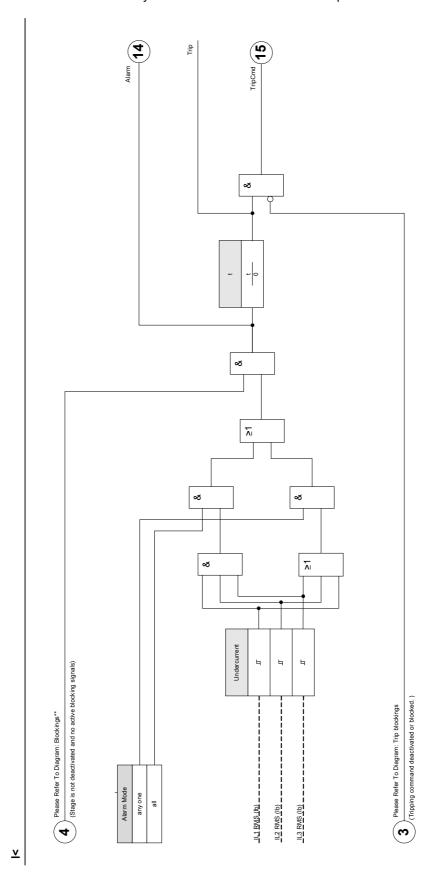
Refer to the under load protection limit - the left vertical line in the "Under load and Jam Trip Function example". In the example, the under load trip is set at 60% of lb (FLA). The protective device can be configured for under load alarm (if the trip command is blocked) and under load trip.

Under load and JAM Trip Function



Page 388 EN MRM4 12/11

These would be represented by two such vertical lines, both below the normal load current. Be sure to set the alarm level **above** the trip level. Each element has its own delay timer. Use the start delay to block tripping until the load stabilizes after a start. Use run delays to avoid nuisance alarms or trips for load transients.



Device Planning Parameters of the Under load Module

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	I<[1]: use	[Device planning]
		use	I<[2]: do not use	
			I<[3]: do not use	

Global Protection Parameters of the Under load Module

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
	3			/ <
				/I<[1]]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and cause of the coordinate of			/ <
				/I<[1]]
ExBlo3	External blocking of the module, if blocking is	,	MStart.Blo-I <start< td=""><td>[Protection Para</td></start<>	[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.	MStart.Blo-GOCStart,		/Global Prot Para
		MStart.Blo-IOCStart,		/ <
		MStart.Blo-I <start,< td=""><td></td><td>/I<[1]]</td></start,<>		/I<[1]]
		MStart.Blo-JamStart,		
		MStart.Blo-UnbalStart,		
		MStart.Blo-Generic1,		
		MStart.Blo-Generic2,		
		MStart.Blo-Generic3,		
		MStart.Blo-Generic4,		
		MStart.Blo-Generic5		
ExBlo	External blocking of the Trip Command of the	1n, Assignment List		[Protection Para
TripCmd	module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the			/Global Prot Para
	assigned signal is true.			/ <
				/I<[1]]
Ex rev Interl	External blocking of the module by external	1n, Assignment List		[Protection Para
	reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of			/Global Prot Para
	the assigned signal is true.			/ <
				/I<[1]]

Page 390 EN MRM4 12/11

Setting Group Parameters of the Under load Module

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/ <
				/I<[1]]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/I<
	those modules/stages are blocked that are configured "ExBlo Fc=active".			/I<[1]]
Ex rev Interl	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/ <
	those modules/stages are blocked that are configured "Ex rev Interl Fc = active".			/I<[1]]
Blo TripCmd	Permanent blocking of the Trip Command of the	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/ <
				/I<[1]]
ExBlo	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
TripCmd Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/ <
	those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".			/I<[1]]
Undercurrent	Under load Pickup based on a multiplier of lb	0.05 – 0.90 lb	0.50 lb	[Protection Para
				/<14>
				/ <
				/I<[1]]
Alarm Mode	Indicates if one, two of three or all phases are	any one,	any one	[Protection Para
	required for operation	all		/<14>
				/I<
				/I<[1]]
t	Tripping delay	0.4 – 1200.0 s	I<[1]: 10.0 s	[Protection Para
			I<[2]: 10.0 s	/<14>
			I<[3]: 0.4 s	/ <
				/I<[1]]
Meas Circuit	Measuring Circuit Supervision	inactive	inactive	[Protection Para
Superv				/<14>
				/ <
				/I<[1]]

Under load Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/I<
		/I<[1]]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/I<
		/I<[1]]
ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command	[Protection Para
		/Global Prot Para
		/I<
		/I<[1]]
Ex rev Interl-I	Module input state: External reverse	[Protection Para
	interlocking	/Global Prot Para
		/I<
		/I<[1]]

Under load Module Signals (Output States)

Name	Description	
active	Signal: active	
ExBlo	Signal: External Blocking	
Ex rev Interl	Signal: External reverse Interlocking	
Blo TripCmd	Signal: Trip Command blocked	
ExBlo TripCmd	Signal: External Blocking of the Trip Command	
Alarm	Signal: Alarm	
Trip	Signal: Trip	
TripCmd	Signal: Trip Command	

Under load Module Counter Values

Value	Description	Default	Size	Menu path
NumberOfAlarms	Number of alarms since last reset.	0	0 - 9999999999	[Operation
				/History
				/AlarmCr]

Page 392 EN MRM4 12/11

Value	Description	Default	Size	Menu path
NumberOfTrips	Number of trips since last reset	0	0 - 9999999999	[Operation
				/History
				/TripCr]

Commissioning: Undercurrent [ANSI 37]

Object to be tested

- Testing the pickup value for Undercurrent protection
- Testing the trip delay
- Testing the drop-out ratio

Necessary means

- 3-phase current source
- Ammeter
- Timer for measuring of the tripping time

Procedure

Testing the threshold values (single-phase, three phase)

Feed in a testing current significantly greater than the pickup value. For testing the threshold values and dropout values, the test current has to be decreased until the relay is energized. When comparing the displayed values with those of the ammeter, the deviation must be within the permissible tolerances.

Testing the trip delay

For testing the trip delay, a timer is to be connected to the contact of the associated trip relay. Feed in a testing current significantly greater than the pickup value, the test current has to be decreased suddenly below the threshold value. The timer is started when the limiting value of the tripping current falls below the threshold and the operating time is elapsed and it is stopped when the relay trips.

Testing the drop-out ratio

Enlarge the measuring quantity to more than 103% of the trip value. The relay must only fall back at 103% of the trip value at the earliest.

Successful test result

The measured tripping delays, threshold values and drop-out ratio comply with those specified in the adjustment list. Permissible deviations/tolerances can be taken from Technical Data.

JAM [51LR]

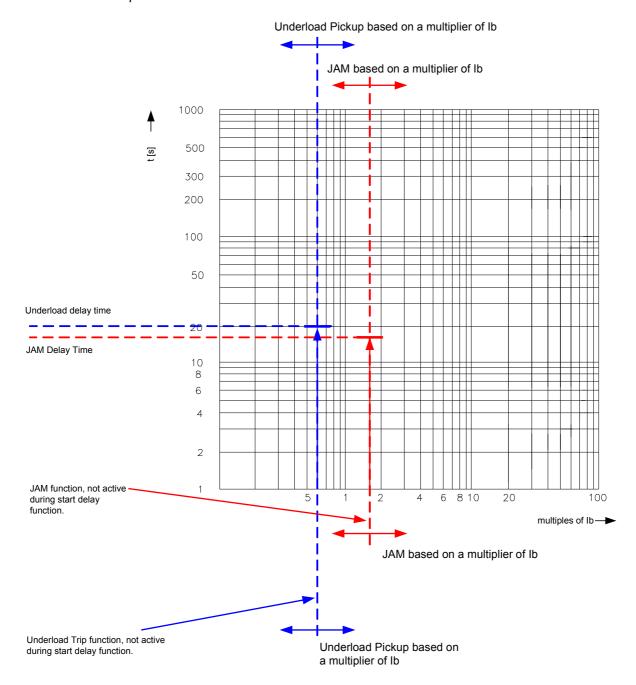
Elements
Jam[1],Jam[2]

Functional Description

When the motor is running, a current increase above normal load may be an indication of a malfunction in the load. <u>JAM</u> protection recognizes mechanical problems, such as broken drive gears. Refer to the <u>JAM</u> protection limit (the right vertical line in the "Under load and JAM Trip Function" curve example). In this curve example, the

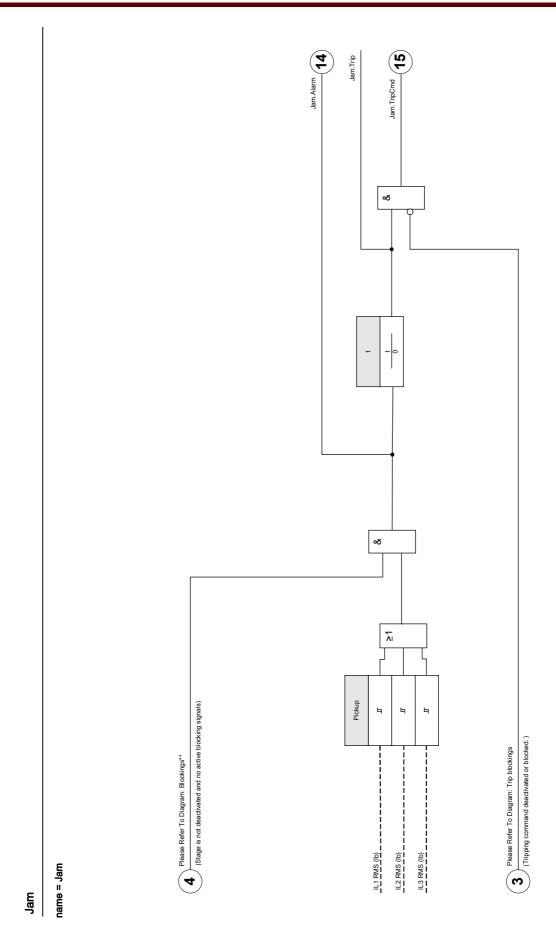
JAM trip is set at 150% of lb (FLA).

Under load and JAM Trip Function



The protective device issues an alarm when the pickup is exceeded. If the timer has elapsed, a trip signal will be issued. In the "Under load and JAM Trip Function" curve, the »TRIP« settings are represented by vertical lines, well above the normal load current. This curve also applies to JAM setting configured as an alarm element (blocked trip command). The trips are held off by the delay timer »t«. Use the start delay to block tripping and alarming until the motor current drops to continuous load level. Use run delays to avoid nuisance alarms or trips for load transients.

Page 394 EN MRM4 12/11



Device Planning Parameters for JAM Protection

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	Jam[1]: use	[Device planning]
		use	Jam[2]: do not	
			use	

Global Protection Parameters for JAM Protection

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
				/JAM-Prot
				/Jam[1]]
ExBlo2	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
				/JAM-Prot
				/Jam[1]]
ExBlo3	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	,	MStart.Blo-	[Protection Para
		MStart.Blo-GOCStart,	JamStart	/Global Prot Para
	are state of the designed signal to trace.	MStart.Blo-IOCStart,		/JAM-Prot
		MStart.Blo-I <start,< td=""><td></td><td>/Jam[1]]</td></start,<>		/Jam[1]]
		MStart.Blo-JamStart,		
		MStart.Blo-UnbalStart,		
		MStart.Blo-Generic1,		
		MStart.Blo-Generic2,		
		MStart.Blo-Generic3,		
		MStart.Blo-Generic4,		
		MStart.Blo-Generic5		
ExBlo	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
TripCmd				/Global Prot Para
				/JAM-Prot
				/Jam[1]]
Ex rev Interl	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
				/JAM-Prot
				/Jam[1]]

Page 396 EN MRM4 12/11

Setting Group Parameters for JAM Protection

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive,	inactive	[Protection Para
		active		/<14>
				/JAM-Prot
				/Jam[1]]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/JAM-Prot
	those modules/stages are blocked that are configured "ExBlo Fc=active".			/Jam[1]]
Ex rev Interl	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/JAM-Prot
	those modules/stages are blocked that are configured "Ex rev Interl Fc = active".			/Jam[1]]
Blo TripCmd	Permanent blocking of the Trip Command of the module/stage.	inactive,	Jam[1]: inactive	[Protection Para
		active	Jam[2]: active	/<14>
				/JAM-Prot
				/Jam[1]]
ExBlo	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
TripCmd Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true,	active		/<14>
				/JAM-Prot
	those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".			/Jam[1]]
Pickup	JAM based on a multiplier of lb	1.00 – 12.00 lb	Jam[1]: 10 lb	[Protection Para
			Jam[2]: 5 lb	/<14>
				/JAM-Prot
				/Jam[1]]
t	Tripping delay	0.0 – 1200.0 s	Jam[1]: 2.0 s	[Protection Para
			Jam[2]: 10.0 s	/<14>
				/JAM-Prot
				/Jam[1]]

JAM Protection Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/JAM-Prot
		/Jam[1]]

Name	Description	Assignment via
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/JAM-Prot
		/Jam[1]]
ExBlo TripCmd-I	Module input state: External Blocking of the	[Protection Para
	Trip Command	/Global Prot Para
		/JAM-Prot
		/Jam[1]]
Ex rev Interl-I	interlocking	[Protection Para
		/Global Prot Para
		/JAM-Prot
		/Jam[1]]

JAM Protection Signals (Output States)

Name	Description	
active	Signal: active	
ExBlo	Signal: External Blocking	
Ex rev Interl	Signal: External reverse Interlocking	
Blo TripCmd	Signal: Trip Command blocked	
ExBlo TripCmd	Signal: External Blocking of the Trip Command	
Alarm	Signal: Alarm	
Trip	Signal: Trip	
TripCmd	Signal: Trip Command	

JAM Protection Values

Value	Description	Default	Size	Menu path
NumberOfAlarms	Number of alarms since last reset.	0	0 - 9999999999	[Operation
				/History
				/AlarmCr]
NumberOfTrips	Number of trips since last reset	0	0 - 9999999999	[Operation
				/History
				/TripCr]

Commissioning: JAM [51LR]

Object to be tested

- Testing the pickup value for JAM protection
- Testing the trip delay

Page 398 EN MRM4 12/11

Testing the drop-out ratio

Necessary means

- 3-phase current source
- Ammeter
- Timer for measuring of the tripping time

Procedure

Testing the threshold values(single-phase)

Feed in a testing current significantly smaller than the pickup value. For testing the threshold values and dropout values, the test current has to be increased until the relay is energized. When comparing the displayed values with those of the ammeter, the deviation must be within the permissible tolerances.

Testing the tripping delay

For testing the trip delay, a timer is to be connected to the contact of the associated trip relay. Feed in a testing current significantly smaller than the pickup value, the test current has to be increased suddenly above the threshold value. The timer is started when the limiting value of the tripping current exceeded the threshold and the operating time is elapsed and it is stopped when the relay trips.

Testing the drop-out ratio

Enlarge the measuring quantity to less than 97% of the trip value. The relay must only fall back at 98% of the trip value at the earliest.

Successful test result

The measured tripping delays, threshold values and drop-out ratio comply with those values, specified in the adjustment list. Permissible deviations/tolerances can be found under Technical data.

LRC - Locked Rotor during Start

Functional Description

The Locked-rotor protection function is an integral part of the thermal model and is used to protect the motor in the event that the motor fails to start or accelerate after being energized. The heating in the motor during this period of time can be significantly higher than the heating at rated current, ranging from 10 to 50 times the normal rated heating. The time that a motor can remain at a standstill after being energized varies with the applied voltage and has an I²T limit.

When determining the heat in the motor during this period of time, both the negative and positive sequence currents are used in the equation that approximates the heat generated in a locked rotor condition. The heat can be approximated by the equation:

$$|_{1}^{2}|_{1} = |_{1}^{2} + |_{2}^{2}$$

where:

 I_1 = the per unit stator positive sequence current;

K = weighting factor for the value of I₂ resulting from the disproportionate heating caused by the negative sequence current component due to skin effect in the rotor bar; and

 I_2 = per unit stator negative sequence current.

Settings for the Locked Rotor Current can be found under the [System Parameters]. The LRC value is a multiplier of Ib (FLA).

MLS - Mechanical Load Shedding

Available elements: MLS

Functional Description

In some applications, the protective device can forestall a JAM alarm or trip, or a thermal trip, by sending a signal to the process to reduce loading. The load-shedding function, if enabled, closes or opens a relay contact to shed process load when the motor load current goes above the Load-shed threshold, for a time exceeding the **xt-Pickup Delay**. This could, for example, be connected to stop flow of material into the driven process until the load current falls below the threshold, for the time determined by the **xt-Drop Delay**.

Set the load-shed drop current comfortably below the JAM trip level. It may be useful to set it below the Ultimate Trip Current, particularly if Remote Temperature Detection is not used.

The load shed function, is active only during the »RUN« state of the motor.

Page 400 EN MRM4 12/11

[S] t-Drop Delay Pickup Threshold

Note: Load Shedding function is only active when motor is in RUN mode.

Three Phase Currents

Device Planning Parameters of the Load Shedding

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Global Protection Parameters of the Load Shedding

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	are state or the designed digital to due.			/MLS]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	are state of the assigned digital to true.			/MLS]

Setting Group Parameters of the Load Shedding

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive,	inactive	[Protection Para
		active		/<14>
				/MLS]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".			/MLS]
Pickup	Mechanical load shedding pickup current as multiplier of Ib	0.50 – 1.50 lb	0.90l b	[Protection Para
Threshold				/<14>
				/MLS]
t-Pickup	Trip delay time	0.0 – 5.0 s	1.0 s	[Protection Para
Delay				/<14>
				/MLS]
Dropout	Mechanical load reclosure current (Dropout of Load shedding) as multiplier of lb	0.50 – 1.50 lb	0.50 lb	[Protection Para
Threshold				/<14>
				/MLS]
t-Drop Delay	Dropout delay time	0.0 – 5.0 s	1.0 s	[Protection Para
				/<14>
				/MLS]

Page 402 EN MRM4 12/11

Load Shedding Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/MLS]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/MLS]

Load Shedding Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Alarm	Signal: Alarm
Trip	Signal: Trip

Commissioning: Mechanical Load Shedding

Object to be tested

- Testing the pickup and drop-out thresholds
- Testing the delay times

Necessary means

- 3-phase current source
- Ammeter
- Timer for measuring of the tripping times

Procedure

Testing the threshold values (three-phase)

This test is only possible, if the motor is in run mode.

Testing pickup threshold

The drop-out delay time should be 0s for this test.

Feed in a testing current significantly lower than the threshold of the mechanical load shedding. The test current has to be increased until the relay is energized. When comparing the measured values with those of the ammeter, the deviation must be within the permissible tolerances.

Testing drop-out threshold

For testing the drop-out threshold the testing current has to be significantly greater than the pickup threshold value. The test current has to be decreased until the relay is falls back. When comparing the measured values with those of the ammeter, the deviation must be within the permissible tolerances.

Testing the delay times

This test is only possible, if the motor is in run mode.

Testing the trip delay

For testing the pickup delay, a timer is to be connected to the contact of the associated trip relay. Feed in a testing current significantly lower than the pickup value, the test current has to be increased suddenly above the threshold. The timer is started when the limiting value of the tripping current exceeded the threshold and it is stopped when the relay trips and the operating time is elapsed.

Testing the drop-out delay

For testing the drop-out threshold, the testing current has to be significantly greater than the pickup threshold. A timer is to be connected to the contact of the associated trip relay. The test current has to be decreased suddenly below the drop-out threshold. The timer has to be started when the limiting value of the tripping current falls below the threshold and it has to be stopped when the relay falls back.

Successful test result

The measured tripping delays and threshold values comply with those values, specified in the adjustment list. Permissible deviations/tolerances can be found under Technical data.

Theta - Thermal Model [49M, 49R]

Available Elements: ThR

General - Principle Use

Thermal Protection and Alarm

This protective device provides a thermal model. The thermal model can work with or without the URTD. The RTD-based direct temperature trips and alarms are independent of the thermal model. Without the URTD, meaning the URTD is not connected to the protective device or it is connected but not configured for the thermal protection trips, the thermal model protection will be solely based on the following settings:

- 1.lb Full Load Ampere (FLA);
- 2.Locked Rotor Current (LRC);
- 3. Maximum Allowable Stall Time (Tc);
- 4.UTC (Ultimate Trip Current) or k-Factor;
- 5. Thermal Model Trip Threshold if enabled;
- 6.Trip Delay;
- 7. Thermal Model Alarm Threshold if enabled; and
- 8.Alarm Delay.

The first four settings (1-4) dictate the maximum allowable thermal limit curve of the protected equipment, and the last four settings (6-9) define the thermal trip and alarm curves relative to the thermal limit curve.

Mathematically, the thermal limit curve can be expressed as the following:

$$Trip Time = \frac{I_{LR}^2 * T_{LR}}{I_{ef}^2} \quad \text{when} \quad I_{ef} > k_{Factor} * I_b$$

If the direct stator temperature measurements are available, the thermal replica model will be modified to include the heat loss between stator and rotor. As a result, the motor will be able to run longer under overload conditions. The heat loss serves as a cooling. At some point, the cooling effect will cancel the heat increment so that the thermal capacity used will reach some steady-state level that may be below the trip or alarm limit. This equivalently raises the *»k-factor«* and shifts the trip curve right.

Page 404 EN MRM4 12/11

If the thermal capacity used is held at a level that is below the trip threshold, the thermal model will not trip. To prevent the protected equipment from overheating, the direct temperature trip function must be enabled. Keep in mind that in order for the stator temperature to be effective in the thermal replica model, the following conditions must be met:

- Some RTD channels must be configured to measure the winding temperatures; and
- These RTD channels must be enabled for trip.

In addition, at least one of these winding temperatures must be valid.

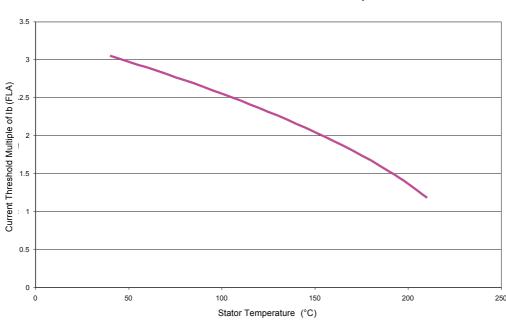
Knowing the maximum steady stator temperature Θ_s (°C), the thermal capacity used can be estimated by the following formula.

$$TC_{Used}\% = (\frac{\Theta_S}{240} + \frac{I_{ef}^2 * 50}{I_{LR}^2 * T_{LR}})$$
 when $I_{ef} > Ith * FLA$

Take for example, ILR = 6 * FLA, TLR = 15, and thermal trip level of 100%. The relationship between the effective current threshold and the stator temperature can be seen in the Stator Temperature Effect on Current Threshold Curve.

Stator Temperature Effect on Current Threshold Curve

From the graph, it is seen that the lower the stator temperature, the higher the effective current threshold.



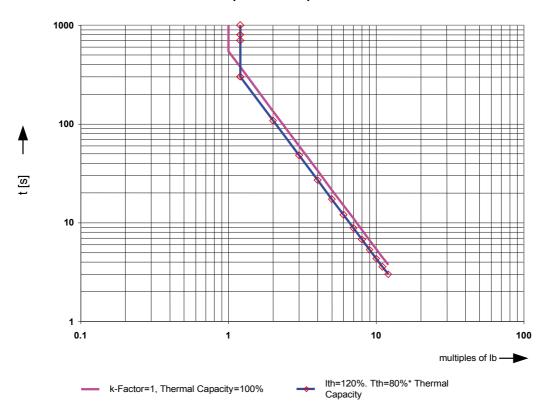
Effective Current Threshold vs. Maximum Stator Temperature

Without stator temperature, given the current threshold of 1.0 * Ib (FLA) and 2.0 * Ib (FLA) of the stator phase current, the thermal model will use the full thermal capacity in 139.54 seconds. However, if the stator temperature is known as 100°C (212°F), the effective ultimate trip current threshold is raised to 2.55 * Ib (FLA) and the thermal capacity used will reach a steady state of 77.5%. As a result, the thermal model will never trip under this condition. From this example, it can be seen that the stator RTD could keep the motor running under

overload condition. In this case, the appropriate direct stator temperature trip function must be enabled.

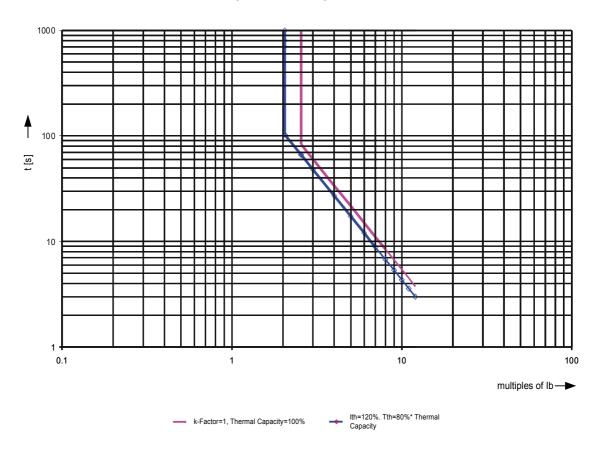
In the Thermal Replica Model Trip Curves with and without RTD, the unmarked lines are the thermal limit curves and the marked lines are the trip curves. From the curve without RTD, it can be seen that one can change the thermal current threshold to shift the upper portion of the trip curve right to allow the motor to run at a higher overload condition than is specified with the service factor. From the curve with RTD, it can be seen that the stator RTD pushes the effective thermal current threshold to 2.55 * lb (FLA) on the thermal limit curve (unmarked line). The marked line is the trip curve with 80% thermal capacity trip threshold, so actual effective thermal current threshold for the trip curve is about 2.05 * lb (FLA). Although in this case, the thermal current threshold is set to 1.50 * lb (FLA), it is effectively raised to a higher level with the stator RTD. Keep in mind that thermal limit and trip curves shown are based on the example above. They will vary with other sets of the settings.

Thermal Replica and Trip Curves without RTD



Page 406 EN MRM4 12/11

Thermal Replica Limit and Trip Curves with RTD=100°C



Global Protection Parameters of the Thermal Model

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and close or and doorgined digital to trace			/I-Prot
				/ThR]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List	-,-	[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
				/I-Prot
				/ThR]
ExBlo	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
TripCmd				/Global Prot Para
				/I-Prot
				/ThR]
Use RTD	Take RTD values into account for the calculation	inactive,	inactive	[Protection Para
values	of the Thermal Model	active		/Global Prot Para
				/I-Prot
				/ThR]

Parameter	Description	Setting range	Default	Menu path
K2	This value represents the negative sequence current weighting factor of the motor.	0.10 - 10.00	6.01	[Protection Para
				/Global Prot Para
				/I-Prot
				/ThR]
т-сооІ	Cooling time constant	5 - 240	60	[Protection Para
				/Global Prot Para
				/I-Prot
				/ThR]

Setting Group Parameters of the Thermal Model

Parameter	Description	Setting range	Default	Menu path
Function		inactive,	active	[Protection Para
	module/stage.	active		/<14>
				/I-Prot
				/ThR]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/I-Prot
	those modules/stages are blocked that are configured "ExBlo Fc=active".			/ThR]
Blo TripCmd	Permanent blocking of the Trip Command of the	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/I-Prot
				/ThR]
ExBlo	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
TripCmd Fc	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/I-Prot
	those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".			/ThR]
Trip Function	Turn on or off the trip function	inactive,	active	[Protection Para
		active		/<14>
				/I-Prot
				/ThR]
Trip	Trip threshold at which the thermal model will trip,	0.60 - 0.99	0.99	[Protection Para
Threshold	based on percentage of thermal capacity used. This value should typically always be set at 0.99			/<14>
	. , , ,			/I-Prot
	Only available if: Trip Function = active			/ThR]

Page 408 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
t-Trip Delay	Thermal capacity used trip delay	0.0 – 3600.0 s	0.0 s	[Protection Para
	Only available if: Trip Function = active			/<14>
	City available in trip i arieller.			/I-Prot
				/ThR]
Alarm	Turn on or off the alarm function	inactive,	active	[Protection Para
Function		active		/<14>
				/I-Prot
				/ThR]
Alarm	Alarm threshold at which the thermal model will	0.60 - 0.99	0.70	[Protection Para
Threshold	trip, based on percentage of thermal capacity used			/<14>
				/I-Prot
	Only available if: Alarm Function = active			/ThR]
t-Alarm	Thermal capacity used alarm delay	1 – 360 min	1 min	[Protection Para
Delay	Only available if: Alarm Function = active			/<14>
	City available in Auditin and and			/I-Prot
				/ThR]

Thermal Model Module Input States

Name	Description	Assignment via
ExBlo1	Module input state: External blocking	[Protection Para
		/Global Prot Para
		/I-Prot
		/ThR]
ExBlo2	Module input state: External blocking	[Protection Para
		/Global Prot Para
		/I-Prot
		/ThR]
ExBlo TripCmd	Module input state: External Blocking of the	[Protection Para
	Trip Command	
		/I-Prot
		/ThR]

Thermal Model Module Signals (Output States)

Name	Description
Alarm Pickup	Signal: Alarm Pickup
Alarm Timeout	Signal: Alarm Timeout
RTD effective	RTD effective

Name	Description
Load above SF	Load above Service Factor
active	Signal: active
ExBlo	Signal: External Blocking
Blo TripCmd	Signal: Trip Command blocked
ExBlo TripCmd	Signal: External Blocking of the Trip Command
Alarm	Signal: Alarm
Trip	Signal: Trip
TripCmd	Signal: Trip Command

Direct Commands of the Thermal Model Module

Parameter	Description	Setting range	Default	Menu path
Res I2T	Reset thermal capacity used.	inactive,	inactive	[Operation
Used		active		/Reset/Acknowledge
				/Reset]

Thermal Model Module Counter Values

Value	Description	Default	Size	Menu path
I2T Used	Thermal capacity used.	0%	0 - 1000%	[Operation
				/Measured values
				/ThR]
I2T Remained	Thermal capacity remained.	0%	0 - 1000%	[Operation
				/Measured values
				/ThR]
NumberOfTrips	Number of trips since last reset	0	0 - 65535	[Operation
				/History
				/TripCr]
NumberOfAlarms	Number of alarms since last reset.	0	0 - 65535	[Operation
				/History
				/AlarmCr]

UTC - Ultimate Trip Current

Functional Description

The Ultimate Trip Current (UTC) sets the current level at which a trip eventually occurs and is settable to a value as a multiples of »*Ib*« (Full Load Amps (FLA)). This value represents the vertical line on the upper portion of the non-RTD as shown in the protection trip curve labeled "Motor Protection Curve Example 2 (without RTD)". The ultimate trip current setting in this example is at 1 times the of »*Ib*« (FLA).

Page 410 EN MRM4 12/11

The User has to set the k-Factor which can be calculated by the following formula:

$$k_{Factor} = \frac{UTC}{CT_{PRI}} = \frac{Overload_{factor} * I_b}{CT_{PRI}}$$

Please note that the settings for k-Factor and Ib have to be set within the <u>System Parameter</u> menu.

For normal use, set »UTC« to the »k-Factor« times 100%. The »k-Factor« is found on the motor nameplate or in the manufacturer's data. Note that the relay does not trip at the moment the current goes above »UTC« during motor running. Instead, it models the gradual stator heating for currents above »UTC«, and trips only after some time has passed. The trip time depends on a variety of setting and operating factors, including the motor nameplate data contained in other setting values.

Use a conservative value. In this case, a lower value of »UTC« than that dictated by the »k-Factor« if the motor ambient temperatures may rise above 40°C (104°F) and the optional <u>URTD</u> Module is not used, otherwise stator insulation damage or loss of motor life may occur. Also, consider lowering the »UTC« value if the motor is suitably rated, yet additional safety is critical for the application.



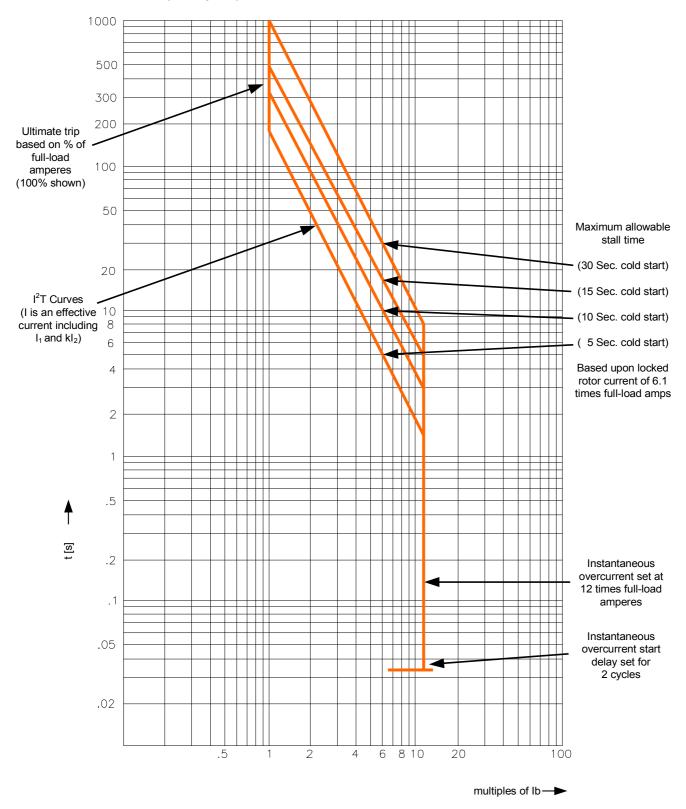
ACAUTION If UTC is set above 100% times the service factor, motor damage could result.

In systems where an RTD is used the »UTC« pickup point is biased by the measured temperature. This is shown in the example trip curve labeled "Motor Protection Curve Example 3" (with RTD) were you will see a shift in the »UTC« value to 2 times of »Ib« (FLA)

If stator temperature measurements are available, the algorithm may keep from tripping, even if the effective current is above the ultimate trip current setting, depending on stator temperature reports. It is still important to set a correct ultimate trip current so that the motor is well protected. If the RTDs, the module, or its communications to the relay fail, the algorithm falls back to use of »UTC«. Also, note that if all RTD channels are set to »OFF«, the algorithm reverts to the non-RTD calculation, which is based strictly on »UTC«.

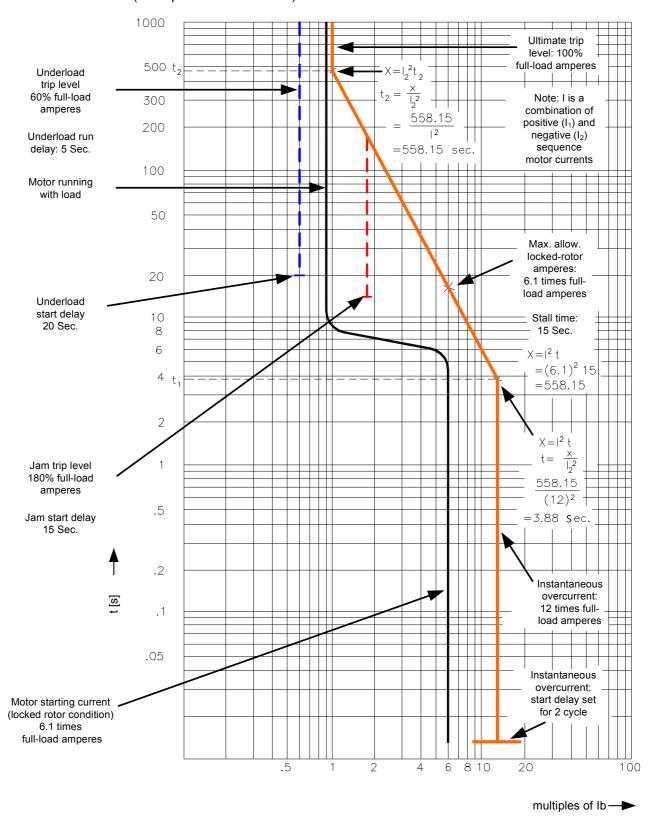
Motor Protection Curves

Motor Protection Curve (Example 1)

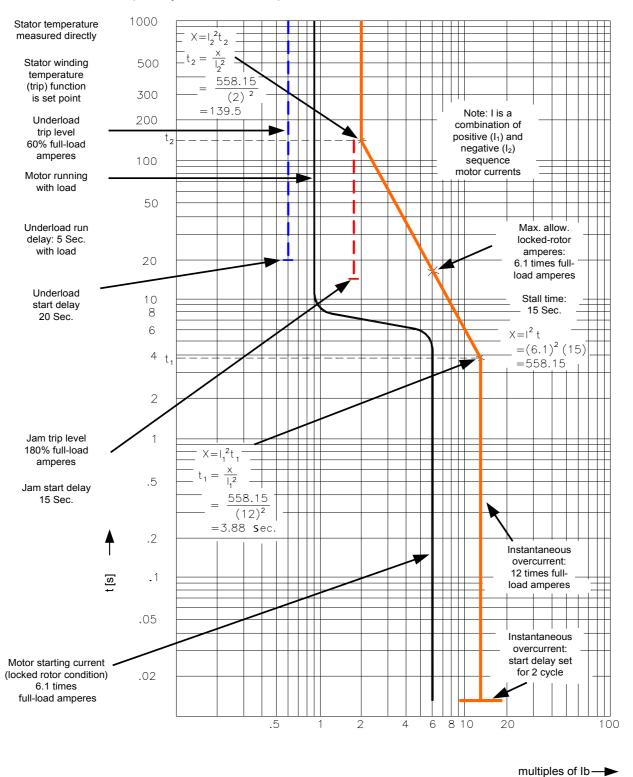


Page 412 EN MRM4 12/11

Motor Protection Curve (Example 2 - without RTDs)



Motor Protection Curve (Example 3 - with RTDs)



Page 414 EN MRM4 12/11

SOTF - Switch Onto Fault

SOTF

In case a faulty line is energized (e.g.: when an earthing switch is in the ON-Position), an instantaneous trip is required. The <u>SOTF</u> module is provided to generate a permissive signal for other protection functions such as overcurrents to accelerate their trips (via adaptive parameters). The <u>SOTF</u> condition is recognized according to the User's operation mode that can be based on:

- The breaker state (Bkr. Pos);
- No current flowing (I<);
- Breaker state and no current flowing(Bkr. Pos and I<);
- Breaker switched on manually (Bkr. manually On); and/or
- An external trigger (Ex SOTF).

This protection module can initiate a high speed trip of the overcurrent protection modules.

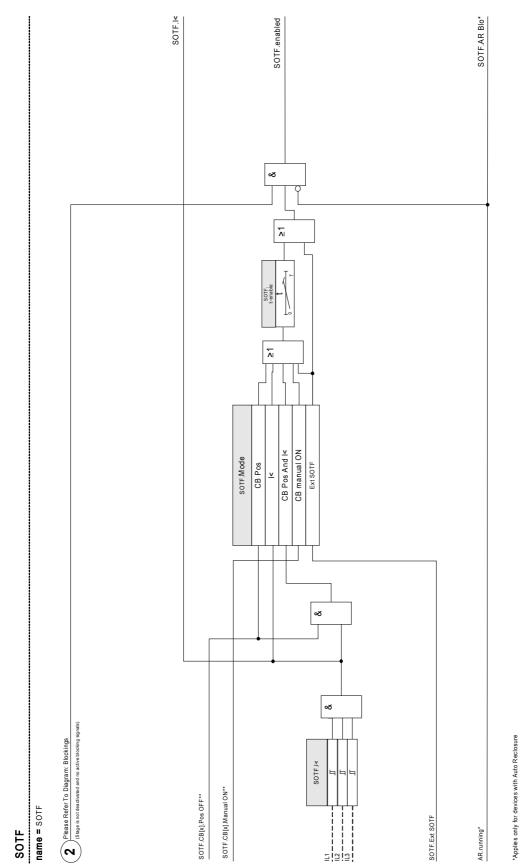


This module issues a signal only (the module is not armed and does not issue a trip command).

In order to influence the trip settings of the overcurrent protection in case of switching onto a fault, the User has to assign the signal "SOTF.ENABLED" onto an Adaptive Parameter Set. Please refer to Parameter / Adaptive Parameter Sets sections. Within the Adaptive Parameter Set, the User has to modify the trip characteristic of the overcurrent protection according to the User's needs.

NOTICE

This Notice applies to protective devices that offer control functionality only! This protective element requires, that a switchgear (breaker is assigned to it. It is allowed only to assign switchgears (breakers) to this protective element, whose measuring transformers provide measuring data to the protective device.



**This signal is the output of the switchgear that is assigned to this protective element. This applies to protective devices that offer control functionality.

Page 416 EN MRM4 12/11

Device Planning Parameters of the Switch Onto Fault Module

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	do not use	[Device planning]
		use		

Global Protection Parameters of the Switch Onto Fault Module

Parameter	Description	Setting range	Default	Menu path
Mode	Mode	Bkr. Pos,	Bkr. manual ON	[Protection Para
		l<,		/Global Prot Para
		Bkr. Pos And I<,		/SOTF]
		Bkr. manual ON,		
		Ext SOTF		
ExBlo1	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	allo state or allo assignou signal to aus.			/SOTF]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and state or and assigned eighten to also.			/SOTF]
Ex rev Interl	External blocking of the module by external	1n, Assignment List		[Protection Para
	reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of			/Global Prot Para
	the assigned signal is true.			/SOTF]
Ext SOTF	External Switch Onto Fault	1n, DI-LogicList		[Protection Para
	Only available if: Mode = Ext SOTF			/Global Prot Para
	2, 2			/SOTF]

Setting Group Parameters of the Switch Onto Fault Module

Parameter	Description	Setting range	Default	Menu path
Function	tion Permanent activation or deactivation of module/stage.	inactive,	inactive	[Protection Para
		active		/<14>
				/SOTF]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".			/SOTF]

Parameter	Description	Setting range	Default	Menu path
Ex rev Interl Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "Ex rev Interl Fc = active".	inactive, active	inactive	[Protection Para /<14> /SOTF]
K	The Bkr. is in the OFF Position, if the measured current is less than this parameter.	0.01 – 1.00 ln	0.01 In	[Protection Para /<14> /SOTF]
t-enable	While this timer is running, and while the module is not blocked, the Switch Onto Fault Module is effective (SOTF is armed).	0.10 – 10.00 s	2 s	[Protection Para /<14> /SOTF]

Switch Onto Fault Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking	[Protection Para
		/Global Prot Para
		/SOTF]
ExBlo2-l	Module input state: External blocking	[Protection Para
		/Global Prot Para
		/SOTF]
Ex rev Interl-I	Module input state: External reverse	[Protection Para
	interlocking	/Global Prot Para
		/SOTF]
Ext SOTF-I	Module input state: External Switch Onto	[Protection Para
	Fault Alarm	/Global Prot Para
		/SOTF]

Signals of the Switch Onto Fault Module (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Ex rev Interl	Signal: External reverse Interlocking
enabled	Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.
ı<	Signal: No Load Current.

Page 418 EN MRM4 12/11

Commissioning: Switch Onto Fault

Object to be tested

Testing the module <u>Switch Onto Fault</u> according to the configured operating mode:

- The breaker state (Bkr. Pos);
- No current flowing (I<);
- Breaker state and no current flowing(Bkr. Pos and I<);
- Breaker switched on manually (Bkr. manually On); and/or
- An external trigger (Ex SOTF).

Necessary means:

- Three-phase current source (If the Enable-Mode depends on current);
- Ampere meters (May be needed if the Enable-Mode depends on current); and
- Timer.

Test Example for Mode Bkr. Manual ON



Mode I<: In order to test the effectiveness: Initially do not feed any current. Start the timer and feed with an abrupt change current that is distinctly greater than the I<-threshold to the measuring inputs of the relay.

Mode I< and Bkr state: Simultaneous switch on the breaker manually and feed with an abrupt change current that is distinctly greater than the I<-threshold.

Mode Bkr state: The breaker has to be in the OFF Position. The signal "SOTF.enabled"=0 is untrue. If the breaker is switched on, the signal "SOTF.enabled"=1 becomes true as long as the timer t-enabled is running.

- The Breaker has to be in the OFF Position. There must be no load current.
- The Status Display of the device shows the signal "SOTF.ENABLED"=1.

Testing

- Switch the Breaker manually ON and start the timer at the same time.
- After the hold time t-enable is expired the state of the signal has to change to "SOTF.ENABLED"=0.
- Write down the measured time.

Successful test result

The measured total tripping delays or individual tripping delays, threshold values and drop-out ratios correspond with those values, specified in the adjustment list. Permissible deviations/tolerances can be found under Technical Data.

ExP - External Protection

Available stages: <u>ExP[1]</u>, <u>ExP[2]</u>, <u>ExP[3]</u>, <u>ExP[4]</u>



All 4 stages of the external protection *ExP[1]...[4]* are identically structured.

By using the module <u>External Protection</u> the following can be incorporated into the device function: trip commands, alarms and blockades of external protection facilities. Devices which are not provided with a communication interface can be connected to the control system as well.

Page 420 EN MRM4 12/11

name. Trip-I name.Alarm-l ∞ ∞ಶ Please Refer To Diagram: Trip blockings (Tripping command deactivated or blocked.) Please Refer To Diagram: Blockings (Stage is not deactivated and no active blocking signals) 1..n, Assignment List 1..n, Assignment List name.Alarm name.Trip

ExP[1]...[n]name = ExP[1]...[n]

Device Planning Parameters of the Module External Protection

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	do not use	[Device planning]
		use		

Global Protection Parameters of the Module External Protection

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
	and state of the designed digital to das:			/ExP
				/ExP[1]]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and state of the designed digital to das:			/ExP
				/ExP[1]]
ExBlo	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
TripCmd				/Global Prot Para
				/ExP
				/ExP[1]]
Alarm	Assignment for External Alarm	1n, Assignment List		[Protection Para
				/Global Prot Para
				/ExP
				/ExP[1]]
Trip	External trip of the Bkr. if the state of the assigned	1n, Assignment List	7.7	[Protection Para
	signal is true.			/Global Prot Para
				/ExP
				/ExP[1]]

Setting Group Parameters of the Module External Protection

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/ExP
				/ExP[1]]

Page 422 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".	inactive, active	inactive	[Protection Para /<14> /ExP /ExP[1]]
Blo TripCmd	Permanent blocking of the Trip Command of the module/stage.	inactive, active	inactive	[Protection Para /<14> /ExP /ExP[1]]
ExBlo TripCmd Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".	inactive, active	inactive	[Protection Para /<14> /ExP /ExP[1]]

Module External Protection Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/ExP
		/ExP[1]]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/ExP
		/ExP[1]]
ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command	[Protection Para
		/Global Prot Para
		/ExP
		/ExP[1]]
Alarm-I	Module input state: Alarm	[Protection Para
		/Global Prot Para
		/ExP
		/ExP[1]]
Trip-I	Module input state: Trip	[Protection Para
		/Global Prot Para
		/ExP
		/ExP[1]]

Module External Protection Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Blo TripCmd	Signal: Trip Command blocked
ExBlo TripCmd	Signal: External Blocking of the Trip Command
Alarm	Signal: Alarm
Trip	Signal: Trip
TripCmd	Signal: Trip Command

Commissioning: External Protection

Object to be tested

Test of the module External Protection

Necessary means

Depending on the application

Procedure

Simulate the functionality of the External Protection (Alarm, Trip, Blockings...) by (de-)energizing of the digital inputs.

Successful test result

All external alarms, external trips and external blockings are correctly recognized and processed by the device.

Page 424 EN MRM4 12/11

Supervision

CBF- Breaker Failure [50BF]

Available elements: CBF

Principle - General Use

The breaker failure (BF) protection is used to provide backup protection in the event that a breaker fails to operate properly during fault clearing. A breaker failure condition is recognized if current is still flowing through the breaker after tripping or opening breaker commands have been issued for a specified time. The User can select different trigger modes. In addition, up to three additional trigger events can be assigned from all the protection modules.

Trigger Modes

There are three trigger modes for the breaker failure available. In addition, there are three assignable trigger inputs available.

- All Trips: All trip signals that are assigned to this breaker (within the trip manager) will start the BF
 module.
- Current Trips: All current trips that are assigned to this breaker (within the trip manager) will start the BF
 module.
- External Trips: All external trips that are assigned to this breaker (within the trip manager) will start the BF module.
- In addition, the User can also select *none* (e.g.: if the User intends to use one of the three additional assignable trigger inputs).

NOTICE

Those trips can exclusively start the breaker failures that are assigned within the trip manager to the breaker that is to be supervised.

NOTICE

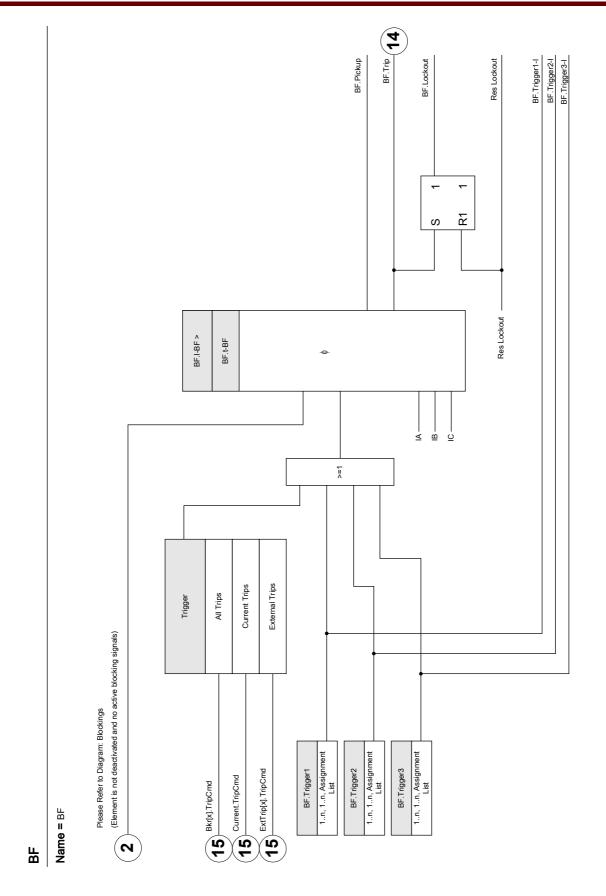
Select the winding side from which the measured currents should be taken in case this protective device is a transformer differential protection.

NOTICE

This Notice applies to protective devices that offer control functionality only! This protective element requires, that a switchgear (breaker is assigned to it. It is allowed only to assign switchgears (breakers) to this protective element, whose measuring transformers provide measuring data to the protective device.

Breaker Failure Lockout

The signal of the Breaker Failure is latched. This signal can be used within the interlockings of the breaker.



Page 426 EN MRM4 12/11

Explanation:

By triggering the CBF element, a delay timer will be started. Once the timer is started, it won't be stopped if the trigger falls back.

The timer will be stopped, if the current magnitudes fall below the set threshold. The CBF function is now in the rejected mode until the trigger falls back.

Once the delay timer has expired and if the current magnitudes of any of the three phase currents still exceed the set threshold, the Breaker Failure signal will be issued (becomes active).

This signal will remain active as long as the current magnitudes exceed the set threshold. This signal will become inactive (falls back) as soon as all current magnitudes fall below the set threshold, e.g. if the upstream protective device has interrupted the current (OFF command to the upstream breaker) by processing the CBF signal of the downstream breaker.

Once a Breaker Failure is detected, the Breaker Failure signal will set a Lockout Signal. The Lockout Signal is a persistent alarm and has to be acknowledged manually at the HMI.

Device Planning Parameters of the CBF

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Global Protection Parameters of the CBF

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and state of the assigned signal is that			/Supervision
				/CBF]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
				/Supervision
				/CBF]
Trigger	Determining the trigger mode for the Breaker Failure.	,	All Trips	[Protection Para
		All Trips,		/Global Prot Para
		I Fc,		/Supervision
		ExP Fc		/CBF]
Trigger1	Trigger that will start the CBF	Trigger		[Protection Para
				/Global Prot Para
				/Supervision
				/CBF]

Parameter	Description	Setting range	Default	Menu path
Trigger2	Trigger that will start the CBF	Trigger		[Protection Para
				/Global Prot Para
				/Supervision
				/CBF]
Trigger3	Trigger that will start the CBF	Trigger		[Protection Para
				/Global Prot Para
				/Supervision
				/CBF]

Direct Commands of the CBF

Parameter	Description	Setting range	Default	Menu path
Res Lockout	Reset Lockout	inactive,	inactive	[Operation
		active		/Reset/Acknowledge
				/Reset]

Setting Group Parameters of the CBF



In order to prevent a faulty activation of the BF Module, the pickup (alarm) time must be greater than the sum of:

- The close-open time of the breaker (please refer to the technical data of the manufacturer of the breaker);
- + The tripping delay of the device (please refer to the Technical Data section);
- · + The security margin; and
- + The operating time.

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/Supervision
				/CBF]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/Supervision
	those modules/stages are blocked that are configured "ExBlo Fc=active".			/CBF]

Page 428 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
I-CBF >	Current level that needs to exist after Trip Command has been given.	0.02 – 0.10 ln	0.02 In	[Protection Para
				/<14>
				/Supervision
				/CBF]
t-CBF	If the delay time is expired, an CBF alarm is given out.	0.00 – 10.00 s	0.20 s	[Protection Para
				/<14>
				/Supervision
				/CBF]

CBF Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/Supervision
		/CBF]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/Supervision
		/CBF]
Trigger1	Module Input: Trigger that will start the CBF	[Protection Para
		/Global Prot Para
		/Supervision
		/CBF]
Trigger2	Module Input: Trigger that will start the CBF	[Protection Para
		/Global Prot Para
		/Supervision
		/CBF]
Trigger3	Module Input: Trigger that will start the CBF	[Protection Para
		/Global Prot Para
		/Supervision
		/CBF]

CBF Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
running	Signal: CBF-Module started

Name	Description
Alarm	Signal: Breaker Failure
Lockout	Signal: Lockout
Res Lockout	Signal: Reset Lockout

CBF Trigger (Current Trips) Functions

These trips will start the BF module if all trips have been selected as the trigger event.

Name	Description
	No assignment
MStart.TripCmd	Signal: Trip Command
I[1].TripCmd	Signal: Trip Command
I[2].TripCmd	Signal: Trip Command
I[3].TripCmd	Signal: Trip Command
I[4].TripCmd	Signal: Trip Command
I[5].TripCmd	Signal: Trip Command
I[6].TripCmd	Signal: Trip Command
IG[1].TripCmd	Signal: Trip Command
IG[2].TripCmd	Signal: Trip Command
IG[3].TripCmd	Signal: Trip Command
IG[4].TripCmd	Signal: Trip Command
I2>[1].TripCmd	Signal: Trip Command
I2>[2].TripCmd	Signal: Trip Command
ThR.TripCmd	Signal: Trip Command
Jam[1].TripCmd	Signal: Trip Command
Jam[2].TripCmd	Signal: Trip Command
I<[1].TripCmd	Signal: Trip Command
I<[2].TripCmd	Signal: Trip Command
I<[3].TripCmd	Signal: Trip Command
RTD.TripCmd	Signal: Trip Command
ExP[1].TripCmd	Signal: Trip Command
ExP[2].TripCmd	Signal: Trip Command
ExP[3].TripCmd	Signal: Trip Command
ExP[4].TripCmd	Signal: Trip Command
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input

Page 430 EN MRM4 12/11

Name	Description
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input
Logic.LE1.Gate Out	Signal: Output of the logic gate
Logic.LE1.Timer Out	Signal: Timer Output
Logic.LE1.Out	Signal: Latched Output (Q)
Logic.LE1.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE2.Gate Out	Signal: Output of the logic gate
Logic.LE2.Timer Out	Signal: Timer Output
Logic.LE2.Out	Signal: Latched Output (Q)
Logic.LE2.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE3.Gate Out	Signal: Output of the logic gate
Logic.LE3.Timer Out	Signal: Timer Output
Logic.LE3.Out	Signal: Latched Output (Q)
Logic.LE3.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE4.Gate Out	Signal: Output of the logic gate
Logic.LE4.Timer Out	Signal: Timer Output
Logic.LE4.Out	Signal: Latched Output (Q)
Logic.LE4.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE5.Gate Out	Signal: Output of the logic gate
Logic.LE5.Timer Out	Signal: Timer Output
Logic.LE5.Out	Signal: Latched Output (Q)
Logic.LE5.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE6.Gate Out	Signal: Output of the logic gate
Logic.LE6.Timer Out	Signal: Timer Output
Logic.LE6.Out	Signal: Latched Output (Q)
Logic.LE6.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE7.Gate Out	Signal: Output of the logic gate
Logic.LE7.Timer Out	Signal: Timer Output
Logic.LE7.Out	Signal: Latched Output (Q)
Logic.LE7.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE8.Gate Out	Signal: Output of the logic gate
Logic.LE8.Timer Out	Signal: Timer Output
Logic.LE8.Out	Signal: Latched Output (Q)
Logic.LE8.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE9.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE9.Timer Out	Signal: Timer Output
Logic.LE9.Out	Signal: Latched Output (Q)
Logic.LE9.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE10.Gate Out	Signal: Output of the logic gate
Logic.LE10.Timer Out	Signal: Timer Output
Logic.LE10.Out	Signal: Latched Output (Q)
Logic.LE10.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE11.Gate Out	Signal: Output of the logic gate
Logic.LE11.Timer Out	Signal: Timer Output
Logic.LE11.Out	Signal: Latched Output (Q)
Logic.LE11.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE12.Gate Out	Signal: Output of the logic gate
Logic.LE12.Timer Out	Signal: Timer Output
Logic.LE12.Out	Signal: Latched Output (Q)
Logic.LE12.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE13.Gate Out	Signal: Output of the logic gate
Logic.LE13.Timer Out	Signal: Timer Output
Logic.LE13.Out	Signal: Latched Output (Q)
Logic.LE13.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE14.Gate Out	Signal: Output of the logic gate
Logic.LE14.Timer Out	Signal: Timer Output
Logic.LE14.Out	Signal: Latched Output (Q)
Logic.LE14.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE15.Gate Out	Signal: Output of the logic gate
Logic.LE15.Timer Out	Signal: Timer Output
Logic.LE15.Out	Signal: Latched Output (Q)
Logic.LE15.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE16.Gate Out	Signal: Output of the logic gate
Logic.LE16.Timer Out	Signal: Timer Output
Logic.LE16.Out	Signal: Latched Output (Q)
Logic.LE16.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE17.Gate Out	Signal: Output of the logic gate
Logic.LE17.Timer Out	Signal: Timer Output
Logic.LE17.Out	Signal: Latched Output (Q)
Logic.LE17.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE18.Gate Out	Signal: Output of the logic gate
Logic.LE18.Timer Out	Signal: Timer Output
Logic.LE18.Out	Signal: Latched Output (Q)
Logic.LE18.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 432 EN MRM4 12/11

Name	Description	
Logic.LE19.Gate Out	Signal: Output of the logic gate	
Logic.LE19.Timer Out	Signal: Timer Output	
Logic.LE19.Out	Signal: Latched Output (Q)	
Logic.LE19.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE20.Gate Out	Signal: Output of the logic gate	
Logic.LE20.Timer Out	Signal: Timer Output	
Logic.LE20.Out	Signal: Latched Output (Q)	
Logic.LE20.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE21.Gate Out	Signal: Output of the logic gate	
Logic.LE21.Timer Out	Signal: Timer Output	
Logic.LE21.Out	Signal: Latched Output (Q)	
Logic.LE21.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE22.Gate Out	Signal: Output of the logic gate	
Logic.LE22.Timer Out	Signal: Timer Output	
Logic.LE22.Out	Signal: Latched Output (Q)	
Logic.LE22.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE23.Gate Out	Signal: Output of the logic gate	
Logic.LE23.Timer Out	Signal: Timer Output	
Logic.LE23.Out	Signal: Latched Output (Q)	
Logic.LE23.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE24.Gate Out	Signal: Output of the logic gate	
Logic.LE24.Timer Out	Signal: Timer Output	
Logic.LE24.Out	Signal: Latched Output (Q)	
Logic.LE24.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE25.Gate Out	Signal: Output of the logic gate	
Logic.LE25.Timer Out	Signal: Timer Output	
Logic.LE25.Out	Signal: Latched Output (Q)	
Logic.LE25.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE26.Gate Out	Signal: Output of the logic gate	
Logic.LE26.Timer Out	Signal: Timer Output	
Logic.LE26.Out	Signal: Latched Output (Q)	
Logic.LE26.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE27.Gate Out	Signal: Output of the logic gate	
Logic.LE27.Timer Out	Signal: Timer Output	
Logic.LE27.Out	Signal: Latched Output (Q)	
Logic.LE27.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE28.Gate Out	Signal: Output of the logic gate	
Logic.LE28.Timer Out	Signal: Timer Output	
Logic.LE28.Out	Signal: Latched Output (Q)	

Name	Description
Logic.LE28.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE29.Gate Out	Signal: Output of the logic gate
Logic.LE29.Timer Out	Signal: Timer Output
Logic.LE29.Out	Signal: Latched Output (Q)
Logic.LE29.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE30.Gate Out	Signal: Output of the logic gate
Logic.LE30.Timer Out	Signal: Timer Output
Logic.LE30.Out	Signal: Latched Output (Q)
Logic.LE30.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE31.Gate Out	Signal: Output of the logic gate
Logic.LE31.Timer Out	Signal: Timer Output
Logic.LE31.Out	Signal: Latched Output (Q)
Logic.LE31.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE32.Gate Out	Signal: Output of the logic gate
Logic.LE32.Timer Out	Signal: Timer Output
Logic.LE32.Out	Signal: Latched Output (Q)
Logic.LE32.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE33.Gate Out	Signal: Output of the logic gate
Logic.LE33.Timer Out	Signal: Timer Output
Logic.LE33.Out	Signal: Latched Output (Q)
Logic.LE33.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE34.Gate Out	Signal: Output of the logic gate
Logic.LE34.Timer Out	Signal: Timer Output
Logic.LE34.Out	Signal: Latched Output (Q)
Logic.LE34.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE35.Gate Out	Signal: Output of the logic gate
Logic.LE35.Timer Out	Signal: Timer Output
Logic.LE35.Out	Signal: Latched Output (Q)
Logic.LE35.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE36.Gate Out	Signal: Output of the logic gate
Logic.LE36.Timer Out	Signal: Timer Output
Logic.LE36.Out	Signal: Latched Output (Q)
Logic.LE36.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE37.Gate Out	Signal: Output of the logic gate
Logic.LE37.Timer Out	Signal: Timer Output
Logic.LE37.Out	Signal: Latched Output (Q)
Logic.LE37.Out inverted	
	Signal: Negated Latched Output (Q NOT)
Logic.LE38.Gate Out	Signal: Negated Latched Output (Q NOT) Signal: Output of the logic gate

Page 434 EN MRM4 12/11

Name	Description	
Logic.LE38.Out	Signal: Latched Output (Q)	
Logic.LE38.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE39.Gate Out	Signal: Output of the logic gate	
Logic.LE39.Timer Out	Signal: Timer Output	
Logic.LE39.Out	Signal: Latched Output (Q)	
Logic.LE39.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE40.Gate Out	Signal: Output of the logic gate	
Logic.LE40.Timer Out	Signal: Timer Output	
Logic.LE40.Out	Signal: Latched Output (Q)	
Logic.LE40.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE41.Gate Out	Signal: Output of the logic gate	
Logic.LE41.Timer Out	Signal: Timer Output	
Logic.LE41.Out	Signal: Latched Output (Q)	
Logic.LE41.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE42.Gate Out	Signal: Output of the logic gate	
Logic.LE42.Timer Out	Signal: Timer Output	
Logic.LE42.Out	Signal: Latched Output (Q)	
Logic.LE42.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE43.Gate Out	Signal: Output of the logic gate	
Logic.LE43.Timer Out	Signal: Timer Output	
Logic.LE43.Out	Signal: Latched Output (Q)	
Logic.LE43.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE44.Gate Out	Signal: Output of the logic gate	
Logic.LE44.Timer Out	Signal: Timer Output	
Logic.LE44.Out	Signal: Latched Output (Q)	
Logic.LE44.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE45.Gate Out	Signal: Output of the logic gate	
Logic.LE45.Timer Out	Signal: Timer Output	
Logic.LE45.Out	Signal: Latched Output (Q)	
Logic.LE45.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE46.Gate Out	Signal: Output of the logic gate	
Logic.LE46.Timer Out	Signal: Timer Output	
Logic.LE46.Out	Signal: Latched Output (Q)	
Logic.LE46.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE47.Gate Out	Signal: Output of the logic gate	
Logic.LE47.Timer Out	Signal: Timer Output	
Logic.LE47.Out	Signal: Latched Output (Q)	
Logic.LE47.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE48.Gate Out	Signal: Output of the logic gate	

Name	Description
Logic.LE48.Timer Out	Signal: Timer Output
Logic.LE48.Out	Signal: Latched Output (Q)
Logic.LE48.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE49.Gate Out	Signal: Output of the logic gate
Logic.LE49.Timer Out	Signal: Timer Output
Logic.LE49.Out	Signal: Latched Output (Q)
Logic.LE49.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE50.Gate Out	Signal: Output of the logic gate
Logic.LE50.Timer Out	Signal: Timer Output
Logic.LE50.Out	Signal: Latched Output (Q)
Logic.LE50.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE51.Gate Out	Signal: Output of the logic gate
Logic.LE51.Timer Out	Signal: Timer Output
Logic.LE51.Out	Signal: Latched Output (Q)
Logic.LE51.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE52.Gate Out	Signal: Output of the logic gate
Logic.LE52.Timer Out	Signal: Timer Output
Logic.LE52.Out	Signal: Latched Output (Q)
Logic.LE52.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE53.Gate Out	Signal: Output of the logic gate
Logic.LE53.Timer Out	Signal: Timer Output
Logic.LE53.Out	Signal: Latched Output (Q)
Logic.LE53.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE54.Gate Out	Signal: Output of the logic gate
Logic.LE54.Timer Out	Signal: Timer Output
Logic.LE54.Out	Signal: Latched Output (Q)
Logic.LE54.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE55.Gate Out	Signal: Output of the logic gate
Logic.LE55.Timer Out	Signal: Timer Output
Logic.LE55.Out	Signal: Latched Output (Q)
Logic.LE55.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE56.Gate Out	Signal: Output of the logic gate
Logic.LE56.Timer Out	Signal: Timer Output
Logic.LE56.Out	Signal: Latched Output (Q)
Logic.LE56.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE57.Gate Out	Signal: Output of the logic gate
Logic.LE57.Timer Out	Signal: Timer Output
Logic.LE57.Out	Signal: Latched Output (Q)
Logic.LE57.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 436 EN MRM4 12/11

Name	Description	
Logic.LE58.Gate Out	Signal: Output of the logic gate	
Logic.LE58.Timer Out	Signal: Timer Output	
Logic.LE58.Out	Signal: Latched Output (Q)	
Logic.LE58.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE59.Gate Out	Signal: Output of the logic gate	
Logic.LE59.Timer Out	Signal: Timer Output	
Logic.LE59.Out	Signal: Latched Output (Q)	
Logic.LE59.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE60.Gate Out	Signal: Output of the logic gate	
Logic.LE60.Timer Out	Signal: Timer Output	
Logic.LE60.Out	Signal: Latched Output (Q)	
Logic.LE60.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE61.Gate Out	Signal: Output of the logic gate	
Logic.LE61.Timer Out	Signal: Timer Output	
Logic.LE61.Out	Signal: Latched Output (Q)	
Logic.LE61.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE62.Gate Out	Signal: Output of the logic gate	
Logic.LE62.Timer Out	Signal: Timer Output	
Logic.LE62.Out	Signal: Latched Output (Q)	
Logic.LE62.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE63.Gate Out	Signal: Output of the logic gate	
Logic.LE63.Timer Out	Signal: Timer Output	
Logic.LE63.Out	Signal: Latched Output (Q)	
Logic.LE63.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE64.Gate Out	Signal: Output of the logic gate	
Logic.LE64.Timer Out	Signal: Timer Output	
Logic.LE64.Out	Signal: Latched Output (Q)	
Logic.LE64.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE65.Gate Out	Signal: Output of the logic gate	
Logic.LE65.Timer Out	Signal: Timer Output	
Logic.LE65.Out	Signal: Latched Output (Q)	
Logic.LE65.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE66.Gate Out	Signal: Output of the logic gate	
Logic.LE66.Timer Out	Signal: Timer Output	
Logic.LE66.Out	Signal: Latched Output (Q)	
Logic.LE66.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE67.Gate Out	Signal: Output of the logic gate	
Logic.LE67.Timer Out	Signal: Timer Output	
Logic.LE67.Out	Signal: Latched Output (Q)	

Name	Description	
Logic.LE67.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE68.Gate Out	Signal: Output of the logic gate	
Logic.LE68.Timer Out	Signal: Timer Output	
Logic.LE68.Out	Signal: Latched Output (Q)	
Logic.LE68.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE69.Gate Out	Signal: Output of the logic gate	
Logic.LE69.Timer Out	Signal: Timer Output	
Logic.LE69.Out	Signal: Latched Output (Q)	
Logic.LE69.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE70.Gate Out	Signal: Output of the logic gate	
Logic.LE70.Timer Out	Signal: Timer Output	
Logic.LE70.Out	Signal: Latched Output (Q)	
Logic.LE70.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE71.Gate Out	Signal: Output of the logic gate	
Logic.LE71.Timer Out	Signal: Timer Output	
Logic.LE71.Out	Signal: Latched Output (Q)	
Logic.LE71.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE72.Gate Out	Signal: Output of the logic gate	
Logic.LE72.Timer Out	Signal: Timer Output	
Logic.LE72.Out	Signal: Latched Output (Q)	
Logic.LE72.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE73.Gate Out	Signal: Output of the logic gate	
Logic.LE73.Timer Out	Signal: Timer Output	
Logic.LE73.Out	Signal: Latched Output (Q)	
Logic.LE73.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE74.Gate Out	Signal: Output of the logic gate	
Logic.LE74.Timer Out	Signal: Timer Output	
Logic.LE74.Out	Signal: Latched Output (Q)	
Logic.LE74.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE75.Gate Out	Signal: Output of the logic gate	
Logic.LE75.Timer Out	Signal: Timer Output	
Logic.LE75.Out	Signal: Latched Output (Q)	
Logic.LE75.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE76.Gate Out	Signal: Output of the logic gate	
Logic.LE76.Timer Out	Signal: Timer Output	
Logic.LE76.Out	Signal: Latched Output (Q)	
Logic.LE76.Out inverted	Cianal, Nagatad Latabad Output (O NOT)	
L:- L E77 O-t- O-t	Signal: Negated Latched Output (Q NOT)	
Logic.LE77.Gate Out	Signal: Output of the logic gate	

Page 438 EN MRM4 12/11

Name	Description
Logic.LE77.Out	Signal: Latched Output (Q)
Logic.LE77.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE78.Gate Out	Signal: Output of the logic gate
Logic.LE78.Timer Out	Signal: Timer Output
Logic.LE78.Out	Signal: Latched Output (Q)
Logic.LE78.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE79.Gate Out	Signal: Output of the logic gate
Logic.LE79.Timer Out	Signal: Timer Output
Logic.LE79.Out	Signal: Latched Output (Q)
Logic.LE79.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE80.Gate Out	Signal: Output of the logic gate
Logic.LE80.Timer Out	Signal: Timer Output
Logic.LE80.Out	Signal: Latched Output (Q)
Logic.LE80.Out inverted	Signal: Negated Latched Output (Q NOT)

These trips will start the BF module if all current functions have been selected as the trigger event.

Name	Description
	No assignment
I[1].TripCmd	Signal: Trip Command
I[2].TripCmd	Signal: Trip Command
I[3].TripCmd	Signal: Trip Command
I[4].TripCmd	Signal: Trip Command
I[5].TripCmd	Signal: Trip Command
I[6].TripCmd	Signal: Trip Command
IG[1].TripCmd	Signal: Trip Command
IG[2].TripCmd	Signal: Trip Command
IG[3].TripCmd	Signal: Trip Command
IG[4].TripCmd	Signal: Trip Command
I2>[1].TripCmd	Signal: Trip Command
12>[2].TripCmd	Signal: Trip Command
ThR.TripCmd	Signal: Trip Command
Jam[1].TripCmd	Signal: Trip Command
Jam[2].TripCmd	Signal: Trip Command
I<[1].TripCmd	Signal: Trip Command
I<[2].TripCmd	Signal: Trip Command
I<[3].TripCmd	Signal: Trip Command

These trips will start the BF module if external trips have been selected as the trigger event.

Name	Description
	No assignment
ExP[1].TripCmd	Signal: Trip Command
ExP[2].TripCmd	Signal: Trip Command
ExP[3].TripCmd	Signal: Trip Command
ExP[4].TripCmd	Signal: Trip Command

NOTICE



Page 440 EN MRM4 12/11

TCS - Trip Circuit Supervision [74TC]

Available elements: TCS

The trip circuit monitoring is used for monitoring if the trip circuit is ready for operations. The monitoring can be fulfilled in two ways. The first assumes only »Aux On (52a)« is used in the trip circuit. The second assumes that, in addition to »Aux On (52a), »Aux Off(52b)« is also used for the circuit monitoring.

With »Aux On (52a), only in the trip circuit, the monitoring is only effective when the breaker is closed while if both »Aux On (52a), and »Aux Off(52b)« are used, the trip circuit will be monitored all time as long as the control power is on.

Note that the digital inputs used for this purpose must be configured properly based on the trip circuit control voltage. If the trip circuit is detected broken, an alarm will be issued with a specified delay, which must be longer than the time when a trip contact is closed to the time when the breaker status is clearly recognized by the relay.

NOTICE

In Slot 1 has 2 digital inputs, each of which has a separate root (contact separation) for the trip circuit supervision.

NOTICE

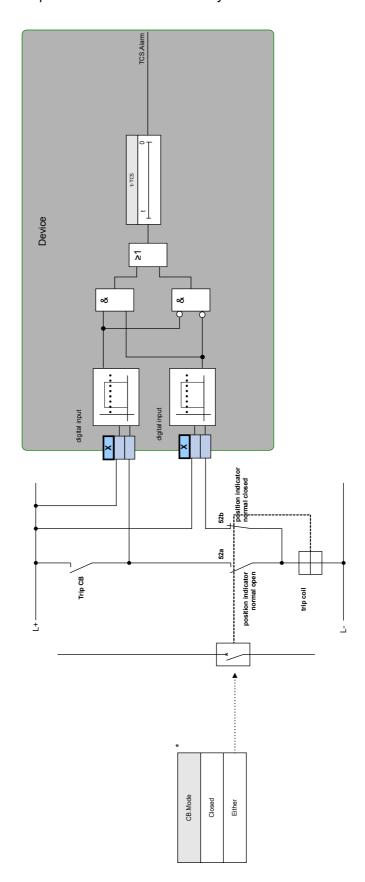
This Notice applies to protective devices that offer control functionality only! This protective element requires, that a switchgear (breaker is assigned to it.

In this case, the trip circuit supply voltage serves also as supply voltage for the digital inputs and so the supply voltage failure of a trip circuit can be detected directly.

In order to identify a conductor failure in the trip circuit on the supply line or in the trip coil, the off-coil has to be looped-in to the supervision circuit.

The time delay is to be set in a way that switching actions cannot cause false trips in this module.

Connection example: Trip circuit supervision with two Bkr. auxiliary contacts.

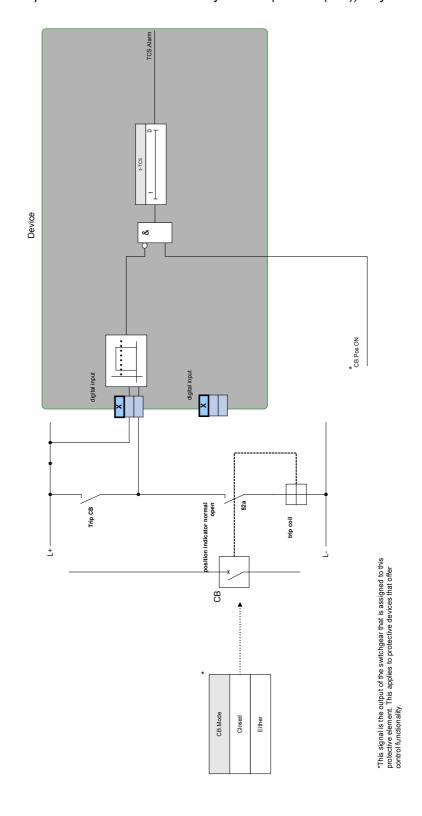


*This signal is the output of the switchgear that is assigned to this protective element. This applies to protective devices that offer control functionality.

TCS

Page 442 EN MRM4 12/11

Connection example: Trip circuit supervision with one Bkr. auxiliary contact (Aux On (52a)) only.



TCS

Device Planning Parameters of the Trip Circuit Supervision

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Global Protection Parameters of the Trip Circuit Supervision

Parameter	Description	Setting range	Default	Menu path
Bkr. Pos Detect	Criterion by which the Breaker Switch Position is to be detected.	,		[Protection Para
		SG.Pos		/Global Prot Para
				/Supervision
				/TCS]
Mode	Select if trip circuit is going to be monitored when	Closed,	Closed	[Protection Para
	the breaker is closed or when the breaker is either open or close.	Either		/Global Prot Para
	SP3.1 3. 3.333.			/Supervision
				/TCS]
Input 1	when the breaker is closed.	,		[Protection Para
		DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/Supervision
		DI Slot X1.DI 3,		/TCS]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		

Page 444 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Input 2	Select the input configured to monitor the trip coil	,		[Protection Para
	when the breaker is open. Only available if Mode set to "Either".	DI Slot X1.DI 1,		/Global Prot Para
		DI Slot X1.DI 2,		/Supervision
	Only available if: Mode = Either	DI Slot X1.DI 3,		/TCS]
		DI Slot X1.DI 4,		
		DI Slot X1.DI 1,		
		DI Slot X1.DI 2,		
		DI Slot X1.DI 3,		
		DI Slot X1.DI 4,		
		DI Slot X1.DI 5,		
		DI Slot X1.DI 6,		
		DI Slot X1.DI 7,		
		DI Slot X1.DI 8		
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
	3			/Supervision
				/TCS]
ExBlo2	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
	3			/Supervision
				/TCS]

Setting Group Parameters of the Trip Circuit Supervision

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive,	inactive	[Protection Para
		active		/<14>
				/Supervision
				/TCS]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".	active		/<14>
				/Supervision
				/TCS]
t-TCS	Tripping delay time of the Trip Circuit Supervision	0.10 – 10.00 s	0.2 s	[Protection Para
				/<14>
				/Supervision
				/TCS]

Trip Circuit Supervision Input States

Name	Description	Assignment via
Aux ON-I	hack signal of the Bkr (52a)	[Protection Para
		/Global Prot Para
		/Supervision
		/TCS]
Aux OFF-I	Module input state: Position indicator/check-	[Protection Para
	back signal of the Bkr. (52b)	/Global Prot Para
		/Supervision
		/TCS]
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/Supervision
		/TCS]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/Supervision
		/TCS]
Bkr. Pos Detect-I	Module input state: Criterion by which the	[Protection Para
	Breaker Switch Position is to be detected.	/Global Prot Para
		/Supervision
		/TCS]

Trip Circuit Supervision Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Alarm	Signal: Alarm Trip Circuit Supervision
Not Possible	Not possible because no state indicator assigned to the breaker.

Commissioning: Trip Circuit Supervision [74TC]



For Bkrs. that trip by means of little energy (e.g. via an optocoupler), it has to be ensured that the current applied by the digital inputs will not cause false tripping of the Bkr.

Page 446 EN MRM4 12/11

Procedure, part 1

Simulate failure of the control voltage in the power circuits.

Successful test result, part 1

After expiry of *»t-TCS«* the trip circuit supervision *TCS* of the device should signal an alarm.

Procedure, part 2

Simulate a broken cable in the Bkr. control circuit.

Successful test result, part 2

After expiry of *»t-TCS«* the trip circuit supervision *TCS* of the device should signal an alarm.

CTS - Current Transformer Supervision [60L]

Available elements:

CTS

Wire breaks and failures within measuring circuits cause current transformer failures.

The module <u>»CTS«</u> can detect a failure of the CT if the calculated earth current does not match the measured one. If an adjustable threshold value (Difference of measured and calculated earth current) is exceeded, a CT failure can be assumed. This is signaled through a message/alarm.

The precondition is that the conductor currents are measured by the device and the earth current, for instance, by a ring core type current transformer.

The measuring principles of the circuit supervision are based on comparing the measured and the calculated residual currents:

In an ideal case these are:

$$(I\vec{L}I + I\vec{L}2 + I\vec{L}3) + KI * I\vec{G} = 3 * I_0 + KI * I\vec{G} = 0$$

KI represents a correction factor which takes the different transformation ratio of the phase- and earth current transformers into account. The device automatically calculates this factor from the rated system parameters, i.e. the relation between the rated primary and secondary current values of the phase- and earth current transformers.

For compensating the current proportional ratio error of the measuring circuits, the dynamic correction factor Kd can be used. As a function of the measured max. current this factor is considering the linear rising measuring error.

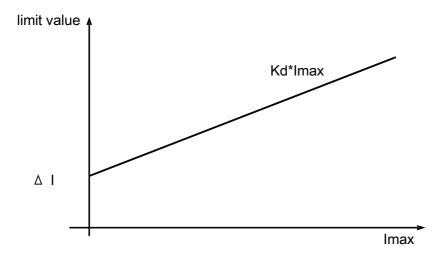
The limiting value of the CT supervision is calculated as follows:

 ΔI = deviation I (rated value) Kd = correction factor Imax = current maximum Limiting value = $\Delta I + Kd \times Imax$

Precondition for identifying an error

$$3*\vec{I_0} + KI*\vec{IG} \ge Delta\ I + Kd*Imax$$

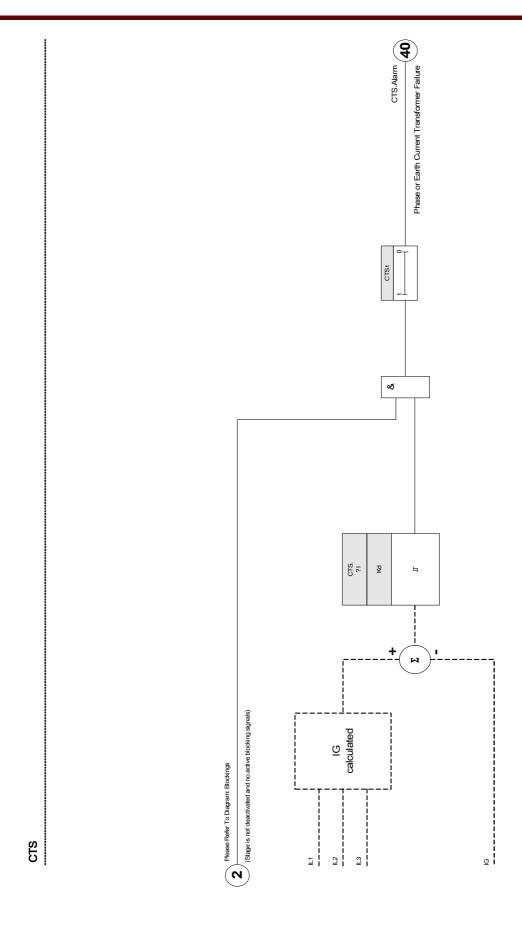
The evaluation method of the circuit supervision by using factor Kd can be graphically represented as follows:



CAUTION

If the current is measured in two phases only (for instant only IL1/IL3) or if there is no separate earth current measuring (e.g. normally via a cable-type CT), the supervision function should be deactivated.

Page 448 EN MRM4 12/11



Device Planning Parameters of the Current Transformer Supervision

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Global Protection Parameter of the Current Transformer Supervision

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and state or and doorgined orginal to a door			/Supervision
				/CTS]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	and state or and doorgined orginal to a door			/Supervision
				/CTS]

Setting Group Parameters of the Current Transformer Supervision

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive, active	inactive	[Protection Para /<14> /Supervision /CTS]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo Fc=active".	inactive, active	inactive	[Protection Para /<14> /Supervision /CTS]
ΔΙ	In order to prevent faulty tripping of phase selective protection functions that use the current as tripping criterion. If the difference of the measured earth current and the calculated value I0 is higher than the pick up value ΔI , an alarm event is generated after expiring of the excitation time. In such a case, a fuse failure, a broken wire or a faulty measuring circuit can be assumed.	0.10 – 1.00 ln	0.50 ln	[Protection Para /<14> /Supervision /CTS]
Alarm delay	Alarm delay	0.1 – 9999.0 s	1.0 s	[Protection Para /<14> /Supervision /CTS]

Page 450 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Kd	Dynamic correction factor for the evaluation of the difference between calculated and measured earth current. This correction factor allows transformer faults, caused by higher currents, to be compensated.	0.00 - 0.99	0.00	[Protection Para /<14> /Supervision /CTS]

Current Transformer Supervision Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/Supervision
		/CTS]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/Supervision
		/CTS]

Current Transformer Supervision Signals (Outputs States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Alarm	Signal: Alarm Current Transformer Measuring Circuit Supervision

Commissioning: Current Transformer Failure Supervision



Precondition:

- 1. Measurement of all three phase currents (are applied to the measuring inputs of the device).
- 2. The earth current is detected via a cable-type transformer (not in Holmgreen connection).

Object to be tested

Check of the CT supervision (by comparing the calculated with the measured earth current).

Necessary means

Three-phase current source

Procedure, part 1

- Set the limiting value of the CTS to »delta I=0.1*In«.
- Feed a three-phase, symmetrical current system (approx. nominal current) to the secondary side.

- Disconnect the current of one phase from one of the measuring inputs (the symmetrical feeding at secondary side has to be maintained).
- Make sure that the signal »CTS.ALARM« is generated now.

Successful test result, part 1

The signal »CTS.ALARM« is generated.

Procedure, part 2

- Feed a three-phase, symmetrical current system (approx. nominal current) to the secondary side.
- Feed a current that is higher than the threshold value for the measuring circuit supervision to the earth current measuring input.
- Ascertain that the signal »CTS.ALARM« is generated now.

Successful test result, part 2

The signal »CTS.ALARM« is generated.

Self Supervision

HighPROTEC devices are continuously monitored and supervised through different methods during normal operation as well as during start-up phase.

Results of this supervision may be:

- messages appearing within event-recorder (from release 1.2 or later),
- indications within the display or Smart View,
- corrective measures,
- disabling of protection functions,
- restart of the device

or any combination out of these.

In case of failures that cannot be corrected immediately three restarts within 20 minutes are accepted before the device will be deactivated. The device should be removed in for service in such case. Contact data and address can be found at the end of this manual.

In case of any failures the recorders of the device should be left untouched to ensure an easy diagnosis and proper repair at the factory. Besides the records and visible indications to the customer there exists internal information about failures. These allow service personnel to make a detailed analysis of files with failure reports, at least at factory site.

Self supervision is applied by different functions at different cyclic or non-cyclic timings to the following parts and functions of the device:

- faultless cyclic execution of software,
- functional capability of memory boards,
- consistency of data,
- functional capability of hardware sub-assemblies and
- faultless operation of the measuring unit.

Faultless cyclic operation of software is supervised by timing analysis and checking results of different functions.

Page 452 EN MRM4 12/11

Errors of the software function (watchdog function) lead to restarting the device and switching off the self-supervision relay (life-contact). Also the System-OK LED will blink red, after three unsuccessful attempts to restart the device within a time-period of 20 minutes.

The main processor cyclically monitors the operation of the signal processor and initiates corrective actions or restart of the device in case of faulty operation.

Data and files are generally secured against unintended overwriting or faulty changes by checksums.

The measuring unit continuously checks the measured data by comparing received data with data from a second channel sampled in parallel.

The auxiliary voltage is monitored continuously. If the voltage of one of the different supply circuits falls below a certain threshold, a restart of the device is initiated. If the voltage staggers around the threshold, the device also starts again after several seconds. Additionally the level of all internal supply voltage groups are continuously monitored.

Independent of these separate monitoring functions, the intermediate voltage circuit is buffered until all important and relevant operational and fault-data have been saved and the device initiates a restart.

Error Messages / Codes

After a reboot of the device the reason for rebooting will be displayed under

[Operation/Status Display/Sys/Reset].

For more information about the reboot reason please follow this chapter.

The reboot will also be logged within the event recorder. Rebooting causes an event named: Sys.reboot.

Numerical reboot codes:

	Error messages / codes		
1.	Reboot after clean switching off of the device normal reboot after clean shut down of the device.		
2.	Reboot by User command User-initiated reboot through panel-command.		
3.	Super reset: reset to factory settings		
4.	Restart by debugger; internally for system-analysis purpose.		

5.	Restart because of configuration changes.
6.	General failure: reboot.
7.	Reboot by SW-system abort (HOST-side); summary of several reboot reasons detected by software, i.e. wrong pointer, corrupted files etc.
8.	Reboot by watchdog timeout (HOST-side) - Signaling if the protection-class-task hangs.
9.	Reboot by system abort (DSP-side); summary of several reboot reasons detected by software, i.e. wrong pointer, DSP-side.
10.	Reboot by watchdog timeout (DSP-side) - Appears when DSP sequence needs too long for one cycle.
11.	Loss of auxiliary voltage or low voltage reboot after loss of auxiliary voltage or voltage dropping below reboot-level but not becoming zero.
12.	Faulty memory access: message of MMU (memory mapping unit) that prohibited memory access has occurred.

Commissioning

Before starting work on an opened switchboard it is imperative that the complete switchboard is dead and the following 5 safety regulations are always met: ,



Safety precautions:

- Disconnect from the power supply
- Secure against reconnection
- Verify if the equipment is dead
- Connect to ground and short-circuit all phases
- Cover or safeguard all live adjacent parts



The secondary circuit of a current transformer must never be opened during operation. The prevailing high voltages are dangerous to life.



Even when the auxiliary voltage is switched off, it is likely that there are still hazardous voltages at the component connections.

All locally applicable national and international installation and safety regulations for working at electrical power installations must always to be followed (e.g. VDE, EN, DIN, IEC).



Prior to the initial voltage connection, the following must be guaranteed:

- Correct grounding of the device
- That all signal circuits are tested

Page 454 EN MRM4 12/11

- That all control circuits are tested
- Transformer wiring is checked
- Correct rating of the CTs
- Correct burden of the CTs
- That the operational conditions are in line with the Technical Data
- Correct rating of the transformer protection
- Function of the transformer fuses
- Correct wiring of all digital inputs
- Polarity and capacity of the supply voltage
- Correct wiring of the analogue inputs and outputs

NOTICE

The permissible deviations of measuring values and device adjustment are dependent on the technical data/tolerances.

Commissioning/Protection Test



Putting into operation/Protection test must be carried out by authorized and qualified personnel. Before the device is put into operation the related documentation has to be read and understood.



With any test of the protection functions the following has to be checked:

- Is activation/tripping saved in the event recorder?
- Is tripping saved in the fault recorder?
- Is tripping saved in the disturbance recorder?
- Are all signals/messages correctly generated?
- Do all general configured blocking functions work properly?
- Do all temporary configured (via DI) blocking functions work properly?
- To enable checks on all LEDs and relay functions, these have to be provided with the relevant alarm and tripping functions of the respective protection functions/elements. This has to be tested in practical operation.



Check of all temporary blockings (via digital inputs):

 In order to avoid malfunctions, all blockings related to tripping/non-tripping of protection function have to be tested.
 The test can be very complex and should therefore be performed by the same people who set up the protection concept.

CAUTION

Check of all general trip blockings:

All general trip blockings have to be tested.

NOTICE

Prior to the initial operation of the protection device all tripping times and values shown in the adjustment list have to be confirmed by a secondary test

NOTICE

Any description of functions, parameters, inputs or outputs that does not match the device in hand, can be ignored.

Putting Out of Operation - Plug Out the Relay



Warning! Dismounting the relay will lead to a loss of the protection functionality. Ensure that there is a back-up protection. If you are not aware of the consequences of dismounting the device – stop! Don't start.



Inform SCADA before you start.

Switch-off the power supply.

Ensure, that the cabinet is dead and that there are no voltages that could lead to personal injury.

Plug-out the terminals at the rear-side of the device. Do not pull any cable – pull on the plugs! If it is stuck use for example a screw driver.

Fasten the cables and terminals in the cabinet by means of cable clips to ensure that no accidental electrical connections are caused.

Hold the device at the front-side while opening the mounting nuts.

Remove the device carefully out of the cabinet.

In case no other device is to be mounted or replaced cover/close the cut-out in the front-door.

Close the cabinet.

Page 456 EN MRM4 12/11

RTD Protection Module

Elements: RTD

General - Principle Use



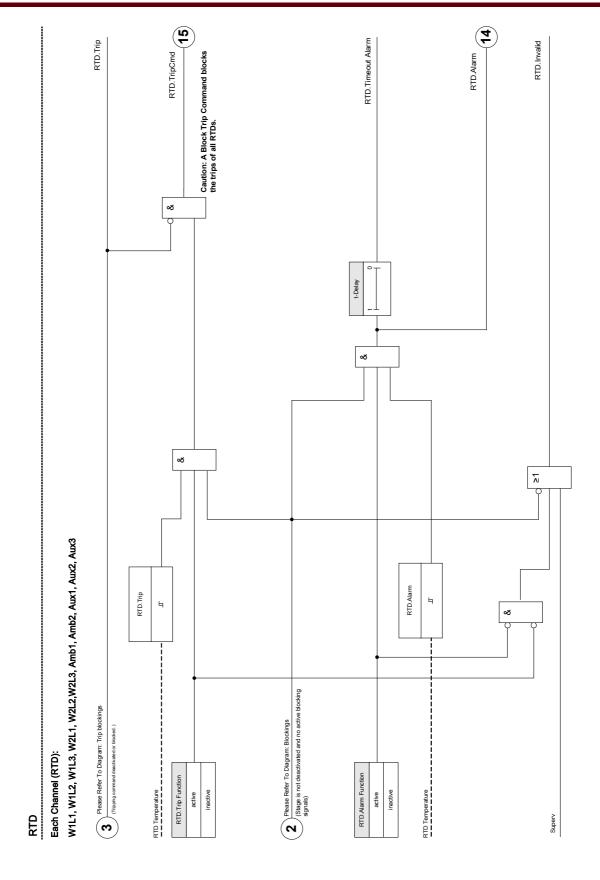
The Resistance-based Temperature Detector (RTD) Protection Module uses temperature data that are provided by a Universal Resistance-based Temperature Detector (URTD) module (please refer to the URTD Module section).

The protective device provides tripping and alarming functions based on the direct temperature measurements read from the URTD device that has 11 temperature sensor channels. Each channel will have one trip function without an intended delay and one alarm function with a delay.

- The "trip" function has only a threshold setting.
- Each individual »Alarm Function« will have a threshold setting range, and can be individually enabled or disabled. Since the temperature cannot change instantaneously (which is a way that temperature differs from current), the "delay" is essentially built in to the function due to the fact that the temperature will take some time to increase from room temperature to the "trip threshold" level.
- The dropout ratio for both trip and alarm is 0.99.

The temperature rise is limited by the RTD driver.

The entire function can be turned off or on, or individual channels can be turned off or on.



Page 458 EN MRM4 12/11

Device Planning Parameters of the RTD Temperature Protection Module

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Global Protection Parameters of the RTD Temperature Protection Module

Parameter	Description	Setting range	Default	Menu path
ExBlo1	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
				/Global Prot Para
	the state of the designed signal is true.			/Temp-Prot
				/RTD]
ExBlo2	External blocking of the module, if blocking is	1n, Assignment List		[Protection Para
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Global Prot Para
	the state of the designed signal is true.			/Temp-Prot
				/RTD]
ExBlo	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	1n, Assignment List		[Protection Para
TripCmd				/Global Prot Para
				/Temp-Prot
				/RTD]
Developer	all channels from 0 to "Group1MaxChannel" are	0 - 12	6	[Protection Para
only: wd	members of group 1			/Global Prot Para
				/Temp-Prot
				/RTD]
Developer	all channels from "Group1MaxChannel" to	0 - 12	8	[Protection Para
only: mb	"Group2MaxChannel" are members of group 2			/Global Prot Para
				/Temp-Prot
				/RTD]
Developer	all channels from "Group2MaxChannel" to	0 - 12	10	[Protection Para
only: lb	"Group3MaxChannel" are members of group 3			/Global Prot Para
				/Temp-Prot
				/RTD]

Setting Group Parameters of the RTD Temperature Protection Module

Parameter	Description	Setting range	Default	Menu path
Function	module/stage	inactive,	inactive	[Protection Para
		active		/<14>
				/Temp-Prot
				/RTD
				/General settings]
ExBlo Fc	Activate (allow) or inactivate (disallow) blocking of	inactive,	inactive	[Protection Para
	the module/stage. This parameter is only effective if a signal is assigned to the corresponding global	active		/<14>
	protection parameter. If the signal becomes true,			/Temp-Prot
	those modules/stages are blocked that are configured "ExBlo Fc=active".			/RTD
	configured Exploite delive.			/General settings]
Blo TripCmd	Permanent blocking of the Trip Command of the	inactive,	inactive	[Protection Para
	module/stage.	active		/<14>
				/Temp-Prot
				/RTD
				/General settings]
ExBlo	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are configured "ExBlo TripCmd Fc=active".	inactive,	inactive	[Protection Para
TripCmd Fc		active		/<14>
				/Temp-Prot
				/RTD
				/General settings]
Windg 1	Winding 1 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
Tanodon				/Temp-Prot
				/RTD
				/Windg 1]
Windg 1 Trip	Winding 1 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 1]
Windg 1	Winding 1 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg 1]

Page 460 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Windg 1 t-	Winding 1 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg 1]
Windg 1 Trip	Winding 1 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg 1]
Windg 2	Winding 2 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
T direction				/Temp-Prot
				/RTD
				/Windg 2]
Windg 2 Trip	Winding 2 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 2]
Windg 2	Winding 2 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg 2]
Windg 2 t-	Winding 2 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg 2]
Windg 2 Trip	Winding 2 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg 2]

Parameter	Description	Setting range	Default	Menu path
Windg 3	Winding 3 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 3]
Windg 3 Trip	Winding 3 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 3]
Windg 3	Winding 3 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg 3]
Windg 3 t-	Winding 3 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg 3]
Windg 3 Trip	Winding 3 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg 3]
Windg 4	Winding 4 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
Tanodon				/Temp-Prot
				/RTD
				/Windg 4]
Windg 4 Trip	Winding 4 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 4]

Page 462 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Windg 4	Winding 4 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg 4]
Windg 4 t-	Winding 4 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg 4]
Windg 4 Trip	Winding 4 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg 4]
Windg 5	Winding 5 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
Tunction				/Temp-Prot
				/RTD
				/Windg 5]
Windg 5 Trip	Winding 5 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 5]
Windg 5	Winding 5 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg 5]
Windg 5 t-	Winding 5 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg 5]

Parameter	Description	Setting range	Default	Menu path
Windg 5 Trip	Winding 5 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg 5]
Windg 6	Winding 6 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
Tanodon				/Temp-Prot
				/RTD
				/Windg 6]
Windg 6 Trip	Winding 6 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg 6]
Windg 6	Winding 6 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg 6]
Windg 6 t-	Winding 6 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg 6]
Windg 6 Trip	Winding 6 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg 6]
MotBear 1	Motor Bearing 1 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
. 31100011				/Temp-Prot
				/RTD
				/MotBear 1]

Page 464 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
MotBear 1	Motor Bearing 1 Trip Function	inactive,	active	[Protection Para
Trip Function		active		/<14>
				/Temp-Prot
				/RTD
				/MotBear 1]
MotBear 1	Motor Bearing 1 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/MotBear 1]
MotBear 1 t-	Motor Bearing 1 If this time is expired a	0 – 360 min	1 min	[Protection Para
Delay	Temperature Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function = use			/Temp-Prot
				/RTD
				/MotBear 1]
MotBear 1	Motor Bearing 1 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
Trip	Only available if: Device planning: Trip Function = use			/<14>
				/Temp-Prot
				/RTD
				/MotBear 1]
MotBear 2	Motor Bearing 2 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
Turicuon				/Temp-Prot
				/RTD
				/MotBear 2]
MotBear 2	Motor Bearing 2 Trip Function	inactive,	active	[Protection Para
Trip Function		active		/<14>
				/Temp-Prot
				/RTD
				/MotBear 2]
MotBear 2	Motor Bearing 2 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Marm Eunstion			/<14>
	Only available if: Device planning: Alarm Function = use			/Temp-Prot
				/RTD
				/MotBear 2]

Parameter	Description	Setting range	Default	Menu path
MotBear 2 t-	Motor Bearing 2 If this time is expired a	0 – 360 min	1 min	[Protection Para
Delay	Temperature Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/MotBear 2]
MotBear 2	Motor Bearing 2 Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
Trip	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/MotBear 2]
LoadBear 1	Load Bearing 1 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
Turicuon				/Temp-Prot
				/RTD
				/LoadBear 1]
LoadBear 1	Load Bearing 1 Trip Function	inactive,	active	[Protection Para
Trip Function		active		/<14>
				/Temp-Prot
				/RTD
				/LoadBear 1]
LoadBear 1	Load Bearing 1 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/LoadBear 1]
LoadBear 1	Load Bearing 1 If this time is expired a	0 – 360 min	1 min	[Protection Para
t-Delay	Temperature Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/LoadBear 1]
LoadBear 1	Load Bearing 1 Threshold for Temperature Trip	0 - 200°C	80°C	[Protection Para
Trip	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/LoadBear 1]

Page 466 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
LoadBear 2	Load Bearing 2 Alarm Function	inactive,	active	[Protection Para
Alarm Function		active		/<14>
T directori				/Temp-Prot
				/RTD
				/LoadBear 2]
LoadBear 2	Load Bearing 2 Trip Function	inactive,	active	[Protection Para
LoadBear		active		/<14>
				/Temp-Prot
				/RTD
				/LoadBear 2]
LoadBear 2	Load Bearing 2 Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/LoadBear 2]
LoadBear 2	Load Bearing 2 If this time is expired a	0 – 360 min	1 min	[Protection Para
t-Delay	Temperature Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/LoadBear 2]
LoadBear 2	Load Bearing 2 Threshold for Temperature Trip	0 - 200°C	80°C	[Protection Para
Trip	Only available if: Device planning: Trip Function = use			/<14>
				/Temp-Prot
				/RTD
				/LoadBear 2]
Aux1 Alarm	Auxiliary Alarm Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Aux 1]
Aux1 Trip	Auxiliary Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Aux 1]

Parameter	Description	Setting range	Default	Menu path
Aux1 Alarm	Auxiliary Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
	Only available if: Device planning: Alarm			/<14>
	Function1 = use Only available if: Device			/Temp-Prot
	planning: Alarm Function2 = use			/RTD
				/Aux 1]
Aux1 t-Delay	Auxiliary If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm			/Temp-Prot
	Function1 = use Only available if: Device planning: Alarm Function2 = use			/RTD
	planning. Alarm Function2 – use			/Aux 1]
Aux1 Trip	Auxiliary Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function2			/<14>
	= use Only available if: Device planning: Trip			/Temp-Prot
	Function2 = use			/RTD
				/Aux 1]
Aux2 Alarm	Auxiliary 2 Alarm Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Aux 2]
Aux2 Trip	Auxiliary 2 Trip Function	inactive,	active	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Aux 2]
Aux2 Alarm	Auxiliary 2 Threshold for Temperature Alarm	0 - 200°C	105°C	[Protection Para
				/<14>
				/Temp-Prot
				/RTD
				/Aux 2]
Aux2 t-Delay	Auxiliary 2 If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
	Alarm will be generated.			/<14>
				/Temp-Prot
				/RTD
				/Aux 2]

Page 468 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Aux2 Trip	Auxiliary 2 Threshold for Temperature Trip	0 - 200°C	110°C	[Protection Para
				/<14>
				/Temp-Prot
				/RTD
				/Aux 2]
Windg Alarm	Winding Alarm Function	inactive,	inactive	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg]
Windg Trip	Winding Trip Function	inactive,	inactive	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Windg]
Windg Alarm	Winding Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/Windg]
Windg t-	Winding If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/Windg]
Windg Trip	Winding Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function =			/<14>
	use			/Temp-Prot
				/RTD
				/Windg]
MotBear	Motor Bearing Alarm Function	inactive,	inactive	[Protection Para
Alarm Function		active		/<14>
i dilottori				/Temp-Prot
				/RTD
				/MotBear]

Parameter	Description	Setting range	Default	Menu path
MotBear Trip	Motor Bearing Trip Function	inactive,	inactive	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/MotBear]
MotBear	Motor Bearing Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use			/Temp-Prot
				/RTD
				/MotBear]
MotBear t-	Motor Bearing If this time is expired a	0 – 360 min	1 min	[Protection Para
Delay	Temperature Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use			/RTD
				/MotBear]
MotBear Trip	Motor Bearing Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
	Only available if: Device planning: Trip Function = use			/<14>
				/Temp-Prot
				/RTD
				/MotBear]
LoadBear	Load Bearing Alarm Function	inactive,	inactive	[Protection Para
Alarm Function		active		/<14>
Turicuon				/Temp-Prot
				/RTD
				/LoadBear]
LoadBear	Load Bearing Trip Function	inactive,	active	[Protection Para
Trip Function		active		/<14>
				/Temp-Prot
				/RTD
				/LoadBear]
LoadBear	Load Bearing Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
Alarm	Only available if: Device planning: Alarm Function			/<14>
	= use Only available if: Device planning: Alarm			/Temp-Prot
	Function = use			/RTD
				/LoadBear]

Page 470 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
LoadBear t-	Load Bearing If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
Delay	Alarm will be generated.			/<14>
	Only available if: Device planning: Alarm Function			/Temp-Prot
	= use Only available if: Device planning: Alarm Function = use			/RTD
	Tanoasii ass			/LoadBear]
LoadBear	Load Bearing Threshold for Temperature Trip	0 - 200°C	80°C	[Protection Para
Trip	Only available if: Device planning: Trip Function =			/<14>
	use Only available if: Device planning: Aux = use			/Temp-Prot
				/RTD
				/LoadBear]
Aux Alarm	Auxiliary Alarm Function	inactive,	inactive	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Aux]
Aux Trip	Auxiliary Trip Function	inactive,	inactive	[Protection Para
Function		active		/<14>
				/Temp-Prot
				/RTD
				/Aux]
Aux Alarm	Auxiliary Threshold for Temperature Alarm	0 - 200°C	80°C	[Protection Para
				/<14>
				/Temp-Prot
				/RTD
				/Aux]
Aux t-Delay	Auxiliary If this time is expired a Temperature	0 – 360 min	1 min	[Protection Para
	Alarm will be generated.			/<14>
				/Temp-Prot
				/RTD
				/Aux]
Aux Trip	Auxiliary Threshold for Temperature Trip	0 - 200°C	100°C	[Protection Para
				/<14>
				/Temp-Prot
				/RTD
				/Aux]

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive,	inactive	[Protection Para
		active		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Voting 1	Voting: This parameter defines how many of the	1 - 11	1	[Protection Para
	selected channels must be over its threshold level for getting a voting trip			/<14>
	lor getting a voting trip			/Temp-Prot
				/RTD
				/Voting1]
Windg 1	Winding 1	no,	yes	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Windg 2	Winding 2	no,	yes	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Windg 3	Winding 3	no,	yes	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Windg 4	Winding 4	no,	yes	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Windg 5	Winding 5	no,	yes	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]

Page 472 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Windg 6	Winding 6	no,	yes	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
MotBear 1	Motor Bearing 1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
MotBear 2	Motor Bearing 2	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
LoadBear 1	Load Bearing 1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
LoadBear 2	Load Bearing 2	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Aux1	Auxiliary1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]
Aux2	Auxiliary2	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting1]

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive,	inactive	[Protection Para
		active		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Voting 2	Voting: This parameter defines how many of the	1 - 11	1	[Protection Para
	selected channels must be over its threshold level for getting a voting trip			/<14>
	lor getting a voting trip			/Temp-Prot
				/RTD
				/Voting2]
Windg 1	Winding 1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Windg 2	Winding 2	no,	no	[Protection Para
-		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Windg 3	Winding 3	no,	no	[Protection Para
-		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Windg 4	Winding 4	no,	no	[Protection Para
-		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Windg 5	Winding 5	no,	no	[Protection Para
<u>-</u>		yes		/<14>
		-		/Temp-Prot
				/RTD
				Noting2]

Page 474 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Windg 6	Winding 6	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
MotBear 1	Motor Bearing 1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
MotBear 2	Motor Bearing 2	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
LoadBear 1	Load Bearing 1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
LoadBear 2	Load Bearing 2	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Aux1	Auxiliary1	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]
Aux2	Auxiliary2	no,	no	[Protection Para
		yes		/<14>
				/Temp-Prot
				/RTD
				/Voting2]

RTD Temperature Protection Module Input States

Name	Description	Assignment via
ExBlo1-I	Module input state: External blocking1	[Protection Para
		/Global Prot Para
		/Temp-Prot
		/RTD]
ExBlo2-I	Module input state: External blocking2	[Protection Para
		/Global Prot Para
		/Temp-Prot
		/RTD]
ExBlo TripCmd-I	Module input state: External Blocking of the	[Protection Para
Trip Command	Trip Command	/Global Prot Para
		/Temp-Prot
		/RTD]

RTD Temperature Protection Module Signals (Output States)

Name	Description
active	Signal: active
ExBlo	Signal: External Blocking
Blo TripCmd	Signal: Trip Command blocked
ExBlo TripCmd	Signal: External Blocking of the Trip Command
Alarm	Alarm RTD Temperature Protection
Trip	Signal: Trip
TripCmd	Signal: Trip Command
Windg 1 Trip	Winding 1 Signal: Trip
Windg 1 Alarm	Winding 1 Alarm RTD Temperature Protection
Windg 1 Timeout Alarm	Winding 1 Timeout Alarm
Windg 1 Invalid	Winding 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Windg 2 Trip	Winding 2 Signal: Trip
Windg 2 Alarm	Winding 2 Alarm RTD Temperature Protection
Windg 2 Timeout Alarm	Winding 2 Timeout Alarm
Windg 2 Invalid	Winding 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Windg 3 Trip	Winding 3 Signal: Trip
Windg 3 Alarm	Winding 3 Alarm RTD Temperature Protection
Windg 3 Timeout Alarm	Winding 3 Timeout Alarm
Windg 3 Invalid	Winding 3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Windg 4 Trip	Winding 4 Signal: Trip

Page 476 EN MRM4 12/11

Name	Description
Windg 4 Alarm	Winding 4 Alarm RTD Temperature Protection
Windg 4 Timeout Alarm	Winding 4 Timeout Alarm
Windg 4 Invalid	Winding 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Windg 5 Trip	Winding 5 Signal: Trip
Windg 5 Alarm	Winding 5 Alarm RTD Temperature Protection
Windg 5 Timeout Alarm	Winding 5 Timeout Alarm
Windg 5 Invalid	Winding 5 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Windg 6 Trip	Winding 6 Signal: Trip
Windg 6 Alarm	Winding 6 Alarm RTD Temperature Protection
Windg 6 Timeout Alarm	Winding 6 Timeout Alarm
Windg 6 Invalid	Winding 6 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
MotBear 1 Trip	Motor Bearing 1 Signal: Trip
MotBear 1 Alarm	Motor Bearing 1 Alarm RTD Temperature Protection
MotBear 1 Timeout Alarm	Motor Bearing 1 Timeout Alarm
MotBear 1 Invalid	Motor Bearing 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
MotBear 2 Trip	Motor Bearing 2 Signal: Trip
MotBear 2 Alarm	Motor Bearing 2 Alarm RTD Temperature Protection
MotBear 2 Timeout Alarm	Motor Bearing 2 Timeout Alarm
MotBear 2 Invalid	Motor Bearing 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
LoadBear 1 Trip	Load Bearing 1 Signal: Trip
LoadBear 1 Alarm	Load Bearing 1 Alarm RTD Temperature Protection
LoadBear 1 Timeout Alarm	Load Bearing 1 Timeout Alarm
LoadBear 1 Invalid	Load Bearing 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
LoadBear 2 Trip	Load Bearing 2 Signal: Trip
LoadBear 2 Alarm	Load Bearing 2 Alarm RTD Temperature Protection
LoadBear 2 Timeout Alarm	Load Bearing 2 Timeout Alarm
LoadBear 2 Invalid	Load Bearing 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Aux1 Trip	Auxiliary 1 Signal: Trip
Aux1 Alarm	Auxiliary 1 Alarm RTD Temperature Protection
Aux1 Timeout Alarm	Auxiliary 1 Timeout Alarm
Aux1 Invalid	Auxiliary 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Aux2 Trip	Auxiliary 2 Signal: Trip
Aux2 Alarm	Auxiliary 2 Alarm RTD Temperature Protection
Aux2 Timeout Alarm	Auxiliary 2 Timeout Alarm

Name	Description
Aux2 Invalid	Auxiliary 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Trip WD Group	Trip all Windings
Alarm WD Group	Alarm all Windings
TimeoutAlmWDGrp	Timeout Alarm all Windings
Windg Group Invalid	Winding Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Trip MB Group	Trip all Motor Bearings
Alarm MB Group	Alarm all Motor Bearings
TimeoutAlmMBGrp	Timeout Alarm all Motor Bearings
MotBear Group Invalid	Motor Bearing Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Trip LB Group	Trip all Load Bearings
Alarm LB Group	Alarm all Load Bearings
TimeoutAlmLBGrp	Timeout Alarm all Load Bearings
LoadBear Group Invalid	Load Bearing Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
Trip Any Group	Trip Any Group
Alarm Any Group	Alarm Any Group
TimeoutAlmAnyGrp	Timeout Alarm Any Group
Trip Group 1	Trip Group 1
Trip Group 2	Trip Group 2
Timeout Alarm	Alarm timeout expired
Trip Aux Group	Trip Auxiliary Group
Alarm Aux Group	Alarm Auxiliary Group
TimeoutAlmAuxGrp	Timeout Alarm Auxiliary Group
AuxGrpInvalid	Invalid Auxiliary Group

RTD Temperature Protection Module Counter Values

Value	Description	Default	Size	Menu path
HottestWindingTemp	Hottest motor winding temperature in	0°C	0 - 200°C	[Operation
	degrees C.			/Measured values
				/URTD]
HighestWdTemp	Highest motor winding temperature in	0°C	0 - 250°C	[Operation
	degrees.			/History
				/OperationsCr]

Page 478 EN MRM4 12/11

Value	Description	Default	Size	Menu path
HighestMbTemp	Highest motor bearing temperature in	0°C	0 - 250°C	[Operation
	degrees.			/History
				/OperationsCr]
HighestLbTemp	Highest load bearing temperature in	0°C	0 - 250°C	[Operation
	degrees.			/History
				/OperationsCr]
HighestAuxTemp	Highest Auxiliary temperature in degrees.	0°C	0 - 250°C	[Operation
				/History
				/OperationsCr]
nWdAlarms	Number of winding temperature alarms since	0	0 - 65535	[Operation
	last reset.			/History
				/AlarmCr]
nMbAlarms	Number of motor bearing temperature	0	0 - 65535	[Operation
	alarms since last reset.			/History
				/AlarmCr]
nLbAlarms	Number of load bearing temperature alarms	0	0 - 65535	[Operation
	since last reset.			/History
				/AlarmCr]
nAuxAlarms	Number of auxiliary temperature alarms	0	0 - 65535	[Operation
	since last reset.			/History
				/AlarmCr]
nWdTrips	Number of winding temperature trips since	0	0 - 65535	[Operation
	last reset.			/History
				/TripCr]
nMbTrips	Number of motor bearing temperature trips	0	0 - 65535	[Operation
	since last reset.			/History
				/TripCr]
nLbTrips	Number of load bearing temperature trips	0	0 - 65535	[Operation
	since last reset.			/History
				/TripCr]
nAuxTrips	Number of auxiliary temperature trips since	0	0 - 65535	[Operation
	last reset.			/History
				/TripCr]
nChannelFails	Number of RTD channel failures.	0	0 - 65535	[Operation
				/History
				/AlarmCr]

URTDII Module Interface*

*=Availability on request

URTD

Principle - General Use

The optional Universal Resistance-based Temperature Detector II (URTDII) Module provides temperature data to the protective device from up to 12 RTDs embedded in the motor, generator, transformer, or cable connector and driven equipment. The temperature data will be shown as measured values and statistics in the Operating Data menu. In addition, each channel will be monitored. The measured data provided by the URTDII Module can also be used for temperature protection (please refer to the Temperature Protection section).

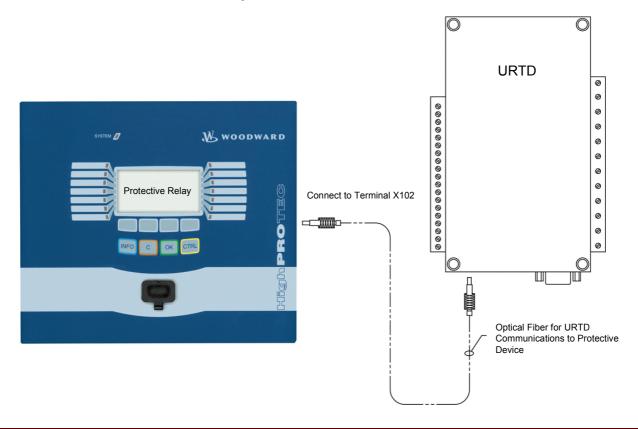
The URTDII conveys multiplexed temperature data back to the relay via a single optical fiber. The URTDII may be mounted remotely from the protective device. The fiber optic connector is located on the **X102** terminal of the protective device.

Consider the benefit of mounting the URTDII module away from the protective device and as close to the protected equipment as possible. The big bundle of RTD wires to the protected equipment becomes much shorter. The URTDII may be placed up to 400 ft (121.9 m) from the protective device with the optical fiber connection. Note that the URTDII will require a power supply connection at its remote location.

Connect a suitable source to the power terminals J10A-1 and J10A-2 on the URTDII module.

<u>Style</u>	Power Supply
URTDII-01	48-240 V AC 48-250 V DC
URTDII-02	24-48 V DC

URTDII Module Fiber Optic Connection to the Protective Device



Page 480 EN MRM4 12/11

The figure above shows the fiber optic connections between the URTDII Module and the protective device. The protective device supports the optical fiber connection.

Preassembled plastic optical fibers with connectors can be ordered from any distributor of optical fiber products. In addition, these same distributors offer long rolls of cable with connectors that can be installed in the field. Some distributors will make custom lengths to order.

NOTICE

Surplus length of a pre-cut fiber does not cause a problems. Simply coil and tie the excess fiber at a convenient point. Avoid high tie pressure. Bending radius of the fiber should be greater than 2 in. (50.8 mm).

The fiber termination at the URTDII simply snaps into or out of the connector. To connect the fiber termination at the protective device, push the plug of the fiber optic onto the device interface then turn it until it "snaps".

CAUTION

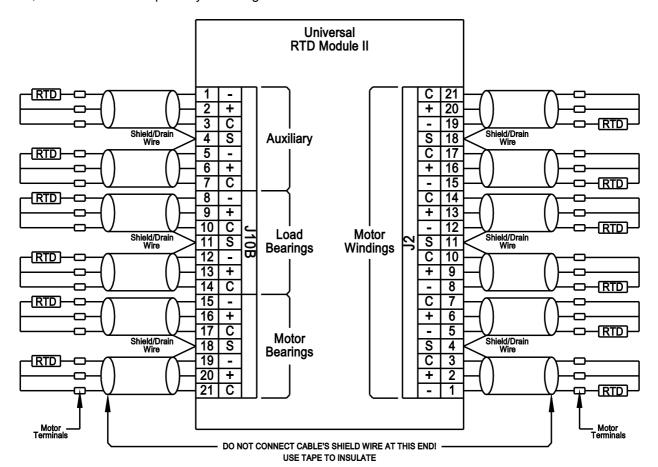
The protective device as well as the URTDII have various power supply options. Make certain that the power supply is acceptable for both units before connecting the same power supply to both devices.

NOTICE

Consult the URTDII Module Instruction Leaflet for complete instructions.

Three URTD terminals are provided for each RTD input.

The three terminals for any unused RTD input channel should be wired together. For example, if MW5 and MW6 are unused, MW5 terminals J2-15, J2-16, and J2-17 should be wired together and MW6 terminals J2-19, J2-20, J2- 21 should be separately wired together.



See the figure above for wiring of RTDs to the URTD inputs. Use three-conductor shielded cable. Note the connection rules in the figure. When making connections to a two-lead RTD, connect two of the cable conductors to one of the RTD leads as shown. Make this connection as close to the protected object as possible. Connect the third cable conductor to the remaining RTD lead.

Connect the shield / drain wire to the Shield terminal as shown in the figure. The RTD cable shield should be connected only at the URTD end, and insulated at the RTD end. The RTD's themselves must not be grounded at the object to be protected.

Remember to set the URTDII module DIP switches according to the types of RTDs in each of the channels.

Direct Commands of the URTD Module

Parameter	Description	Setting range	Default	Menu path
Function	Permanent activation or deactivation of module/stage.	inactive, active	inactive	[Service
				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Winding 1	0 - 200°C	0°C	[Service
Windg1				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Winding 2	0 - 200°C	0°C	[Service
Windg2				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Winding 3	0 - 200°C	0°C	[Service
Windg3				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Winding 4	0 - 200°C	0°C	[Service
Windg4				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Winding 5	0 - 200°C	0°C	[Service
Windg5				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Winding 6	0 - 200°C	0°C	[Service
Windg6				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Motor Bearing 1	0 - 200°C	0°C	[Service
MotBear1				/Test Mode (Prot inhibit)
				/URTD]

Page 482 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
Force	Force Motor Bearing 2	0 - 200°C	0°C	[Service
MotBear2				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Load Bearing 1	0 - 200°C	0°C	[Service
LoadBear1				/Test Mode (Prot inhibit)
				/URTD]
Force	Force Load Bearing 2	0 - 200°C	0°C	[Service
LoadBear2				/Test Mode (Prot inhibit)
				/URTD]
Force Aux1	Force Auxiliary1	0 - 200°C	0°C	[Service
				/Test Mode (Prot inhibit)
				/URTD]
Force Aux2	Force Auxiliary2	0 - 200°C	0°C	[Service
				/Test Mode (Prot inhibit)
				/URTD]

Global Protection Parameters of the URTD Module

Parameter	Description	Setting range	Default	Menu path
Force Mode	By means of this function the normal Relay Output Contact States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	permanent, Timeout	permanent	[Service /Test Mode (Prot inhibit) /URTD]
t-Timeout Force	The Output State will be set by force for the duration of this time. That means for the duration of this time the Relay Output Contact does not show the state of the signals that are assigned on it. Only available if: Mode = Timeout DISARM	0.00 – 300.00 s	0.03 s	[Service /Test Mode (Prot inhibit) /URTD]
Temperature Unit	Temperature Unit	Celsius, Fahrenheit	Celsius	[Device Para /Measurem Display]

URTD Signals (Output States)

Name	Description
Windg1 Superv	Signal: Supervision Channel Windg1
Windg2 Superv	Signal: Supervision Channel Windg2
Windg3 Superv	Signal: Supervision Channel Windg3
Windg4 Superv	Signal: Supervision Channel Windg4
Windg5 Superv	Signal: Supervision Channel Windg5
Windg6 Superv	Signal: Supervision Channel Windg6
MotBear1 Superv	Signal: Supervision Channel MotBear1
MotBear2 Superv	Signal: Supervision Channel MotBear2
LoadBear1 Superv	Signal: Supervision Channel LoadBear1
LoadBear2 Superv	Signal: Supervision Channel LoadBear2
Aux1 Superv	Signal: Supervision Channel Aux1
Aux2 Superv	Signal: Supervision Channel Aux2
Superv	Signal: URTD Supervision Channel
active	Signal: URTD active
Outs forced	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.

URTD Module Statistics

Value	Description	Menu path
Windg1 max	Winding1 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Windg1 min	Winding1 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
Windg2 max	Winding2 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Windg2 min	Winding2 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]

Page 484 EN MRM4 12/11

Value	Description	Menu path
Windg3 max	Winding3 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Windg3 min	Winding3 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
Windg4 max	Winding4 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Windg4 min	Winding4 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
Windg5 max	Winding5 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Windg5 min	Winding5 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
Windg6 max	Winding6 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Windg6 min	Winding6 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
MotBear1 max	Motor Bearing1 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]

Value	Description	Menu path
MotBear1 min	Motor Bearing1 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
MotBear2 max	Motor Bearing2 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
MotBear2 min	Motor Bearing2 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
LoadBear1 max	Load Bearing1 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
LoadBear1 min	Load Bearing1 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
LoadBear2 max	Load Bearing2 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
LoadBear2 min	Load Bearing2 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]
Aux1 max	Auxiliary1 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Aux1 min	Auxiliary1 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]

Page 486 EN MRM4 12/11

Value	Description	Menu path
Aux2 max	Auxiliary2 Maximum Value	[Operation
		/Statistics
		/Max
		/URTD]
Aux2 min	Auxiliary2 Minimum Value	[Operation
		/Statistics
		/Min
		/URTD]

URTD Measured Values

Value	Description	Menu path
Windg1	Winding 1	[Operation
		/Measured values
		/URTD]
Windg2	Winding 2	[Operation
		/Measured values
		/URTD]
Windg3	Winding 3	[Operation
		/Measured values
		/URTD]
Windg4	Winding 4	[Operation
		/Measured values
		/URTD]
Windg5	Winding 5	[Operation
		/Measured values
		/URTD]
Windg6	Winding 6	[Operation
		/Measured values
		/URTD]
MotBear1	Motor Bearing 1	[Operation
		/Measured values
		/URTD]
MotBear2	Motor Bearing 2	[Operation
		/Measured values
		/URTD]
LoadBear1	Load Bearing 1	[Operation
		/Measured values
		/URTD]

URTDII Module Interface*

Value	Description	Menu path
LoadBear2	Load Bearing 2	[Operation
		/Measured values
		/URTD]
Aux1	Auxiliary1	[Operation
		/Measured values
		/URTD]
Aux2	Auxiliary2	[Operation
		/Measured values
		/URTD]
RTD Max	Maximum temperature of all channels.	[Operation
		/Measured values
		/URTD]

Page 488 EN MRM4 12/11

Programmable Logic

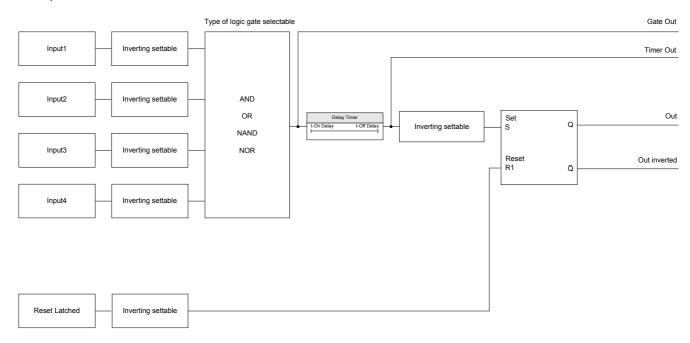
Available Elements (Equations): Logic

General Description

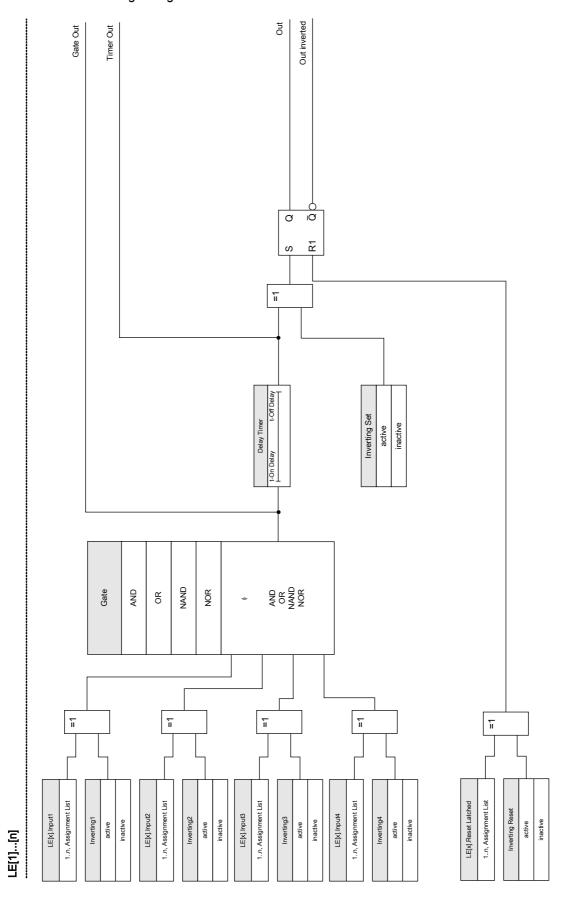
The Protective Relay includes programmable Logic Equations for programming relay output contacts, blocking of protective functions and custom logic functions in the relay.

The logic provides control of the relay output contacts based on the state of the inputs that can be chosen from the assignment list (protective function pickups, protective function states, breaker states, system alarms, and module inputs). The User can use the outputs signals of a Logic Equation as inputs in higher equations (e.g. the output signal of Logic Equation 10 might be used as an input of Logic Equation 11).

Principle Overview



Detailed Overview - Overall Logic diagram

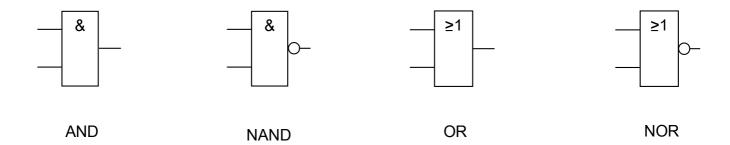


Page 490 EN MRM4 12/11

Available Gates (Operators)

Within the Logic Equation, the following Gates can be used:

Gate



Input Signals

The User can assign up to 4 Input signals (from the assignment list) to the inputs of the gate.

As an option, each of the 4 input signals can be inverted (negated)

Timer Gate (On Delay and Off Delay)

The output of the gate can be delayed. The User has the option to set an On and an Off delay.

Latching

The timer issues two signals. An unlatched and a latched signal. The latched input can optionally be inverted. In order to reset the latched signal the User has to assign an reset signal from the assignment list. The reset signal can also optionally be inverted.

Cascading Logical Outputs

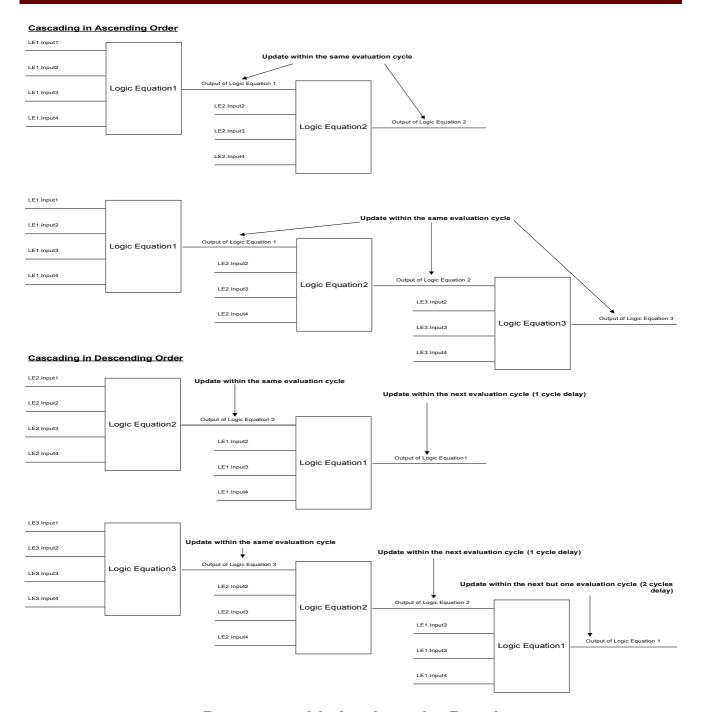
The device will evaluate output states of the Logic Equations starting from Logic Equation 1 up to the Logic Equation with the highest number. This evaluation (device) cycle will be continuously repeated.

Cascading Logic Equations in an ascending sequence

Cascading in an ascending sequence means that the User uses the output signal of "Logic Equation \mathbf{n} " as input of "Logic Equation \mathbf{n} ". If the state of "Logic Equation \mathbf{n} " changes, the state of the output of "Logic Equation \mathbf{n} " will be updated within the same cycle.

Cascading Logic Equations in a descending sequence

Cascading in a descending sequence means that the User uses the output signal of "Logic Equation **n+1**" as input of "Logic Equation **n**". If the output of "Logic Equation **n+1**" changes, this change of the feed back signal at the input of "Logic Equation **n**" will be delayed for one cycle.



Programmable Logic at the Panel



WARNING improper use of Logic Equations might result in personal injury or damage the electrical equipment.

Don't use Logic Equations unless that you can ensure the safe functionality.

How to configure a Logic Equation?

- Call up menu [Logic/LE [x]]:
- Set the Input Signals (where necessary, invert them).

Page 492 EN MRM4 12/11

- If required, configure the timer (»On delay« and »Off delay«).
- If the latched output signal is used assign a reset signal to the reset input.
- Within the »status display«, the User can check the status of the logical inputs and outputs of the Logic Equation.

In case that Logic Equations should be cascaded the User has to be aware of timing delays (cycles) in case of descending sequences (Please refer to section: Cascading Logical Outputs).

By means the Status Display [Operation/Status Display] the logical states can be verified.]

Programmable Logic via Smart View



WARNING improper use of Logic Equations might result in personal injury or damage the electrical equipment.

Don't use Logic Equations unless that you can ensure the safe functionality.



It is recommended to configure the logic via Smart View.

How to configure a Logic Equation?

- Call up menu [Logic/LE [x]:
- Call up the Logic Editor
- Set the Input Signals (where necessary, invert them).
- If required, configure the timer (»On delay« and »Off delay«).
- If the latched output signal is used assign a reset signal to the reset input.
- Within the »status display«, the User can check the status of the logical inputs and outputs of the Logic Equation.

In case that Logic Equations should be cascaded the User has to be aware of timing delays (cycles) in case of descending sequences (Please refer to section: Cascading Logical Outputs).

By means the Status Display [Operation/Status Display] the logical states can be verified.]

Device Planning Parameters of the Programmable Logic

Parameter	Description	Options	Default	Menu path
No of	Number of required Logic Equations:	0,	20	[Device planning]
Equations:		5,		
		10,		
		20,		
		40,		
		80		

Selection List for Logic Input Signals

Name	Description
-,-	No assignment
Prot.active	Signal: active
Prot.available	Signal: Protection is available
Prot.I dir fwd	Signal: Phase current failure forward direction
Prot.I dir n poss	Signal: Phase fault - missing reference voltage
Prot.I dir rev	Signal: Phase current failure reverse direction
Prot.IG calc dir fwd	Signal: Ground fault (calculated) forward
Prot.IG calc dir n poss	Signal: Ground fault (calculated) direction detection not possible
Prot.IG calc rev dir	Signal: Ground fault (calculated) reverse direction
Prot.IG meas dir fwd	Signal: Ground fault (measured) forward
Prot.IG meas dir n poss	Signal: Ground fault (measured) direction detection not possible
Prot.IG meas rev dir	Signal: Ground fault (measured) reverse direction
Prot.Alarm	Signal: General Alarm
Prot.Trip	Signal: General Trip
SG.SI SingleContactInd	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.
SG.Pos not ON	Signal: Pos not ON
SG.Pos ON	Signal: Breaker is in ON-Position
SG.Pos OFF	Signal: Breaker is in OFF-Position
SG.Pos Indeterm	Signal: Breaker is in Indeterminate Position
SG.Pos Disturb	Signal: Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
SG.Ready	Signal: Breaker is ready for operation.
SG.Interl ON	Signal: One or more IL_On inputs are active.
SG.Interl OFF	Signal: One or more IL_Off inputs are active.
SG.CES succesf	Command Execution Supervision: Switching command executed successfully.

Page 494 EN MRM4 12/11

Name	Description
SG.CES Disturbed	Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.
SG.CES Fail TripCmd	Command Execution Supervision: Command execution failed because trip command is pending.
SG.CES SwitchgDir	Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.
SG.CES ON d OFF	Command Execution Supervision: On Command during a pending OFF Command.
SG.CES SG not ready	Command Execution Supervision: Switchgear not ready
SG.CES Fiel Interl	Command Execution Supervision: Switching Command not executed because of field interlocking.
SG.CES SyncTimeout	Command Execution Supervision: Switching Command not executed No Synchronization signal while t-sync was running.
SG.TripCmd	Signal: Trip Command
SG.Ack TripCmd	Signal: Acknowledge Trip Command
SG.SGwear Slow SG	Signal: Alarm, the breaker (load-break switch) becomes slower
SG.Res SGwear SI SG	Signal: Resetting the slow Switchgear Alarm
SG.ON Cmd	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
SG.OFF Cmd	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
SG.ON Cmd manual	Signal: ON Cmd manual
SG.OFF Cmd manual	Signal: OFF Cmd manual
I[1].Alarm	Signal: Alarm
I[1].Trip	Signal: Trip
I[1].TripCmd	Signal: Trip Command
I[2].Alarm	Signal: Alarm
I[2].Trip	Signal: Trip
I[2].TripCmd	Signal: Trip Command
I[3].Alarm	Signal: Alarm
I[3].Trip	Signal: Trip
I[3].TripCmd	Signal: Trip Command
I[4].Alarm	Signal: Alarm
I[4].Trip	Signal: Trip
I[4].TripCmd	Signal: Trip Command
I[5].Alarm	Signal: Alarm
I[5].Trip	Signal: Trip
I[5].TripCmd	Signal: Trip Command

Name	Description
I[6].Alarm	Signal: Alarm
I[6].Trip	Signal: Trip
I[6].TripCmd	Signal: Trip Command
IG[1].Alarm	Signal: Alarm IG
IG[1].Trip	Signal: Trip
IG[1].TripCmd	Signal: Trip Command
IG[2].Alarm	Signal: Alarm IG
IG[2].Trip	Signal: Trip
IG[2].TripCmd	Signal: Trip Command
IG[3].Alarm	Signal: Alarm IG
IG[3].Trip	Signal: Trip
IG[3].TripCmd	Signal: Trip Command
IG[4].Alarm	Signal: Alarm IG
IG[4].Trip	Signal: Trip
IG[4].TripCmd	Signal: Trip Command
I2>[1].Alarm	Signal: Alarm Negative Sequence
12>[1].Trip	Signal: Trip
I2>[1].TripCmd	Signal: Trip Command
12>[2].Alarm	Signal: Alarm Negative Sequence
12>[2].Trip	Signal: Trip
12>[2].TripCmd	Signal: Trip Command
RTD.Alarm	Alarm RTD Temperature Protection
RTD.Trip	Signal: Trip
RTD.TripCmd	Signal: Trip Command
ExP[1].Alarm	Signal: Alarm
ExP[1].Trip	Signal: Trip
ExP[1].TripCmd	Signal: Trip Command
ExP[2].Alarm	Signal: Alarm
ExP[2].Trip	Signal: Trip
ExP[2].TripCmd	Signal: Trip Command
ExP[3].Alarm	Signal: Alarm
ExP[3].Trip	Signal: Trip
ExP[3].TripCmd	Signal: Trip Command
ExP[4].Alarm	Signal: Alarm
ExP[4].Trip	Signal: Trip
ExP[4].TripCmd	Signal: Trip Command
SOTF.active	Signal: active
SOTF.enabled	Signal: Switch Onto Fault enabled. This Signal can be used to
SOTT .enabled	modify Overcurrent Protection Settings.

Page 496 EN MRM4 12/11

Name	Description
CBF.Lockout	Signal: Lockout
CBF.running	Signal: CBF-Module started
CBF.Alarm	Signal: Breaker Failure
TCS.Not Possible	Not possible because no state indicator assigned to the breaker.
TCS.Alarm	Signal: Alarm Trip Circuit Supervision
CTS.Alarm	Signal: Alarm Current Transformer Measuring Circuit Supervision
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input
IEC61850.VirtOut1-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut2-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut3-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut4-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut5-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut6-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut7-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut8-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut9-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut10-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut11-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut12-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut13-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut14-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut15-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut16-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtInp1	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp2	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp3	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp4	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp5	Signal: Virtual Input (IEC61850 GGIO Ind)

Name	Description
IEC61850.VirtInp6	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp7	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp8	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp9	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp10	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp11	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp12	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp13	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp14	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp15	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp16	Signal: Virtual Input (IEC61850 GGIO Ind)
Logic.LE1.Gate Out	Signal: Output of the logic gate
Logic.LE1.Timer Out	Signal: Timer Output
Logic.LE1.Out	Signal: Latched Output (Q)
Logic.LE1.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE2.Gate Out	Signal: Output of the logic gate
Logic.LE2.Timer Out	Signal: Timer Output
Logic.LE2.Out	Signal: Latched Output (Q)
Logic.LE2.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE3.Gate Out	Signal: Output of the logic gate
Logic.LE3.Timer Out	Signal: Timer Output
Logic.LE3.Out	Signal: Latched Output (Q)
Logic.LE3.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE4.Gate Out	Signal: Output of the logic gate
Logic.LE4.Timer Out	Signal: Timer Output
Logic.LE4.Out	Signal: Latched Output (Q)
Logic.LE4.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE5.Gate Out	Signal: Output of the logic gate
Logic.LE5.Timer Out	Signal: Timer Output
Logic.LE5.Out	Signal: Latched Output (Q)
Logic.LE5.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE6.Gate Out	Signal: Output of the logic gate
Logic.LE6.Timer Out	Signal: Timer Output
Logic.LE6.Out	Signal: Latched Output (Q)
Logic.LE6.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE7.Gate Out	Signal: Output of the logic gate
Logic.LE7.Timer Out	Signal: Timer Output
Logic.LE7.Out	Signal: Latched Output (Q)
Logic.LE7.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 498 EN MRM4 12/11

Name	Description
Logic.LE8.Gate Out	Signal: Output of the logic gate
Logic.LE8.Timer Out	Signal: Timer Output
Logic.LE8.Out	Signal: Latched Output (Q)
Logic.LE8.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE9.Gate Out	Signal: Output of the logic gate
Logic.LE9.Timer Out	Signal: Timer Output
Logic.LE9.Out	Signal: Latched Output (Q)
Logic.LE9.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE10.Gate Out	Signal: Output of the logic gate
Logic.LE10.Timer Out	Signal: Timer Output
Logic.LE10.Out	Signal: Latched Output (Q)
Logic.LE10.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE11.Gate Out	Signal: Output of the logic gate
Logic.LE11.Timer Out	Signal: Timer Output
Logic.LE11.Out	Signal: Latched Output (Q)
Logic.LE11.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE12.Gate Out	Signal: Output of the logic gate
Logic.LE12.Timer Out	Signal: Timer Output
Logic.LE12.Out	Signal: Latched Output (Q)
Logic.LE12.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE13.Gate Out	Signal: Output of the logic gate
Logic.LE13.Timer Out	Signal: Timer Output
Logic.LE13.Out	Signal: Latched Output (Q)
Logic.LE13.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE14.Gate Out	Signal: Output of the logic gate
Logic.LE14.Timer Out	Signal: Timer Output
Logic.LE14.Out	Signal: Latched Output (Q)
Logic.LE14.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE15.Gate Out	Signal: Output of the logic gate
Logic.LE15.Timer Out	Signal: Timer Output
Logic.LE15.Out	Signal: Latched Output (Q)
Logic.LE15.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE16.Gate Out	Signal: Output of the logic gate
Logic.LE16.Timer Out	Signal: Timer Output
Logic.LE16.Out	Signal: Latched Output (Q)
Logic.LE16.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE17.Gate Out	Signal: Output of the logic gate
Logic.LE17.Timer Out	Signal: Timer Output
Logic.LE17.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE17.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE18.Gate Out	Signal: Output of the logic gate
Logic.LE18.Timer Out	Signal: Timer Output
Logic.LE18.Out	Signal: Latched Output (Q)
Logic.LE18.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE19.Gate Out	Signal: Output of the logic gate
Logic.LE19.Timer Out	Signal: Timer Output
Logic.LE19.Out	Signal: Latched Output (Q)
Logic.LE19.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE20.Gate Out	Signal: Output of the logic gate
Logic.LE20.Timer Out	Signal: Timer Output
Logic.LE20.Out	Signal: Latched Output (Q)
Logic.LE20.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE21.Gate Out	Signal: Output of the logic gate
Logic.LE21.Timer Out	Signal: Timer Output
Logic.LE21.Out	Signal: Latched Output (Q)
Logic.LE21.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE22.Gate Out	Signal: Output of the logic gate
Logic.LE22.Timer Out	Signal: Timer Output
Logic.LE22.Out	Signal: Latched Output (Q)
Logic.LE22.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE23.Gate Out	Signal: Output of the logic gate
Logic.LE23.Timer Out	Signal: Timer Output
Logic.LE23.Out	Signal: Latched Output (Q)
Logic.LE23.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE24.Gate Out	Signal: Output of the logic gate
Logic.LE24.Timer Out	Signal: Timer Output
Logic.LE24.Out	Signal: Latched Output (Q)
Logic.LE24.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE25.Gate Out	Signal: Output of the logic gate
Logic.LE25.Timer Out	Signal: Timer Output
Logic.LE25.Out	Signal: Latched Output (Q)
Logic.LE25.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE26.Gate Out	Signal: Output of the logic gate
Logic.LE26.Timer Out	Signal: Timer Output
Logic.LE26.Out	Signal: Latched Output (Q)
Logic.LE26.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE27.Gate Out	Signal: Output of the logic gate

Page 500 EN MRM4 12/11

Name	Description
Logic.LE27.Out	Signal: Latched Output (Q)
Logic.LE27.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE28.Gate Out	Signal: Output of the logic gate
Logic.LE28.Timer Out	Signal: Timer Output
Logic.LE28.Out	Signal: Latched Output (Q)
Logic.LE28.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE29.Gate Out	Signal: Output of the logic gate
Logic.LE29.Timer Out	Signal: Timer Output
Logic.LE29.Out	Signal: Latched Output (Q)
Logic.LE29.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE30.Gate Out	Signal: Output of the logic gate
Logic.LE30.Timer Out	Signal: Timer Output
Logic.LE30.Out	Signal: Latched Output (Q)
Logic.LE30.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE31.Gate Out	Signal: Output of the logic gate
Logic.LE31.Timer Out	Signal: Timer Output
Logic.LE31.Out	Signal: Latched Output (Q)
Logic.LE31.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE32.Gate Out	Signal: Output of the logic gate
Logic.LE32.Timer Out	Signal: Timer Output
Logic.LE32.Out	Signal: Latched Output (Q)
Logic.LE32.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE33.Gate Out	Signal: Output of the logic gate
Logic.LE33.Timer Out	Signal: Timer Output
Logic.LE33.Out	Signal: Latched Output (Q)
Logic.LE33.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE34.Gate Out	Signal: Output of the logic gate
Logic.LE34.Timer Out	Signal: Timer Output
Logic.LE34.Out	Signal: Latched Output (Q)
Logic.LE34.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE35.Gate Out	Signal: Output of the logic gate
Logic.LE35.Timer Out	Signal: Timer Output
Logic.LE35.Out	Signal: Latched Output (Q)
Logic.LE35.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE36.Gate Out	Signal: Output of the logic gate
Logic.LE36.Timer Out	Signal: Timer Output
Logic.LE36.Out	Signal: Latched Output (Q)
Logic.LE36.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE37.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE37.Timer Out	Signal: Timer Output
Logic.LE37.Out	Signal: Latched Output (Q)
Logic.LE37.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE38.Gate Out	Signal: Output of the logic gate
Logic.LE38.Timer Out	Signal: Timer Output
Logic.LE38.Out	Signal: Latched Output (Q)
Logic.LE38.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE39.Gate Out	Signal: Output of the logic gate
Logic.LE39.Timer Out	Signal: Timer Output
Logic.LE39.Out	Signal: Latched Output (Q)
Logic.LE39.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE40.Gate Out	Signal: Output of the logic gate
Logic.LE40.Timer Out	Signal: Timer Output
Logic.LE40.Out	Signal: Latched Output (Q)
Logic.LE40.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE41.Gate Out	Signal: Output of the logic gate
Logic.LE41.Timer Out	Signal: Timer Output
Logic.LE41.Out	Signal: Latched Output (Q)
Logic.LE41.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE42.Gate Out	Signal: Output of the logic gate
Logic.LE42.Timer Out	Signal: Timer Output
Logic.LE42.Out	Signal: Latched Output (Q)
Logic.LE42.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE43.Gate Out	Signal: Output of the logic gate
Logic.LE43.Timer Out	Signal: Timer Output
Logic.LE43.Out	Signal: Latched Output (Q)
Logic.LE43.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE44.Gate Out	Signal: Output of the logic gate
Logic.LE44.Timer Out	Signal: Timer Output
Logic.LE44.Out	Signal: Latched Output (Q)
Logic.LE44.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE45.Gate Out	Signal: Output of the logic gate
Logic.LE45.Timer Out	Signal: Timer Output
Logic.LE45.Out	Signal: Latched Output (Q)
Logic.LE45.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE46.Gate Out	Signal: Output of the logic gate
Logic.LE46.Timer Out	Signal: Timer Output
Logic.LE46.Out	Signal: Latched Output (Q)
Logic.LE46.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 502 EN MRM4 12/11

Name	Description
Logic.LE47.Gate Out	Signal: Output of the logic gate
Logic.LE47.Timer Out	Signal: Timer Output
Logic.LE47.Out	Signal: Latched Output (Q)
Logic.LE47.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE48.Gate Out	Signal: Output of the logic gate
Logic.LE48.Timer Out	Signal: Timer Output
Logic.LE48.Out	Signal: Latched Output (Q)
Logic.LE48.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE49.Gate Out	Signal: Output of the logic gate
Logic.LE49.Timer Out	Signal: Timer Output
Logic.LE49.Out	Signal: Latched Output (Q)
Logic.LE49.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE50.Gate Out	Signal: Output of the logic gate
Logic.LE50.Timer Out	Signal: Timer Output
Logic.LE50.Out	Signal: Latched Output (Q)
Logic.LE50.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE51.Gate Out	Signal: Output of the logic gate
Logic.LE51.Timer Out	Signal: Timer Output
Logic.LE51.Out	Signal: Latched Output (Q)
Logic.LE51.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE52.Gate Out	Signal: Output of the logic gate
Logic.LE52.Timer Out	Signal: Timer Output
Logic.LE52.Out	Signal: Latched Output (Q)
Logic.LE52.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE53.Gate Out	Signal: Output of the logic gate
Logic.LE53.Timer Out	Signal: Timer Output
Logic.LE53.Out	Signal: Latched Output (Q)
Logic.LE53.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE54.Gate Out	Signal: Output of the logic gate
Logic.LE54.Timer Out	Signal: Timer Output
Logic.LE54.Out	Signal: Latched Output (Q)
Logic.LE54.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE55.Gate Out	Signal: Output of the logic gate
Logic.LE55.Timer Out	Signal: Timer Output
Logic.LE55.Out	Signal: Latched Output (Q)
Logic.LE55.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE56.Gate Out	Signal: Output of the logic gate
Logic.LE56.Timer Out	Signal: Timer Output
Logic.LE56.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE56.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE57.Gate Out	Signal: Output of the logic gate
Logic.LE57.Timer Out	Signal: Timer Output
Logic.LE57.Out	Signal: Latched Output (Q)
Logic.LE57.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE58.Gate Out	Signal: Output of the logic gate
Logic.LE58.Timer Out	Signal: Timer Output
Logic.LE58.Out	Signal: Latched Output (Q)
Logic.LE58.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE59.Gate Out	Signal: Output of the logic gate
Logic.LE59.Timer Out	Signal: Timer Output
Logic.LE59.Out	Signal: Latched Output (Q)
Logic.LE59.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE60.Gate Out	Signal: Output of the logic gate
Logic.LE60.Timer Out	Signal: Timer Output
Logic.LE60.Out	Signal: Latched Output (Q)
Logic.LE60.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE61.Gate Out	Signal: Output of the logic gate
Logic.LE61.Timer Out	Signal: Timer Output
Logic.LE61.Out	Signal: Latched Output (Q)
Logic.LE61.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE62.Gate Out	Signal: Output of the logic gate
Logic.LE62.Timer Out	Signal: Timer Output
Logic.LE62.Out	Signal: Latched Output (Q)
Logic.LE62.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE63.Gate Out	Signal: Output of the logic gate
Logic.LE63.Timer Out	Signal: Timer Output
Logic.LE63.Out	Signal: Latched Output (Q)
Logic.LE63.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE64.Gate Out	Signal: Output of the logic gate
Logic.LE64.Timer Out	Signal: Timer Output
Logic.LE64.Out	Signal: Latched Output (Q)
Logic.LE64.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE65.Gate Out	Signal: Output of the logic gate
Logic.LE65.Timer Out	Signal: Timer Output
Logic.LE65.Out	Signal: Latched Output (Q)
Logic.LE65.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE66.Gate Out	Signal: Output of the logic gate
Logic.LE66.Timer Out	Signal: Timer Output

Page 504 EN MRM4 12/11

Name	Description	
Logic.LE66.Out	Signal: Latched Output (Q)	
Logic.LE66.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE67.Gate Out	Signal: Output of the logic gate	
Logic.LE67.Timer Out	Signal: Timer Output	
Logic.LE67.Out	Signal: Latched Output (Q)	
Logic.LE67.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE68.Gate Out	Signal: Output of the logic gate	
Logic.LE68.Timer Out	Signal: Timer Output	
Logic.LE68.Out	Signal: Latched Output (Q)	
Logic.LE68.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE69.Gate Out	Signal: Output of the logic gate	
Logic.LE69.Timer Out	Signal: Timer Output	
Logic.LE69.Out	Signal: Latched Output (Q)	
Logic.LE69.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE70.Gate Out	Signal: Output of the logic gate	
Logic.LE70.Timer Out	Signal: Timer Output	
Logic.LE70.Out	Signal: Latched Output (Q)	
Logic.LE70.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE71.Gate Out	Signal: Output of the logic gate	
Logic.LE71.Timer Out	Signal: Timer Output	
Logic.LE71.Out	Signal: Latched Output (Q)	
Logic.LE71.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE72.Gate Out	Signal: Output of the logic gate	
Logic.LE72.Timer Out	Signal: Timer Output	
Logic.LE72.Out	Signal: Latched Output (Q)	
Logic.LE72.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE73.Gate Out	Signal: Output of the logic gate	
Logic.LE73.Timer Out	Signal: Timer Output	
Logic.LE73.Out	Signal: Latched Output (Q)	
Logic.LE73.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE74.Gate Out	Signal: Output of the logic gate	
Logic.LE74.Timer Out	Signal: Timer Output	
Logic.LE74.Out	Signal: Latched Output (Q)	
Logic.LE74.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE75.Gate Out	Signal: Output of the logic gate	
Logic.LE75.Timer Out	Signal: Timer Output	
Logic.LE75.Out	Signal: Latched Output (Q)	
Logic.LE75.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE76.Gate Out	Signal: Output of the logic gate	

Name	Description	
Logic.LE76.Timer Out	Signal: Timer Output	
Logic.LE76.Out	Signal: Latched Output (Q)	
Logic.LE76.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE77.Gate Out	Signal: Output of the logic gate	
Logic.LE77.Timer Out	Signal: Timer Output	
Logic.LE77.Out	Signal: Latched Output (Q)	
Logic.LE77.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE78.Gate Out	Signal: Output of the logic gate	
Logic.LE78.Timer Out	Signal: Timer Output	
Logic.LE78.Out	Signal: Latched Output (Q)	
Logic.LE78.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE79.Gate Out	Signal: Output of the logic gate	
Logic.LE79.Timer Out	Signal: Timer Output	
Logic.LE79.Out	Signal: Latched Output (Q)	
Logic.LE79.Out inverted	Signal: Negated Latched Output (Q NOT)	
Logic.LE80.Gate Out	Signal: Output of the logic gate	
Logic.LE80.Timer Out	Signal: Timer Output	
Logic.LE80.Out	Signal: Latched Output (Q)	
Logic.LE80.Out inverted	Signal: Negated Latched Output (Q NOT)	
Sys.AFRMS active	Signal: Arcflash Reduction Maintenance active	
Sys.AFRMS SCADA	Signal: Arcflash Reduction Maintenance SCADA Mode	
Sys.AFRMS DI	Signal: Arcflash Reduction Maintenance Digital Input Mode	
Sys.AFRMS inactive	Signal: Arcflash Reduction Maintenance inactive	
Sys.AFRMS manually	Signal: Arcflash Reduction Maintenance Manual Mode	
Sys.AFRMS-I	Module input state: Arcflash Reduction Maintenance Switch	
Sys.min 1 param changed	Signal: At least one parameter has been changed	
Sys.PS 1	Signal: Parameter Set 1	
Sys.PS 2	Signal: Parameter Set 2	
Sys.PS 3	Signal: Parameter Set 3	
Sys.PS 4	Signal: Parameter Set 4	
Sys.PS1-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.	
Sys.PS2-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.	
Sys.PS3-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.	
Sys.PS4-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.	
Sys.PSS manual	Signal: Manual Switch over of a Parameter Set	
Sys.PSS via Scada	Signal: Parameter Set Switch via Scada	
Sys.PSS via Inp fct	Signal: Parameter Set Switch via input function	

Page 506 EN MRM4 12/11

Name	Description
Sys.Res AlarmCr	Signal:: Res AlarmCr
Sys.Res OperationsCr	Signal:: Res OperationsCr
Sys.Res TotalCr	Signal:: Res TotalCr
Sys.Res TripCr	Signal:: Res TripCr

Global Protection Parameter of the Programmable Logic

Parameter	Description	Setting range	Default	Menu path
LE1.Gate	Logic gate	AND,	AND	[Logic
		OR,		/LE 1]
		NAND,		
		NOR		
LE1.Input1	Assignment of the Input Signal	1n, LogicList	-,-	[Logic
				/LE 1]
LE1.Inverting1	Inverting the input signals.	inactive,	inactive	[Logic
	Only available if an input signal has been assigned.	active		/LE 1]
LE1.Input2	Assignment of the Input Signal	1n, LogicList		[Logic
				/LE 1]
LE1.Inverting2	Inverting the input signals.	inactive,	inactive	[Logic
	Only available if an input signal has been assigned.	active		/LE 1]
LE1.Input3	Assignment of the Input Signal	1n, LogicList	-,-	[Logic
				/LE 1]
LE1.Inverting3	Inverting the input signals.	inactive,	inactive	[Logic
	Only available if an input signal has been assigned.	active		/LE 1]
LE1.Input4	Assignment of the Input Signal	1n, LogicList		[Logic
				/LE 1]
LE1.Inverting4	Inverting the input signals.	inactive,	inactive	[Logic
	Only available if an input signal has been assigned.	active		/LE 1]
LE1.t-On Delay	Switch On Delay	0.00 – 36000.00 s	0.00 s	[Logic
				/LE 1]
LE1.t-Off Delay	Switch Off Delay	0.00 – 36000.00 s	0.00 s	[Logic
				/LE 1]
LE1.Reset	Reset Signal for the Latching	1n, LogicList		[Logic
Latched				/LE 1]

Parameter	Description	Setting range	Default	Menu path
LE1.Inverting	Inverting Reset Signal for the Latching	inactive,	inactive	[Logic
Reset		active		/LE 1]
LE1.Inverting	Inverting the Setting Signal for the Latching	inactive,	inactive	[Logic
Set		active		/LE 1]

Programmable Logic Inputs

Name	Description	Assignment via
LE1.Gate In1-I	State of the module input: Assignment of	[Logic
	the Input Signal	/LE 1]
LE1.Gate In2-I	State of the module input: Assignment of	[Logic
	the Input Signal	/LE 1]
LE1.Gate In3-I	State of the module input: Assignment of	[Logic
	the Input Signal	/LE 1]
LE1.Gate In4-I State of the module input: Assignment of		[Logic
the Input Signal	/LE 1]	
	State of the module input: Reset Signal for	[Logic
	the Latching	

Programmable Logic Outputs

Name	Description
LE1.Gate Out	Signal: Output of the logic gate
LE1.Timer Out	Signal: Timer Output
LE1.Out	Signal: Latched Output (Q)
LE1.Out inverted	Signal: Negated Latched Output (Q NOT)

Service and Commissioning Support

Within the service menu various functions support maintenance and commissioning of the device.

General

Within the menu [Service/General], the User can initiate a reboot of the device.

Page 508 EN MRM4 12/11

Forcing the Relay Output Contacts



The parameters, their defaults, and setting ranges have to be taken from Relay Output Contacts section.

Principle – General Use



The User MUST ENSURE that the relay output contacts operate normally after maintenance is completed. If the relay output contacts do not operate normally, the protective device WILL NOT provide protection.

For commissioning purposes or for maintenance, relay output contacts can be set by force.

Within this mode [Service/Test Mode/Force OR/BO Slot X(2/5)], relay output contacts can be set by force:

- Permanent: or
- Via timeout.

If they are set with a time out, they will keep their "Force Position" only as long as this timer runs. If the timer expires, the relay will operate normally. If they are set as Permanent, they will keep the "Force Position" continuously.

There are two options available:

- Forcing a single relay »Force ORx«; and
- Forcing an entire group of relay output contacts »Force all Outs«.

Forcing an entire group takes precedence over forcing a single relay output contact!

NOTICE

A relay output contact <u>will NOT follow a force command</u> as long as it is disarmed at the same time.

NOTICE

A relay output contact will follow a force command:

- If it is not disarmed: and
- If the Direct Command is applied to the relay(s).

Keep in mind, that the forcing of all relay output contacts (of the same assembly group) takes precedence over the force command of a single relay output contact.

Disarming the Relay Output Contacts

NOTICE

The parameters, their defaults, and setting ranges have to be taken from the Relay Output Contacts section.

Principle – General Use

Within this mode [Service/Test Mode/DISARMED], entire groups of relay output contacts can be disabled. By means of this test mode, contact outputs switching actions of the relay output contacts are prevented. If the relay

output contacts are disarmed, maintenance actions can be carried out without the risk of taking entire processes off-line.



The User MUST ENSURE that the relay output contacts are ARMED AGAIN after maintenance is complete. If they are not armed, the protective device WILL NOT provide protection.

NOTICE

Zone Interlocking Output and the Supervision Contact cannot be disarmed.

Within this mode [Service/Test Mode/DISARMED] entire groups of relay output contacts can be disarmed:

- Permanent; or
- Via time out

If they are set with a time out, they will keep their "Disarm Position" only as long as this timer runs. If the timer expires, the relay output contacts will operate normally. If they are set Permanent, they will keep the "Disarm State" continuously.

NOTICE

A relay output contact will NOT be disarmed as long as:

- It's latched (and not yet reset).
- As long as a running t-OFF-delay timer is not yet expired (hold time of a relay output contact).
- The Disarm Control is not set to active.
- The Direct Command is not applied.

NOTICE

A relay output contact will be disarmed if it's not latched and

- There is no running t-OFF-delay timer (hold time of a relay output contact) and
- The DISARM Control is set to active and
- The Direct Command Disarm is applied.

Page 510 EN MRM4 12/11

Forcing RTDs*

•= Availability depends on ordered device.



The parameters, their defaults, and setting ranges have to be taken from RTD/UTRD section.

Principle - General Use



The User MUST ENSURE that the RTDs operate normally after maintenance is completed. If the RTDs do not operate normally, the protective device WILL NOT provide protection.

For commissioning purposes or for maintenance, RTD temperatures can be set by force.

Within this mode [Service/Test Mode/URTD], RTD temperatures can be set by force:

- Permanent; or
- Via timeout.

If they are set with a timeout, they will keep their "Forced Temperature" only as long as this timer runs. If the timer expires, the RTD will operate normally. If they are set as "">Permanent", they will keep the "Forced Temperature" continuously. This menu will show the measured values of the RTDs until the User activates the force mode by calling up the ""Function". As soon as the force mode is activated, the shown values will be frozen as long as this mode is active. Now the User can force RTD values. As soon as the force mode is deactivated, measured values will be shown again.

Forcing Analog Outputs*

•= Availability depends on ordered device.



The parameters, their defaults, and setting ranges have to be taken from Analog Output section.

Principle - General Use



The User MUST ENSURE that the Analog Outputs operate normally after maintenance is completed. Do not use this mode if forced Analog Outputs cause issues in external processes.

For commissioning purposes or for maintenance, Analog Outputs can be set by force.

Within this mode [Service/Test Mode/Analog Output(x)], Analog Outputs can be set by force:

- Permanent: or
- Via timeout.

If they are set with a timeout, they will keep their "Forced Value" only as long as this timer runs. If the timer expires, the Analog Output will operate normally. If they are set as "">Permanent*", they will keep the "Forced Value" continuously. This menu will show the current value that is assigned onto the Analog Output until the User activates the force mode by calling up the ""Function*". As soon as the force mode is activated, the shown values will be frozen as long as this mode is active. Now the User can force Analog Output values. As soon as the force mode is deactivated, measured values will be shown again.

Forcing Analog Inputs*

•= Availability depends on ordered device.



The parameters, their defaults, and setting ranges have to be taken from Analog Inputs section.

Principle - General Use



The User MUST ENSURE that the Analog Inputs operate normally after maintenance is completed.

For commissioning purposes or for maintenance, Analog Inputs can be set by force.

Within this mode [Service/Test Mode (Prot inhibit)/WARNING! Cont?/Analog Inputs], Analog Inputs can be set by force:

- Permanent; or
- Via timeout.

If they are set with a timeout, they will keep their "Forced Value" only as long as this timer runs. If the timer expires, the Analog Input will operate normally. If they are set as "">»Permanent", they will keep the "Forced Value" continuously. This menu will show the current value that is fed to the Analog Input until the User activates the force mode by calling up the ""Function". As soon as the force mode is activated, the shown value will be frozen as long as this mode is active. Now the User can force the Analog Input value. As soon as the force mode is deactivated, measured value will be shown again.

Failure Simulator (Sequencer)*

Available Elements: Sgen

* = Availability depends on ordered device.

For commissioning support and in order to analyze failures, the protective device offers the option to simulate measuring quantities. The simulation menu can be found within the [Service/Test Mode/Sgen] menu. The simulation cycle consists of three states:

- Pre-failure;
- Failure: and
- Post-failure State (Phase).

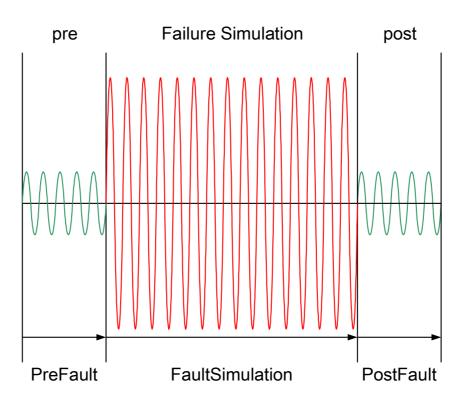
Within the [Service/Test Mode/Sgen/Configuration/Times] sub-menu, the duration of each phase can be set. In addition; the measuring quantities to be simulated can be determined (e.g.: voltages, currents, and the corresponding angles) for each phase (and ground).



Setting the device into the simulation mode means taking the protective device out of operation for the duration of the simulation. Do not use this feature during operation of the device if the User cannot guarantee that there is a running and properly working backup protection.

Page 512 EN MRM4 12/11





The energy counters will be stopped while the failure simulator is running.



The simulation voltages are always phase to neutral voltages, irrespectively of the mains voltage transformers' connection method (Phase-to-phase / Wey / Open Delta).

Application Options of the Fault Simulator**:

Stop Options	Cold Simulation (Option 1)	Hot Simulation (Option 2)
Do not stop	Simulation without tripping the breaker:	Simulation is authorized to trip the breaker:
Run complete: Pre Failure, Failure, Post Failure. How To?: Call up [Service/Test Mode/Sgen /Process] Ex Force Post = no assignment	Blocking protective Trips to the Breaker. That means verifying if the protective device generates a trip without energizing the trip coil of the breaker (similar to disarm the relay output contact).	How To?: Call up [Service/Test Mode/Sgen /Process] TripCmd Mode = With TripCmd
Press/Call up Start Simulation.	How To?: Call up [Service/Test Mode/Sgen /Process]	
	TripCmd Mode = No TripCmd	

Stop Options	Cold Simulation (Option 1)	Hot Simulation (Option 2)
Stop by external signal		
Force Post: As soon as this signal becomes true, the Fault Simulation will be forced to switch into the Post Failure mode.		
How To?: Call up [Service/Test Mode/Sgen /Process]		
Ex Force Post = Assigned Signal		
Manual stop		
As soon as this signal becomes true, the Fault Simulation will be terminated and the device changes back to normal operation.		
How To?: Call up [Service/Test Mode/Sgen /Process]		
Press/Call up Stop Simulation.		

^{**}Please note: Due to internal dependencies, the frequency of the simulation module is 0.16% greater than the rated one.

Device Planning Parameters of the Failure Simulator

Parameter	Description	Options	Default	Menu path
Mode	Mode	do not use,	use	[Device planning]
		use		

Global Protection Parameter of the Failure Simulator

Parameter	Description	Setting range	Default	Menu path
PreFault	Pre Fault Duration	0.00 – 300.00 s	0.0 s	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/Times]
FaultSimulati	Duration of Fault Simulation	0.00 – 10800.00 s	0.0 s	[Service
on				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/Times]

Page 514 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
PostFault	PostFault	0.00 – 300.00 s	0.0 s	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/Times]
TripCmd	Trip Command Mode	No TripCmd,	No TripCmd	[Service
Mode		With TripCmd		/Test Mode (Prot inhibit)
				/Sgen
				/Process]
ExBlo	External blocking of the module, if blocking is	1n, Assignment List	SG.Pos ON	[Service
	activated (allowed) within a parameter set and if the state of the assigned signal is true.			/Test Mode (Prot inhibit)
				/Sgen
				/Process]
Ex ForcePost	Force Post state. Abort simulation.	1n, Assignment List		[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Process]

Current Parameter of the Failure Simulator

Parameter	Description	Setting range	Default	Menu path
IL1	Current Fundamental Magnitude in Pre State:	0.00 – 40.00 In	0.0 In	[Service
	phase L1			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]
IL2	Current Fundamental Magnitude in Pre State:	0.00 – 40.00 In	0.0 In	[Service
	phase L2			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]

Parameter	Description	Setting range	Default	Menu path
IL3	Current Fundamental Magnitude in Pre State: phase L3	0.00 – 40.00 In	0.0 In	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]
IG meas	Current Fundamental Magnitude in Pre State: IG	0.00 – 25.00 ln	0.0 In	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]
phi IL1	Start Position respectively Start Angle of the Current Phasor during Pre-Phase:phase L1	-360 - 360°	0°	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]
phi IL2	Start Position respectively Start Angle of the	-360 - 360°	240°	[Service
	Current Phasor during Pre-Phase:phase L2			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]
phi IL3	Start Position respectively Start Angle of the	-360 - 360°	120°	[Service
	Current Phasor during Pre-Phase:phase L3			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]

Page 516 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
phi IG meas	Start Position respectively Start Angle of the Current Phasor during Pre-Phase: IG	-360 - 360°	0°	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PreFault
				/Current]
IL1	Current Fundamental Magnitude in Fault State:	0.00 – 40.00 In	0.0 In	[Service
	phase L1			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]
IL2	Current Fundamental Magnitude in Fault State: phase L2	0.00 – 40.00 In	0.0 In	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]
IL3	Current Fundamental Magnitude in Fault State:	0.00 – 40.00 In	0.0 In	[Service
	phase L3			/Test Mode (Prot
				inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
IC mass	Current Fundamental Magnitude in Fault State	0.00 25.00 lp	0.0 lp	/Current]
IG meas	Current Fundamental Magnitude in Fault State: IG	0.00 – 25.00 In	0.0 In	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]

Parameter	Description	Setting range	Default	Menu path
phi IL1	Start Position respectively Start Angle of the Current Phasor during Fault-Phase:phase L1	-360 - 360°	0°	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]
phi IL2	Start Position respectively Start Angle of the	-360 - 360°	240°	[Service
	Current Phasor during Fault-Phase:phase L2			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]
phi IL3	Start Position respectively Start Angle of the Current Phasor during Fault-Phase:phase L3	-360 - 360°	120°	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]
phi IG meas	Start Position respectively Start Angle of the	-360 - 360°	0°	[Service
	Current Phasor during Fault-Phase: IG			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/FaultSimulation
				/Current]
IL1	Current Fundamental Magnitude during Post	0.00 – 40.00 In	0.0 In	[Service
	phase: phase L1			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]

Page 518 EN MRM4 12/11

Parameter	Description	Setting range	Default	Menu path
IL2	Current Fundamental Magnitude during Post phase: phase L2	0.00 – 40.00 In	0.0 In	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]
IL3	Current Fundamental Magnitude during Post	0.00 – 40.00 In	0.0 In	[Service
	phase: phase L3			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]
IG meas	Current Fundamental Magnitude during Post phase: IG	0.00 – 25.00 ln	0.0 In	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]
phi IL1	Start Position respectively Start Angle of the	-360 - 360°	0°	[Service
	Current Phasor during Post phase: phase L1			/Test Mode (Prot
				inhibit)
				/Sgen
				/Configuration
				/PostFault
1:110	0. 10. 11.	000 0000	0.400	/Current]
phi IL2	Start Position respectively Start Angle of the Current Phasor during Post phase: phase L2	-360 - 360°	240°	[Service
				/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]

Parameter	Description	Setting range	Default	Menu path
phi IL3	Start Position respectively Start Angle of the	-360 - 360°	120°	[Service
	Current Phasor during Post phase: phase L3			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]
phi IG meas	Start Position respectively Start Angle of the	-360 - 360°	0°	[Service
	Current Phasor during Post phase: IG			/Test Mode (Prot inhibit)
				/Sgen
				/Configuration
				/PostFault
				/Current]

States of the Inputs of the Failure Simulator

Name	Description	Assignment via
ExBlo	Module input state: External blocking	[Service
		/Test Mode (Prot inhibit)
		/Sgen
		/Process]
Ex ForcePost-I	State of the module input:Force Post state.	[Service
	Abort simulation.	/Test Mode (Prot inhibit)
		/Sgen
		/Process]

Signals of the Failure Simulator (States of the Outputs)

Name	Description
Running	Signal; Measuring value simulation is running
	Signal: Wave generation states: 0=AdcNormal, 1=PreFault, 2=Fault, 3=Post, 4=InitReset

Page 520 EN MRM4 12/11

Direct Commands of the Failure Simulator

Parameter	Description	Setting range	Default	Menu path
Start	Start Fault Simulation (Using the test parameters)	inactive,	inactive	[Service
Simulation		active		/Test Mode (Prot inhibit)
				/Sgen
				/Process]
Stop	Stop Fault Simulation (Using the test parameters)	inactive,	inactive	[Service
Simulation		active		/Test Mode (Prot inhibit)
				/Sgen
				/Process]

Failure Simulator Values

Value	Description	Default	Size	Menu path
	,	L1 L2 L3 Normal	L1 L2 L3 Normal,	[Service
	1=PreFault, 2=Fault, 3=Post, 4=InitReset	, 2=Fault, 3=Post, 4=InitReset t-	t-PreFault,	/Test Mode (Prot inhibit)
			t-FaultSimulation,	/Sgen
			t-PostFault,	/State]
			Init Res	

Technical Data



Use Copper conductors only, 75°C. Conductor size AWG 14 [2.5 mm²].

Climatic Environmental Conditions

Storage Temperature:	Operating Temperature:
-30°C up to +70°C (-22°F to 158°F)	-20°C up to +60°C (-4°F to 140°F)

Permissible Humidity at Ann. Average: <75% rel. (on 56d up to 95% rel.)
Permissible Installation Altitude: <2000 m (6561.67 ft) above sea level

If 4000 m (13123.35 ft) altitude apply a changed classification of

the operating and test voltages may be necessary.

Degree of Protection EN 60529

HMI front panel with seal IP54
HMI front panel without seal IP50
Rear side terminals IP20

Routine Test

Insulation test acc. to IEC60255-5: All tests to be carried out against earth and other input- and

output circuits 2.5 kV (eff) / 50 Hz

Aux. voltage supply, digital inputs, current measuring inputs, signal relay

output contacts:

Voltage measuring inputs: 3.0 kV (eff) / 50 Hz

All wire-bound communication interfaces: 1.5 kV DC

Housing

Housing B2: height/-width 173 mm (6.811" / 4 U)/ 212.7 mm (8.374" / 42 HP)

Housing depth (incl. terminals): 208 mm (8.189")

Material, housing: Aluminum extruded section

Material, front panel: Aluminum/Foil front

Mounting position: Horizontal (±45° around the X-axis must be permitted)

Weight: approx. 2.4 kg (5.291 lb)

Page 522 EN MRM4 12/11

Current and Earth Current Measurement

Plug-in Connectors with Integrated Short-Circuiter

(Conventional Current Inputs)

Nominal currents: 1 A / 5 A

Max. measuring range: up to 40 x In (phase currents)

up to 25 x In (earth current standard) up to 2.5 x In (earth current sensitive)

Continuous loading capacity: Phase current/Earth current Earth current sensitive

4 x In/continuously 2 x In/continuously

Overcurrent proof: Phase current/Earth current Earth current sensitive

30 x ln/10 s 10 x ln/10 s 100 x ln/1 s 25 x ln/1 s

250 x ln/10 ms (1 half-wave) 100 x ln/10 ms (1 half-wave)

Power consumption: Phase current inputs:

at In = 1 A S = 0.15 mVAat In = 5 A S = 0.15 mVA

Earth current input: Sensitive earth current input:

Frequency range: 50 Hz / 60 Hz ±10%

Terminals: Screw-type terminals with integrated short-circuiters (contacts)

Screws: M4, captive type acc. to VDEW

Connection Cross Sections: 1 x or 2 x 2.5 mm² (2 x AWG 14) with wire end ferrule

1 x or 2 x 4.0 mm² (2 x AWG 12) with ring cable sleeve or cable sleeve 1 x or 2 x 6 mm² (2 x AWG 10) with ring cable sleeve or cable sleeve

The current measuring board's terminal blocks may be used as with 2 (double) conductors AWG 10,12,14 otherwise with single conductors

only.

Voltage Supply

Aux. Voltage: 24V - 270 V DC/48 - 230 V AC (-20/+10%)

Buffer time in case of supply failure: >= 50 ms at minimal aux. voltage

communication is permitted to be interrupted

Max. permissible making current: 18 A peak value for <0.25 ms

12 A peak value for <1 ms

The voltage supply must be protected by a fuse of:

2,5 A time-lag miniature fuse 5x20 mm (approx. 1/5" x 0.8") according to IEC 60127

• 3,5 A time-lag miniature fuse 6,3x32 mm (approx. 1/4" x 1 1/4") according to UL 248-14

Power Consumption

Power supply range: Power consumption Max. power consumption

in idle mode

24-270 V DC: 6 W 8.5 W 48-230 V AC 6 VA 8.5 VA

(for frequencies of 50-60 Hz):

Display

Display type: LCD with LED background illumination

Resolution graphics display: 128 x 64 pixel

LED-Type: Two colored: red/green

Number of LEDs, Housing B2: 8

Front Interface RS232

Baud rates: 115200 Baud
Handshake: RTS and CTS
Connection: 9-pole D-Sub plug

Real Time Clock

Running reserve of the real time clock: 1 year min.

Digital Inputs

Max. input voltage: 300 V DC/259 V AC

Input current: <4 mA
Reaction time: <20 ms
Drop-out time: <30 ms

(Safe state of the digital inputs)

4 Switching thresholds: Un = 24 V DC, 48 V DC, 60 V DC,

110 V AC/DC, 230 V AC/DC

Un = 24 V DC:

Switching threshold 1 ON: min. 19.2 V DC Switching threshold 1 OFF: max. 9.6 V DC

Un = 48 V/60V DC:

Switching threshold 2 ON: Min. 42.6 V DC Switching threshold 2 OFF: max. 21.3 V DC

Un = 110 V AC/DC:

Switching threshold 3 ON: min. 88.0 V DC/88.0 V AC Switching threshold 3 OFF: max. 44.0 V DC/44.0 V AC

Un = 230 V AC/DC:

Switching threshold 4 ON: min. 184 V DC/184 V AC Switching threshold 4 OFF max. 92 V DC/92 V AC Terminals: Screw-type terminals

Page 524 EN MRM4 12/11

Binary Relay Output Contacts

Continuous current: 5 A AC/DC

Max. Switch-on current: 25 A AC/DC for 4 s

30 A / 230 Vac according to ANSI IEEE Std C37.90-2005 30 A / 250 Vdc according to ANSI IEEE Std C37.90-2005

Max. breaking current: 5 A AC up to 240 V AC

5 A DC up to 30 V (resistive) 0.3 A DC at 250 V (resistive)

Max. switching voltage: 250 V AC/250 V DC

Switching capacity: 1250 VA

Contact type: 1 changeover contact Terminals: Screw-type terminals

Time Synchronization IRIG

Nominal input voltage: 5 V

Connection: Screw-type terminals (twisted pair)

Analog Output

Shielded cable for the analog output is recommended. The terminal of the HF shield should be used, when connecting the shield to earth on both sides of the cable is not possible. On one side of the cable the shield has to be directly connected to earth. In case of the use of unshielded twisted pair cables, the length must not exceed 10 m.

 $\begin{array}{ll} \text{Range:} & \text{0-20 mA} \\ \text{Max. load resistance:} & \text{1 k}\Omega \end{array}$

Accuracy 0.5% of the nominal value 20 mA

Influence of temperature to accuracy <1%

Test voltage of output against other

electrical groups

2.5 kV

Test voltage of output against earth 1.0 kV

RS485*

Master/Slave: Slave

Connection: 9-pole D-Sub socket

(external terminating resistors/in D-Sub)

or 6 screw-clamping terminals RM 3.5 mm (138 MIL)

(terminating resistors internal)

CAUTION

In case that the RS485 interface is realized via terminals, the communication cable has to be shielded. The shielding has to be fixed at the screw that is marked with the ground symbol (rear side of the device).

Fiber Optic*

Master/Slave: Slave Connection: ST-Plug

URTD-Interface*

Connection: Versatile Link

*availability depends on device

Boot phase

After switching on the power supply the protection will be available in approximately 9 seconds. After approximately 1 min 5 seconds the boot phase is completed (HMI and Communication initialized).

Page 526 EN MRM4 12/11

Standards

Approvals

GOST-R

UL- File No.: E217753CSA File No.: 251990**

CEI 0-16* (Tested by EuroTest Laboratori S.r.I, Italy)*

Design Standards

Generic standard EN 61000-6-2

EN 61000-6-3

Product standard IEC 60255-6

EN 50178

UL 508 (Industrial Control Equipment)

CSA C22.2 No. 14-95 (Industrial Control Equipment)

ANSI C37.90

High Voltage Tests (IEC 60255-6)

High frequency interference test

IEC 60255-22-1

class 3

Within one circuit

1 kV/2 s

Circuit to earth

2.5 kV/2 s

Circuit to circuit

2.5 kV/2 s

Insulation voltage test

IEC 60255-5 EN 50178 All circuits to other circuits and exposed conductive parts

2.5 kV (eff.)/50Hz, 1 min.

Except interfaces

1,5 kV DC, 1 min.

and Voltage measuring input

3 kV (eff.)/50 Hz, 1 min.

Impulse voltage test

IEC 60255-5

5 kV/0.5J, 1.2/50 μs

EMC Immunity Tests

Fast transient disturbance immunity test (Burst)

IEC 60255-22-4

Power supply, mains inputs

±4 kV, 2.5 kHz

IEC 61000-4-4

class 4

Other in- and outputs

±2 kV, 5 kHz

^{* =} applies to MRU4

^{** =} applies to (MRA4, MRU4, MRI4, MRDT4, MRM4)

Surge immunity test

IEC 61000-4-5 Within one circuit 2 kV

class 4

Circuit to earth 4 kV

Class 3 Communication cables to earth 2 kV

Electrical discharge immunity test

IEC 60255-22-2 Air discharge 8 kV

IEC 61000-4-2

class 3 Contact discharge 6 kV

Radiated radio-frequency electromagnetic field immunity test

IEC 61000-4-3 26 MHz – 80 MHz 10 V/m
ANSI C37.90.2 80 MHz – 1 GHz 35 V/m
1 GHz – 3 GHz 10 V/m

Immunity to conducted disturbances induced by radio frequency fields

IEC 61000-4-6

class 3

Power frequency magnetic field immunity test

IEC 61000-4-8 continues 30 A/m class 4 3 sec 300 A/m

EMC Emission Tests

Radio interference suppression test

IEC/CISPR11 Limit value class B

Radio interference radiation test

IEC/CISPR11 Limit value class B

Environmental Tests

Classification:

IEC 60068-1 Climatic 20/060/56

classification

IEC 60721-3-1 Classification of ambient conditions 1K5/1B1/1C1L/1S1/1M2

(Storage) but min. -30°C

IEC 60721-3-2 Classification of ambient conditions 2K4/2B1/2C1/2S1/2M2

(Transportation) but min. -30°C

IEC 60721-3-3 Classification of ambient conditions 3K6/3B1/3C1/3S1/3M2 (Stationary use at weather protected but min. -20°C/max +60°C

locations)

Test Ad: Cold

IEC 60068-2-1 Temperature -20°C

test duration 16 h

Test Ad: Cold

CEI 0-16* (IEC 60068-2-1) Temperature -25°C

test duration 16 h

Page 528 EN MRM4 12/11

Test Bd: Dry Heat IEC 60068-2-2	Temperature Relative humidity test duration	60°C <50% 72 h
Test Bd: Dry Heat CEI 0-16* (IEC 60068-2-2)	Temperature Relative humidity test duration	70°C <50% 72 h
Test Db: Damp Heat (cyclic) IEC 60068-2-30	Temperature Relative humidity Cycles (12 + 12-hour)	60°C 95% 2

^{*} applies to MRU4 only

Mechanical Tests

Test Fc: Vibration response test

IEC 60068-2-6 (10 Hz – 59 Hz) 0.035 mm

IEC 60255-21-1 Displacement

class 1 (59Hz – 150Hz) 0.5 gn Acceleration

Number of cycles in each axis 1

Test Fc: Vibration endurance test

IEC 60068-2-6 (10 Hz – 150 Hz) 1.0 gn

IEC 60255-21-1 Acceleration

class 1 Number of cycles in each axis 20

Test Ea: Shock tests

Shock resistance test 15 gn, 11 ms, 3 impulses in each

direction

Test Eb: Shock endurance

test

IEC 60068-2-29 Shock endurance test 10 gn, 16 ms, 1000 impulses in each

IEC 60255-21-2 direction

class 1

Test Fe: Earthquake test

IEC 60068-3-3 Single axis earthquake vibration test 3 – 7 Hz: Horizontal 10 mm,

KTA 3503 1 cycle each axis IEC 60255-21-3

class 2 7 – 35 Hz Horizontal: 2 gn,

1 cycle each axis

Page 530 EN MRM4 12/11

Tolerances

It has to be observed, that the set pickup and release values (hysteresis) including tolerances, are always within the permissible measuring range.

Real Time Clock Tolerances

Resolution: 1 ms

Tolerance: <1 minute / month (+20°C) <±1ms if synchronized via IRIG-B

Measured Value Acquisition Tolerances

Phase and Earth Current Measuring

Frequency range: $50 \text{ Hz} / 60 \text{ Hz} \pm 10\%$

Accuracy: Class 0.5

Amplitude error if $I < 1 \times IN$: $\pm 0.5\%$ of the rated value Amplitude error if $I > 1 \times IN < 2 \times IN$: $\pm 0.5\%$ of the measured value Amplitude error if $I > 2 \times IN$: $\pm 1.0\%$ of the measured value

Resolution: 0.01 A

Harmonics up to 20% 3rd harmonic ±2%

up to 20% 5th harmonic ±2%

Frequency influence $\frac{\pm 2\%}{Hz}$ in the range of ± 5 Hz of

the configured nominal frequency

Temperature influence <±1% within the range of -20°C up to +60°C

Protection Stages Tolerances

Note:

The tripping delay relates to the time between alarm and trip. The tolerance of the operating time relates to the time between the measured value has exceeded the threshold until the protection stage is alarmed.

Overcurrent protection stages: I[x]	Tolerance
>	±1.5% of the setting value resp. 1% In
Resetting ratio	97% or 0.5% x In
t	DEFT ±1% resp. ±10 ms
Operating time	<35 ms
Starting from I higher than 1.1 x I>	
Release time	<45 ms
t-char	±5% IEC NINV, IEC VINV, IEC LINV, IEC EINV, ANSI MINV, ANSI ANSI VINV, ANSI EINV, Therm Flat, IT, I2T, I4T
t-reset	±1% resp. ±10 ms
Only available if: Characteristic = INV	

Earth current stages: IG[x]	Tolerance
IG>	±1.5% of the setting value resp. 1% In
Resetting ratio	97% or 0.5% x In
t	DEFT ±1% resp. ±10 ms
Operating time Starting from IG higher than 1.1 x IG>	<35 ms
Release time	<45 ms
t-char	±5% IEC NINV, IEC VINV, IEC LINV, IEC EINV, ANSI MINV, ANSI ANSI VINV, ANSI EINV, Therm Flat, IT, I2T, I4T
t-reset Only available if: Characteristic = INV	±1% resp. ±10 ms

Negative phase sequence current: I2[x]	Tolerance
12	±2% of the setting value resp. ±1% In
Resetting ratio	97% or 0.5% x In
t Tripping delay time (DEFT)	DEFT ±1% resp. ±10 ms
Operating time Starting from I2 higher than 1.1 x I>	<65 ms
Release time	<45 ms

Thermal Replica: ThR	Tolerance
Ib	±2% of the setting value resp. 1% In
Alarm ThR	±1.5 % of the setting value

Unbalanced load :I2>[x]	Tolerance
12>	±2% of the setting value resp.1% In
Resetting ratio	97% or 0.5% x In
t	DEFT ±1% resp. ±10 ms
Operating time Starting from I2 higher than 1.3 x I2>	<65 ms
Release time	<45 ms
k	±5% INV

Page 532 EN MRM4 12/11

Unbalanced load :I2>[x]	Tolerance
T-COOl	±5% INV

JAM Protection: JAM [x]	Tolerance
Pickup	±1.5% of the setting value resp. 1% In
Resetting ratio	
	97% or 0.5% x In
t	DEFT
	±1% resp. ±10 ms
Operating time	<65 ms
Release time	<45 ms

Undercurrent Protection: I< [x]	Tolerance
I<	±1.5% of the setting value resp. 1% In
Resetting ratio	97% or 0.5% x In
t	DEFT ±1% resp. ±10 ms
Operating time	<65 ms
Release time	<45 ms

Mechanical Load Shedding MLS	Tolerance
Pickup Threshold	±1.5% of the setting value resp. 1% In
Resetting ratio	97% or 0.5% x In
t-Pickup Delay	DEFT ±1% resp. ±10 ms
Drop-out Threshold	±1.5% of the setting value resp. 1% In
Resetting ratio	97% or 0.5% x In
t-Drop Delay	DEFT ±1% resp. ±10 ms
Operating time	<65 ms
Release time	<45 ms

Breaker Failure Protection CBF	Tolerance
I-CBF>	±1.5% of the setting value resp.1% In
resetting ratio	0.5% x ln

Tolerance
±1% resp. ±10 ms
<40 ms
<40 ms
<

Trip Circuit Supervision TCS	Tolerance
t-TCS	±1% resp. ±10 ms

Tolerance
±2% of the setting value resp.1.5% In
94%
±1% resp. ±10 ms

Page 534 EN MRM4 12/11

Assignment List

The »ASSIGNMENT LIST« below summarizes all module outputs (signals) and inputs (e.g. states of the assignments).

Name	Description
	No assignment
Prot.available	Signal: Protection is available
Prot.active	Signal: active
Prot.ExBlo	Signal: External Blocking
Prot.Alarm L1	Signal: General-Alarm L1
Prot.Alarm L2	Signal: General-Alarm L2
Prot.Alarm L3	Signal: General-Alarm L3
Prot.Alarm G	Signal: General-Alarm - Earth fault
Prot.Alarm	Signal: General Alarm
Prot.Trip L1	Signal: General Trip L1
Prot.Trip L2	Signal: General Trip L2
Prot.Trip L3	Signal: General Trip L3
Prot.Trip G	Signal: General Trip Ground fault
Prot.Trip	Signal: General Trip
Prot.Res Fault a Mains No	Signal: Resetting of fault number and number of grid faults.
Prot.ExBlo1-I	Module input state: External blocking1
Prot.ExBlo2-I	Module input state: External blocking2
Ctrl.Local	Switching Authority: Local
Ctrl.Remote	Switching Authority: Remote
SG.SI SingleContactInd	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.
SG.Pos not ON	Signal: Pos not ON
SG.Pos ON	Signal: Breaker is in ON-Position
SG.Pos OFF	Signal: Breaker is in OFF-Position
SG.Pos Indeterm	Signal: Breaker is in Indeterminate Position
SG.Pos Disturb	Signal: Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
SG.Ready	Signal: Breaker is ready for operation.
SG.Interl ON	Signal: One or more IL_On inputs are active.
SG.Interl OFF	Signal: One or more IL_Off inputs are active.
SG.CES succesf	Command Execution Supervision: Switching command executed successfully.
SG.CES Disturbed	Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.
SG.CES Fail TripCmd	Command Execution Supervision: Command execution failed because trip command is pending.

SG.CES SwitchgDir	Command Execution Supervision respectively Switching Direction
	Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.
SG.CES ON d OFF	Command Execution Supervision: On Command during a pending OFF Command.
SG.CES SG not ready	Command Execution Supervision: Switchgear not ready
SG.CES Fiel Interl	Command Execution Supervision: Switching Command not executed because of field interlocking.
SG.CES SyncTimeout	Command Execution Supervision: Switching Command not executed No Synchronization signal while t-sync was running.
SG.TripCmd	Signal: Trip Command
SG.Ack TripCmd	Signal: Acknowledge Trip Command
SG.SGwear Slow SG	Signal: Alarm, the breaker (load-break switch) becomes slower
SG.Res SGwear SI SG	Signal: Resetting the slow Switchgear Alarm
SG.ON Cmd	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
SG.OFF Cmd	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
SG.ON Cmd manual	Signal: ON Cmd manual
SG.OFF Cmd manual	Signal: OFF Cmd manual
SG.Aux ON-I	Module Input State: Position indicator/check-back signal of the Bkr. (52a)
SG.Aux OFF-I	Module input state: Position indicator/check-back signal of the Bkr. (52b)
SG.Ready-I	Module input state: Bkr. ready
SG.Ack TripCmd-I	State of the module input: Acknowledgment Signal (only for automatic acknowledgment) Module input signal
SG.Interl ON1-l	State of the module input: Interlocking of the ON command
SG.Interl ON2-I	State of the module input: Interlocking of the ON command
SG.Interl ON3-I	State of the module input: Interlocking of the ON command
SG.Interl OFF1-I	State of the module input: Interlocking of the OFF command
SG.Interl OFF2-I	State of the module input: Interlocking of the OFF command
SG.Interl OFF3-I	State of the module input: Interlocking of the OFF command
SG.SCmd ON-I	State of the module input: Switching ON Command, e.g. the state of the Logic or the state of the digital input
SG.SCmd OFF-I	State of the module input: Switching OFF Command, e.g. the state of the Logic or the state of the digital input
SG.Operations Alarm	Signal: Service Alarm, too many Operations
SG.Isum Intr trip: IL1	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1

Page 536 EN MRM4 12/11

Name	Description
SG.Isum Intr trip: IL2	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2
SG.Isum Intr trip: IL3	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3
SG.Isum Intr trip	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.
SG.Res TripCmd Cr	Signal: Resetting of the Counter: total number of trip commands
SG.Res Sum trip	Signal: Reset summation of the tripping currents
SG.WearLevel Alarm	Signal: Threshold for the Alarm
SG.WearLevel Lockout	Signal: Threshold for the Lockout Level
SG.Res SGwear Curve	Signal: Res SGwear Curve
SG.Isum Intr ph Alm	Signal: Isum Intr ph Alm
SG.Res Isum Intr ph Alm	Signal: Res Isum Intr ph Alm
MStart.active	Signal: active
MStart.Blo TripCmd	Signal: Trip Command blocked
MStart.Trip	Signal: Trip
MStart.TripCmd	Signal: Trip Command
MStart.Start	Signal: Motor is in start mode
MStart.Run	Signal: Motor is in run mode
MStart.Stop	Signal: Motor is in stop mode
MStart.Blo	Signal: Motor is blocked for starting or transition to Run mode
MStart.NOCSBlocked	Signal: Motor is prohibited to start due to number of cold start limits
MStart.SPHBlocked	Signal: Motor is prohibited to start due to starts per hour limits
MStart.SPHBlockAlarm	Signal: Motor is prohibited to start due to starts per hour limits, would come active in the next stop
MStart.TBSBlocked	Signal: Motor is prohibited to start due to time between starts limits
MStart.ThermalBlo	Signal: Thermal block
MStart.RemBlockStart	Signal: Motor is prohibited to start due to external blocking through digital input DI
MStart.TransitionTrip	Signal: Start transition fail trip
MStart.ZSSTrip	Signal: Zero speed trip (possible locked rotor)
MStart.INSQSP2STFaill	Signal: Fail to transit from stop to start based on reported back time
MStart.INSQSt2RunFail	Signal: Fail to transit from start to run based on reported back time
MStart.LATBlock	Signal: Long acceleration timer enforced
MStart.ColdStartSeq	Signal: Motor cold start sequence flag
MStart.ForcedStart	Signal: Motor being forced to start
MStart.TripPhaseReverse	Signal: Relay tripped because of phase reverse detection
MStart.EmergOverrideDI	Signal: Emergency override start blocking through digital input DI
MStart.EmergOverrideUI	Signal: Emergency override start blocking through front panel

Name	Description
MStart.ABSActive	Signal: Anti-backspin is active. For certain applications, such as pumping a fluid up a pipe, the motor may be driven backward for a period of time after it stops. The anti-backspin timer prevents starting the motor while it is spinning in the reverse direction.
MStart.Blo-GOCStart	Signal: Ground Instantaneous Overcurrent Start Delay. GOC (Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
MStart.Blo-IOCStart	Signal: Phase Instantaneous Overcurrent Start Delay. IOC (Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
MStart.Blo-I <start< td=""><td>Signal: Under load Start Delay. Under load(Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter</td></start<>	Signal: Under load Start Delay. Under load(Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
MStart.Blo-JamStart	Signal: JAM Start Delay. JAM(Instantaneous Overcurrent) elements are blocked for the time programmed under this parameter
MStart.Blo-UnbalStart	Signal: Motor start block current unbalance signal
MStart.Blo-Generic1	Generic Start Delay. This value can be used to block any protective element.1
MStart.Blo-Generic2	Generic Start Delay. This value can be used to block any protective element.2
MStart.Blo-Generic3	Generic Start Delay. This value can be used to block any protective element.3
MStart.Blo-Generic4	Generic Start Delay. This value can be used to block any protective element.4
MStart.Blo-Generic5	Generic Start Delay. This value can be used to block any protective element.5
MStart.I_Transit	Signal: Current transition signal
MStart.T_Transit	Signal: Time transition signal
MStart.StartMotorCmd	Signal: Start motor command
MStart.MotorStopBlo	Signal: Motor stop block other protection functions
MStart.Rotating forward	Signal: Rotation Direction forward
MStart.Rotating backward	Signal: Rotation Direction reverse
MStart.Start Signal-I	State of the module input: Motor Start Signal. User can tie a digital input to this Input. If "Start-I" becomes true, "StartMotorCommand" becomes true for at least 500ms.
MStart.Stop-I	State of the module input: Stop Motor Signal
MStart.StartBlock-I	State of the module input: Start Motor Signal
MStart.EmgOvr-I	State of the module input: Emergency Override. Signal has to be active in order to release the thermal capacity of the motor. Please notice that by doing this you run the risk of damaging the motor. "EMGOVR" has to be set to "DI" or "DI or UI" for this input to take effect
MStart.INSQ-I	State of the module input: INcomplete SeQuence
MStart.ThermSwitch-I	State of the module input: Therm Switch
MStart.ZSS-I	State of the module input: Zero Speed Switch

Page 538 EN MRM4 12/11

Name	Description
MStart.STPC Blo-I	State of the module input: With this setting a Digital Input keeps the Motor in the RUN mode, even when the motor current drops below STPC (motor stop current).
I[1].active	Signal: active
I[1].ExBlo	Signal: External Blocking
I[1].Ex rev Interl	Signal: External reverse Interlocking
I[1].Blo TripCmd	Signal: Trip Command blocked
I[1].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I[1].Alarm L1	Signal: Alarm L1
I[1].Alarm L2	Signal: Alarm L2
I[1].Alarm L3	Signal: Alarm L3
I[1].Alarm	Signal: Alarm
I[1].Trip L1	Signal: General Trip Phase L1
I[1].Trip L2	Signal: General Trip Phase L2
I[1].Trip L3	Signal: General Trip Phase L3
[[1].Trip	Signal: Trip
I[1].TripCmd	Signal: Trip Command
I[1].DefaultSet	Signal: Default Parameter Set
I[1].AdaptSet 1	Signal: Adaptive Parameter 1
I[1].AdaptSet 2	Signal: Adaptive Parameter 2
I[1].AdaptSet 3	Signal: Adaptive Parameter 3
I[1].AdaptSet 4	Signal: Adaptive Parameter 4
I[1].ExBlo1-I	Module input state: External blocking1
I[1].ExBlo2-I	Module input state: External blocking2
I[1].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I[1].Ex rev Interl-I	Module input state: External reverse interlocking
I[1].AdaptSet1-I	Module input state: Adaptive Parameter1
I[1].AdaptSet2-I	Module input state: Adaptive Parameter2
I[1].AdaptSet3-I	Module input state: Adaptive Parameter3
I[1].AdaptSet4-I	Module input state: Adaptive Parameter4
I[2].active	Signal: active
I[2].ExBlo	Signal: External Blocking
I[2].Ex rev Interl	Signal: External reverse Interlocking
I[2].Blo TripCmd	Signal: Trip Command blocked
I[2].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I[2].Alarm L1	Signal: Alarm L1
I[2].Alarm L2	Signal: Alarm L2
I[2].Alarm L3	Signal: Alarm L3
I[2].Alarm	Signal: Alarm
I[2].Trip L1	Signal: General Trip Phase L1

Name	Description
I[2].Trip L2	Signal: General Trip Phase L2
I[2].Trip L3	Signal: General Trip Phase L3
I[2].Trip	Signal: Trip
I[2].TripCmd	Signal: Trip Command
I[2].DefaultSet	Signal: Default Parameter Set
I[2].AdaptSet 1	Signal: Adaptive Parameter 1
I[2].AdaptSet 2	Signal: Adaptive Parameter 2
I[2].AdaptSet 3	Signal: Adaptive Parameter 3
I[2].AdaptSet 4	Signal: Adaptive Parameter 4
I[2].ExBlo1-I	Module input state: External blocking1
I[2].ExBlo2-I	Module input state: External blocking2
I[2].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I[2].Ex rev Interl-I	Module input state: External reverse interlocking
I[2].AdaptSet1-I	Module input state: Adaptive Parameter1
I[2].AdaptSet2-I	Module input state: Adaptive Parameter2
I[2].AdaptSet3-I	Module input state: Adaptive Parameter3
I[2].AdaptSet4-I	Module input state: Adaptive Parameter4
I[3].active	Signal: active
I[3].ExBlo	Signal: External Blocking
I[3].Ex rev Interl	Signal: External reverse Interlocking
I[3].Blo TripCmd	Signal: Trip Command blocked
I[3].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I[3].Alarm L1	Signal: Alarm L1
I[3].Alarm L2	Signal: Alarm L2
I[3].Alarm L3	Signal: Alarm L3
I[3].Alarm	Signal: Alarm
I[3].Trip L1	Signal: General Trip Phase L1
I[3].Trip L2	Signal: General Trip Phase L2
I[3].Trip L3	Signal: General Trip Phase L3
I[3].Trip	Signal: Trip
I[3].TripCmd	Signal: Trip Command
I[3].DefaultSet	Signal: Default Parameter Set
I[3].AdaptSet 1	Signal: Adaptive Parameter 1
I[3].AdaptSet 2	Signal: Adaptive Parameter 2
I[3].AdaptSet 3	Signal: Adaptive Parameter 3
I[3].AdaptSet 4	Signal: Adaptive Parameter 4
I[3].ExBlo1-I	Module input state: External blocking1
I[3].ExBlo2-I	Module input state: External blocking2
I[3].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command

Page 540 EN MRM4 12/11

[3] Ex rev Interf-I Module input state: External reverse interlocking [3] AdaptSet1-I Module input state: Adaptive Parameter1 [3] AdaptSet2-I Module input state: Adaptive Parameter2 [3] AdaptSet3-I Module input state: Adaptive Parameter3 [3] AdaptSet4-I Module input state: Adaptive Parameter4 [4] active Signal: External Blocking Signal: External Blocking [4] Ex rev InterI Signal: External Blocking [4] Ex rev InterI Signal: External Blocking [4] Ex Rib TripCmd Signal: External Blocking of the Trip Command [4] Ex Rib TripCmd Signal: External Blocking of the Trip Command [4] Ex Rib TripCmd Signal: Alarm L1 Signal: Alarm L2 Signal: Alarm L3 Signal: Alarm L4 Signal: Alarm L5 Signal: Alarm L6 Signal: Alarm L8 Signal: Alarm L9	Name	Description
[3] AdaptSet2-1 Module input state: Adaptive Parameter2 [13] AdaptSet3-1 Module input state: Adaptive Parameter3 [13] AdaptSet4-1 Module input state: Adaptive Parameter4 [14] active Signal: active Signal: External Blocking [14] Ex rev Interf Signal: External Blocking of the Trip Command [14] Alarm L1 Signal: Alarm L2 Signal: Alarm L2 Signal: Alarm L2 Signal: Alarm L2 Signal: Alarm L3 Signal: Beneral Trip Phase L1 [14] Trip L1 Signal: General Trip Phase L2 [14] Trip L2 Signal: General Trip Phase L2 [14] Trip L3 Signal: General Trip Phase L2 [14] Trip Command [14] DefaultSet Signal: Default Parameter Set [14] AdaptSet 1 Signal: Adaptive Parameter Set [14] AdaptSet 1 Signal: Adaptive Parameter 1 [14] AdaptSet 2 Signal: Adaptive Parameter 2 [14] AdaptSet 3 Signal: Adaptive Parameter 4 [14] ExBlo-1 Module input state: External blocking 1 [14] ExBlo-1 Module input state: External blocking 2 [14] ExBlo-1 Module input state: External blocking 2 [14] ExBlo-1 Module input state: External blocking 4 [14] AdaptSet-1 Module input state: External blocking 6 Trip Command [14] Ex rev Interf-1 Module input state: Adaptive Parameter 2 [14] AdaptSet-1 Module input state: External Blocking 6 Trip Command [14] Ex rev Interf-1 Module input state: Adaptive Parameter 2 [14] AdaptSet-1 Module input state: Adaptive Parameter 3 [14] AdaptSet-1 Module input state: Adaptive Parameter	I[3].Ex rev Interl-I	Module input state: External reverse interlocking
I[3] AdaptSet3-1 Module input state: Adaptive Parameter3 I[3] AdaptSet4-1 Module input state: Adaptive Parameter4 I[4] active Signal: active Signal: active Signal: External Blocking I[4] Ex Rib Signal: External reverse Interlocking I[4] Ex Rib TripCmd Signal: External Ribocking I[4] Bio TripCmd Signal: External Ribocking I[4] Ex Rib TripCmd Signal: External Blocking of the Trip Command I[4] Alarm L1 Signal: Alarm L1 Signal: Alarm L2 I[4] Alarm L3 Signal: Alarm L4 Signal: Alarm L5 Signal: Alarm L6 Signal: Alarm L7 Signal: General Trip Phase L1 I[4] Trip L1 Signal: General Trip Phase L2 Signal: General Trip Phase L2 I[4] Trip Signal: General Trip Phase L3 Signal: General Trip Phase L3 Signal: General Trip Phase L3 Signal: Trip Signal: Trip Signal: Trip Signal: Trip Signal: Trip Signal: Trip Signal: Adaptive Parameter Set I[4] AdaptSet 1 Signal: Adaptive Parameter 2 Signal: Adaptive Parameter 2 Signal: Adaptive Parameter 3 Signal: Adaptive Parameter 3 Signal: Adaptive Parameter 4 Module input state: External blocking 1 I[4] ExBlo-1 Module input state: External blocking 2 I[4] ExBlo-1 Module input state: External blocking 2 I[4] AdaptSet-1 Module input state: External Blocking 0 The Trip Command I[4] ExBlo-1 Module input state: Adaptive Parameter 1 I[4] AdaptSet-1 Module input state: Adaptive Parameter 3 I[4] AdaptSet-1 Module input state: Adaptive Parameter 3 I[4] AdaptSet-1 Module input state: Adaptive Parameter 3 I[I[3].AdaptSet1-I	Module input state: Adaptive Parameter1
	I[3].AdaptSet2-I	Module input state: Adaptive Parameter2
[4] active	I[3].AdaptSet3-I	Module input state: Adaptive Parameter3
[4] ExBlo Signal: External Blocking [4] Ex rev Inter! Signal: External Blocking [4] Ex rev Inter! Signal: External reverse Interlocking [4] Ex Rev Inter! Signal: External Blocking of the Trip Command [4] Alarm L1 Signal: Alarm L2 [4] Alarm L2 Signal: Alarm L2 [4] Alarm L3 Signal: Alarm L3 [4] Alarm Signal: Alarm L3 [4] Alarm Signal: Alarm L3 [4] Alarm Signal: Alarm L3 [4] Trip L1 Signal: General Trip Phase L1 [4] Trip L2 Signal: General Trip Phase L2 [4] Trip L3 Signal: General Trip Phase L3 [4] Trip Signal: Trip [4] Trip Signal: Trip Command [4] DefaultSet Signal: Trip Command [4] DefaultSet Signal: Adaptive Parameter Set [4] AdaptSet 1 Signal: Adaptive Parameter 1 [4] AdaptSet 2 Signal: Adaptive Parameter 2 [4] AdaptSet 3 Signal: Adaptive Parameter 3 [4] AdaptSet 4 Signal: Adaptive Parameter 4 [4] ExBlo-1 Module input state: External blocking 1 [4] ExBlo-2 Module input state: External Blocking of the Trip Command [4] ExBlo-3 Module input state: External Blocking of the Trip Command [4] Ex rev Inter-1 Module input state: External Blocking of the Trip Command [4] Ex rev Inter-1 Module input state: Adaptive Parameter 1 [4] AdaptSet-3 Module input state: External Blocking of the Trip Command [4] Ex rev Inter-1 Module input state: Adaptive Parameter 1 [4] AdaptSet-3 Module input state: External Blocking of the Trip Command [5] Ex rev Inter-1 Signal: External Blocking [5] Ex rev Inter-1 Signal: External Blocking [5] Ex rev Inter-1 Signal: External Blocking of the Trip Command [5] Ex Ex look Signal: External Blocking of the Trip Command [5] Ex Rev Inter-1 Signal: Alarm L1 [5] Ex Rev Inter-1 Signal: Alarm L1 [5] Ex Rev Inter-1 Signal: Alarm L1 [5] Ex Rev Inter-1 Signal: External Blocking of the Trip Command [5] Ex Rev Inter-1 Signal: Alarm L1 [5] Ex Rev Inter-1 Signal: Alarm L1 [5] Ex	I[3].AdaptSet4-I	Module input state: Adaptive Parameter4
[4], Ex rev Interf Signal: External reverse Interfocking [4], Blo TripCmd Signal: Trip Command blocked [4], ExBlo TripCmd Signal: Alarm L1 [4], Alarm L1 Signal: Alarm L2 [4], Alarm L3 Signal: Alarm L3 [4], Alarm Signal: Alarm Signal: Alarm [4], Trip L1 Signal: General Trip Phase L1 [4], Trip L1 Signal: General Trip Phase L2 [4], Trip L3 Signal: Trip Phase L3 [4], Trip L4 Signal: Trip Phase L3 [4], Trip L5 Signal: Trip Phase L4 [4], Trip L6 Signal: Trip Phase L5 [4], Trip L7 Signal: Trip Phase L8 [4], Trip L8 Signal: Trip Phase L9 [4], Trip L9 Signal: Trip Phase L9 [4], AdaptSet 1 Signal: Adaptive Parameter Set [4], AdaptSet 2 Signal: Adaptive Parameter 1 [4], AdaptSet 3 Signal: Adaptive Parameter 2 [4], AdaptSet 4 Signal: Adaptive Parameter 3 [4], AdaptSet 4 Signal: Adaptive Parameter 4 [4], ExBlo1-1 Module input state: External blocking 1 [4], ExBlo2-1 Module input state: External blocking 2 [4], ExBlo3-1 Module input state: External blocking 0 [4], Ex rev Interf Module input state: External reverse interiocking [4], AdaptSet1-1 Module input state: Adaptive Parameter 1 [4], AdaptSet2-1 Module input state: Adaptive Parameter 2 [4], AdaptSet3-1 Module input state: Adaptive Parameter 3 [4], AdaptSet4-1 Module input state: Adaptive Parameter 1 [4], AdaptSet4-1 Module input state: Adaptive Parameter 2 [4], AdaptSet4-1 Module input state: Adaptive Parameter 3 [5], Ex rev Interf Signal: Adaptive Parameter 1 [6], Ex rev Interf Signal: External Blocking 0 Signal: External Blocking of the Trip Command [6], Ex rev Interf Signal: Alarm L1 [6], Ex rev Interf Signal: Alarm L1 [I[4].active	Signal: active
[4] Blo TripCmd Signal: Trip Command blocked [4] ExBlo TripCmd Signal: External Blocking of the Trip Command [4] Alarm L1 Signal: Alarm L2 [4] Alarm L3 Signal: Alarm L3 [4] Alarm L3 Signal: Alarm L3 [4] Alarm L3 Signal: Alarm L3 [4] Trip L1 Signal: General Trip Phase L1 [4] Trip L2 Signal: General Trip Phase L2 [4] Trip L3 Signal: General Trip Phase L3 [4] Trip L3 Signal: Trip [4] Trip Cmd Signal: Trip [4] Trip Signal: Trip [4] Trip Signal: Trip Command [4] DefaultSet Signal: Default Parameter Set [4] AdaptSet 1 Signal: Adaptive Parameter 1 [4] AdaptSet 2 Signal: Adaptive Parameter 2 [4] AdaptSet 3 Signal: Adaptive Parameter 3 [4] ExBlo-1 Module input state: External blocking 1 [4] ExBlo-1 Module input state: External Blocking of the Trip Command [4] Ex Iver Interl-1 Module input state: External Provement 7 [4] AdaptSet-1 Module input state: External Blocking of the Trip Command [4] Ex Iver Interl-1 Module input state: Adaptive Parameter 1 [4] AdaptSet-1 Module input state: External Blocking of the Trip Command [4] Ex Iver Interl-1 Module input state: Adaptive Parameter 1 [4] AdaptSet-1 Module input state: Adaptive Parameter 2 [4] AdaptSet-1 Module input state: Adaptive Parameter 3 [4] A	I[4].ExBlo	Signal: External Blocking
[4], ExBlo TripCmd Signal: External Blocking of the Trip Command [4], Alarm L1 Signal: Alarm L2 Signal: Alarm L3 Signal: Alarm Signal: Alarm Signal: Alarm Signal: Alarm Signal: Alarm Signal: General Trip Phase L1 [4], Trip L1 Signal: General Trip Phase L2 [4], Trip L3 Signal: General Trip Phase L3 Signal: General Trip Phase L3 Signal: General Trip Phase L3 Signal: Trip Command Signal: Trip Command Signal: Trip Command Signal: Trip Command Signal: Adaptive Parameter Set Signal: Adaptive Parameter 1 [4], DefaultSet Signal: Adaptive Parameter 2 Signal: Adaptive Parameter 3 Signal: Adaptive Parameter 3 Signal: Adaptive Parameter 4 Signal: Sig	I[4].Ex rev Interl	Signal: External reverse Interlocking
[4], Alarm L1	I[4].Blo TripCmd	Signal: Trip Command blocked
[4] Alarm L2	I[4].ExBlo TripCmd	Signal: External Blocking of the Trip Command
[4],Alarm L3 Signal: Alarm L3 [4],Alarm Signal: Alarm [4],Trip L1 Signal: General Trip Phase L1 [4],Trip L2 Signal: General Trip Phase L2 [4],Trip L3 Signal: General Trip Phase L3 [4],Trip L3 Signal: Trip Phase L3 [4],Trip Signal: Trip [4],Trip Signal: Trip Command [4],DefaultSet Signal: Default Parameter Set [4],AdaptSet 1 Signal: Adaptive Parameter 1 [4],AdaptSet 2 Signal: Adaptive Parameter 2 [4],AdaptSet 3 Signal: Adaptive Parameter 3 [4],AdaptSet 4 Signal: Adaptive Parameter 4 [4],ExBlo1-1 Module input state: External blocking 1 [4],ExBlo2-1 Module input state: External Blocking of the Trip Command [4],Ex re Intert-1 Module input state: Adaptive Parameter 1 [4],AdaptSet1-1 Module input state: Adaptive Parameter 1 [4],AdaptSet2-1 Module input state: Adaptive Parameter 1 [4],AdaptSet3-1 Module input state: Adaptive Parameter 2 [4],AdaptSet3-1 Module input state: Adaptive Parameter 3 [4],AdaptSet4-1 Module input state: Adaptive Parameter 1 [4],AdaptSet4-1 Module input state: Adaptive Parameter 2 [4],AdaptSet3-1 Module input state: Adaptive Parameter 3 [4],AdaptSet4-1 Module input state: Adaptive Parameter 3 [4],AdaptSet4-1 Module input state: Adaptive Parameter 3 [4],AdaptSet4-1 Module input state: Adaptive Parameter 3 [5], ExBlo Signal: External Blocking Signal: External Blocking [5], ExBlo Signal: External Blocking Signal: External Blocking [5], ExBlo TripCmd Signal: External Blocking Signal:	I[4].Alarm L1	Signal: Alarm L1
[4], Alarm Signal: Alarm Signal: Alarm Signal: General Trip Phase L1 [4], Trip L1 Signal: General Trip Phase L2 [4], Trip L2 Signal: General Trip Phase L2 [4], Trip L3 Signal: Trip Signal: Trip Phase L3 [4], Trip Cmd Signal: Trip Command [4], DefaultSet Signal: Default Parameter Set [4], AdaptSet 1 Signal: Adaptive Parameter 1 [4], AdaptSet 2 Signal: Adaptive Parameter 2 [4], AdaptSet 3 Signal: Adaptive Parameter 3 [4], AdaptSet 4 Signal: Adaptive Parameter 4 [4], ExBlo1-1 Module input state: External blocking1 [4], ExBlo2-1 Module input state: External blocking2 [4], ExBlo TripCmd-1 Module input state: External Blocking of the Trip Command [4], Ex rev Interl-1 Module input state: Adaptive Parameter 1 [4], AdaptSet2-1 Module input state: Adaptive Parameter 1 [4], AdaptSet3-1 Module input state: Adaptive Parameter 2 [4], AdaptSet4-1 Module input state: Adaptive Parameter 3 [4], AdaptSet4-1 Module input state: Adaptive Parameter 2 [4], AdaptSet4-1 Module input state: Adaptive Parameter 3 [4], AdaptSet4-1 Module input state: Adaptive Parameter 4 [5], ExBlo Signal: External Blocking Signal: Adarm L1	I[4].Alarm L2	Signal: Alarm L2
[4], Trip L1 Signal: General Trip Phase L1 [4], Trip L2 Signal: General Trip Phase L2 [4], Trip L3 Signal: General Trip Phase L3 [4], Trip L3 Signal: Trip [4], Trip Cmd Signal: Trip [4], Trip Cmd Signal: Trip [4], DefaultSet Signal: Default Parameter Set [4], AdaptSet 1 Signal: Adaptive Parameter 1 [4], AdaptSet 2 Signal: Adaptive Parameter 2 [4], AdaptSet 3 Signal: Adaptive Parameter 3 [4], AdaptSet 4 Signal: Adaptive Parameter 4 [4], ExBlo1-I Module input state: External blocking 1 [4], ExBlo2-I Module input state: External Blocking of the Trip Command [4], Ex rev Interl-I Module input state: External reverse interlocking [4], AdaptSet2-I Module input state: Adaptive Parameter 1 [4], AdaptSet3-I Module input state: Adaptive Parameter 2 [4], AdaptSet4-I Module input state: Adaptive Parameter 3 [4], AdaptSet4-I Module input state: Adaptive Parameter 4 [5], Ex Ribo Signal: External Blocking [5], Ex Ribo Signal: External Blocking [5], Ex Ribo Signal: External Ribocking [5], Ex Ribo Signal: External Ribocking [5], Ex Ribo Signal: External Blocking [5], Ex Ribo Signal:	I[4].Alarm L3	Signal: Alarm L3
I[4]. Trip L2 Signal: General Trip Phase L2 I[4]. Trip L3 Signal: General Trip Phase L3 I[4]. Trip Signal: Trip Signal: Trip I[4]. Trip Signal: Trip Signal: Trip Signal: Trip I[4]. Trip Signal: Trip Signal: Trip Signal: Trip Signal: Trip Signal: Adaptive Parameter Module input state: External blocking I[4]. ExBlo1- Module input state: External blocking Module input state: External blocking Signal: Adaptive Parameter Module input state: Adaptive Parameter Signal: Adaptive Parameter Signal: External Blocking Signal: External	I[4].Alarm	Signal: Alarm
[4], Trip L3 Signal: General Trip Phase L3 [4], Trip Signal: Trip [4], TripCmd Signal: Trip Command [4], DefaultSet Signal: Default Parameter Set [4], AdaptSet 1 Signal: Adaptive Parameter 1 [4], AdaptSet 2 Signal: Adaptive Parameter 2 [4], AdaptSet 3 Signal: Adaptive Parameter 3 [4], AdaptSet 4 Signal: Adaptive Parameter 4 [4], ExBlo1-l Module input state: External blocking 1 [4], ExBlo2-l Module input state: External blocking 2 [4], ExBlo TripCmd-l Module input state: External reverse interlocking [4], AdaptSet1-l Module input state: Adaptive Parameter 1 [4], AdaptSet2-l Module input state: Adaptive Parameter 1 [4], AdaptSet3-l Module input state: Adaptive Parameter 2 [4], AdaptSet4-l Module input state: Adaptive Parameter 2 [4], AdaptSet4-l Module input state: Adaptive Parameter 3 [4], AdaptSet4-l Module input state: Adaptive Parameter 3 [5], ExBlo Signal: External Blocking [5], ExBlo Signal: External Blocking [5], ExBlo Signal: External Blocking [5], ExBlo TripCmd Signal: External Blocking of the Trip Command [5], Alarm L1 Signal: Alarm L1	I[4].Trip L1	Signal: General Trip Phase L1
[4].Trip Signal: Trip [4].TripCmd Signal: Trip Command [4].DefaultSet Signal: Default Parameter Set [4].AdaptSet 1 Signal: Adaptive Parameter 1 [4].AdaptSet 2 Signal: Adaptive Parameter 2 [4].AdaptSet 3 Signal: Adaptive Parameter 3 [4].AdaptSet 4 Signal: Adaptive Parameter 4 [4].ExBlo1-l Module input state: External blocking1 [4].ExBlo2-l Module input state: External blocking2 [4].ExBlo TripCmd-l Module input state: External blocking of the Trip Command [4].Ex rev Interl-l Module input state: External reverse interlocking [4].AdaptSet1-l Module input state: Adaptive Parameter 1 [4].AdaptSet2-l Module input state: Adaptive Parameter 2 [4].AdaptSet3-l Module input state: Adaptive Parameter 3 [4].AdaptSet4-l Module input state: Adaptive Parameter 3 [5].ExBlo Signal: External Blocking [5].ExBlo Signal: External Blocking [5].ExBlo TripCmd Signal: External Blocking of the Trip Command [5].ExBlo TripCmd Signal: External Blocking of the Trip Command [5].Alarm L1 Signal: Alarm L1	I[4].Trip L2	Signal: General Trip Phase L2
[4].TripCmd Signal: Trip Command [4].DefaultSet Signal: Default Parameter Set [4].AdaptSet 1 Signal: Adaptive Parameter 1 [4].AdaptSet 2 Signal: Adaptive Parameter 2 [4].AdaptSet 3 Signal: Adaptive Parameter 3 [4].AdaptSet 4 Signal: Adaptive Parameter 4 [4].ExBlo1-l Module input state: External blocking1 [4].ExBlo2-l Module input state: External blocking2 [4].ExBlo TripCmd-l Module input state: External Blocking of the Trip Command [4].Ex rev Interl-l Module input state: External reverse interlocking [4].AdaptSet1-l Module input state: Adaptive Parameter1 [4].AdaptSet2-l Module input state: Adaptive Parameter2 [4].AdaptSet3-l Module input state: Adaptive Parameter3 [4].AdaptSet4-l Module input state: Adaptive Parameter4 [5].active Signal: active Signal: External Blocking [5].ExBlo Signal: External reverse Interlocking [6].Ex rev Interl Signal: External Blocking [6].Ex External Blocking Signal: External Bloc	I[4].Trip L3	Signal: General Trip Phase L3
[4],DefaultSet Signal: Default Parameter Set [4],AdaptSet 1 Signal: Adaptive Parameter 1 [4],AdaptSet 2 Signal: Adaptive Parameter 2 [4],AdaptSet 3 Signal: Adaptive Parameter 3 [4],AdaptSet 4 Signal: Adaptive Parameter 4 [4],ExBlo1-I Module input state: External blocking 1 [4],ExBlo2-I Module input state: External blocking 2 [4],ExBlo TripCmd-I Module input state: External Blocking of the Trip Command [4],Ex rev Intert-I Module input state: External reverse interlocking [4],AdaptSet1-I Module input state: Adaptive Parameter 1 [4],AdaptSet2-I Module input state: Adaptive Parameter 2 [4],AdaptSet3-I Module input state: Adaptive Parameter 3 [4],AdaptSet4-I Module input state: Adaptive Parameter 4 [5],active Signal: active [5],ExBlo Signal: External Blocking [5],Ex rev Intert Signal: External reverse Interlocking [5],Ex rev Interd Signal: External Blocking [5],ExBlo TripCmd Signal: External Blocking [5],ExBlo TripCmd Signal: External Blocking [5],ExBlo TripCmd Signal: External Blocking [5],Alarm L1 Signal: Alarm L1	I[4].Trip	Signal: Trip
[4].AdaptSet 1 Signal: Adaptive Parameter 1 [4].AdaptSet 2 Signal: Adaptive Parameter 2 [4].AdaptSet 3 Signal: Adaptive Parameter 3 [4].AdaptSet 4 Signal: Adaptive Parameter 4 [4].ExBlo1-l Module input state: External blocking1 [4].ExBlo2-l Module input state: External blocking2 [4].ExBlo TripCmd-l Module input state: External Blocking of the Trip Command [4].Ex rev Interl-l Module input state: External reverse interlocking [4].AdaptSet1-l Module input state: Adaptive Parameter1 [4].AdaptSet2-l Module input state: Adaptive Parameter2 [4].AdaptSet3-l Module input state: Adaptive Parameter3 [4].AdaptSet4-l Module input state: Adaptive Parameter4 [5].active Signal: active [5].ExBlo Signal: External Blocking [5].Ex rev Interl Signal: External Blocking [5].Ex rev Interl Signal: External Blocking [5].Ex rev Interl Signal: External Blocking [5].ExBlo TripCmd Signal: External Blocking [5].ExBlo TripCmd Signal: External Blocking [5].ExBlo TripCmd Signal: External Blocking [5].Alarm L1 Signal: Alarm L1	I[4].TripCmd	Signal: Trip Command
[4].AdaptSet 2 Signal: Adaptive Parameter 2 [4].AdaptSet 3 Signal: Adaptive Parameter 3 [4].AdaptSet 4 Signal: Adaptive Parameter 4 [4].ExBlo1-l Module input state: External blocking1 [4].ExBlo2-l Module input state: External blocking2 [4].ExBlo TripCmd-l Module input state: External Blocking of the Trip Command [4].Ex rev Interl-l Module input state: External reverse interlocking [4].AdaptSet1-l Module input state: Adaptive Parameter1 [4].AdaptSet2-l Module input state: Adaptive Parameter2 [4].AdaptSet3-l Module input state: Adaptive Parameter3 [4].AdaptSet4-l Module input state: Adaptive Parameter4 [5].active Signal: active [5].ExBlo Signal: External Blocking [6].Ex rev Interl Signal: External reverse Interlocking [6].Ex rev Interl Signal: External Blocking [6].Ex rev Interl Signal: External Blocking [6].Ex rev Interl Signal: External Blocking [7].ExBlo TripCmd Signal: External Blocking of the Trip Command [8].Alarm L1 Signal: Alarm L1	I[4].DefaultSet	Signal: Default Parameter Set
I[4].AdaptSet 3 I[4].AdaptSet 4 Signal: Adaptive Parameter 4 I[4].ExBlo1-I Module input state: External blocking1 I[4].ExBlo2-I Module input state: External blocking2 I[4].ExBlo TripCmd-I Module input state: External Blocking of the Trip Command I[4].Ex rev Interl-I Module input state: External reverse interlocking I[4].AdaptSet1-I Module input state: Adaptive Parameter1 I[4].AdaptSet2-I Module input state: Adaptive Parameter2 I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Signal: active Signal: External Blocking I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External Blocking I[5].Ex rev Interl Signal: External Blocking Signal: External Blocking I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1	I[4].AdaptSet 1	Signal: Adaptive Parameter 1
I[4].AdaptSet 4 Signal: Adaptive Parameter 4 I[4].ExBlo1-I Module input state: External blocking1 I[4].ExBlo2-I Module input state: External blocking2 I[4].ExBlo TripCmd-I Module input state: External Blocking of the Trip Command I[4].Ex rev Interl-I Module input state: External reverse interlocking I[4].AdaptSet1-I Module input state: Adaptive Parameter1 I[4].AdaptSet2-I Module input state: Adaptive Parameter2 I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active Signal: External Blocking I[5].ExBlo Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].AdaptSet 2	Signal: Adaptive Parameter 2
I[4].ExBlo1-I I[4].ExBlo2-I Module input state: External blocking2 I[4].ExBlo TripCmd-I Module input state: External Blocking of the Trip Command I[4].Ex rev Interl-I Module input state: External Blocking of the Trip Command I[4].Ex rev Interl-I Module input state: External reverse interlocking I[4].AdaptSet1-I Module input state: Adaptive Parameter1 I[4].AdaptSet2-I Module input state: Adaptive Parameter2 I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: External Blocking of the Trip Command I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1	I[4].AdaptSet 3	Signal: Adaptive Parameter 3
I[4].ExBlo2-I I[4].ExBlo TripCmd-I Module input state: External Blocking of the Trip Command I[4].Ex rev Interl-I Module input state: External Blocking of the Trip Command I[4].AdaptSet1-I Module input state: Adaptive Parameter1 I[4].AdaptSet2-I Module input state: Adaptive Parameter2 I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: External Blocking of the Trip Command I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].ExBlo TripCmd	I[4].AdaptSet 4	Signal: Adaptive Parameter 4
I[4].ExBlo TripCmd-I I[4].Ex rev Interl-I Module input state: External Blocking of the Trip Command I[4].Ex rev Interl-I Module input state: External reverse interlocking I[4].AdaptSet1-I Module input state: Adaptive Parameter1 I[4].AdaptSet2-I Module input state: Adaptive Parameter2 I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: External Blocking of the Trip Command I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].ExBlo1-I	Module input state: External blocking1
I[4].Ex rev Interl-I Module input state: External reverse interlocking I[4].AdaptSet1-I Module input state: Adaptive Parameter1 I[4].AdaptSet2-I Module input state: Adaptive Parameter2 I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].ExBlo2-I	Module input state: External blocking2
I[4].AdaptSet1-I I[4].AdaptSet2-I I[4].AdaptSet3-I I[4].AdaptSet3-I I[4].AdaptSet4-I I[5].active I[5].ExBlo I[5].Ex rev Interl I[5].Ex rev Interl I[5].ExBlo TripCmd I[5].ExBlo TripCmd I[5].AdaptSet4 I[5].Alarm L1 Module input state: Adaptive Parameter3 Module input state: Adaptive Parameter4 I[6].Ex rev Interl I[7].Ex rev Interl I[8].Ex rev Interl I[9].Ex rev Interl I[9].Ex rev Interl I[1].Ex rev Interl I[2].Ex rev Interl I[3].Ex rev Interl I[4].Ex rev Interl I[5].Ex rev Interl I[6].Ex rev Interl I[7].Ex rev Interl I[8].Ex rev Interl I[8].Ex rev Interl I[9].Ex rev Interl I[1].Ex rev Interl I[2].Ex rev Interl I[3].Ex rev Interl I[4].Ex rev Interl I[5].Ex rev Interl I[6].Ex rev Interl I[7].Ex rev Interl I[8].Ex rev Interl I[I[4].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I[4].AdaptSet2-I I[4].AdaptSet3-I Module input state: Adaptive Parameter2 I[4].AdaptSet4-I Module input state: Adaptive Parameter3 I[5].active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].Ex rev Interl-I	Module input state: External reverse interlocking
I[4].AdaptSet3-I Module input state: Adaptive Parameter3 I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].AdaptSet1-I	Module input state: Adaptive Parameter1
I[4].AdaptSet4-I Module input state: Adaptive Parameter4 I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].AdaptSet2-I	Module input state: Adaptive Parameter2
I[5].active Signal: active I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].AdaptSet3-I	Module input state: Adaptive Parameter3
I[5].ExBlo Signal: External Blocking I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[4].AdaptSet4-I	Module input state: Adaptive Parameter4
I[5].Ex rev Interl Signal: External reverse Interlocking I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[5].active	Signal: active
I[5].Blo TripCmd Signal: Trip Command blocked I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[5].ExBlo	Signal: External Blocking
I[5].ExBlo TripCmd Signal: External Blocking of the Trip Command I[5].Alarm L1 Signal: Alarm L1	I[5].Ex rev Interl	Signal: External reverse Interlocking
I[5].Alarm L1 Signal: Alarm L1	I[5].Blo TripCmd	Signal: Trip Command blocked
I[5].Alarm L1 Signal: Alarm L1	I[5].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I[5].Alarm L2 Signal: Alarm L2	I[5].Alarm L1	Signal: Alarm L1
	I[5].Alarm L2	Signal: Alarm L2

Name	Description
I[5].Alarm L3	Signal: Alarm L3
I[5].Alarm	Signal: Alarm
I[5].Trip L1	Signal: General Trip Phase L1
I[5].Trip L2	Signal: General Trip Phase L2
I[5].Trip L3	Signal: General Trip Phase L3
I[5].Trip	Signal: Trip
I[5].TripCmd	Signal: Trip Command
I[5].DefaultSet	Signal: Default Parameter Set
I[5].AdaptSet 1	Signal: Adaptive Parameter 1
I[5].AdaptSet 2	Signal: Adaptive Parameter 2
I[5].AdaptSet 3	Signal: Adaptive Parameter 3
I[5].AdaptSet 4	Signal: Adaptive Parameter 4
I[5].ExBlo1-I	Module input state: External blocking1
I[5].ExBlo2-I	Module input state: External blocking2
I[5].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I[5].Ex rev Interl-I	Module input state: External reverse interlocking
I[5].AdaptSet1-I	Module input state: Adaptive Parameter1
I[5].AdaptSet2-I	Module input state: Adaptive Parameter2
I[5].AdaptSet3-I	Module input state: Adaptive Parameter3
I[5].AdaptSet4-I	Module input state: Adaptive Parameter4
I[6].active	Signal: active
I[6].ExBlo	Signal: External Blocking
I[6].Ex rev Interl	Signal: External reverse Interlocking
I[6].Blo TripCmd	Signal: Trip Command blocked
I[6].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I[6].Alarm L1	Signal: Alarm L1
I[6].Alarm L2	Signal: Alarm L2
I[6].Alarm L3	Signal: Alarm L3
I[6].Alarm	Signal: Alarm
I[6].Trip L1	Signal: General Trip Phase L1
I[6].Trip L2	Signal: General Trip Phase L2
I[6].Trip L3	Signal: General Trip Phase L3
I[6].Trip	Signal: Trip
I[6].TripCmd	Signal: Trip Command
I[6].DefaultSet	Signal: Default Parameter Set
I[6].AdaptSet 1	Signal: Adaptive Parameter 1
I[6].AdaptSet 2	Signal: Adaptive Parameter 2
I[6].AdaptSet 3	Signal: Adaptive Parameter 3
I[6].AdaptSet 4	Signal: Adaptive Parameter 4

Page 542 EN MRM4 12/11

Name	Description
I[6].ExBlo1-I	Module input state: External blocking1
I[6].ExBlo2-I	Module input state: External blocking2
I[6].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I[6].Ex rev Interl-I	Module input state: External reverse interlocking
I[6].AdaptSet1-I	Module input state: Adaptive Parameter1
I[6].AdaptSet2-I	Module input state: Adaptive Parameter2
I[6].AdaptSet3-I	Module input state: Adaptive Parameter3
I[6].AdaptSet4-I	Module input state: Adaptive Parameter4
IG[1].active	Signal: active
IG[1].ExBlo	Signal: External Blocking
IG[1].Ex rev Interl	Signal: External reverse Interlocking
IG[1].Blo TripCmd	Signal: Trip Command blocked
IG[1].ExBlo TripCmd	Signal: External Blocking of the Trip Command
IG[1].Alarm	Signal: Alarm IG
IG[1].Trip	Signal: Trip
IG[1].TripCmd	Signal: Trip Command
IG[1].DefaultSet	Signal: Default Parameter Set
IG[1].AdaptSet 1	Signal: Adaptive Parameter 1
IG[1].AdaptSet 2	Signal: Adaptive Parameter 2
IG[1].AdaptSet 3	Signal: Adaptive Parameter 3
IG[1].AdaptSet 4	Signal: Adaptive Parameter 4
IG[1].ExBlo1-I	Module input state: External blocking1
IG[1].ExBlo2-I	Module input state: External blocking2
IG[1].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
IG[1].Ex rev Interl-I	Module input state: External reverse interlocking
IG[1].AdaptSet1-I	Module input state: Adaptive Parameter1
IG[1].AdaptSet2-I	Module input state: Adaptive Parameter2
IG[1].AdaptSet3-I	Module input state: Adaptive Parameter3
IG[1].AdaptSet4-I	Module input state: Adaptive Parameter4
IG[2].active	Signal: active
IG[2].ExBlo	Signal: External Blocking
IG[2].Ex rev Interl	Signal: External reverse Interlocking
IG[2].Blo TripCmd	Signal: Trip Command blocked
IG[2].ExBlo TripCmd	Signal: External Blocking of the Trip Command
IG[2].Alarm	Signal: Alarm IG
IG[2].Trip	Signal: Trip
IG[2].TripCmd	Signal: Trip Command
IG[2].DefaultSet	Signal: Default Parameter Set
IG[2].AdaptSet 1	Signal: Adaptive Parameter 1
	I

Name	Description
IG[2].AdaptSet 2	Signal: Adaptive Parameter 2
IG[2].AdaptSet 3	Signal: Adaptive Parameter 3
IG[2].AdaptSet 4	Signal: Adaptive Parameter 4
IG[2].ExBlo1-I	Module input state: External blocking1
IG[2].ExBlo2-I	Module input state: External blocking2
IG[2].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
IG[2].Ex rev Interl-I	Module input state: External reverse interlocking
IG[2].AdaptSet1-I	Module input state: Adaptive Parameter1
IG[2].AdaptSet2-I	Module input state: Adaptive Parameter2
IG[2].AdaptSet3-I	Module input state: Adaptive Parameter3
IG[2].AdaptSet4-I	Module input state: Adaptive Parameter4
IG[3].active	Signal: active
IG[3].ExBlo	Signal: External Blocking
IG[3].Ex rev Interl	Signal: External reverse Interlocking
IG[3].Blo TripCmd	Signal: Trip Command blocked
IG[3].ExBlo TripCmd	Signal: External Blocking of the Trip Command
IG[3].Alarm	Signal: Alarm IG
IG[3].Trip	Signal: Trip
IG[3].TripCmd	Signal: Trip Command
IG[3].DefaultSet	Signal: Default Parameter Set
IG[3].AdaptSet 1	Signal: Adaptive Parameter 1
IG[3].AdaptSet 2	Signal: Adaptive Parameter 2
IG[3].AdaptSet 3	Signal: Adaptive Parameter 3
IG[3].AdaptSet 4	Signal: Adaptive Parameter 4
IG[3].ExBlo1-I	Module input state: External blocking1
IG[3].ExBlo2-I	Module input state: External blocking2
IG[3].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
IG[3].Ex rev Interl-I	Module input state: External reverse interlocking
IG[3].AdaptSet1-I	Module input state: Adaptive Parameter1
IG[3].AdaptSet2-I	Module input state: Adaptive Parameter2
IG[3].AdaptSet3-I	Module input state: Adaptive Parameter3
IG[3].AdaptSet4-I	Module input state: Adaptive Parameter4
IG[4].active	Signal: active
IG[4].ExBlo	Signal: External Blocking
IG[4].Ex rev Interl	Signal: External reverse Interlocking
IG[4].Blo TripCmd	Signal: Trip Command blocked
IG[4].ExBlo TripCmd	Signal: External Blocking of the Trip Command
IG[4].Alarm	Signal: Alarm IG
IG[4].Trip	Signal: Trip

Page 544 EN MRM4 12/11

Name	Description
IG[4].TripCmd	Signal: Trip Command
IG[4].DefaultSet	Signal: Default Parameter Set
IG[4].AdaptSet 1	Signal: Adaptive Parameter 1
IG[4].AdaptSet 2	Signal: Adaptive Parameter 2
IG[4].AdaptSet 3	Signal: Adaptive Parameter 3
IG[4].AdaptSet 4	Signal: Adaptive Parameter 4
IG[4].ExBlo1-l	Module input state: External blocking1
IG[4].ExBlo2-I	Module input state: External blocking2
IG[4].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
IG[4].Ex rev Interl-I	Module input state: External reverse interlocking
IG[4].AdaptSet1-I	Module input state: Adaptive Parameter1
IG[4].AdaptSet2-I	Module input state: Adaptive Parameter2
IG[4].AdaptSet3-I	Module input state: Adaptive Parameter3
IG[4].AdaptSet4-I	Module input state: Adaptive Parameter4
12>[1].active	Signal: active
12>[1].ExBlo	Signal: External Blocking
I2>[1].Blo TripCmd	Signal: Trip Command blocked
I2>[1].ExBlo TripCmd	Signal: External Blocking of the Trip Command
12>[1].Alarm	Signal: Alarm Negative Sequence
12>[1].Trip	Signal: Trip
I2>[1].TripCmd	Signal: Trip Command
12>[1].ExBlo1-	Module input state: External blocking1
12>[1].ExBlo2-	Module input state: External blocking2
I2>[1].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
12>[2].active	Signal: active
12>[2].ExBlo	Signal: External Blocking
I2>[2].Blo TripCmd	Signal: Trip Command blocked
I2>[2].ExBlo TripCmd	Signal: External Blocking of the Trip Command
12>[2].Alarm	Signal: Alarm Negative Sequence
12>[2].Trip	Signal: Trip
12>[2].TripCmd	Signal: Trip Command
12>[2].ExBlo1-	Module input state: External blocking1
12>[2].ExBlo2-	Module input state: External blocking2
I2>[2].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
ThR.Alarm Pickup	Signal: Alarm Pickup
ThR.Alarm Timeout	Signal: Alarm Timeout
ThR.RTD effective	RTD effective
ThR.Load above SF	Load above Service Factor
ThR.active	Signal: active

Name	Description
ThR.ExBlo	Signal: External Blocking
ThR.Blo TripCmd	Signal: Trip Command blocked
ThR.ExBlo TripCmd	Signal: External Blocking of the Trip Command
ThR.Alarm	Signal: Alarm
ThR.Trip	Signal: Trip
ThR.TripCmd	Signal: Trip Command
ThR.ExBlo1	Module input state: External blocking
ThR.ExBlo2	Module input state: External blocking
ThR.ExBlo TripCmd	Module input state: External Blocking of the Trip Command
Jam[1].active	Signal: active
Jam[1].ExBlo	Signal: External Blocking
Jam[1].Ex rev Interl	Signal: External reverse Interlocking
Jam[1].Blo TripCmd	Signal: Trip Command blocked
Jam[1].ExBlo TripCmd	Signal: External Blocking of the Trip Command
Jam[1].Alarm	Signal: Alarm
Jam[1].Trip	Signal: Trip
Jam[1].TripCmd	Signal: Trip Command
Jam[1].ExBlo1-l	Module input state: External blocking1
Jam[1].ExBlo2-l	Module input state: External blocking2
Jam[1].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
Jam[1].Ex rev Interl-I	Module input state: External reverse interlocking
Jam[2].active	Signal: active
Jam[2].ExBlo	Signal: External Blocking
Jam[2].Ex rev Interl	Signal: External reverse Interlocking
Jam[2].Blo TripCmd	Signal: Trip Command blocked
Jam[2].ExBlo TripCmd	Signal: External Blocking of the Trip Command
Jam[2].Alarm	Signal: Alarm
Jam[2].Trip	Signal: Trip
Jam[2].TripCmd	Signal: Trip Command
Jam[2].ExBlo1-l	Module input state: External blocking1
Jam[2].ExBlo2-l	Module input state: External blocking2
Jam[2].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
Jam[2].Ex rev Interl-I	Module input state: External reverse interlocking
I<[1].active	Signal: active
I<[1].ExBlo	Signal: External Blocking
I<[1].Ex rev Interl	Signal: External reverse Interlocking
I<[1].Blo TripCmd	Signal: Trip Command blocked
I<[1].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I<[1].Alarm	Signal: Alarm

Page 546 EN MRM4 12/11

Name	Description
I<[1].Trip	Signal: Trip
I<[1].TripCmd	Signal: Trip Command
I<[1].ExBlo1-I	Module input state: External blocking1
I<[1].ExBlo2-I	Module input state: External blocking2
I<[1].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I<[1].Ex rev Interl-I	Module input state: External reverse interlocking
I<[2].active	Signal: active
I<[2].ExBlo	Signal: External Blocking
I<[2].Ex rev Interl	Signal: External reverse Interlocking
I<[2].Blo TripCmd	Signal: Trip Command blocked
I<[2].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I<[2].Alarm	Signal: Alarm
I<[2].Trip	Signal: Trip
I<[2].TripCmd	Signal: Trip Command
I<[2].ExBlo1-I	Module input state: External blocking1
I<[2].ExBlo2-I	Module input state: External blocking2
I<[2].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I<[2].Ex rev Interl-I	Module input state: External reverse interlocking
I<[3].active	Signal: active
I<[3].ExBlo	Signal: External Blocking
I<[3].Ex rev Interl	Signal: External reverse Interlocking
I<[3].Blo TripCmd	Signal: Trip Command blocked
I<[3].ExBlo TripCmd	Signal: External Blocking of the Trip Command
I<[3].Alarm	Signal: Alarm
I<[3].Trip	Signal: Trip
I<[3].TripCmd	Signal: Trip Command
I<[3].ExBlo1-I	Module input state: External blocking1
I<[3].ExBlo2-I	Module input state: External blocking2
I<[3].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
I<[3].Ex rev Interl-I	Module input state: External reverse interlocking
MLS.active	Signal: active
MLS.ExBlo	Signal: External Blocking
MLS.Alarm	Signal: Alarm
MLS.Trip	Signal: Trip
MLS.ExBlo1-I	Module input state: External blocking1
MLS.ExBlo2-I	Module input state: External blocking2
URTD.Windg1 Superv	Signal: Supervision Channel Windg1
URTD.Windg2 Superv	Signal: Supervision Channel Windg2
URTD.Windg3 Superv	Signal: Supervision Channel Windg3

Name	Description
URTD.Windg4 Superv	Signal: Supervision Channel Windg4
URTD.Windg5 Superv	Signal: Supervision Channel Windg5
URTD.Windg6 Superv	Signal: Supervision Channel Windg6
URTD.MotBear1 Superv	Signal: Supervision Channel MotBear1
URTD.MotBear2 Superv	Signal: Supervision Channel MotBear2
URTD.LoadBear1 Superv	Signal: Supervision Channel LoadBear1
URTD.LoadBear2 Superv	Signal: Supervision Channel LoadBear2
URTD.Aux1 Superv	Signal: Supervision Channel Aux1
URTD.Aux2 Superv	Signal: Supervision Channel Aux2
URTD.Superv	Signal: URTD Supervision Channel
URTD.active	Signal: URTD active
URTD.Outs forced	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
RTD.active	Signal: active
RTD.ExBlo	Signal: External Blocking
RTD.Blo TripCmd	Signal: Trip Command blocked
RTD.ExBlo TripCmd	Signal: External Blocking of the Trip Command
RTD.Alarm	Alarm RTD Temperature Protection
RTD.Trip	Signal: Trip
RTD.TripCmd	Signal: Trip Command
RTD.Windg 1 Trip	Winding 1 Signal: Trip
RTD.Windg 1 Alarm	Winding 1 Alarm RTD Temperature Protection
RTD.Windg 1 Timeout Alarm	Winding 1 Timeout Alarm
RTD.Windg 1 Invalid	Winding 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Windg 2 Trip	Winding 2 Signal: Trip
RTD.Windg 2 Alarm	Winding 2 Alarm RTD Temperature Protection
RTD.Windg 2 Timeout Alarm	Winding 2 Timeout Alarm
RTD.Windg 2 Invalid	Winding 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Windg 3 Trip	Winding 3 Signal: Trip
RTD.Windg 3 Alarm	Winding 3 Alarm RTD Temperature Protection
RTD.Windg 3 Timeout Alarm	Winding 3 Timeout Alarm
RTD.Windg 3 Invalid	Winding 3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Windg 4 Trip	Winding 4 Signal: Trip
RTD.Windg 4 Alarm	Winding 4 Alarm RTD Temperature Protection
RTD.Windg 4 Timeout Alarm	Winding 4 Timeout Alarm
RTD.Windg 4 Invalid	Winding 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)

Page 548 EN MRM4 12/11

Name	Description
RTD.Windg 5 Trip	Winding 5 Signal: Trip
RTD.Windg 5 Alarm	Winding 5 Alarm RTD Temperature Protection
RTD.Windg 5 Timeout Alarm	Winding 5 Timeout Alarm
RTD.Windg 5 Invalid	Winding 5 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Windg 6 Trip	Winding 6 Signal: Trip
RTD.Windg 6 Alarm	Winding 6 Alarm RTD Temperature Protection
RTD.Windg 6 Timeout Alarm	Winding 6 Timeout Alarm
RTD.Windg 6 Invalid	Winding 6 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.MotBear 1 Trip	Motor Bearing 1 Signal: Trip
RTD.MotBear 1 Alarm	Motor Bearing 1 Alarm RTD Temperature Protection
RTD.MotBear 1 Timeout Alarm	Motor Bearing 1 Timeout Alarm
RTD.MotBear 1 Invalid	Motor Bearing 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.MotBear 2 Trip	Motor Bearing 2 Signal: Trip
RTD.MotBear 2 Alarm	Motor Bearing 2 Alarm RTD Temperature Protection
RTD.MotBear 2 Timeout Alarm	Motor Bearing 2 Timeout Alarm
RTD.MotBear 2 Invalid	Motor Bearing 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.LoadBear 1 Trip	Load Bearing 1 Signal: Trip
RTD.LoadBear 1 Alarm	Load Bearing 1 Alarm RTD Temperature Protection
RTD.LoadBear 1 Timeout Alarm	Load Bearing 1 Timeout Alarm
RTD.LoadBear 1 Invalid	Load Bearing 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.LoadBear 2 Trip	Load Bearing 2 Signal: Trip
RTD.LoadBear 2 Alarm	Load Bearing 2 Alarm RTD Temperature Protection
RTD.LoadBear 2 Timeout Alarm	Load Bearing 2 Timeout Alarm
RTD.LoadBear 2 Invalid	Load Bearing 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Aux1 Trip	Auxiliary 1 Signal: Trip
RTD.Aux1 Alarm	Auxiliary 1 Alarm RTD Temperature Protection
RTD.Aux1 Timeout Alarm	Auxiliary 1 Timeout Alarm
RTD.Aux1 Invalid	Auxiliary 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Aux2 Trip	Auxiliary 2 Signal: Trip
RTD.Aux2 Alarm	Auxiliary 2 Alarm RTD Temperature Protection
RTD.Aux2 Timeout Alarm	Auxiliary 2 Timeout Alarm
RTD.Aux2 Invalid	Auxiliary 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Trip WD Group	Trip all Windings
RTD.Alarm WD Group	Alarm all Windings

Name	Description
RTD.TimeoutAlmWDGrp	Timeout Alarm all Windings
RTD.Windg Group Invalid	Winding Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Trip MB Group	Trip all Motor Bearings
RTD.Alarm MB Group	Alarm all Motor Bearings
RTD.TimeoutAlmMBGrp	Timeout Alarm all Motor Bearings
RTD.MotBear Group Invalid	Motor Bearing Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Trip LB Group	Trip all Load Bearings
RTD.Alarm LB Group	Alarm all Load Bearings
RTD.TimeoutAlmLBGrp	Timeout Alarm all Load Bearings
RTD.LoadBear Group Invalid	Load Bearing Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD.Trip Any Group	Trip Any Group
RTD.Alarm Any Group	Alarm Any Group
RTD.TimeoutAlmAnyGrp	Timeout Alarm Any Group
RTD.Trip Group 1	Trip Group 1
RTD.Trip Group 2	Trip Group 2
RTD.Timeout Alarm	Alarm timeout expired
RTD.Trip Aux Group	Trip Auxiliary Group
RTD.Alarm Aux Group	Alarm Auxiliary Group
RTD.TimeoutAlmAuxGrp	Timeout Alarm Auxiliary Group
RTD.AuxGrpInvalid	Invalid Auxiliary Group
RTD.ExBlo1-I	Module input state: External blocking1
RTD.ExBlo2-I	Module input state: External blocking2
RTD.ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
ExP[1].active	Signal: active
ExP[1].ExBlo	Signal: External Blocking
ExP[1].Blo TripCmd	Signal: Trip Command blocked
ExP[1].ExBlo TripCmd	Signal: External Blocking of the Trip Command
ExP[1].Alarm	Signal: Alarm
ExP[1].Trip	Signal: Trip
ExP[1].TripCmd	Signal: Trip Command
ExP[1].ExBlo1-I	Module input state: External blocking1
ExP[1].ExBlo2-I	Module input state: External blocking2
ExP[1].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
ExP[1].Alarm-I	Module input state: Alarm
ExP[1].Trip-I	Module input state: Trip

Page 550 EN MRM4 12/11

Name	Description
ExP[2].ExBlo	Signal: External Blocking
ExP[2].Blo TripCmd	Signal: Trip Command blocked
ExP[2].ExBlo TripCmd	Signal: External Blocking of the Trip Command
ExP[2].Alarm	Signal: Alarm
ExP[2].Trip	Signal: Trip
ExP[2].TripCmd	Signal: Trip Command
ExP[2].ExBlo1-I	Module input state: External blocking1
ExP[2].ExBlo2-I	Module input state: External blocking2
ExP[2].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
ExP[2].Alarm-I	Module input state: Alarm
ExP[2].Trip-I	Module input state: Trip
ExP[3].active	Signal: active
ExP[3].ExBlo	Signal: External Blocking
ExP[3].Blo TripCmd	Signal: Trip Command blocked
ExP[3].ExBlo TripCmd	Signal: External Blocking of the Trip Command
ExP[3].Alarm	Signal: Alarm
ExP[3].Trip	Signal: Trip
ExP[3].TripCmd	Signal: Trip Command
ExP[3].ExBlo1-l	Module input state: External blocking1
ExP[3].ExBlo2-I	Module input state: External blocking2
ExP[3].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
ExP[3].Alarm-I	Module input state: Alarm
ExP[3].Trip-I	Module input state: Trip
ExP[4].active	Signal: active
ExP[4].ExBlo	Signal: External Blocking
ExP[4].Blo TripCmd	Signal: Trip Command blocked
ExP[4].ExBlo TripCmd	Signal: External Blocking of the Trip Command
ExP[4].Alarm	Signal: Alarm
ExP[4].Trip	Signal: Trip
ExP[4].TripCmd	Signal: Trip Command
ExP[4].ExBlo1-I	Module input state: External blocking1
ExP[4].ExBlo2-I	Module input state: External blocking2
ExP[4].ExBlo TripCmd-I	Module input state: External Blocking of the Trip Command
ExP[4].Alarm-I	Module input state: Alarm
ExP[4].Trip-I	Module input state: Trip
SOTF.active	Signal: active
SOTF.ExBlo	Signal: External Blocking
SOTF.Ex rev Interl	Signal: External reverse Interlocking
SOTF.enabled	Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.

Name	Description
SOTF.I<	Signal: No Load Current.
SOTF.ExBlo1-I	Module input state: External blocking
SOTF.ExBlo2-I	Module input state: External blocking
SOTF.Ex rev Interl-I	Module input state: External reverse interlocking
SOTF.Ext SOTF-I	Module input state: External Switch Onto Fault Alarm
CBF.active	Signal: active
CBF.ExBlo	Signal: External Blocking
CBF.running	Signal: CBF-Module started
CBF.Alarm	Signal: Breaker Failure
CBF.Lockout	Signal: Lockout
CBF.Res Lockout	Signal: Reset Lockout
CBF.ExBlo1-I	Module input state: External blocking1
CBF.ExBlo2-I	Module input state: External blocking2
CBF.Trigger1	Module Input: Trigger that will start the CBF
CBF.Trigger2	Module Input: Trigger that will start the CBF
CBF.Trigger3	Module Input: Trigger that will start the CBF
TCS.active	Signal: active
TCS.ExBlo	Signal: External Blocking
TCS.Alarm	Signal: Alarm Trip Circuit Supervision
TCS.Not Possible	Not possible because no state indicator assigned to the breaker.
TCS.Aux ON-I	Module Input State: Position indicator/check-back signal of the Bkr. (52a)
TCS.Aux OFF-I	Module input state: Position indicator/check-back signal of the Bkr. (52b)
TCS.ExBlo1-I	Module input state: External blocking1
TCS.ExBlo2-I	Module input state: External blocking2
CTS.active	Signal: active
CTS.ExBlo	Signal: External Blocking
CTS.Alarm	Signal: Alarm Current Transformer Measuring Circuit Supervision
CTS.ExBlo1-I	Module input state: External blocking1
CTS.ExBlo2-I	Module input state: External blocking2
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input

Page 552 EN MRM4 12/11

Name	Description
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input
OR-3AI X2.BO 1	Signal: Binary Relay Output Contact
OR-3AI X2.BO 2	Signal: Binary Relay Output Contact
OR-3AI X2.BO 3	Signal: Binary Relay Output Contact
OR-3AI X2.DISARMED!	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
OR-3Al X2.Outs forced	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
BO-5 X2.BO 1	Signal: Binary Relay Output Contact
BO-5 X2.BO 2	Signal: Binary Relay Output Contact
BO-5 X2.BO 3	Signal: Binary Relay Output Contact
BO-5 X2.BO 4	Signal: Binary Relay Output Contact
BO-5 X2.BO 5	Signal: Binary Relay Output Contact
BO-5 X2.DISARMED!	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
BO-5 X2.Outs forced	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
Analog Output.Force Mode	For commissioning purposes or for maintenance, Analog Outputs can be set by force. By means of this function the normal Analog Outputs can be overwritten (forced).
Event rec.Res all records	Signal: All records deleted
Disturb rec.recording	Signal: Recording
Disturb rec.memory full	Signal: Memory full
Disturb rec.Clear fail	Signal: Clear failure in memory
Disturb rec.Res all records	Signal: All records deleted
Disturb rec.Res rec	Signal: Delete record
Disturb rec.Man Trigger	Signal: Manual Trigger
Disturb rec.Start1-I	State of the module input:: Trigger event / start recording if:
Disturb rec.Start2-I	State of the module input:: Trigger event / start recording if:
Disturb rec.Start3-I	State of the module input:: Trigger event / start recording if:
Disturb rec.Start4-I	State of the module input:: Trigger event / start recording if:
Disturb rec.Start5-I	State of the module input:: Trigger event / start recording if:
Disturb rec.Start6-I	State of the module input:: Trigger event / start recording if:
Disturb rec.Start7-I	State of the module input:: Trigger event / start recording if:

Name	Description
Disturb rec.Start8-I	State of the module input:: Trigger event / start recording if:
Fault rec.Res rec	Signal: Delete record
Fault rec.Man Trigger	Signal: Manual Trigger
Fault rec.Start1-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start2-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start3-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start4-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start5-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start6-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start7-I	State of the module input:: Trigger event / start recording if:
Fault rec.Start8-I	State of the module input:: Trigger event / start recording if:
Start rec.Storing	Signal: Data are saved
Start rec.MotorStart	Module input state: Start of recorder
Start rec.MotorRun	Module input state: Motor is in run mode
Start rec.Motor Speed2	Module input state: Motor operates in speed 2
Start rec.ITransit	Module input state: Motor operations transition on current
Trend rec.Hand Reset	Hand Reset
Modbus.Transmission	Signal: SCADA active
Modbus.Scada Cmd 1	Scada Command
Modbus.Scada Cmd 2	Scada Command
Modbus.Scada Cmd 3	Scada Command
Modbus.Scada Cmd 4	Scada Command
Modbus.Scada Cmd 5	Scada Command
Modbus.Scada Cmd 6	Scada Command
Modbus.Scada Cmd 7	Scada Command
Modbus.Scada Cmd 8	Scada Command
Modbus.Scada Cmd 9	Scada Command
Modbus.Scada Cmd 10	Scada Command
Modbus.Scada Cmd 11	Scada Command
Modbus.Scada Cmd 12	Scada Command
Modbus.Scada Cmd 13	Scada Command
Modbus.Scada Cmd 14	Scada Command
Modbus.Scada Cmd 15	Scada Command
Modbus.Scada Cmd 16	Scada Command
IEC 103.Scada Cmd 1	Scada Command
IEC 103.Scada Cmd 2	Scada Command
IEC 103.Scada Cmd 3	Scada Command
IEC 103.Scada Cmd 4	Scada Command
IEC 103.Scada Cmd 5	Scada Command

Page 554 EN MRM4 12/11

Name	Description
IEC 103.Scada Cmd 6	Scada Command
IEC 103.Scada Cmd 7	Scada Command
IEC 103.Scada Cmd 8	Scada Command
IEC 103.Scada Cmd 9	Scada Command
IEC 103.Scada Cmd 10	Scada Command
IEC 103.Transmission	Signal: SCADA active
IEC 103.Fail phy Interf	Failure in the physical interface
IEC 103.Failure Event lost	Failure event lost
Profibus.Data OK	Data within the Input field are OK (Yes=1)
Profibus.SubModul Err	Assignable Signal, Failure in Sub-Module, Communication Failure.
Profibus.Connection active	Connection active
Profibus.Scada Cmd 1	Scada Command
Profibus.Scada Cmd 2	Scada Command
Profibus.Scada Cmd 3	Scada Command
Profibus.Scada Cmd 4	Scada Command
Profibus.Scada Cmd 5	Scada Command
Profibus.Scada Cmd 6	Scada Command
Profibus.Scada Cmd 7	Scada Command
Profibus.Scada Cmd 8	Scada Command
Profibus.Scada Cmd 9	Scada Command
Profibus.Scada Cmd 10	Scada Command
Profibus.Scada Cmd 11	Scada Command
Profibus.Scada Cmd 12	Scada Command
Profibus.Scada Cmd 13	Scada Command
Profibus.Scada Cmd 14	Scada Command
Profibus.Scada Cmd 15	Scada Command
Profibus.Scada Cmd 16	Scada Command
IEC61850.VirtInp1	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp2	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp3	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp4	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp5	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp6	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp7	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp8	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp9	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp10	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp11	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp12	Signal: Virtual Input (IEC61850 GGIO Ind)

Name	Description
IEC61850.VirtInp13	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp14	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp15	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtInp16	Signal: Virtual Input (IEC61850 GGIO Ind)
IEC61850.VirtOut1-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut2-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut3-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut4-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut5-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut6-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut7-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut8-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut9-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut10-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut11-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut12-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut13-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut14-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut15-I	Module input state: Binary state of the Virtual Output (GGIO)
IEC61850.VirtOut16-I	Module input state: Binary state of the Virtual Output (GGIO)
IRIG-B.active	Signal: active
IRIG-B.inverted	Signal: IRIG-B inverted
IRIG-B.Control Signal1	Signal: IRIG-B Control Signal
IRIG-B.Control Signal2	Signal: IRIG-B Control Signal
IRIG-B.Control Signal4	Signal: IRIG-B Control Signal
IRIG-B.Control Signal5	Signal: IRIG-B Control Signal
IRIG-B.Control Signal6	Signal: IRIG-B Control Signal
IRIG-B.Control Signal7	Signal: IRIG-B Control Signal
IRIG-B.Control Signal8	Signal: IRIG-B Control Signal
IRIG-B.Control Signal9	Signal: IRIG-B Control Signal
IRIG-B.Control Signal10	Signal: IRIG-B Control Signal
IRIG-B.Control Signal11	Signal: IRIG-B Control Signal
IRIG-B.Control Signal12	Signal: IRIG-B Control Signal
IRIG-B.Control Signal13	Signal: IRIG-B Control Signal
IRIG-B.Control Signal14	Signal: IRIG-B Control Signal
IRIG-B.Control Signal15	Signal: IRIG-B Control Signal
IRIG-B.Control Signal16	Signal: IRIG-B Control Signal
IRIG-B.Control Signal16 IRIG-B.Control Signal17	Signal: IRIG-B Control Signal Signal: IRIG-B Control Signal

Page 556 EN MRM4 12/11

Name	Description
SNTP.SNTP active	Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.
Statistics.ResFc all	Signal: Resetting of all Statistic values (Current Demand, Power Demand, Min, Max)
Statistics.ResFc I Demand	Signal: Resetting of Statistics - Current Demand (avg, peak avg)
Statistics.ResFc P Demand	Signal: Resetting of Statistics - Power Demand (avg, peak avg)
Statistics.ResFc Max	Signal: Resetting of all Maximum values
Statistics.ResFc Min	Signal: Resetting of all Minimum values
Statistics.StartFc 1-I	State of the module input: Start of Statistics 1
Statistics.StartFc 2-I	State of the module input: Start of Statistics 2
Logic.LE1.Gate Out	Signal: Output of the logic gate
Logic.LE1.Timer Out	Signal: Timer Output
Logic.LE1.Out	Signal: Latched Output (Q)
Logic.LE1.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE1.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE1.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE1.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE1.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE1.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE2.Gate Out	Signal: Output of the logic gate
Logic.LE2.Timer Out	Signal: Timer Output
Logic.LE2.Out	Signal: Latched Output (Q)
Logic.LE2.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE2.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE2.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE2.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE2.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE2.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE3.Gate Out	Signal: Output of the logic gate
Logic.LE3.Timer Out	Signal: Timer Output
Logic.LE3.Out	Signal: Latched Output (Q)
Logic.LE3.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE3.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE3.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE3.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE3.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE3.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE4.Gate Out	Signal: Output of the logic gate
Logic.LE4.Timer Out	Signal: Timer Output
Logic.LE4.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE4.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE4.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE4.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE4.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE4.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE4.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE5.Gate Out	Signal: Output of the logic gate
Logic.LE5.Timer Out	Signal: Timer Output
Logic.LE5.Out	Signal: Latched Output (Q)
Logic.LE5.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE5.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE5.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE5.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE5.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE5.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE6.Gate Out	Signal: Output of the logic gate
Logic.LE6.Timer Out	Signal: Timer Output
Logic.LE6.Out	Signal: Latched Output (Q)
Logic.LE6.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE6.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE6.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE6.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE6.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE6.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE7.Gate Out	Signal: Output of the logic gate
Logic.LE7.Timer Out	Signal: Timer Output
Logic.LE7.Out	Signal: Latched Output (Q)
Logic.LE7.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE7.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE7.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE7.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE7.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE7.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE8.Gate Out	Signal: Output of the logic gate
Logic.LE8.Timer Out	Signal: Timer Output
Logic.LE8.Out	Signal: Latched Output (Q)
Logic.LE8.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE8.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE8.Gate In2-I	State of the module input: Assignment of the Input Signal

Page 558 EN MRM4 12/11

Name	Description
Logic.LE8.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE8.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE8.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE9.Gate Out	Signal: Output of the logic gate
Logic.LE9.Timer Out	Signal: Timer Output
Logic.LE9.Out	Signal: Latched Output (Q)
Logic.LE9.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE9.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE9.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE9.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE9.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE9.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE10.Gate Out	Signal: Output of the logic gate
Logic.LE10.Timer Out	Signal: Timer Output
Logic.LE10.Out	Signal: Latched Output (Q)
Logic.LE10.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE10.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE10.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE10.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE10.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE10.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE11.Gate Out	Signal: Output of the logic gate
Logic.LE11.Timer Out	Signal: Timer Output
Logic.LE11.Out	Signal: Latched Output (Q)
Logic.LE11.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE11.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE11.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE11.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE11.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE11.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE12.Gate Out	Signal: Output of the logic gate
Logic.LE12.Timer Out	Signal: Timer Output
Logic.LE12.Out	Signal: Latched Output (Q)
Logic.LE12.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE12.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE12.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE12.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE12.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE12.Reset Latch-I	State of the module input: Reset Signal for the Latching
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Name	Description
Logic.LE13.Gate Out	Signal: Output of the logic gate
Logic.LE13.Timer Out	Signal: Timer Output
Logic.LE13.Out	Signal: Latched Output (Q)
Logic.LE13.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE13.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE13.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE13.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE13.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE13.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE14.Gate Out	Signal: Output of the logic gate
Logic.LE14.Timer Out	Signal: Timer Output
Logic.LE14.Out	Signal: Latched Output (Q)
Logic.LE14.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE14.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE14.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE14.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE14.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE14.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE15.Gate Out	Signal: Output of the logic gate
Logic.LE15.Timer Out	Signal: Timer Output
Logic.LE15.Out	Signal: Latched Output (Q)
Logic.LE15.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE15.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE15.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE15.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE15.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE15.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE16.Gate Out	Signal: Output of the logic gate
Logic.LE16.Timer Out	Signal: Timer Output
Logic.LE16.Out	Signal: Latched Output (Q)
Logic.LE16.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE16.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE16.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE16.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE16.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE16.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE17.Gate Out	Signal: Output of the logic gate
Logic.LE17.Timer Out	Signal: Timer Output
Logic.LE17.Out	Signal: Latched Output (Q)

Page 560 EN MRM4 12/11

Name	Description
Logic.LE17.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE17.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE17.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE17.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE17.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE17.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE18.Gate Out	Signal: Output of the logic gate
Logic.LE18.Timer Out	Signal: Timer Output
Logic.LE18.Out	Signal: Latched Output (Q)
Logic.LE18.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE18.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE18.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE18.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE18.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE18.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE19.Gate Out	Signal: Output of the logic gate
Logic.LE19.Timer Out	Signal: Timer Output
Logic.LE19.Out	Signal: Latched Output (Q)
Logic.LE19.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE19.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE19.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE19.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE19.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE19.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE20.Gate Out	Signal: Output of the logic gate
Logic.LE20.Timer Out	Signal: Timer Output
Logic.LE20.Out	Signal: Latched Output (Q)
Logic.LE20.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE20.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE20.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE20.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE20.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE20.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE21.Gate Out	Signal: Output of the logic gate
Logic.LE21.Timer Out	Signal: Timer Output
Logic.LE21.Out	Signal: Latched Output (Q)
Logic.LE21.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE21.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE21.Gate In2-I	State of the module input: Assignment of the Input Signal

Name	Description
Logic.LE21.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE21.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE21.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE22.Gate Out	Signal: Output of the logic gate
Logic.LE22.Timer Out	Signal: Timer Output
Logic.LE22.Out	Signal: Latched Output (Q)
Logic.LE22.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE22.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE22.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE22.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE22.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE22.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE23.Gate Out	Signal: Output of the logic gate
Logic.LE23.Timer Out	Signal: Timer Output
Logic.LE23.Out	Signal: Latched Output (Q)
Logic.LE23.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE23.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE23.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE23.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE23.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE23.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE24.Gate Out	Signal: Output of the logic gate
Logic.LE24.Timer Out	Signal: Timer Output
Logic.LE24.Out	Signal: Latched Output (Q)
Logic.LE24.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE24.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE24.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE24.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE24.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE24.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE25.Gate Out	Signal: Output of the logic gate
Logic.LE25.Timer Out	Signal: Timer Output
Logic.LE25.Out	Signal: Latched Output (Q)
Logic.LE25.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE25.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE25.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE25.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE25.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE25.Reset Latch-I	State of the module input: Reset Signal for the Latching

Page 562 EN MRM4 12/11

Name	Description
Logic.LE26.Gate Out	Signal: Output of the logic gate
Logic.LE26.Timer Out	Signal: Timer Output
Logic.LE26.Out	Signal: Latched Output (Q)
Logic.LE26.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE26.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE26.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE26.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE26.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE26.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE27.Gate Out	Signal: Output of the logic gate
Logic.LE27.Timer Out	Signal: Timer Output
Logic.LE27.Out	Signal: Latched Output (Q)
Logic.LE27.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE27.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE27.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE27.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE27.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE27.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE28.Gate Out	Signal: Output of the logic gate
Logic.LE28.Timer Out	Signal: Timer Output
Logic.LE28.Out	Signal: Latched Output (Q)
Logic.LE28.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE28.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE28.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE28.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE28.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE28.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE29.Gate Out	Signal: Output of the logic gate
Logic.LE29.Timer Out	Signal: Timer Output
Logic.LE29.Out	Signal: Latched Output (Q)
Logic.LE29.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE29.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE29.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE29.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE29.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE29.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE30.Gate Out	Signal: Output of the logic gate
Logic.LE30.Timer Out	Signal: Timer Output
Logic.LE30.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE30.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE30.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE30.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE30.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE30.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE30.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE31.Gate Out	Signal: Output of the logic gate
Logic.LE31.Timer Out	Signal: Timer Output
Logic.LE31.Out	Signal: Latched Output (Q)
Logic.LE31.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE31.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE31.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE31.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE31.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE31.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE32.Gate Out	Signal: Output of the logic gate
Logic.LE32.Timer Out	Signal: Timer Output
Logic.LE32.Out	Signal: Latched Output (Q)
Logic.LE32.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE32.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE32.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE32.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE32.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE32.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE33.Gate Out	Signal: Output of the logic gate
Logic.LE33.Timer Out	Signal: Timer Output
Logic.LE33.Out	Signal: Latched Output (Q)
Logic.LE33.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE33.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE33.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE33.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE33.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE33.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE34.Gate Out	Signal: Output of the logic gate
Logic.LE34.Timer Out	Signal: Timer Output
Logic.LE34.Out	Signal: Latched Output (Q)
Logic.LE34.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE34.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE34.Gate In2-I	State of the module input: Assignment of the Input Signal

Page 564 EN MRM4 12/11

Name	Description
Logic.LE34.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE34.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE34.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE35.Gate Out	Signal: Output of the logic gate
Logic.LE35.Timer Out	Signal: Timer Output
Logic.LE35.Out	Signal: Latched Output (Q)
Logic.LE35.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE35.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE35.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE35.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE35.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE35.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE36.Gate Out	Signal: Output of the logic gate
Logic.LE36.Timer Out	Signal: Timer Output
Logic.LE36.Out	Signal: Latched Output (Q)
Logic.LE36.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE36.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE36.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE36.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE36.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE36.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE37.Gate Out	Signal: Output of the logic gate
Logic.LE37.Timer Out	Signal: Timer Output
Logic.LE37.Out	Signal: Latched Output (Q)
Logic.LE37.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE37.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE37.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE37.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE37.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE37.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE38.Gate Out	Signal: Output of the logic gate
Logic.LE38.Timer Out	Signal: Timer Output
Logic.LE38.Out	Signal: Latched Output (Q)
Logic.LE38.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE38.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE38.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE38.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE38.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE38.Reset Latch-I	State of the module input: Reset Signal for the Latching

Name	Description
Logic.LE39.Gate Out	Signal: Output of the logic gate
Logic.LE39.Timer Out	Signal: Timer Output
Logic.LE39.Out	Signal: Latched Output (Q)
Logic.LE39.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE39.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE39.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE39.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE39.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE39.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE40.Gate Out	Signal: Output of the logic gate
Logic.LE40.Timer Out	Signal: Timer Output
Logic.LE40.Out	Signal: Latched Output (Q)
Logic.LE40.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE40.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE40.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE40.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE40.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE40.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE41.Gate Out	Signal: Output of the logic gate
Logic.LE41.Timer Out	Signal: Timer Output
Logic.LE41.Out	Signal: Latched Output (Q)
Logic.LE41.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE41.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE41.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE41.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE41.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE41.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE42.Gate Out	Signal: Output of the logic gate
Logic.LE42.Timer Out	Signal: Timer Output
Logic.LE42.Out	Signal: Latched Output (Q)
Logic.LE42.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE42.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE42.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE42.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE42.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE42.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE43.Gate Out	Signal: Output of the logic gate
Logic.LE43.Timer Out	Signal: Timer Output
Logic.LE43.Out	Signal: Latched Output (Q)

Page 566 EN MRM4 12/11

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ogic.LE43.Out inverted Signal	ignal: Negated Latched Output (Q NOT)
ogic.LE43.Gate In1-I	tate of the module input: Assignment of the Input Signal
ogic.LE43.Gate In2-I	tate of the module input: Assignment of the Input Signal
ogic.LE43.Gate In3-I	tate of the module input: Assignment of the Input Signal
ogic.LE43.Gate In4-I	tate of the module input: Assignment of the Input Signal
ogic.LE43.Reset Latch-I	tate of the module input: Reset Signal for the Latching
ogic.LE44.Gate Out Signature	ignal: Output of the logic gate
ogic.LE44.Timer Out Si	ignal: Timer Output
ogic.LE44.Out Si	ignal: Latched Output (Q)
ogic.LE44.Out inverted Signal	ignal: Negated Latched Output (Q NOT)
ogic.LE44.Gate In1-I	tate of the module input: Assignment of the Input Signal
ogic.LE44.Gate In2-I	tate of the module input: Assignment of the Input Signal
ogic.LE44.Gate In3-I	tate of the module input: Assignment of the Input Signal
ogic.LE44.Gate In4-I	tate of the module input: Assignment of the Input Signal
ogic.LE44.Reset Latch-I	tate of the module input: Reset Signal for the Latching
ogic.LE45.Gate Out Si	ignal: Output of the logic gate
ogic.LE45.Timer Out Si	ignal: Timer Output
ogic.LE45.Out Si	ignal: Latched Output (Q)
ogic.LE45.Out inverted Signature	ignal: Negated Latched Output (Q NOT)
ogic.LE45.Gate In1-I	tate of the module input: Assignment of the Input Signal
ogic.LE45.Gate In2-I	tate of the module input: Assignment of the Input Signal
ogic.LE45.Gate In3-I	tate of the module input: Assignment of the Input Signal
ogic.LE45.Gate In4-I	tate of the module input: Assignment of the Input Signal
ogic.LE45.Reset Latch-I	tate of the module input: Reset Signal for the Latching
ogic.LE46.Gate Out Sig	ignal: Output of the logic gate
ogic.LE46.Timer Out Signature	ignal: Timer Output
ogic.LE46.Out Si	ignal: Latched Output (Q)
ogic.LE46.Out inverted Signature	ignal: Negated Latched Output (Q NOT)
ogic.LE46.Gate In1-I	tate of the module input: Assignment of the Input Signal
ogic.LE46.Gate In2-I	tate of the module input: Assignment of the Input Signal
ogic.LE46.Gate In3-I	tate of the module input: Assignment of the Input Signal
ogic.LE46.Gate In4-I	tate of the module input: Assignment of the Input Signal
ogic.LE46.Reset Latch-I	tate of the module input: Reset Signal for the Latching
ogic.LE47.Gate Out Sig	ignal: Output of the logic gate
ogic.LE47.Timer Out Sig	ignal: Timer Output
ogic.LE47.Out Sig	ignal: Latched Output (Q)
ogic.LE47.Out inverted Signature	ignal: Negated Latched Output (Q NOT)
ogic.LE47.Gate In1-I	tate of the module input: Assignment of the Input Signal
ogic.LE47.Gate In2-I	tate of the module input: Assignment of the Input Signal

Name	Description
Logic.LE47.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE47.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE47.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE48.Gate Out	Signal: Output of the logic gate
Logic.LE48.Timer Out	Signal: Timer Output
Logic.LE48.Out	Signal: Latched Output (Q)
Logic.LE48.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE48.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE48.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE48.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE48.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE48.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE49.Gate Out	Signal: Output of the logic gate
Logic.LE49.Timer Out	Signal: Timer Output
Logic.LE49.Out	Signal: Latched Output (Q)
Logic.LE49.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE49.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE49.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE49.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE49.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE49.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE50.Gate Out	Signal: Output of the logic gate
Logic.LE50.Timer Out	Signal: Timer Output
Logic.LE50.Out	Signal: Latched Output (Q)
Logic.LE50.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE50.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE50.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE50.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE50.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE50.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE51.Gate Out	Signal: Output of the logic gate
Logic.LE51.Timer Out	Signal: Timer Output
Logic.LE51.Out	Signal: Latched Output (Q)
Logic.LE51.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE51.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE51.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE51.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE51.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE51.Reset Latch-I	State of the module input: Reset Signal for the Latching

Page 568 EN MRM4 12/11

Name	Description
Logic.LE52.Gate Out	Signal: Output of the logic gate
Logic.LE52.Timer Out	Signal: Timer Output
Logic.LE52.Out	Signal: Latched Output (Q)
Logic.LE52.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE52.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE52.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE52.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE52.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE52.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE53.Gate Out	Signal: Output of the logic gate
Logic.LE53.Timer Out	Signal: Timer Output
Logic.LE53.Out	Signal: Latched Output (Q)
Logic.LE53.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE53.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE53.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE53.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE53.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE53.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE54.Gate Out	Signal: Output of the logic gate
Logic.LE54.Timer Out	Signal: Timer Output
Logic.LE54.Out	Signal: Latched Output (Q)
Logic.LE54.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE54.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE54.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE54.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE54.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE54.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE55.Gate Out	Signal: Output of the logic gate
Logic.LE55.Timer Out	Signal: Timer Output
Logic.LE55.Out	Signal: Latched Output (Q)
Logic.LE55.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE55.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE55.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE55.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE55.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE55.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE56.Gate Out	Signal: Output of the logic gate
Logic.LE56.Timer Out	Signal: Timer Output
Logic.LE56.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE56.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE56.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE56.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE56.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE56.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE56.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE57.Gate Out	Signal: Output of the logic gate
Logic.LE57.Timer Out	Signal: Timer Output
Logic.LE57.Out	Signal: Latched Output (Q)
Logic.LE57.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE57.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE57.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE57.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE57.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE57.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE58.Gate Out	Signal: Output of the logic gate
Logic.LE58.Timer Out	Signal: Timer Output
Logic.LE58.Out	Signal: Latched Output (Q)
Logic.LE58.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE58.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE58.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE58.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE58.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE58.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE59.Gate Out	Signal: Output of the logic gate
Logic.LE59.Timer Out	Signal: Timer Output
Logic.LE59.Out	Signal: Latched Output (Q)
Logic.LE59.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE59.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE59.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE59.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE59.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE59.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE60.Gate Out	Signal: Output of the logic gate
Logic.LE60.Timer Out	Signal: Timer Output
Logic.LE60.Out	Signal: Latched Output (Q)
Logic.LE60.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE60.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE60.Gate In2-I	State of the module input: Assignment of the Input Signal

Page 570 EN MRM4 12/11

Name	Description
Logic.LE60.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE60.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE60.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE61.Gate Out	Signal: Output of the logic gate
Logic.LE61.Timer Out	Signal: Timer Output
Logic.LE61.Out	Signal: Latched Output (Q)
Logic.LE61.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE61.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE61.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE61.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE61.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE61.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE62.Gate Out	Signal: Output of the logic gate
Logic.LE62.Timer Out	Signal: Timer Output
Logic.LE62.Out	Signal: Latched Output (Q)
Logic.LE62.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE62.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE62.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE62.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE62.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE62.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE63.Gate Out	Signal: Output of the logic gate
Logic.LE63.Timer Out	Signal: Timer Output
Logic.LE63.Out	Signal: Latched Output (Q)
Logic.LE63.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE63.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE63.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE63.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE63.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE63.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE64.Gate Out	Signal: Output of the logic gate
Logic.LE64.Timer Out	Signal: Timer Output
Logic.LE64.Out	Signal: Latched Output (Q)
Logic.LE64.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE64.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE64.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE64.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE64.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE64.Reset Latch-I	State of the module input: Reset Signal for the Latching

Name	Description
Logic.LE65.Gate Out	Signal: Output of the logic gate
Logic.LE65.Timer Out	Signal: Timer Output
Logic.LE65.Out	Signal: Latched Output (Q)
Logic.LE65.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE65.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE65.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE65.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE65.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE65.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE66.Gate Out	Signal: Output of the logic gate
Logic.LE66.Timer Out	Signal: Timer Output
Logic.LE66.Out	Signal: Latched Output (Q)
Logic.LE66.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE66.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE66.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE66.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE66.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE66.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE67.Gate Out	Signal: Output of the logic gate
Logic.LE67.Timer Out	Signal: Timer Output
Logic.LE67.Out	Signal: Latched Output (Q)
Logic.LE67.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE67.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE67.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE67.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE67.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE67.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE68.Gate Out	Signal: Output of the logic gate
Logic.LE68.Timer Out	Signal: Timer Output
Logic.LE68.Out	Signal: Latched Output (Q)
Logic.LE68.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE68.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE68.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE68.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE68.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE68.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE69.Gate Out	Signal: Output of the logic gate
Logic.LE69.Timer Out	Signal: Timer Output
Logic.LE69.Out	Signal: Latched Output (Q)

Page 572 EN MRM4 12/11

Name	Description
Logic.LE69.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE69.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE69.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE69.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE69.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE69.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE70.Gate Out	Signal: Output of the logic gate
Logic.LE70.Timer Out	Signal: Timer Output
Logic.LE70.Out	Signal: Latched Output (Q)
Logic.LE70.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE70.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE70.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE70.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE70.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE70.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE71.Gate Out	Signal: Output of the logic gate
Logic.LE71.Timer Out	Signal: Timer Output
Logic.LE71.Out	Signal: Latched Output (Q)
Logic.LE71.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE71.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE71.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE71.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE71.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE71.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE72.Gate Out	Signal: Output of the logic gate
Logic.LE72.Timer Out	Signal: Timer Output
Logic.LE72.Out	Signal: Latched Output (Q)
Logic.LE72.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE72.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE72.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE72.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE72.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE72.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE73.Gate Out	Signal: Output of the logic gate
Logic.LE73.Timer Out	Signal: Timer Output
Logic.LE73.Out	Signal: Latched Output (Q)
Logic.LE73.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE73.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE73.Gate In2-I	State of the module input: Assignment of the Input Signal

Name	Description
Logic.LE73.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE73.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE73.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE74.Gate Out	Signal: Output of the logic gate
Logic.LE74.Timer Out	Signal: Timer Output
Logic.LE74.Out	Signal: Latched Output (Q)
Logic.LE74.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE74.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE74.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE74.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE74.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE74.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE75.Gate Out	Signal: Output of the logic gate
Logic.LE75.Timer Out	Signal: Timer Output
Logic.LE75.Out	Signal: Latched Output (Q)
Logic.LE75.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE75.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE75.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE75.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE75.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE75.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE76.Gate Out	Signal: Output of the logic gate
Logic.LE76.Timer Out	Signal: Timer Output
Logic.LE76.Out	Signal: Latched Output (Q)
Logic.LE76.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE76.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE76.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE76.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE76.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE76.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE77.Gate Out	Signal: Output of the logic gate
Logic.LE77.Timer Out	Signal: Timer Output
Logic.LE77.Out	Signal: Latched Output (Q)
Logic.LE77.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE77.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE77.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE77.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE77.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE77.Reset Latch-I	State of the module input: Reset Signal for the Latching

Page 574 EN MRM4 12/11

Name	Description
Logic.LE78.Gate Out	Signal: Output of the logic gate
Logic.LE78.Timer Out	Signal: Timer Output
Logic.LE78.Out	Signal: Latched Output (Q)
Logic.LE78.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE78.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE78.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE78.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE78.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE78.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE79.Gate Out	Signal: Output of the logic gate
Logic.LE79.Timer Out	Signal: Timer Output
Logic.LE79.Out	Signal: Latched Output (Q)
Logic.LE79.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE79.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE79.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE79.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE79.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE79.Reset Latch-I	State of the module input: Reset Signal for the Latching
Logic.LE80.Gate Out	Signal: Output of the logic gate
Logic.LE80.Timer Out	Signal: Timer Output
Logic.LE80.Out	Signal: Latched Output (Q)
Logic.LE80.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE80.Gate In1-I	State of the module input: Assignment of the Input Signal
Logic.LE80.Gate In2-I	State of the module input: Assignment of the Input Signal
Logic.LE80.Gate In3-I	State of the module input: Assignment of the Input Signal
Logic.LE80.Gate In4-I	State of the module input: Assignment of the Input Signal
Logic.LE80.Reset Latch-I	State of the module input: Reset Signal for the Latching
Sgen.Running	Signal; Measuring value simulation is running
Sgen.ExBlo	Module input state: External blocking
Sgen.Ex ForcePost-I	State of the module input:Force Post state. Abort simulation.
Sys.PS 1	Signal: Parameter Set 1
Sys.PS 2	Signal: Parameter Set 2
Sys.PS 3	Signal: Parameter Set 3
Sys.PS 4	Signal: Parameter Set 4
Sys.PSS manual	Signal: Manual Switch over of a Parameter Set
Sys.PSS via Scada	Signal: Parameter Set Switch via Scada
Sys.PSS via Inp fct	Signal: Parameter Set Switch via input function
Sys.min 1 param changed	Signal: At least one parameter has been changed
Sys.Ack LED	Signal: LEDs acknowledgment

Name	Description
Sys.Ack BO	Signal: Acknowledgment of the Binary Outputs
Sys.Ack Scada	Signal: Acknowledge Scada
Sys.Ack TripCmd	Signal: Reset Trip Command
Sys.Ack LED-HMI	Signal: LEDs acknowledgment :HMI
Sys.Ack BO-HMI	Signal: Acknowledgment of the Binary Outputs :HMI
Sys.Ack Scada-HMI	Signal: Acknowledge Scada :HMI
Sys.Ack TripCmd-HMI	Signal: Reset Trip Command :HMI
Sys.Ack LED-Sca	Signal: LEDs acknowledgment :SCADA
Sys.Ack BO-Sca	Signal: Acknowledgment of the Binary Outputs :SCADA
Sys.Ack Counter-Sca	Signal: Reset of all Counters :SCADA
Sys.Ack Scada-Sca	Signal: Acknowledge Scada :SCADA
Sys.Ack TripCmd-Sca	Signal: Reset Trip Command :SCADA
Sys.Res OperationsCr	Signal:: Res OperationsCr
Sys.Res AlarmCr	Signal:: Res AlarmCr
Sys.Res TripCr	Signal:: Res TripCr
Sys.Res TotalCr	Signal:: Res TotalCr
Sys.Ack LED-I	Module input state: LEDs acknowledgment by digital input
Sys.Ack BO-I	Module input state: Acknowledgment of the binary Relay Output Contacts
Sys.Ack Scada-I	Module input state: Acknowledge Scada via digital input. The replica that SCADA has got from the device is to be reset.
Sys.PS1-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
Sys.PS2-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
Sys.PS3-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
Sys.PS4-I	State of the module input respectively of the signal, that should activate this Parameter Setting Group.

Signals of the Digital Inputs and Logic

The following list comprises the signals of the Digital Inputs and the Logic. This list is used in various protective elements.

Name	Description
	No assignment
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 1	Signal: Digital Input
DI Slot X1.DI 2	Signal: Digital Input

Page 576 EN MRM4 12/11

Name	Description
DI Slot X1.DI 3	Signal: Digital Input
DI Slot X1.DI 4	Signal: Digital Input
DI Slot X1.DI 5	Signal: Digital Input
DI Slot X1.DI 6	Signal: Digital Input
DI Slot X1.DI 7	Signal: Digital Input
DI Slot X1.DI 8	Signal: Digital Input
Logic.LE1.Gate Out	Signal: Output of the logic gate
Logic.LE1.Timer Out	Signal: Timer Output
Logic.LE1.Out	Signal: Latched Output (Q)
Logic.LE1.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE2.Gate Out	Signal: Output of the logic gate
Logic.LE2.Timer Out	Signal: Timer Output
Logic.LE2.Out	Signal: Latched Output (Q)
Logic.LE2.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE3.Gate Out	Signal: Output of the logic gate
Logic.LE3.Timer Out	Signal: Timer Output
Logic.LE3.Out	Signal: Latched Output (Q)
Logic.LE3.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE4.Gate Out	Signal: Output of the logic gate
Logic.LE4.Timer Out	Signal: Timer Output
Logic.LE4.Out	Signal: Latched Output (Q)
Logic.LE4.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE5.Gate Out	Signal: Output of the logic gate
Logic.LE5.Timer Out	Signal: Timer Output
Logic.LE5.Out	Signal: Latched Output (Q)
Logic.LE5.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE6.Gate Out	Signal: Output of the logic gate
Logic.LE6.Timer Out	Signal: Timer Output
Logic.LE6.Out	Signal: Latched Output (Q)
Logic.LE6.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE7.Gate Out	Signal: Output of the logic gate
Logic.LE7.Timer Out	Signal: Timer Output
Logic.LE7.Out	Signal: Latched Output (Q)
Logic.LE7.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE8.Gate Out	Signal: Output of the logic gate
Logic.LE8.Timer Out	Signal: Timer Output
Logic.LE8.Out	Signal: Latched Output (Q)
Logic.LE8.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE9.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE9.Timer Out	Signal: Timer Output
Logic.LE9.Out	Signal: Latched Output (Q)
Logic.LE9.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE10.Gate Out	Signal: Output of the logic gate
Logic.LE10.Timer Out	Signal: Timer Output
Logic.LE10.Out	Signal: Latched Output (Q)
Logic.LE10.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE11.Gate Out	Signal: Output of the logic gate
Logic.LE11.Timer Out	Signal: Timer Output
Logic.LE11.Out	Signal: Latched Output (Q)
Logic.LE11.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE12.Gate Out	Signal: Output of the logic gate
Logic.LE12.Timer Out	Signal: Timer Output
Logic.LE12.Out	Signal: Latched Output (Q)
Logic.LE12.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE13.Gate Out	Signal: Output of the logic gate
Logic.LE13.Timer Out	Signal: Timer Output
Logic.LE13.Out	Signal: Latched Output (Q)
Logic.LE13.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE14.Gate Out	Signal: Output of the logic gate
Logic.LE14.Timer Out	Signal: Timer Output
Logic.LE14.Out	Signal: Latched Output (Q)
Logic.LE14.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE15.Gate Out	Signal: Output of the logic gate
Logic.LE15.Timer Out	Signal: Timer Output
Logic.LE15.Out	Signal: Latched Output (Q)
Logic.LE15.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE16.Gate Out	Signal: Output of the logic gate
Logic.LE16.Timer Out	Signal: Timer Output
Logic.LE16.Out	Signal: Latched Output (Q)
Logic.LE16.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE17.Gate Out	Signal: Output of the logic gate
Logic.LE17.Timer Out	Signal: Timer Output
Logic.LE17.Out	Signal: Latched Output (Q)
Logic.LE17.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE18.Gate Out	Signal: Output of the logic gate
Logic.LE18.Timer Out	Signal: Timer Output
Logic.LE18.Out	Signal: Latched Output (Q)
Logic.LE18.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 578 EN MRM4 12/11

Name	Description
Logic.LE19.Gate Out	Signal: Output of the logic gate
Logic.LE19.Timer Out	Signal: Timer Output
Logic.LE19.Out	Signal: Latched Output (Q)
Logic.LE19.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE20.Gate Out	Signal: Output of the logic gate
Logic.LE20.Timer Out	Signal: Timer Output
Logic.LE20.Out	Signal: Latched Output (Q)
Logic.LE20.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE21.Gate Out	Signal: Output of the logic gate
Logic.LE21.Timer Out	Signal: Timer Output
Logic.LE21.Out	Signal: Latched Output (Q)
Logic.LE21.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE22.Gate Out	Signal: Output of the logic gate
Logic.LE22.Timer Out	Signal: Timer Output
Logic.LE22.Out	Signal: Latched Output (Q)
Logic.LE22.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE23.Gate Out	Signal: Output of the logic gate
Logic.LE23.Timer Out	Signal: Timer Output
Logic.LE23.Out	Signal: Latched Output (Q)
Logic.LE23.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE24.Gate Out	Signal: Output of the logic gate
Logic.LE24.Timer Out	Signal: Timer Output
Logic.LE24.Out	Signal: Latched Output (Q)
Logic.LE24.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE25.Gate Out	Signal: Output of the logic gate
Logic.LE25.Timer Out	Signal: Timer Output
Logic.LE25.Out	Signal: Latched Output (Q)
Logic.LE25.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE26.Gate Out	Signal: Output of the logic gate
Logic.LE26.Timer Out	Signal: Timer Output
Logic.LE26.Out	Signal: Latched Output (Q)
Logic.LE26.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE27.Gate Out	Signal: Output of the logic gate
Logic.LE27.Timer Out	Signal: Timer Output
Logic.LE27.Out	Signal: Latched Output (Q)
Logic.LE27.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE28.Gate Out	Signal: Output of the logic gate
Logic.LE28.Timer Out	Signal: Timer Output
Logic.LE28.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE28.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE29.Gate Out	Signal: Output of the logic gate
Logic.LE29.Timer Out	Signal: Timer Output
Logic.LE29.Out	Signal: Latched Output (Q)
Logic.LE29.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE30.Gate Out	Signal: Output of the logic gate
Logic.LE30.Timer Out	Signal: Timer Output
Logic.LE30.Out	Signal: Latched Output (Q)
Logic.LE30.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE31.Gate Out	Signal: Output of the logic gate
Logic.LE31.Timer Out	Signal: Timer Output
Logic.LE31.Out	Signal: Latched Output (Q)
Logic.LE31.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE32.Gate Out	Signal: Output of the logic gate
Logic.LE32.Timer Out	Signal: Timer Output
Logic.LE32.Out	Signal: Latched Output (Q)
Logic.LE32.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE33.Gate Out	Signal: Output of the logic gate
Logic.LE33.Timer Out	Signal: Timer Output
Logic.LE33.Out	Signal: Latched Output (Q)
Logic.LE33.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE34.Gate Out	Signal: Output of the logic gate
Logic.LE34.Timer Out	Signal: Timer Output
Logic.LE34.Out	Signal: Latched Output (Q)
Logic.LE34.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE35.Gate Out	Signal: Output of the logic gate
Logic.LE35.Timer Out	Signal: Timer Output
Logic.LE35.Out	Signal: Latched Output (Q)
Logic.LE35.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE36.Gate Out	Signal: Output of the logic gate
Logic.LE36.Timer Out	Signal: Timer Output
Logic.LE36.Out	Signal: Latched Output (Q)
Logic.LE36.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE37.Gate Out	Signal: Output of the logic gate
Logic.LE37.Timer Out	Signal: Timer Output
Logic.LE37.Out	Signal: Latched Output (Q)
Logic.LE37.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE38.Gate Out	Signal: Output of the logic gate
Logic.LE38.Timer Out	Signal: Timer Output

Page 580 EN MRM4 12/11

Name	Description
Logic.LE38.Out	Signal: Latched Output (Q)
Logic.LE38.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE39.Gate Out	Signal: Output of the logic gate
Logic.LE39.Timer Out	Signal: Timer Output
Logic.LE39.Out	Signal: Latched Output (Q)
Logic.LE39.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE40.Gate Out	Signal: Output of the logic gate
Logic.LE40.Timer Out	Signal: Timer Output
Logic.LE40.Out	Signal: Latched Output (Q)
Logic.LE40.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE41.Gate Out	Signal: Output of the logic gate
Logic.LE41.Timer Out	Signal: Timer Output
Logic.LE41.Out	Signal: Latched Output (Q)
Logic.LE41.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE42.Gate Out	Signal: Output of the logic gate
Logic.LE42.Timer Out	Signal: Timer Output
Logic.LE42.Out	Signal: Latched Output (Q)
Logic.LE42.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE43.Gate Out	Signal: Output of the logic gate
Logic.LE43.Timer Out	Signal: Timer Output
Logic.LE43.Out	Signal: Latched Output (Q)
Logic.LE43.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE44.Gate Out	Signal: Output of the logic gate
Logic.LE44.Timer Out	Signal: Timer Output
Logic.LE44.Out	Signal: Latched Output (Q)
Logic.LE44.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE45.Gate Out	Signal: Output of the logic gate
Logic.LE45.Timer Out	Signal: Timer Output
Logic.LE45.Out	Signal: Latched Output (Q)
Logic.LE45.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE46.Gate Out	Signal: Output of the logic gate
Logic.LE46.Timer Out	Signal: Timer Output
Logic.LE46.Out	Signal: Latched Output (Q)
Logic.LE46.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE47.Gate Out	Signal: Output of the logic gate
Logic.LE47.Timer Out	Signal: Timer Output
Logic.LE47.Out	Signal: Latched Output (Q)
Logic.LE47.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE48.Gate Out	Signal: Output of the logic gate

Name	Description
Logic.LE48.Timer Out	Signal: Timer Output
Logic.LE48.Out	Signal: Latched Output (Q)
Logic.LE48.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE49.Gate Out	Signal: Output of the logic gate
Logic.LE49.Timer Out	Signal: Timer Output
Logic.LE49.Out	Signal: Latched Output (Q)
Logic.LE49.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE50.Gate Out	Signal: Output of the logic gate
Logic.LE50.Timer Out	Signal: Timer Output
Logic.LE50.Out	Signal: Latched Output (Q)
Logic.LE50.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE51.Gate Out	Signal: Output of the logic gate
Logic.LE51.Timer Out	Signal: Timer Output
Logic.LE51.Out	Signal: Latched Output (Q)
Logic.LE51.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE52.Gate Out	Signal: Output of the logic gate
Logic.LE52.Timer Out	Signal: Timer Output
Logic.LE52.Out	Signal: Latched Output (Q)
Logic.LE52.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE53.Gate Out	Signal: Output of the logic gate
Logic.LE53.Timer Out	Signal: Timer Output
Logic.LE53.Out	Signal: Latched Output (Q)
Logic.LE53.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE54.Gate Out	Signal: Output of the logic gate
Logic.LE54.Timer Out	Signal: Timer Output
Logic.LE54.Out	Signal: Latched Output (Q)
Logic.LE54.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE55.Gate Out	Signal: Output of the logic gate
Logic.LE55.Timer Out	Signal: Timer Output
Logic.LE55.Out	Signal: Latched Output (Q)
Logic.LE55.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE56.Gate Out	Signal: Output of the logic gate
Logic.LE56.Timer Out	Signal: Timer Output
Logic.LE56.Out	Signal: Latched Output (Q)
Logic.LE56.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE57.Gate Out	Signal: Output of the logic gate
Logic.LE57.Timer Out	Signal: Timer Output
Logic.LE57.Out	Signal: Latched Output (Q)
Logic.LE57.Out inverted	Signal: Negated Latched Output (Q NOT)

Page 582 EN MRM4 12/11

Name	Description
Logic.LE58.Gate Out	Signal: Output of the logic gate
Logic.LE58.Timer Out	Signal: Timer Output
Logic.LE58.Out	Signal: Latched Output (Q)
Logic.LE58.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE59.Gate Out	Signal: Output of the logic gate
Logic.LE59.Timer Out	Signal: Timer Output
Logic.LE59.Out	Signal: Latched Output (Q)
Logic.LE59.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE60.Gate Out	Signal: Output of the logic gate
Logic.LE60.Timer Out	Signal: Timer Output
Logic.LE60.Out	Signal: Latched Output (Q)
Logic.LE60.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE61.Gate Out	Signal: Output of the logic gate
Logic.LE61.Timer Out	Signal: Timer Output
Logic.LE61.Out	Signal: Latched Output (Q)
Logic.LE61.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE62.Gate Out	Signal: Output of the logic gate
Logic.LE62.Timer Out	Signal: Timer Output
Logic.LE62.Out	Signal: Latched Output (Q)
Logic.LE62.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE63.Gate Out	Signal: Output of the logic gate
Logic.LE63.Timer Out	Signal: Timer Output
Logic.LE63.Out	Signal: Latched Output (Q)
Logic.LE63.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE64.Gate Out	Signal: Output of the logic gate
Logic.LE64.Timer Out	Signal: Timer Output
Logic.LE64.Out	Signal: Latched Output (Q)
Logic.LE64.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE65.Gate Out	Signal: Output of the logic gate
Logic.LE65.Timer Out	Signal: Timer Output
Logic.LE65.Out	Signal: Latched Output (Q)
Logic.LE65.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE66.Gate Out	Signal: Output of the logic gate
Logic.LE66.Timer Out	Signal: Timer Output
Logic.LE66.Out	Signal: Latched Output (Q)
Logic.LE66.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE67.Gate Out	Signal: Output of the logic gate
Logic.LE67.Timer Out	Signal: Timer Output
Logic.LE67.Out	Signal: Latched Output (Q)

Name	Description
Logic.LE67.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE68.Gate Out	Signal: Output of the logic gate
Logic.LE68.Timer Out	Signal: Timer Output
Logic.LE68.Out	Signal: Latched Output (Q)
Logic.LE68.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE69.Gate Out	Signal: Output of the logic gate
Logic.LE69.Timer Out	Signal: Timer Output
Logic.LE69.Out	Signal: Latched Output (Q)
Logic.LE69.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE70.Gate Out	Signal: Output of the logic gate
Logic.LE70.Timer Out	Signal: Timer Output
Logic.LE70.Out	Signal: Latched Output (Q)
Logic.LE70.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE71.Gate Out	Signal: Output of the logic gate
Logic.LE71.Timer Out	Signal: Timer Output
Logic.LE71.Out	Signal: Latched Output (Q)
Logic.LE71.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE72.Gate Out	Signal: Output of the logic gate
Logic.LE72.Timer Out	Signal: Timer Output
Logic.LE72.Out	Signal: Latched Output (Q)
Logic.LE72.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE73.Gate Out	Signal: Output of the logic gate
Logic.LE73.Timer Out	Signal: Timer Output
Logic.LE73.Out	Signal: Latched Output (Q)
Logic.LE73.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE74.Gate Out	Signal: Output of the logic gate
Logic.LE74.Timer Out	Signal: Timer Output
Logic.LE74.Out	Signal: Latched Output (Q)
Logic.LE74.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE75.Gate Out	Signal: Output of the logic gate
Logic.LE75.Timer Out	Signal: Timer Output
Logic.LE75.Out	Signal: Latched Output (Q)
Logic.LE75.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE76.Gate Out	Signal: Output of the logic gate
Logic.LE76.Timer Out	Signal: Timer Output
Logic.LE76.Out	Signal: Latched Output (Q)
Logic.LE76.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE77.Gate Out	Signal: Output of the logic gate
Logic.LE77.Timer Out	Signal: Timer Output

Page 584 EN MRM4 12/11

Name	Description
Logic.LE77.Out	Signal: Latched Output (Q)
Logic.LE77.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE78.Gate Out	Signal: Output of the logic gate
Logic.LE78.Timer Out	Signal: Timer Output
Logic.LE78.Out	Signal: Latched Output (Q)
Logic.LE78.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE79.Gate Out	Signal: Output of the logic gate
Logic.LE79.Timer Out	Signal: Timer Output
Logic.LE79.Out	Signal: Latched Output (Q)
Logic.LE79.Out inverted	Signal: Negated Latched Output (Q NOT)
Logic.LE80.Gate Out	Signal: Output of the logic gate
Logic.LE80.Timer Out	Signal: Timer Output
Logic.LE80.Out	Signal: Latched Output (Q)
Logic.LE80.Out inverted	Signal: Negated Latched Output (Q NOT)

Notes:

Page 586 EN MRM4 12/11

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