



**MRI4 – Modbus  
HighPROTEC**

Data point list

**Manual DOK-TD-MRI4MDE**

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This Manual is valid for version (applies for Modbus RTU and Modbus TCP):

Version 2.2.c

Build: 19707

## Modbus Parameters

For the Modbus Protocol several parameters have to be set which are relevant for the communication between the control system (SCADA) and the device. The parameters and their setting possibilities or value ranges are shown in the table below.



**ATTENTION!**

The Parameters are described within the appendix of the device manual (chapter Modbus).

## Notes for the SCADA-System

When using Modbus RTU the following times have to be considered by the control system and are fixed within the device :  
The dwell times ( $t_D$ ) before start of a telegram must at least be set to 3.5 characters.

Examples:

3.5 characters 9600 Baud = 4 ms

3.5 characters 19200 Baud = 2 ms

3.6 3.5 characters 38400 Baud = 1 ms

Start of a new telegram is expected when the dwell time ( $t_D$ ) is  $> 3.5$  characters.

The fact that the probability of disruptions during transmission of a telegram increases with its length has to be taken into duly consideration and thus a query to the Slave should be possibly such that the response telegram is not much longer than 32 Byte.

## Specific Modbus Function Codes

For reading out data from the device or to carry out commands, the services listed in the table, also called »Function Codes«, are supported.

Function-code	Designation	Description
3	Read Holding Registers	There are single or several data words read as from a specific data word address. Only status addresses and parameter addresses can be read.
4	Read Input Registers	There are single or several data words read as from a specific data word address. Only measuring values can be read.
5	Write single Output (Bit)	All other values are illegal and will not affect the output. Via this function code acknowledgments can be executed as well as counters reseted or blockings set.
8	Loopback Test	Test function for the communication system
16	Load Multiple Registers	There are single or several data words written as from a specific data word address.

Table 3.1: function codes

On the following pages the Modbus functions are described in detail:

**Function-Code 3/4:**

Query

Slave address	3/4	Register address HI	Register address LO	Register number HI	Register number LO	Check-sum HI	Check-sum LO
---------------	-----	---------------------	---------------------	--------------------	--------------------	--------------	--------------

Response

Slave address	3/4	Byte number	Register 0 HI	Register 0 LO	...	Check-sum HI	Check-sum LO
---------------	-----	-------------	---------------	---------------	-----	--------------	--------------

Register address (HI\*256 + LO)

The data word address from where reading should start.

Register number (HI\*256 + LO)

Number of data words to be read. Valid range: 1..125

Byte number

Number of subsequent Bytes containing data words.

Register

Data words read out of the device (Highbyte and Lowbyte).

**Function Code 5:**

Query

Slave address	5	Register address HI	Register address LO	Register data HI	Register data LO	Check-sum HI	Check-sum LO
---------------	---	---------------------	---------------------	------------------	------------------	--------------	--------------

Response

Slave address	5	Register address HI	Register address LO	Register data HI	Register data LO	Check-sum HI	Check-sum LO
---------------	---	---------------------	---------------------	------------------	------------------	--------------	--------------

Register address (HI\*256 + LO)  
Data word address to be written

Register data  
Value of the data word to be written (Highbyte and Lowbyte).

Permitted value range :

FF00 hex request for a single bit to be on: This often means to reset a counter, execute acknowledgments or set blockings signals.  
0000 hex request for a single bit to be off: This often means to deactivate blocking signals or to reset single bits.

**Function Code 8:**

Query

Slave address	8	Data Diag Code HI 0x00	Data Diag Code LO 0x00	Test data	Test data	Check-sum HI	Check-sum LO
---------------	---	---------------------------	---------------------------	-----------	-----------	--------------	--------------

Response

Slave address	8	Data Diag Code HI	Data Diag Code LO	Test data	Test data	Check-sum HI	Check-sum LO
---------------	---	-------------------	-------------------	-----------	-----------	--------------	--------------

Data Diag Code HI (high), Data Diag Code LO ( Low)  
Diagnostic Code (subfunction code of function code 8) for testing the communication system. The Diagnostic Code „Return Query Data“ (0x00, 0x00) is being supported.

Test Data

By using the Diagnostic Code 0x00 0x00, the transmitted data is sent back to the Master unchanged.

**Function Code 16:**

Query

Slave address	16	Register address HI	Register address LO	Register number HI	Register number LO	Byte number	Register 0 HI	Register 0 LO	...	Check-sum HI	Check-sum LO
---------------	----	---------------------	---------------------	--------------------	--------------------	-------------	---------------	---------------	-----	--------------	--------------

Response

Slave address	16	Register address HI	Register address LO	Register number HI	Register number LO	Check-sum HI	Check-sum LO
---------------	----	---------------------	---------------------	--------------------	--------------------	--------------	--------------

Register address (HI\*256 + LO)

Data word address as from where writing should start.

Register number (HI\*256 + LO)

Query: Number of data words to be written. Valid range: 1..123

Response: Number of data words written.

Byte number

Number of subsequent Bytes to contain data words.

Register

Data words read out of the device (Highbyte und Lowbyte).



## Setting Date and Time

Date and time can be set by means of function code 16 and read with function code 3. If the device address 0 (broadcast address) is selected, the times of all devices connected to this bus are simultaneously reset. The devices do not respond to a broadcast command.

## Supported MODBUS- Error Messages

Exception Response Telegrams are described within the general "Modbus Application Protocol Specification". An exception response table with examples is shown there. The table below contains just the actually used codes. In case the device has recognized an error it will react in the following way:

Exception Code	Designation	Description
1	Illegal Function	The message received includes a function code which is not supported by the Slave.
2	Illegal Data Address	Access was sought on a data word address not included in the data module.
3	Illegal Data Value	The received message contains an invalid data structure (e.g. wrong number of data bytes).
4	Slave Device Failure	An unrecoverable error occurred while the server (or slave) was attempting to perform the requested action.

The response given by the *device* in a failure case has the following format:

Slave Address	0x80 + Function Code	Exception Code	Check-sum HI	Check-sum LO
---------------	----------------------	----------------	--------------	--------------

In the second Byte of the response the Function Code is sent with the highest Bit set to 1. This is equivalent to an addition by 0x80. The third Byte holds the Exception Code of the error message.

## Appendix - Data Point Lists

### Signals

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
AR		46	1	3	Struct			
	active	46	1	3	Bit	0x1 (1)	-	Signal: active
	ExBlo	46	1	3	Bit	0x2 (2)	-	Signal: External Blocking
	running	46	1	3	Bit	0x8 (4)	-	Signal: Auto Reclosing running
	t-dead	46	1	3	Bit	0x10 (5)	-	Signal: Dead time between trip and reclosure attempt
	successful (*)	46	1	3	Bit	0x100 (9)	-	Signal: Auto Reclosing successful
	failed (*)	46	1	3	Bit	0x200 (10)	-	Signal: Auto Reclosing failure
	t-AR Supervision	46	1	3	Bit	0x1000 (13)	-	Signal: AR Supervision
AR		47	1	3	Struct			
	ExBlo1-I	47	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo2-I	47	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	CB ON Cmd	47	1	3	Bit	0x10 (5)	-	Signal: CB switch ON Command
	Pre Shot (*)	47	1	3	Bit	0x20 (6)	-	Pre Shot Control
	Shot 1 (*)	47	1	3	Bit	0x40 (7)	-	Shot Control
	Shot 2 (*)	47	1	3	Bit	0x80 (8)	-	Shot Control
	Shot 3 (*)	47	1	3	Bit	0x100 (9)	-	Shot Control
	Shot 4 (*)	47	1	3	Bit	0x200 (10)	-	Shot Control
	Shot 5 (*)	47	1	3	Bit	0x400 (11)	-	Shot Control
	Shot 6 (*)	47	1	3	Bit	0x800 (12)	-	Shot Control
AR		156	1	3	Struct			
	Ex Lock-I	156	1	3	Bit	0x1 (1)	-	Module input state: External AR lockout.

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Ex Shot Inc-l	156	1	3	Bit	0x2 (2)	-	Module input state: The AR Shot counter will be incremented by this external Signal. This can be used for Zone Coordination (of upstream Auto Reclosure devices). Note: This parameter enables the functionality only. The assignment has to be set within the global parameters.
	Blo	156	1	3	Bit	0x4 (3)	-	Signal: Auto Reclosure is blocked
	t-Blo after CB man ON	156	1	3	Bit	0x8 (4)	-	Signal: AR blocked after circuit breaker was switched on manually. This timer will be started if the circuit breaker was switched on manually. While this timer is running, AR cannot be started.
	Lock	156	1	3	Bit	0x10 (5)	-	Signal: Auto Reclosure is locked out
	t-Reset Lockout	156	1	3	Bit	0x20 (6)	-	Signal: Delay Timer for resetting the AR lockout. The reset of the AR lockout state will be delayed for this time, after the reset signal (e.g digital input or Scada) has been detected .
	Ready	156	1	3	Bit	0x40 (7)	-	Signal: Ready to shoot

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	t-Run2Ready	156	1	3	Bit	0x80 (8)	-	Signal: Examination Time: If the Circuit Breaker remains after a reclosure attempt for the duration of this timer in the Closed position, the AR has been successful and the AR module returns into the ready state.
	Standby	156	1	3	Bit	0x100 (9)	-	Signal: Standby
	Service Alarm 1	156	1	3	Bit	0x200 (10)	-	Signal: AR - Service Alarm 1, too many switching operations
	Service Alarm 2	156	1	3	Bit	0x400 (11)	-	Signal: AR - Service Alarm 2 - too many switching operations
	Max Shots / h exceeded	156	1	3	Bit	0x800 (12)	-	Signal: The maximum allowed number of shots per hour has been exceeded.
BO Slot X2		1003	1	3	Struct			
	BO 1	1003	1	3	Bit	0x1 (1)	-	Signal: Binary Output Relay
	BO 2	1003	1	3	Bit	0x2 (2)	-	Signal: Binary Output Relay
	BO 3	1003	1	3	Bit	0x4 (3)	-	Signal: Binary Output Relay
	BO 4	1003	1	3	Bit	0x8 (4)	-	Signal: Binary Output Relay

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	BO 5	1003	1	3	Bit	0x10 (5)	-	Signal: Binary Output Relay
	DISARMED!	1003	1	3	Bit	0x40 (7)	-	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
	Outs forced	1003	1	3	Bit	0x80 (8)	-	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
CBF		53	1	3	Struct			
	ExBlo1-I	53	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	53	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	53	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	53	1	3	Bit	0x8 (4)	-	Signal: External Blocking

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Trigger1	53	1	3	Bit	0x10 (5)	-	Module Input: Trigger that will start the CBF
	Trigger2	53	1	3	Bit	0x20 (6)	-	Module Input: Trigger that will start the CBF
	Trigger3	53	1	3	Bit	0x40 (7)	-	Module Input: Trigger that will start the CBF
	running	53	1	3	Bit	0x80 (8)	-	Signal: CBF-Module started
	Alarm (*)	53	1	3	Bit	0x100 (9)	-	Signal: Circuit Breaker Failure
	Lockout (*)	53	1	3	Bit	0x200 (10)	-	Signal: Lockout
	Waiting for Trigger (*)	53	1	3	Bit	0x400 (11)	-	Waiting for Trigger
CLPU		66	1	3	Struct			
	ExBlo1-I	66	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	ExBlo2-I	66	1	3	Bit	0x2 (2)	-	Module input state: External blocking
	Ex rev Interl-I	66	1	3	Bit	0x4 (3)	-	Module input state: External reverse interlocking
	active	66	1	3	Bit	0x8 (4)	-	Signal: active



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo	66	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Ex rev Interl	66	1	3	Bit	0x20 (6)	-	Signal: External reverse Interlocking
	CB Pos-I	66	1	3	Bit	0x180 (8)	-	Module input state: Circuit Breaker Position by now (switching position).
	enabled	66	1	3	Bit	0x200 (10)	-	Signal: Cold Load enabled
	detected (*)	66	1	3	Bit	0x400 (11)	-	Signal: Cold Load detected
	I<	66	1	3	Bit	0x800 (12)	-	Signal: No Load Current.
	AR Blo	66	1	3	Bit	0x1000 (13)	-	Signal: Blocked by AR
	Load Inrush	66	1	3	Bit	0x2000 (14)	-	Signal: Load Inrush
	Settle Time	66	1	3	Bit	0x4000 (15)	-	Signal: Settle Time
<b>CTS</b>		<b>137</b>	<b>1</b>	<b>3</b>	<b>Struct</b>			
	ExBlo1-I	137	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo2-I	137	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	137	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	137	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	137	1	3	Bit	0x10 (5)	-	Signal: Alarm Current Transformer Measuring Circuit Supervision
Ctrl		176	1	3	Struct			
	Local	176	1	3	Bit	0x1 (1)	-	Switching Authority: Local
	Remote	176	1	3	Bit	0x2 (2)	-	Switching Authority: Remote
	NonInterl	176	1	3	Bit	0x4 (3)	-	Non-Interlocking is active
	SG Disturb	176	1	3	Bit	0x8 (4)	-	Minimum one Switchgear is disturbed.
	SG Indeterm	176	1	3	Bit	0x10 (5)	-	Minimum one Switchgear is moving (Position cannot be determined).
DI Slot X1		1000	1	3	Struct			
	DI 1	1000	1	3	Bit	0x1 (1)	-	Signal: Digital Input

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	DI 2	1000	1	3	Bit	0x2 (2)	-	Signal: Digital Input
	DI 3	1000	1	3	Bit	0x4 (3)	-	Signal: Digital Input
	DI 4	1000	1	3	Bit	0x8 (4)	-	Signal: Digital Input
	DI 5	1000	1	3	Bit	0x10 (5)	-	Signal: Digital Input
	DI 6	1000	1	3	Bit	0x20 (6)	-	Signal: Digital Input
	DI 7	1000	1	3	Bit	0x40 (7)	-	Signal: Digital Input
	DI 8	1000	1	3	Bit	0x80 (8)	-	Signal: Digital Input
Exp[1]		49	1	3	Struct			
	ExBlo1-I	49	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	49	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	49	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	49	1	3	Bit	0x8 (4)	-	Module input state: Alarm

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Trip-I	49	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	49	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	49	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	49	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	49	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	49	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	49	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	49	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[2]		50	1	3	Struct			
	ExBlo1-I	50	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	50	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	50	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Alarm-I	50	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	50	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	50	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	50	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	50	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	50	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	50	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	50	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	50	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[3]		51	1	3	Struct			
	ExBlo1-I	51	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	51	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo TripCmd-I	51	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	51	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	51	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	51	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	51	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	51	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	51	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	51	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	51	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	51	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[4]		52	1	3	Struct			
	ExBlo1-I	52	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo2-I	52	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	52	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	52	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	52	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	52	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	52	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	52	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	52	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	52	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	52	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	52	1	3	Bit	0x800 (12)	-	Signal: Trip Command
I2>[1]		82	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo1-I	82	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	82	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	82	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	82	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	82	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	82	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	82	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	82	1	3	Bit	0x80 (8)	-	Signal: Alarm Negative Sequence
	Trip (*)	82	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	82	1	3	Bit	0x200 (10)	-	Signal: Trip Command
I2>[2]		83	1	3	Struct			
	ExBlo1-I	83	1	3	Bit	0x1 (1)	-	Module input state: External blocking1



Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo2-I	83	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	83	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	83	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	83	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	83	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	83	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	83	1	3	Bit	0x80 (8)	-	Signal: Alarm Negative Sequence
	Trip (*)	83	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	83	1	3	Bit	0x200 (10)	-	Signal: Trip Command
<b>IG[1]</b>		<b>15</b>	<b>1</b>	<b>3</b>	<b>Struct</b>			
	ExBlo1-I	15	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	15	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo TripCmd-I	15	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	15	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	15	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	15	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	15	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	15	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	15	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	15	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	15	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	15	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	15	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IG[2]		16	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo1-I	16	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	16	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	16	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	16	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	16	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	16	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	16	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	16	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	16	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	16	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	16	1	3	Bit	0x400 (11)	-	Signal: Alarm IG

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Trip (*)	16	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	16	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IG[3]		17	1	3	Struct			
	ExBlo1-I	17	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	17	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	17	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	17	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	17	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	17	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	17	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	17	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	17	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	IGH2 Blo	17	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	17	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	17	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	17	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
<b>IG[4]</b>		<b>18</b>	<b>1</b>	<b>3</b>	<b>Struct</b>			
	ExBlo1-I	18	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	18	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	18	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	18	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	18	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	18	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	18	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Blo TripCmd	18	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	18	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	18	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	18	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	18	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	18	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IH2		22	1	3	Struct			
	ExBlo1-I	22	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	22	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	22	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	22	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Blo L1	22	1	3	Bit	0x10 (5)	-	Signal: Blocked L1

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Blo L2	22	1	3	Bit	0x20 (6)	-	Signal: Blocked L2
	Blo L3	22	1	3	Bit	0x40 (7)	-	Signal: Blocked L3
	Blo IG meas	22	1	3	Bit	0x80 (8)	-	Signal: Blocking of the ground (earth) protection module (measured ground current)
	3-ph Blo	22	1	3	Bit	0x100 (9)	-	Signal: Inrush was detected in at least one phase - trip command blocked.
	Blo IG calc	22	1	3	Bit	0x200 (10)	-	Signal: Blocking of the ground (earth) protection module (calculated ground current)
IRIG-B		148	1	3	Struct			
	active	148	1	3	Bit	0x1 (1)	-	Signal: active
	inverted	148	1	3	Bit	0x2 (2)	-	Signal: IRIG-B inverted
	Control Signal1	148	1	3	Bit	0x4 (3)	-	Signal: IRIG-B Control Signal
	Control Signal2	148	1	3	Bit	0x8 (4)	-	Signal: IRIG-B Control Signal
	Control Signal4	148	1	3	Bit	0x10 (5)	-	Signal: IRIG-B Control Signal

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Control Signal5	148	1	3	Bit	0x20 (6)	-	Signal: IRIG-B Control Signal
	Control Signal6	148	1	3	Bit	0x40 (7)	-	Signal: IRIG-B Control Signal
	Control Signal7	148	1	3	Bit	0x80 (8)	-	Signal: IRIG-B Control Signal
	Control Signal8	148	1	3	Bit	0x100 (9)	-	Signal: IRIG-B Control Signal
	Control Signal9	148	1	3	Bit	0x200 (10)	-	Signal: IRIG-B Control Signal
	Control Signal10	148	1	3	Bit	0x400 (11)	-	Signal: IRIG-B Control Signal
	Control Signal11	148	1	3	Bit	0x800 (12)	-	Signal: IRIG-B Control Signal
	Control Signal12	148	1	3	Bit	0x1000 (13)	-	Signal: IRIG-B Control Signal
	Control Signal13	148	1	3	Bit	0x2000 (14)	-	Signal: IRIG-B Control Signal
	Control Signal14	148	1	3	Bit	0x4000 (15)	-	Signal: IRIG-B Control Signal
	Control Signal15	148	1	3	Bit	0x8000 (16)	-	Signal: IRIG-B Control Signal
IRIG-B		149	1	3	Struct			



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Control Signal16	149	1	3	Bit	0x1 (1)	-	Signal: IRIG-B Control Signal
	Control Signal17	149	1	3	Bit	0x2 (2)	-	Signal: IRIG-B Control Signal
	Control Signal18	149	1	3	Bit	0x4 (3)	-	Signal: IRIG-B Control Signal
I[1]		3	1	3	Struct			
	ExBlo1-I	3	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	3	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	3	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	3	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	3	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	3	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	3	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	3	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo TripCmd	3	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	3	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[1]		4	1	3	Struct			
	Alarm L1	4	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	4	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	4	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	4	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	4	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	4	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	4	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	4	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	4	1	3	Bit	0x100 (9)	-	Signal: Trip Command

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
I[2]		5	1	3	Struct			
	ExBlo1-I	5	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	5	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	5	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	5	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	5	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	5	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	5	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	5	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	5	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	5	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[2]		6	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Alarm L1	6	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	6	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	6	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	6	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	6	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	6	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	6	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	6	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	6	1	3	Bit	0x100 (9)	-	Signal: Trip Command
I[3]		7	1	3	Struct			
	ExBlo1-I	7	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	7	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo TripCmd-I	7	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	7	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	7	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	7	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	7	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	7	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	7	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	7	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[3]		8	1	3	Struct			
	Alarm L1	8	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	8	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	8	1	3	Bit	0x4 (3)	-	Signal: Alarm L3

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Alarm	8	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	8	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	8	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	8	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	8	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	8	1	3	Bit	0x100 (9)	-	Signal: Trip Command
I[4]		9	1	3	Struct			
	ExBlo1-I	9	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	9	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	9	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	9	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	9	1	3	Bit	0x10 (5)	-	Signal: active

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo	9	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	9	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	9	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	9	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	9	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[4]		10	1	3	Struct			
	Alarm L1	10	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	10	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	10	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	10	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	10	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	10	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Trip L3 (*)	10	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	10	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	10	1	3	Bit	0x100 (9)	-	Signal: Trip Command
I[5]		11	1	3	Struct			
	ExBlo1-I	11	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	11	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	11	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	11	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	11	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	11	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	11	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	11	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo TripCmd	11	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	11	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[5]		12	1	3	Struct			
	Alarm L1	12	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	12	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	12	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	12	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	12	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	12	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	12	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	12	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	12	1	3	Bit	0x100 (9)	-	Signal: Trip Command

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
I[6]		13	1	3	Struct			
	ExBlo1-I	13	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	13	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	13	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	13	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	13	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	13	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	13	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	13	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	13	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	13	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[6]		14	1	3	Struct			

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Alarm L1	14	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	14	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	14	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	14	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	14	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	14	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	14	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	14	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	14	1	3	Bit	0x100 (9)	-	Signal: Trip Command
<b>Logics</b>		<b>1100</b>	<b>1</b>	<b>3</b>	<b>Struct</b>			
	LE1.Gate Out	1100	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE1.Timer Out	1100	1	3	Bit	0x2 (2)	-	Signal: Timer Output

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE1.Out	1100	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE1.Out inverted	1100	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE1.Gate In1-l	1100	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In2-l	1100	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In3-l	1100	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In4-l	1100	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE1.Reset Latch-l	1100	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1101	1	3	Struct			
	LE2.Gate Out	1101	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE2.Timer Out	1101	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE2.Out	1101	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE2.Out inverted	1101	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE2.Gate In1-l	1101	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In2-l	1101	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In3-l	1101	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In4-l	1101	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE2.Reset Latch-l	1101	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1102	1	3	Struct			
	LE3.Gate Out	1102	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE3.Timer Out	1102	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE3.Out	1102	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE3.Out inverted	1102	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE3.Gate In1-l	1102	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In2-l	1102	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE3.Gate In3-l	1102	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In4-l	1102	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE3.Reset Latch-l	1102	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1103	1	3	Struct			
	LE4.Gate Out	1103	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE4.Timer Out	1103	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE4.Out	1103	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE4.Out inverted	1103	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE4.Gate In1-l	1103	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In2-l	1103	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In3-l	1103	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In4-l	1103	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE4.Reset Latch-I	1103	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1104	1	3	Struct			
	LE5.Gate Out	1104	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE5.Timer Out	1104	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE5.Out	1104	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE5.Out inverted	1104	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE5.Gate In1-I	1104	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In2-I	1104	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In3-I	1104	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In4-I	1104	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE5.Reset Latch-I	1104	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1105	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE6.Gate Out	1105	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE6.Timer Out	1105	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE6.Out	1105	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE6.Out inverted	1105	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE6.Gate In1-l	1105	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In2-l	1105	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In3-l	1105	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In4-l	1105	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE6.Reset Latch-l	1105	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1106	1	3	Struct			
	LE7.Gate Out	1106	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE7.Timer Out	1106	1	3	Bit	0x2 (2)	-	Signal: Timer Output



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE7.Out	1106	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE7.Out inverted	1106	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE7.Gate In1-l	1106	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In2-l	1106	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In3-l	1106	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In4-l	1106	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE7.Reset Latch-l	1106	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1107	1	3	Struct			
	LE8.Gate Out	1107	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE8.Timer Out	1107	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE8.Out	1107	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE8.Out inverted	1107	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE8.Gate In1-l	1107	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In2-l	1107	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In3-l	1107	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In4-l	1107	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE8.Reset Latch-l	1107	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1108	1	3	Struct			
	LE9.Gate Out	1108	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE9.Timer Out	1108	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE9.Out	1108	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE9.Out inverted	1108	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE9.Gate In1-l	1108	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In2-l	1108	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE9.Gate In3-l	1108	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In4-l	1108	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE9.Reset Latch-l	1108	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1109	1	3	Struct			
	LE10.Gate Out	1109	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE10.Timer Out	1109	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE10.Out	1109	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE10.Out inverted	1109	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE10.Gate In1-l	1109	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In2-l	1109	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In3-l	1109	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In4-l	1109	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE10.Reset Latch-I	1109	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1110	1	3	Struct			
	LE11.Gate Out	1110	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE11.Timer Out	1110	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE11.Out	1110	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE11.Out inverted	1110	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE11.Gate In1-I	1110	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In2-I	1110	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In3-I	1110	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In4-I	1110	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE11.Reset Latch-I	1110	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1111	1	3	Struct			

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE12.Gate Out	1111	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE12.Timer Out	1111	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE12.Out	1111	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE12.Out inverted	1111	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE12.Gate In1-l	1111	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In2-l	1111	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In3-l	1111	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In4-l	1111	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE12.Reset Latch-l	1111	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1112	1	3	Struct			
	LE13.Gate Out	1112	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE13.Timer Out	1112	1	3	Bit	0x2 (2)	-	Signal: Timer Output

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE13.Out	1112	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE13.Out inverted	1112	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE13.Gate In1-I	1112	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In2-I	1112	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In3-I	1112	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In4-I	1112	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE13.Reset Latch-I	1112	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1113	1	3	Struct			
	LE14.Gate Out	1113	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE14.Timer Out	1113	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE14.Out	1113	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE14.Out inverted	1113	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE14.Gate In1-l	1113	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In2-l	1113	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In3-l	1113	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In4-l	1113	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE14.Reset Latch-l	1113	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1114	1	3	Struct			
	LE15.Gate Out	1114	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE15.Timer Out	1114	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE15.Out	1114	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE15.Out inverted	1114	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE15.Gate In1-l	1114	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In2-l	1114	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE15.Gate In3-l	1114	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In4-l	1114	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE15.Reset Latch-l	1114	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1115	1	3	Struct			
	LE16.Gate Out	1115	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE16.Timer Out	1115	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE16.Out	1115	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE16.Out inverted	1115	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE16.Gate In1-l	1115	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In2-l	1115	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In3-l	1115	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In4-l	1115	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE16.Reset Latch-I	1115	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1116	1	3	Struct			
	LE17.Gate Out	1116	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE17.Timer Out	1116	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE17.Out	1116	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE17.Out inverted	1116	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE17.Gate In1-I	1116	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In2-I	1116	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In3-I	1116	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In4-I	1116	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE17.Reset Latch-I	1116	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1117	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE18.Gate Out	1117	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE18.Timer Out	1117	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE18.Out	1117	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE18.Out inverted	1117	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE18.Gate In1-l	1117	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In2-l	1117	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In3-l	1117	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In4-l	1117	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE18.Reset Latch-l	1117	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1118	1	3	Struct			
	LE19.Gate Out	1118	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE19.Timer Out	1118	1	3	Bit	0x2 (2)	-	Signal: Timer Output

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE19.Out	1118	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE19.Out inverted	1118	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE19.Gate In1-l	1118	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In2-l	1118	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In3-l	1118	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In4-l	1118	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE19.Reset Latch-l	1118	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1119	1	3	Struct			
	LE20.Gate Out	1119	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE20.Timer Out	1119	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE20.Out	1119	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE20.Out inverted	1119	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	LE20.Gate In1-l	1119	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In2-l	1119	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In3-l	1119	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In4-l	1119	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE20.Reset Latch-l	1119	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Modbus		1005	1	3	Struct			
	Scada Cmd 1	1005	1	3	Bit	0x1 (1)	-	Scada Command
	Scada Cmd 2	1005	1	3	Bit	0x2 (2)	-	Scada Command
	Scada Cmd 3	1005	1	3	Bit	0x4 (3)	-	Scada Command
	Scada Cmd 4	1005	1	3	Bit	0x8 (4)	-	Scada Command
	Scada Cmd 5	1005	1	3	Bit	0x10 (5)	-	Scada Command
	Scada Cmd 6	1005	1	3	Bit	0x20 (6)	-	Scada Command

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Scada Cmd 7	1005	1	3	Bit	0x40 (7)	-	Scada Command
	Scada Cmd 8	1005	1	3	Bit	0x80 (8)	-	Scada Command
	Scada Cmd 9	1005	1	3	Bit	0x100 (9)	-	Scada Command
	Scada Cmd 10	1005	1	3	Bit	0x200 (10)	-	Scada Command
	Scada Cmd 11	1005	1	3	Bit	0x400 (11)	-	Scada Command
	Scada Cmd 12	1005	1	3	Bit	0x800 (12)	-	Scada Command
	Scada Cmd 13	1005	1	3	Bit	0x1000 (13)	-	Scada Command
	Scada Cmd 14	1005	1	3	Bit	0x2000 (14)	-	Scada Command
	Scada Cmd 15	1005	1	3	Bit	0x4000 (15)	-	Scada Command
	Scada Cmd 16	1005	1	3	Bit	0x8000 (16)	-	Scada Command
Modbus		1006	1	3	Struct			
	Transmission	1006	1	3	Bit	0x1 (1)	-	Signal: SCADA active

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
PSet-Switch		59	1	3	Struct			
	PS 1	59	1	3	Bit	0x1 (1)	-	Signal: Parameter Set 1
	PS 2	59	1	3	Bit	0x2 (2)	-	Signal: Parameter Set 2
	PS 3	59	1	3	Bit	0x4 (3)	-	Signal: Parameter Set 3
	PS 4	59	1	3	Bit	0x8 (4)	-	Signal: Parameter Set 4
	PSS manual	59	1	3	Bit	0x10 (5)	-	Signal: Manual Switch over of a Parameter Set
	PSS via Scada	59	1	3	Bit	0x20 (6)	-	Signal: Parameter Set Switch via Scada
	PSS via Inp fct	59	1	3	Bit	0x40 (7)	-	Signal: Parameter Set Switch via input function
	PS1-I	59	1	3	Bit	0x80 (8)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS2-I	59	1	3	Bit	0x100 (9)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	PS3-I	59	1	3	Bit	0x200 (10)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS4-I	59	1	3	Bit	0x400 (11)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	min 1 param changed (*)	59	1	3	Bit	0x800 (12)	-	Signal: At least one parameter has been changed
Prot		1	1	3	Struct			
	ExBlo1-I	1	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	1	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	1	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	1	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm L1	1	1	3	Bit	0x10 (5)	-	Signal: General-Alarm L1
	Alarm L2	1	1	3	Bit	0x20 (6)	-	Signal: General-Alarm L2
	Alarm L3	1	1	3	Bit	0x40 (7)	-	Signal: General-Alarm L3

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Alarm G	1	1	3	Bit	0x80 (8)	-	Signal: General-Alarm - Earth fault
	Alarm	1	1	3	Bit	0x100 (9)	-	Signal: General Alarm
	Trip L1 (*)	1	1	3	Bit	0x200 (10)	-	Signal: General Trip L1
	Trip L2 (*)	1	1	3	Bit	0x400 (11)	-	Signal: General Trip L2
	Trip L3 (*)	1	1	3	Bit	0x800 (12)	-	Signal: General Trip L3
	Trip G (*)	1	1	3	Bit	0x1000 (13)	-	Signal: General Trip Ground fault
	Trip (*)	1	1	3	Bit	0x2000 (14)	-	Signal: General Trip
Prot		2	1	3	Struct			
	Blo TripCmd	2	1	3	Bit	0x1 (1)	-	Signal: Trip Command blocked
	ExBlo TripCmd-I	2	1	3	Bit	0x2 (2)	-	Module input state: External Blocking of the Trip Command
	ExBlo TripCmd	2	1	3	Bit	0x4 (3)	-	Signal: External Blocking of the Trip Command
Prot		57	1	3	Struct			



Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	FaultNo	57	1	3	Bit	0xffff (1)	-	Disturbance No
Prot		58	1	3	Struct			
	No of grid faults	58	1	3	Bit	0xffff (1)	-	Number of grid faults: A grid fault, e.g. a short circuit, might cause several faults with trip and autoreclosing, each fault being identified by an increased fault number. In this case, the grid fault number remains the same.
SG[1]		177	1	3	Struct			
	Aux OFF-I	177	1	3	Bit	0x1 (1)	-	Module input state: Position indicator/check-back signal of the CB (52b)
	Aux ON-I	177	1	3	Bit	0x2 (2)	-	Module Input State: Position indicator/check-back signal of the CB (52a)
	Ready-I	177	1	3	Bit	0x4 (3)	-	Module input state: CB ready
	Sys-in-Sync-I	177	1	3	Bit	0x8 (4)	-	State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.
	Interl OFF1-I	177	1	3	Bit	0x10 (5)	-	State of the module input: Interlocking of the OFF command

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Interl OFF2-I	177	1	3	Bit	0x20 (6)	-	State of the module input: Interlocking of the OFF command
	Interl OFF3-I	177	1	3	Bit	0x40 (7)	-	State of the module input: Interlocking of the OFF command
	Interl ON1-I	177	1	3	Bit	0x80 (8)	-	State of the module input: Interlocking of the ON command
	Interl ON2-I	177	1	3	Bit	0x100 (9)	-	State of the module input: Interlocking of the ON command
	Interl ON3-I	177	1	3	Bit	0x200 (10)	-	State of the module input: Interlocking of the ON command
	SCmd OFF-I	177	1	3	Bit	0x800 (12)	-	State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input
	SCmd ON-I	177	1	3	Bit	0x1000 (13)	-	State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input
	TripCmd (*)	177	1	3	Bit	0x2000 (14)	-	Signal: Trip Command

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	OFF Cmd	177	1	3	Bit	0x4000 (15)	-	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
	OFF Cmd manual	177	1	3	Bit	0x8000 (16)	-	Signal: OFF Cmd manual
SG[1]		178	1	3	Struct			
	ON Cmd	178	1	3	Bit	0x1 (1)	-	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
	ON Cmd manual	178	1	3	Bit	0x2 (2)	-	Signal: ON Cmd manual
	Sync ON request	178	1	3	Bit	0x4 (3)	-	Signal: Synchronous ON request
	SGwear Slow SG	178	1	3	Bit	0x8 (4)	-	Signal: Alarm, the circuit breaker (load-break switch) becomes slower
	Res SGwear SI SG	178	1	3	Bit	0x10 (5)	-	Signal: Resetting the slow Switchgear Alarm
	CES Disturbed	178	1	3	Bit	0x40 (7)	-	Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	CES Fiel Interl	178	1	3	Bit	0x80 (8)	-	Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.
	CES ON d OFF	178	1	3	Bit	0x400 (11)	-	Signal: Command Execution Supervision: On Command during a pending OFF Command.
	CES SwitchDir	178	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.
	CES SG not ready	178	1	3	Bit	0x1000 (13)	-	Signal: Command Execution Supervision: Switchgear not ready
	CES SyncTimeout	178	1	3	Bit	0x2000 (14)	-	Signal: Command Execution Supervision: Switching Command not executed. No Synchronization signal while t-sync was running.

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	CES succesf	178	1	3	Bit	0x4000 (15)	-	Signal: Command Execution Supervision: Switching command executed successfully.
	Prot ON	178	1	3	Bit	0x8000 (16)	-	Signal: ON Command issued by the Prot module
SG[1]		179	1	3	Struct			
	Pos Disturb	179	1	3	Bit	0x1 (1)	-	Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
	t-Dwell	179	1	3	Bit	0x2 (2)	-	Signal: Dwell time
	Pos Indeterm	179	1	3	Bit	0x4 (3)	-	Signal: Circuit Breaker is in Indeterminate Position
	Pos OFF	179	1	3	Bit	0x8 (4)	-	Signal: Circuit Breaker is in OFF-Position
	Pos ON	179	1	3	Bit	0x10 (5)	-	Signal: Circuit Breaker is in ON-Position
	Ready	179	1	3	Bit	0x20 (6)	-	Signal: Circuit breaker is ready for operation.
	Pos not ON	179	1	3	Bit	0x40 (7)	-	Signal: Pos not ON

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	SI SingleContactInd	179	1	3	Bit	0x80 (8)	-	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.
	Position Ind manipul	179	1	3	Bit	0x100 (9)	-	Signal: Position Indicators faked
	OFF incl TripCmd	179	1	3	Bit	0x200 (10)	-	Signal: The OFF Command includes the OFF Command issued by the Protection module.
	ON incl Prot ON	179	1	3	Bit	0x400 (11)	-	Signal: The ON Command includes the ON Command issued by the Protection module.
	CES Fail TripCmd	179	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision: Command execution failed because trip command is pending.
	Interl OFF	179	1	3	Bit	0x1000 (13)	-	Signal: One or more IL_Off inputs are active.
	Interl ON	179	1	3	Bit	0x2000 (14)	-	Signal: One or more IL_On inputs are active.
SG[1]		195	1	3	Struct			

Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Isum Intr trip	195	1	3	Bit	0x10 (5)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.
	Isum Intr trip: IL1	195	1	3	Bit	0x20 (6)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1
	Isum Intr trip: IL2	195	1	3	Bit	0x40 (7)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2
	Isum Intr trip: IL3	195	1	3	Bit	0x80 (8)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3
	Operations Alarm	195	1	3	Bit	0x100 (9)	-	Signal: Service Alarm, too many Operations
	WearLevel Alarm	195	1	3	Bit	0x200 (10)	-	Signal: Threshold for the Alarm
	WearLevel Lockout	195	1	3	Bit	0x400 (11)	-	Signal: Threshold for the Lockout Level
	Isum Intr ph Alm	195	1	3	Bit	0x800 (12)	-	Signal: Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.
SG[1]		256	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Removed-I	256	1	3	Bit	0x1 (1)	-	State of the module input: The withdrawable circuit breaker is Removed
	CES SG removed	256	1	3	Bit	0x2 (2)	-	Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.
	Removed	256	1	3	Bit	0x4 (3)	-	Signal: The withdrawable circuit breaker is Removed
SOTF		65	1	3	Struct			
	ExBlo1-I	65	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	ExBlo2-I	65	1	3	Bit	0x2 (2)	-	Module input state: External blocking
	Ext SOTF-I	65	1	3	Bit	0x4 (3)	-	Module input state: External Switch Onto Fault Alarm
	Ex rev Interl-I	65	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	65	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	65	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	65	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking



Appendix - Data Point Lists

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	AR Blo	65	1	3	Bit	0x400 (11)	-	Signal: Blocked by AR
	enabled	65	1	3	Bit	0x800 (12)	-	Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.
	I<	65	1	3	Bit	0x2000 (14)	-	Signal: No Load Current.
Sgen		1012	1	3	Struct			
	ExBlo	1012	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	Ex ForcePost-I	1012	1	3	Bit	0x2 (2)	-	State of the module input:Force Post state. Abort simulation.
	Running	1012	1	3	Bit	0x10 (5)	-	Signal; Measuring value simulation is running
	State	1012	1	3	Bit	0xe0 (6)	-	Signal: Wave generation states: 0=Off, 1=PreFault, 2=Fault, 3=PostFault, 4=InitReset
	Ex Start Simulation-I	1012	1	3	Bit	0x100 (9)	-	State of the module input:External Start of Fault Simulation (Using the test parameters)
Sys		154	1	3	Struct			

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	Lock Settings-I	154	1	3	Bit	0x40 (7)	-	State of the module input: No parameters can be changed as long as this input is true. The parameter settings are locked.
	SNTP active	154	1	3	Bit	0x80 (8)	-	Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.
	Setting Lock Bypass	154	1	3	Bit	0x100 (9)	-	Signal: Short-period unlock of the Setting Lock
SysA		173	1	3	Struct			
	ExBlo-I	173	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	ExBlo	173	1	3	Bit	0x2 (2)	-	Signal: External Blocking
	Alm Current Demd	173	1	3	Bit	0x4 (3)	-	Signal: Alarm averaged demand current
	active	173	1	3	Bit	0x8 (4)	-	Signal: active
	Alarm I THD	173	1	3	Bit	0x10 (5)	-	Signal: Alarm Total Harmonic Distortion Current
	Trip Current Demand (*)	173	1	3	Bit	0x1000 (13)	-	Signal: Trip averaged demand current
	Trip I THD (*)	173	1	3	Bit	0x2000 (14)	-	Signal: Trip Total Harmonic Distortion Current

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
TCS		150	1	3	Struct			
	ExBlo1-I	150	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	150	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	150	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	150	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	150	1	3	Bit	0x10 (5)	-	Signal: Alarm Trip Circuit Supervision
	Not Possible	150	1	3	Bit	0x20 (6)	-	Not possible because no state indicator assigned to the breaker.
	CB Pos Detect-I	150	1	3	Bit	0xc0 (7)	-	Module input state: Criterion by which the Circuit Breaker Switch Position is to be detected.
	Aux ON-I	150	1	3	Bit	0x100 (9)	-	Module Input State: Position indicator/check-back signal of the CB (52a)
	Aux OFF-I	150	1	3	Bit	0x200 (10)	-	Module input state: Position indicator/check-back signal of the CB (52b)
ThR		19	1	3	Struct			

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	ExBlo1-I	19	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	19	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	19	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	19	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	19	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	19	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	19	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	19	1	3	Bit	0x80 (8)	-	Signal: Alarm Thermal Overload
	Trip (*)	19	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	19	1	3	Bit	0x200 (10)	-	Signal: Trip Command

Legend \* = These Signals have to be acknowledged by the Scada System.

### Measuring values

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
AR	Total number Cr	20164	2	4	Float IEE754		-	Total number of all executed Automatic Reclosures Attempts
AR	Cr failed	20166	2	4	Float IEE754		-	Total number of unsuccessfully executed automatic reclosure attempts
AR	Cr successfl	20168	2	4	Float IEE754		-	Total number of successfully executed Automatic Reclosures
AR	Cr Service Alarm1	20170	2	4	Float IEE754		-	Remaining numbers of ARs until Service Alarm 1
AR	Cr Service Alarm2	20172	2	4	Float IEE754		-	Remaining numbers of ARs until Service Alarm 2
AR	AR Shot No.	20188	2	4	Float IEE754		-	Counter - Auto Reclosure Attempts
AR	Max Shots / h Cr	20374	2	4	Float IEE754		-	Counter for the maximum allowed shots per hour.
Current	IL1	20100	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
Current	IL2	20102	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
Current	IL3	20104	2	4	Float IEE754		A	Measured value: Phase current (fundamental)

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Current	IG meas	20106	2	4	Float IEE754		A	Measured value (measured): IG (fundamental)
Current	I0	20114	2	4	Float IEE754		A	Measured value (calculated): Zero current (fundamental)
Current	I1	20116	2	4	Float IEE754		A	Measured value (calculated): Positive phase sequence current (fundamental)
Current	I2	20118	2	4	Float IEE754		A	Measured value (calculated): Unbalanced load current (fundamental)
Current	IL1 H2	20120	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL1
Current	IL2 H2	20122	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL2
Current	IL3 H2	20124	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL3
Current	IG H2 meas	20126	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IG (measured)
Current	IG calc	20160	2	4	Float IEE754		A	Measured value (calculated): IG (fundamental)
Current	phi IG calc	20200	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IG calc
Current	phi IG meas	20202	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IG meas

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Current	phi IL1	20204	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL1
Current	phi IL2	20206	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL2
Current	phi IL3	20208	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL3
Current	IL1 THD	20210	2	4	Float IEE754		A	Measured value (calculated): IL1 Total Harmonic Current
Current	IL2 THD	20212	2	4	Float IEE754		A	Measured value (calculated): IL2 Total Harmonic Current
Current	IL3 THD	20214	2	4	Float IEE754		A	Measured value (calculated): IL3 Total Harmonic Current
Current	%IL1 THD	20216	2	4	Float IEE754		%	Measured value (calculated): IL1 Total Harmonic Distortion
Current	%IL2 THD	20218	2	4	Float IEE754		%	Measured value (calculated): IL2 Total Harmonic Distortion
Current	%IL3 THD	20220	2	4	Float IEE754		%	Measured value (calculated): IL3 Total Harmonic Distortion
Current	IL1 RMS	20316	2	4	Float IEE754		A	Measured value: Phase current (RMS)
Current	IL2 RMS	20318	2	4	Float IEE754		A	Measured value: Phase current (RMS)

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Current	IL3 RMS	20320	2	4	Float IEE754		A	Measured value: Phase current (RMS)
Current	IG meas RMS	20322	2	4	Float IEE754		A	Measured value (measured): IG (RMS)
Current	IG calc RMS	20324	2	4	Float IEE754		A	Measured value (calculated): IG (RMS)
Current	%(I2/I1)	20376	2	4	Float IEE754		%	Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.
Current	phi I0	20378	2	4	Float IEE754		°	Measured value (calculated): Angle Zero Sequence System
Current	phi I1	20380	2	4	Float IEE754		°	Measured value (calculated): Angle of Positive Sequence System
Current	phi I2	20382	2	4	Float IEE754		°	Measured Value (calculated): Angle of Negative Sequence System
Current	IG H2 calc	20500	2	4	Float IEE754		%	Measured value (calculated): 2nd harmonic/1st harmonic of IG (calculated)
Current	I1 max	21074	2	4	Float IEE754		A	Maximum value positive phase sequence current (fundamental)
Current	I1 min	21076	2	4	Float IEE754		A	Minimum value positive phase sequence current (fundamental)



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Current	I2 max	21080	2	4	Float IEE754		A	Maximum value unbalanced load (fundamental)
Current	I2 min	21082	2	4	Float IEE754		A	Minimum value unbalanced load current (fundamental)
Current	IL1 avg RMS	21130	2	4	Float IEE754		A	IL1 average value (RMS)
Current	IL2 avg RMS	21132	2	4	Float IEE754		A	IL2 average value (RMS)
Current	IL3 avg RMS	21134	2	4	Float IEE754		A	IL3 average value (RMS)
Current	IL1 max RMS	21136	2	4	Float IEE754		A	IL1 maximum value (RMS)
Current	IL2 max RMS	21138	2	4	Float IEE754		A	IL2 maximum value (RMS)
Current	IL3 max RMS	21140	2	4	Float IEE754		A	IL3 maximum value (RMS)
Current	IL1 min RMS	21142	2	4	Float IEE754		A	IL1 minimum value (RMS)
Current	IL2 min RMS	21144	2	4	Float IEE754		A	IL2 minimum value (RMS)
Current	IL3 min RMS	21146	2	4	Float IEE754		A	IL3 minimum value (RMS)

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Current	IG H2 meas max	21222	2	4	Float IEE754		%	Measured value: Maximum ratio of 2nd harmonic over fundamental of IG (measured)
Current	IG H2 meas min	21224	2	4	Float IEE754		%	Measured value: Minimum ratio of 2nd harmonic over fundamental of IG (measured)
Current	IL1 H2 max	21228	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL1
Current	IL1 H2 min	21230	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic over fundamental of IL1
Current	IL2 H2 max	21234	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL2
Current	IL2 H2 min	21236	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic over fundamental of IL2
Current	IL3 H2 max	21240	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL3
Current	IL3 H2 min	21242	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic/1st harmonic minimum value of IL3
Current	IG calc max RMS	21456	2	4	Float IEE754		A	Measured value (calculated):IG maximum value (RMS)

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Current	IG calc min RMS	21458	2	4	Float IEE754		A	Measured value (calculated):IG minimum value (RMS)
Current	IG meas max RMS	21462	2	4	Float IEE754		A	Measured value: IG maximum value (RMS)
Current	IG meas min RMS	21464	2	4	Float IEE754		A	Measured value: IG minimum value (RMS)
Current	%(I2/I1) max	21468	2	4	Float IEE754		%	Measured value (calculated): I2/I1 maximum value, phase sequence will be taken into account automatically
Current	%(I2/I1) min	21470	2	4	Float IEE754		%	Measured value (calculated): I2/I1 minimum value, phase sequence will be taken into account automatically
Current	IG H2 calc max	21774	2	4	Float IEE754		%	Measured value (calculated): Maximum ratio of 2nd harmonic over fundamental of IG (calculated)
Current	IG H2 calc min	21776	2	4	Float IEE754		%	IG H2 calc min
Date and Time		20000	6	4	Struct			
	y	20000	6	4	Short	Word 0 (1)	-	year
	m	20000	6	4	Short	Word 1 (17)	-	month

<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
	d	20000	6	4	Short	Word 2 (33)	-	days
	h	20000	6	4	Short	Word 3 (49)	-	hours
	min	20000	6	4	Short	Word 4 (65)	-	minute
	ms	20000	6	4	Short	Word 5 (81)	-	milliseconds
IRIG-B	Edges	20298	2	4	Float IEE754		-	Edges
IRIG-B	NoOfFrameErrors	20300	2	4	Float IEE754		-	Total Number of Frame Errors. Physically corrupted Frame.
IRIG-B	NoOfFramesOK	20302	2	4	Float IEE754		-	Total Number valid Frames.
SG[1]	Sum trip IL1	20800	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[1]	Sum trip IL2	20802	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[1]	Sum trip IL3	20804	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[1]	Isum Intr per hour	20806	2	4	Float IEE754		kA	Sum per hour of interrupting currents.

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
SG[1]	SG OPEN capacity	20808	2	4	Float IEE754		%	Used capacity. 100% means, that the switchgear is to be maintained.
SG[1]	TripCmd Cr	20810	2	4	Float IEE754		-	Counter: Total number of trips of the switchgear (circuit breaker, load break switch...). Resettable with Total or All.
Statistics	IL1 Peak demand	24000	2	4	Float IEE754		A	IL1 Peak value, RMS value
Statistics	IL2 Peak demand	24002	2	4	Float IEE754		A	IL2 Peak value, RMS value
Statistics	IL3 Peak demand	24004	2	4	Float IEE754		A	IL3 Peak value, RMS value
ThR	Thermal Cap Used	20110	2	4	Float IEE754		%	Measured value: Thermal Capacity Used
ThR	Time To Trip	20112	2	4	Float IEE754		s	Measured value (calculated/measured): Remaining time until the thermal overload module will trip
ThR	Thermal Cap max	21086	2	4	Float IEE754		%	Thermal Capacity maximum value
ThR	Thermal Cap min	21088	2	4	Float IEE754		%	Thermal Capacity minimum value
Values	Build	20008	2	4	Float IEE754		-	Build

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Values	Operating hours Cr	20010	2	4	Float IEE754		h	Operating hours counter of the protective device

## Commands

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
Acknowledge	LEDs	22000	1	5	0xFF00		-	LEDs
Acknowledge	Binary Outputs	22001	1	5	0xFF00		-	Binary Outputs
Acknowledge	Scada	22002	1	5	0xFF00		-	Scada
Acknowledge	Device	22003	1	5	0xFF00		-	Device
Acknowledge	Ack TripCmd	22005	1	5	0xFF00		-	Signal: Acknowledge Trip Command
Reset	Modbus diagnosis counter	22006	1	5	0xFF00		-	Modbus diagnosis counter
Scada Cmd	Assbl Scada Cmd 1	22020	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 2	22021	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Scada Cmd	Assbl Scada Cmd 3	22022	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 4	22023	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 5	22024	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 6	22025	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 7	22026	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 8	22027	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command



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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Scada Cmd	Assbl Scada Cmd 9	22028	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 10	22029	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 11	22030	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 12	22031	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 13	22032	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 14	22033	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command

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<b>Module</b>	<b>Subgroup Names Functions</b>	<b>Start Register Address</b>	<b>No. of Modbus Registers</b>	<b>Function Code</b>	<b>Format</b>	<b>Bit Mask / (Bit Position)</b>	<b>Unit</b>	<b>Description</b>
Scada Cmd	Assbl Scada Cmd 15	22034	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 16	22035	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
PSet-Switch	Scada PS1	22050	1	5	0xFF00		-	Scada Setting Group1
PSet-Switch	Scada PS2	22051	1	5	0xFF00		-	Scada Setting Group2
PSet-Switch	Scada PS3	22052	1	5	0xFF00		-	Scada Setting Group3
PSet-Switch	Scada PS4	22053	1	5	0xFF00		-	Scada Setting Group4
AFRMS Mode	AFRMS SCADA	22054	1	5	0xFF00= On 0x0000= Off		-	Signal: Arcflash Reduction Maintenance SCADA Mode
SG	SG ControlCmd1	22100	1	5	0xFF00= On 0x0000= Off		-	Control Command Switchgear

## Settings

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
Date and Time		32500	6	3 16	Struct			
	y	32500	6	3 16	Short	Word 0 (1)	-	year
	m	32500	6	3 16	Short	Word 1 (17)	-	month
	d	32500	6	3 16	Short	Word 2 (33)	-	days
	h	32500	6	3 16	Short	Word 3 (49)	-	hours
	min	32500	6	3 16	Short	Word 4 (65)	-	minute
	ms	32500	6	3 16	Short	Word 5 (81)	-	milliseconds

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