



**MRDT4 – Modbus
HighPROTEC**

Data point list

Manual DOK-TD-MRDT4MDE

Table of Contents

TABLE OF CONTENTS	2
MODBUS PARAMETERS	3
Notes for the SCADA-System.....	4
SPECIFIC MODBUS FUNCTION CODES	5
Setting Date and Time.....	9
Supported MODBUS- Error Messages.....	10
APPENDIX - DATA POINT LISTS	11
Signals.....	11
Measuring values.....	107
Commands.....	129
Settings.....	134

This Manual is valid for version (applies for Modbus RTU and Modbus TCP):

Version 2.3.a

Build: 20960

Modbus Parameters

For the Modbus Protocol several parameters have to be set which are relevant for the communication between the control system (SCADA) and the device. The parameters and their setting possibilities or value ranges are shown in the table below.



ATTENTION!

The Parameters are described within the appendix of the device manual (chapter Modbus).

Notes for the SCADA-System

When using Modbus RTU the following times have to be considered by the control system and are fixed within the device :
The dwell times (t_D) before start of a telegram must at least be set to 3.5 characters.

Examples:

3.5 characters 9600 Baud = 4 ms

3.5 characters 19200 Baud = 2 ms

3.6 3.5 characters 38400 Baud = 1 ms

Start of a new telegram is expected when the dwell time (t_D) is > 3.5 characters.

The fact that the probability of disruptions during transmission of a telegram increases with its length has to be taken into duly consideration and thus a query to the Slave should be possibly such that the response telegram is not much longer than 32 Byte.

Specific Modbus Function Codes

For reading out data from the device or to carry out commands, the services listed in the table, also called »Function Codes«, are supported.

Function-code	Designation	Description
3	Read Holding Registers	There are single or several data words read as from a specific data word address. Only status addresses and parameter addresses can be read.
4	Read Input Registers	There are single or several data words read as from a specific data word address. Only measuring values can be read.
5	Write single Output (Bit)	All other values are illegal and will not affect the output. Via this function code acknowledgments can be executed as well as counters reseted or blockings set.
8	Loopback Test	Test function for the communication system
16	Load Multiple Registers	There are single or several data words written as from a specific data word address.

Table 3.1: function codes

On the following pages the Modbus functions are described in detail:

Function-Code 3/4:

Query

Slave address	3/4	Register address HI	Register address LO	Register number HI	Register number LO	Check-sum HI	Check-sum LO
---------------	-----	---------------------	---------------------	--------------------	--------------------	--------------	--------------

Response

Slave address	3/4	Byte number	Register 0 HI	Register 0 LO	...	Check-sum HI	Check-sum LO
---------------	-----	-------------	---------------	---------------	-----	--------------	--------------

Register address ($HI*256 + LO$)

The data word address from where reading should start.

Register number ($HI*256 + LO$)

Number of data words to be read. Valid range: 1..125

Byte number

Number of subsequent Bytes containing data words.

Register

Data words read out of the device (Highbyte and Lowbyte).

Function Code 5:

Query

Slave address	5	Register address HI	Register address LO	Register data HI	Register data LO	Check-sum HI	Check-sum LO
---------------	---	---------------------	---------------------	------------------	------------------	--------------	--------------

Response

Slave address	5	Register address HI	Register address LO	Register data HI	Register data LO	Check-sum HI	Check-sum LO
---------------	---	---------------------	---------------------	------------------	------------------	--------------	--------------

Register address (HI*256 + LO)
Data word address to be written

Register data
Value of the data word to be written (Highbyte and Lowbyte).

Permitted value range :

FF00 hex request for a single bit to be on: This often means to reset a counter, execute acknowledgments or set blockings signals.
0000 hex request for a single bit to be off: This often means to deactivate blocking signals or to reset single bits.

Function Code 8:

Query

Slave address	8	Data Diag Code HI 0x00	Data Diag Code LO 0x00	Test data	Test data	Check-sum HI	Check-sum LO
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Response

Slave address	8	Data Diag Code HI	Data Diag Code LO	Test data	Test data	Check-sum HI	Check-sum LO
---------------	---	-------------------	-------------------	-----------	-----------	--------------	--------------

Data Diag Code HI (high), Data Diag Code LO (Low)
Diagnostic Code (subfunction code of function code 8) for testing the communication system. The Diagnostic Code „Return Query Data“ (0x00, 0x00) is being supported.

Test Data

By using the Diagnostic Code 0x00 0x00, the transmitted data is sent back to the Master unchanged.

Function Code 16:

Query

Slave address	16	Register address HI	Register address LO	Register number HI	Register number LO	Byte number	Register 0 HI	Register 0 LO	...	Check-sum HI	Check-sum LO
---------------	----	---------------------	---------------------	--------------------	--------------------	-------------	---------------	---------------	-----	--------------	--------------

Response

Slave address	16	Register address HI	Register address LO	Register number HI	Register number LO	Check-sum HI	Check-sum LO
---------------	----	---------------------	---------------------	--------------------	--------------------	--------------	--------------

Register address (HI*256 + LO)

Data word address as from where writing should start.

Register number (HI*256 + LO)

Query: Number of data words to be written. Valid range: 1..123

Response: Number of data words written.

Byte number

Number of subsequent Bytes to contain data words.

Register

Data words read out of the device (Highbyte und Lowbyte).

Setting Date and Time

Date and time can be set by means of function code 16 and read with function code 3. If the device address 0 (broadcast address) is selected, the times of all devices connected to this bus are simultaneously reset. The devices do not respond to a broadcast command.

Supported MODBUS- Error Messages

Exception Response Telegrams are described within the general "Modbus Application Protocol Specification". An exception response table with examples is shown there. The table below contains just the actually used codes. In case the device has recognized an error it will react in the following way:

Exception Code	Designation	Description
1	Illegal Function	The message received includes a function code which is not supported by the Slave.
2	Illegal Data Address	Access was sought on a data word address not included in the data module.
3	Illegal Data Value	The received message contains an invalid data structure (e.g. wrong number of data bytes).
4	Slave Device Failure	An unrecoverable error occurred while the server (or slave) was attempting to perform the requested action.

The response given by the *device* in a failure case has the following format:

Slave Address	0x80 + Function Code	Exception Code	Check-sum HI	Check-sum LO
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In the second Byte of the response the Function Code is sent with the highest Bit set to 1. This is equivalent to an addition by 0x80. The third Byte holds the Exception Code of the error message.

Appendix - Data Point Lists

Signals

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
BO Slot X2		1003	1	3	Struct			
	BO 1	1003	1	3	Bit	0x1 (1)	-	Signal: Binary Output Relay
	BO 2	1003	1	3	Bit	0x2 (2)	-	Signal: Binary Output Relay
	BO 3	1003	1	3	Bit	0x4 (3)	-	Signal: Binary Output Relay
	BO 4	1003	1	3	Bit	0x8 (4)	-	Signal: Binary Output Relay
	BO 5	1003	1	3	Bit	0x10 (5)	-	Signal: Binary Output Relay
	BO 6	1003	1	3	Bit	0x20 (6)	-	Signal: Binary Output Relay

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	DISARMED!	1003	1	3	Bit	0x40 (7)	-	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
	Outs forced	1003	1	3	Bit	0x80 (8)	-	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
BO Slot X5		1004	1	3	Struct			
	BO 1	1004	1	3	Bit	0x1 (1)	-	Signal: Binary Output Relay
	BO 2	1004	1	3	Bit	0x2 (2)	-	Signal: Binary Output Relay
	BO 3	1004	1	3	Bit	0x4 (3)	-	Signal: Binary Output Relay
	BO 4	1004	1	3	Bit	0x8 (4)	-	Signal: Binary Output Relay
	BO 5	1004	1	3	Bit	0x10 (5)	-	Signal: Binary Output Relay

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	BO 6	1004	1	3	Bit	0x20 (6)	-	Signal: Binary Output Relay
	DISARMED!	1004	1	3	Bit	0x40 (7)	-	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
	Outs forced	1004	1	3	Bit	0x80 (8)	-	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.
CBF[1]		53	1	3	Struct			
	ExBlo1-I	53	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	53	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	53	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	53	1	3	Bit	0x8 (4)	-	Signal: External Blocking

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trigger1-l	53	1	3	Bit	0x10 (5)	-	Module Input: Trigger that will start the CBF
	Trigger2-l	53	1	3	Bit	0x20 (6)	-	Module Input: Trigger that will start the CBF
	Trigger3-l	53	1	3	Bit	0x40 (7)	-	Module Input: Trigger that will start the CBF
	running	53	1	3	Bit	0x80 (8)	-	Signal: CBF-Module started
	Alarm (*)	53	1	3	Bit	0x100 (9)	-	Signal: Circuit Breaker Failure
	Lockout (*)	53	1	3	Bit	0x200 (10)	-	Signal: Lockout
	Waiting for Trigger (*)	53	1	3	Bit	0x400 (11)	-	Waiting for Trigger
CBF[2]		120	1	3	Struct			
	ExBlo1-l	120	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	120	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	120	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	120	1	3	Bit	0x8 (4)	-	Signal: External Blocking

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trigger1-I	120	1	3	Bit	0x10 (5)	-	Module Input: Trigger that will start the CBF
	Trigger2-I	120	1	3	Bit	0x20 (6)	-	Module Input: Trigger that will start the CBF
	Trigger3-I	120	1	3	Bit	0x40 (7)	-	Module Input: Trigger that will start the CBF
	running	120	1	3	Bit	0x80 (8)	-	Signal: CBF-Module started
	Alarm (*)	120	1	3	Bit	0x100 (9)	-	Signal: Circuit Breaker Failure
	Lockout	120	1	3	Bit	0x200 (10)	-	Signal: Lockout
	Waiting for Trigger (*)	120	1	3	Bit	0x400 (11)	-	Waiting for Trigger
CLPU		66	1	3	Struct			
	ExBlo1-I	66	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	ExBlo2-I	66	1	3	Bit	0x2 (2)	-	Module input state: External blocking
	Ex rev Interl-I	66	1	3	Bit	0x4 (3)	-	Module input state: External reverse interlocking
	active	66	1	3	Bit	0x8 (4)	-	Signal: active

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo	66	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Ex rev Interl	66	1	3	Bit	0x20 (6)	-	Signal: External reverse Interlocking
	CB Pos-I	66	1	3	Bit	0x180 (8)	-	Module input state: Circuit Breaker Position by now (switching position).
	enabled	66	1	3	Bit	0x200 (10)	-	Signal: Cold Load enabled
	detected (*)	66	1	3	Bit	0x400 (11)	-	Signal: Cold Load detected
	I<	66	1	3	Bit	0x800 (12)	-	Signal: No Load Current.
	Load Inrush	66	1	3	Bit	0x2000 (14)	-	Signal: Load Inrush
	Settle Time	66	1	3	Bit	0x4000 (15)	-	Signal: Settle Time
CTS[1]		137	1	3	Struct			
	ExBlo1-I	137	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	137	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	active	137	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	137	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	137	1	3	Bit	0x10 (5)	-	Signal: Alarm Current Transformer Measuring Circuit Supervision
CTS[2]		138	1	3	Struct			
	ExBlo1-l	138	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	138	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	138	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	138	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	138	1	3	Bit	0x10 (5)	-	Signal: Alarm Current Transformer Measuring Circuit Supervision
Ctrl		176	1	3	Struct			
	Local	176	1	3	Bit	0x1 (1)	-	Switching Authority: Local
	Remote	176	1	3	Bit	0x2 (2)	-	Switching Authority: Remote

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	NonInterl	176	1	3	Bit	0x4 (3)	-	Non-Interlocking is active
	SG Disturb	176	1	3	Bit	0x8 (4)	-	Minimum one Switchgear is disturbed.
	SG Indeterm	176	1	3	Bit	0x10 (5)	-	Minimum one Switchgear is moving (Position cannot be determined).
DI Slot X 1		1000	1	3	Struct			
	DI 1	1000	1	3	Bit	0x1 (1)	-	Signal: Digital Input
	DI 2	1000	1	3	Bit	0x2 (2)	-	Signal: Digital Input
	DI 3	1000	1	3	Bit	0x4 (3)	-	Signal: Digital Input
	DI 4	1000	1	3	Bit	0x8 (4)	-	Signal: Digital Input
	DI 5	1000	1	3	Bit	0x10 (5)	-	Signal: Digital Input
	DI 6	1000	1	3	Bit	0x20 (6)	-	Signal: Digital Input
	DI 7	1000	1	3	Bit	0x40 (7)	-	Signal: Digital Input

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	DI 8	1000	1	3	Bit	0x80 (8)	-	Signal: Digital Input
DI Slot X 6		1001	1	3	Struct			
	DI 1	1001	1	3	Bit	0x1 (1)	-	Signal: Digital Input
	DI 2	1001	1	3	Bit	0x2 (2)	-	Signal: Digital Input
	DI 3	1001	1	3	Bit	0x4 (3)	-	Signal: Digital Input
	DI 4	1001	1	3	Bit	0x8 (4)	-	Signal: Digital Input
	DI 5	1001	1	3	Bit	0x10 (5)	-	Signal: Digital Input
	DI 6	1001	1	3	Bit	0x20 (6)	-	Signal: Digital Input
	DI 7	1001	1	3	Bit	0x40 (7)	-	Signal: Digital Input
	DI 8	1001	1	3	Bit	0x80 (8)	-	Signal: Digital Input
Ex Oil Temp		125	1	3	Struct			
	ExBlo1-l	125	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo2-I	125	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	125	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	125	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	125	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	125	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	125	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	125	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Alarm-I	125	1	3	Bit	0x100 (9)	-	Module input state: Alarm
	Trip (*)	125	1	3	Bit	0x200 (10)	-	Signal: Trip
	Trip-I (*)	125	1	3	Bit	0x400 (11)	-	Module input state: Trip
	TripCmd (*)	125	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[1]		49	1	3	Struct			

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo1-I	49	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	49	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	49	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	49	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	49	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	49	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	49	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	49	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	49	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	49	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	49	1	3	Bit	0x400 (11)	-	Signal: Trip

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	TripCmd (*)	49	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[2]		50	1	3	Struct			
	ExBlo1-I	50	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	50	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	50	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	50	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	50	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	50	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	50	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	50	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	50	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	50	1	3	Bit	0x200 (10)	-	Signal: Alarm

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trip (*)	50	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	50	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[3]		51	1	3	Struct			
	ExBlo1-I	51	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	51	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	51	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	51	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	51	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	51	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	51	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	51	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	51	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm	51	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	51	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	51	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[4]		52	1	3	Struct			
	ExBlo1-I	52	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	52	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	52	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	52	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	52	1	3	Bit	0x10 (5)	-	Module input state: Trip
	active	52	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	52	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	52	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo TripCmd	52	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	52	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	52	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	52	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Ext Sudd Press		126	1	3	Struct			
	ExBlo1-I	126	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	126	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	126	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	126	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	126	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	126	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	126	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm	126	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Alarm-I	126	1	3	Bit	0x100 (9)	-	Module input state: Alarm
	Trip (*)	126	1	3	Bit	0x200 (10)	-	Signal: Trip
	Trip-I (*)	126	1	3	Bit	0x400 (11)	-	Module input state: Trip
	TripCmd (*)	126	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Ext Temp Superv[1]		127	1	3	Struct			
	ExBlo1-I	127	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	127	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	127	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	127	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	127	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	127	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo TripCmd	127	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	127	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Alarm-I	127	1	3	Bit	0x100 (9)	-	Module input state: Alarm
	Trip (*)	127	1	3	Bit	0x200 (10)	-	Signal: Trip
	Trip-I (*)	127	1	3	Bit	0x400 (11)	-	Module input state: Trip
	TripCmd (*)	127	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Ext Temp Superv[2]		128	1	3	Struct			
	ExBlo1-I	128	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	128	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	128	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	128	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	128	1	3	Bit	0x10 (5)	-	Signal: External Blocking

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Blo TripCmd	128	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	128	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	128	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Alarm-I	128	1	3	Bit	0x100 (9)	-	Module input state: Alarm
	Trip (*)	128	1	3	Bit	0x200 (10)	-	Signal: Trip
	Trip-I (*)	128	1	3	Bit	0x400 (11)	-	Module input state: Trip
	TripCmd (*)	128	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Ext Temp Superv[3]		129	1	3	Struct			
	ExBlo1-I	129	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	129	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	129	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	129	1	3	Bit	0x8 (4)	-	Signal: active

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo	129	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	129	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	129	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	129	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Alarm-I	129	1	3	Bit	0x100 (9)	-	Module input state: Alarm
	Trip (*)	129	1	3	Bit	0x200 (10)	-	Signal: Trip
	Trip-I (*)	129	1	3	Bit	0x400 (11)	-	Module input state: Trip
	TripCmd (*)	129	1	3	Bit	0x800 (12)	-	Signal: Trip Command
I2>[1]		82	1	3	Struct			
	ExBlo1-I	82	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	82	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	82	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	active	82	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	82	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	82	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	82	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	82	1	3	Bit	0x80 (8)	-	Signal: Alarm Negative Sequence
	Trip (*)	82	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	82	1	3	Bit	0x200 (10)	-	Signal: Trip Command
I2>[2]		83	1	3	Struct			
	ExBlo1-I	83	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	83	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	83	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	83	1	3	Bit	0x8 (4)	-	Signal: active

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo	83	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	83	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	83	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	83	1	3	Bit	0x80 (8)	-	Signal: Alarm Negative Sequence
	Trip (*)	83	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	83	1	3	Bit	0x200 (10)	-	Signal: Trip Command
IG[1]		15	1	3	Struct			
	ExBlo1-I	15	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	15	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	15	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	15	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	15	1	3	Bit	0x10 (5)	-	Signal: active

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo	15	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	15	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	15	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	15	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	15	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	15	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	15	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	15	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IG[2]		16	1	3	Struct			
	ExBlo1-I	16	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	16	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	16	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Ex rev Interl-I	16	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	16	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	16	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	16	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	16	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	16	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	16	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	16	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	16	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	16	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IG[3]		17	1	3	Struct			
	ExBlo1-I	17	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo2-I	17	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	17	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	17	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	17	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	17	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	17	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	17	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	17	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	17	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush
	Alarm	17	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	17	1	3	Bit	0x800 (12)	-	Signal: Trip

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	TripCmd (*)	17	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IG[4]		18	1	3	Struct			
	ExBlo1-I	18	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	18	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	18	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	18	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	18	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	18	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	18	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	18	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	18	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IGH2 Blo	18	1	3	Bit	0x200 (10)	-	Signal: blocked by an inrush

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm	18	1	3	Bit	0x400 (11)	-	Signal: Alarm IG
	Trip (*)	18	1	3	Bit	0x800 (12)	-	Signal: Trip
	TripCmd (*)	18	1	3	Bit	0x1000 (13)	-	Signal: Trip Command
IH2[1]		22	1	3	Struct			
	ExBlo1-l	22	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	22	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	22	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	22	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Blo L1	22	1	3	Bit	0x10 (5)	-	Signal: Blocked L1
	Blo L2	22	1	3	Bit	0x20 (6)	-	Signal: Blocked L2
	Blo L3	22	1	3	Bit	0x40 (7)	-	Signal: Blocked L3

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Blo IG meas	22	1	3	Bit	0x80 (8)	-	Signal: Blocking of the ground (earth) protection module (measured ground current)
	3-ph Blo	22	1	3	Bit	0x100 (9)	-	Signal: Inrush was detected in at least one phase - trip command blocked.
	Blo IG calc	22	1	3	Bit	0x200 (10)	-	Signal: Blocking of the ground (earth) protection module (calculated ground current)
IH2[2]		122	1	3	Struct			
	ExBlo1-I	122	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	122	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	122	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	122	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Blo L1	122	1	3	Bit	0x10 (5)	-	Signal: Blocked L1
	Blo L2	122	1	3	Bit	0x20 (6)	-	Signal: Blocked L2
	Blo L3	122	1	3	Bit	0x40 (7)	-	Signal: Blocked L3

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Blo IG meas	122	1	3	Bit	0x80 (8)	-	Signal: Blocking of the ground (earth) protection module (measured ground current)
	3-ph Blo	122	1	3	Bit	0x100 (9)	-	Signal: Inrush was detected in at least one phase - trip command blocked.
	Blo IG calc	122	1	3	Bit	0x200 (10)	-	Signal: Blocking of the ground (earth) protection module (calculated ground current)
IRIG-B		148	1	3	Struct			
	active	148	1	3	Bit	0x1 (1)	-	Signal: active
	inverted	148	1	3	Bit	0x2 (2)	-	Signal: IRIG-B inverted
	Control Signal1	148	1	3	Bit	0x4 (3)	-	Signal: IRIG-B Control Signal
	Control Signal2	148	1	3	Bit	0x8 (4)	-	Signal: IRIG-B Control Signal
	Control Signal4	148	1	3	Bit	0x10 (5)	-	Signal: IRIG-B Control Signal
	Control Signal5	148	1	3	Bit	0x20 (6)	-	Signal: IRIG-B Control Signal
	Control Signal6	148	1	3	Bit	0x40 (7)	-	Signal: IRIG-B Control Signal

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Control Signal7	148	1	3	Bit	0x80 (8)	-	Signal: IRIG-B Control Signal
	Control Signal8	148	1	3	Bit	0x100 (9)	-	Signal: IRIG-B Control Signal
	Control Signal9	148	1	3	Bit	0x200 (10)	-	Signal: IRIG-B Control Signal
	Control Signal10	148	1	3	Bit	0x400 (11)	-	Signal: IRIG-B Control Signal
	Control Signal11	148	1	3	Bit	0x800 (12)	-	Signal: IRIG-B Control Signal
	Control Signal12	148	1	3	Bit	0x1000 (13)	-	Signal: IRIG-B Control Signal
	Control Signal13	148	1	3	Bit	0x2000 (14)	-	Signal: IRIG-B Control Signal
	Control Signal14	148	1	3	Bit	0x4000 (15)	-	Signal: IRIG-B Control Signal
	Control Signal15	148	1	3	Bit	0x8000 (16)	-	Signal: IRIG-B Control Signal
IRIG-B		149	1	3	Struct			
	Control Signal16	149	1	3	Bit	0x1 (1)	-	Signal: IRIG-B Control Signal
	Control Signal17	149	1	3	Bit	0x2 (2)	-	Signal: IRIG-B Control Signal

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Control Signal18	149	1	3	Bit	0x4 (3)	-	Signal: IRIG-B Control Signal
I[1]		3	1	3	Struct			
	ExBlo1-I	3	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	3	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	3	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	3	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	3	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	3	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	3	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	3	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	3	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	3	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
I[1]		4	1	3	Struct			
	Alarm L1	4	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	4	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	4	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	4	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	4	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	4	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	4	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	4	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	4	1	3	Bit	0x100 (9)	-	Signal: Trip Command
I[2]		5	1	3	Struct			
	ExBlo1-I	5	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo2-I	5	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	5	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	5	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	5	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	5	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	5	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	5	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	5	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	5	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[3]		7	1	3	Struct			
	ExBlo1-I	7	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	7	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo TripCmd-I	7	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	7	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	7	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	7	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	7	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	7	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	7	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	7	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[3]		8	1	3	Struct			
	Alarm L1	8	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	8	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	8	1	3	Bit	0x4 (3)	-	Signal: Alarm L3

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm	8	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	8	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	8	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	8	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	8	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	8	1	3	Bit	0x100 (9)	-	Signal: Trip Command
I[4]		9	1	3	Struct			
	ExBlo1-I	9	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	9	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	9	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Ex rev Interl-I	9	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	9	1	3	Bit	0x10 (5)	-	Signal: active

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo	9	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	9	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	Blo TripCmd	9	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	9	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	IH2 Blo	9	1	3	Bit	0x200 (10)	-	Signal: Blocking the trip command by an inrush
I[4]		10	1	3	Struct			
	Alarm L1	10	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	10	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	10	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	10	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	10	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	10	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trip L3 (*)	10	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	10	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	10	1	3	Bit	0x100 (9)	-	Signal: Trip Command
Id		130	1	3	Struct			
	ExBlo1-l	130	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	130	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-l	130	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	130	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	130	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	130	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	130	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Blo H2	130	1	3	Bit	0x80 (8)	-	Signal: Blocked by Harmonic:2

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Blo H4	130	1	3	Bit	0x100 (9)	-	Signal: Blocked by Harmonic:4
	Blo H5	130	1	3	Bit	0x200 (10)	-	Signal: Blocked by Harmonic:5
	H2,H4,H5 Blo	130	1	3	Bit	0x400 (11)	-	Signal: Blocked by Harmonics (Inhibit)
	Slope Blo	130	1	3	Bit	0x800 (12)	-	Signal: Differential protection was blocked by current transformer saturation. The tripping characteristic was lifted because of current transformer saturation.
	Restraining	130	1	3	Bit	0x1000 (13)	-	Signal: Restraining of the differential protection by means of rising the tripping curve.
	Transient	130	1	3	Bit	0x2000 (14)	-	Signal: Temporary stabilization of the differential protection afterwards the transformer is being energized.
Id		131	1	3	Struct			
	Alarm L1	131	1	3	Bit	0x1 (1)	-	Signal: Alarm System Phase L1
	Alarm L2	131	1	3	Bit	0x2 (2)	-	Signal: Alarm System Phase L2

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm L3	131	1	3	Bit	0x4 (3)	-	Signal: Alarm System L3
	Alarm	131	1	3	Bit	0x8 (4)	-	Signal: Alarm
	Trip L1 (*)	131	1	3	Bit	0x10 (5)	-	Signal: Trip System Phase L1
	Trip L2 (*)	131	1	3	Bit	0x20 (6)	-	Signal: Trip System Phase L2
	Trip L3 (*)	131	1	3	Bit	0x40 (7)	-	Signal: Trip System Phase L3
	Trip (*)	131	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	131	1	3	Bit	0x100 (9)	-	Signal: Trip Command
	Slope Blo: L1	131	1	3	Bit	0x200 (10)	-	Slope Blo: L1
	Slope Blo: L2	131	1	3	Bit	0x400 (11)	-	Slope Blo: L2
	Slope Blo: L3	131	1	3	Bit	0x800 (12)	-	Slope Blo: L3
	Restraining: L1	131	1	3	Bit	0x1000 (13)	-	Restraining: L1

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Restraining: L2	131	1	3	Bit	0x2000 (14)	-	Restraining: L2
	Restraining: L3	131	1	3	Bit	0x4000 (15)	-	Restraining: L3
Id		262	1	3	Struct			
	IH2 Blo L1	262	1	3	Bit	0x1 (1)	-	Signal:Phase L1: Blocking of the Phase Differential Protection because of second Harmonic.
	IH2 Blo L2	262	1	3	Bit	0x2 (2)	-	Signal:Phase L2: Blocking of the Phase Differential Protection because of second Harmonic.
	IH2 Blo L3	262	1	3	Bit	0x4 (3)	-	Signal:Phase L3: Blocking of the Phase Differential Protection because of second Harmonic.
	IH4 Blo L1	262	1	3	Bit	0x8 (4)	-	Signal:Phase L1: Blocking of the Phase Differential Protection because of fourth Harmonic.
	IH4 Blo L2 (*)	262	1	3	Bit	0x10 (5)	-	Signal:Phase L2: Blocking of the Phase Differential Protection because of fourth Harmonic.
	IH4 Blo L3 (*)	262	1	3	Bit	0x20 (6)	-	Signal:Phase L3: Blocking of the Phase Differential Protection because of fourth Harmonic.

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	IH5 Blo L1 (*)	262	1	3	Bit	0x40 (7)	-	Signal:Phase L1: Blocking of the Phase Differential Protection because of fifth Harmonic.
	IH5 Blo L2 (*)	262	1	3	Bit	0x80 (8)	-	Signal:Phase L2: Blocking of the Phase Differential Protection because of fifth Harmonic.
	IH5 Blo L3 (*)	262	1	3	Bit	0x100 (9)	-	Signal:Phase L3: Blocking of the Phase Differential Protection because of fifth Harmonic.
IdGH[1]		134	1	3	Struct			
	ExBlo1-I	134	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	134	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	134	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	134	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	134	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	134	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo TripCmd	134	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	134	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Trip (*)	134	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	134	1	3	Bit	0x200 (10)	-	Signal: Trip Command
IdGH[2]		135	1	3	Struct			
	ExBlo1-I	135	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	135	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	135	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	135	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	135	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	135	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	135	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm	135	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Trip (*)	135	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	135	1	3	Bit	0x200 (10)	-	Signal: Trip Command
IdG[1]		132	1	3	Struct			
	ExBlo1-l	132	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	132	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-l	132	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	132	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	132	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	132	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	132	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	132	1	3	Bit	0x80 (8)	-	Signal: Alarm

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trip (*)	132	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	132	1	3	Bit	0x200 (10)	-	Signal: Trip Command
IdG[2]		133	1	3	Struct			
	ExBlo1-I	133	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	133	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	133	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	133	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	133	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	133	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	133	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	133	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Trip (*)	133	1	3	Bit	0x100 (9)	-	Signal: Trip

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	TripCmd (*)	133	1	3	Bit	0x200 (10)	-	Signal: Trip Command
IdH		136	1	3	Struct			
	ExBlo1-I	136	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	136	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	136	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	136	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	136	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	136	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	136	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	136	1	3	Bit	0x80 (8)	-	Signal: Alarm
	Alarm L1	136	1	3	Bit	0x100 (9)	-	Signal: Alarm System Phase L1
	Alarm L2	136	1	3	Bit	0x200 (10)	-	Signal: Alarm System Phase L2

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm L3	136	1	3	Bit	0x400 (11)	-	Signal: Alarm System L3
	Trip (*)	136	1	3	Bit	0x800 (12)	-	Signal: Trip
	Trip L1 (*)	136	1	3	Bit	0x1000 (13)	-	Signal: Trip System Phase L1
	Trip L2 (*)	136	1	3	Bit	0x2000 (14)	-	Signal: Trip System Phase L2
	Trip L3 (*)	136	1	3	Bit	0x4000 (15)	-	Signal: Trip System Phase L3
	TripCmd (*)	136	1	3	Bit	0x8000 (16)	-	Signal: Trip Command
Logics		1100	1	3	Struct			
	LE1.Gate Out	1100	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE1.Timer Out	1100	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE1.Out	1100	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE1.Out inverted	1100	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE1.Gate In1-l	1100	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE1.Gate In2-l	1100	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In3-l	1100	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In4-l	1100	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE1.Reset Latch-l	1100	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1101	1	3	Struct			
	LE2.Gate Out	1101	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE2.Timer Out	1101	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE2.Out	1101	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE2.Out inverted	1101	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE2.Gate In1-l	1101	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In2-l	1101	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In3-l	1101	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE2.Gate In4-l	1101	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE2.Reset Latch-l	1101	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1102	1	3	Struct			
	LE3.Gate Out	1102	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE3.Timer Out	1102	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE3.Out	1102	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE3.Out inverted	1102	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE3.Gate In1-l	1102	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In2-l	1102	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In3-l	1102	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In4-l	1102	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE3.Reset Latch-l	1102	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Logics		1103	1	3	Struct			
	LE4.Gate Out	1103	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE4.Timer Out	1103	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE4.Out	1103	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE4.Out inverted	1103	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE4.Gate In1-l	1103	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In2-l	1103	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In3-l	1103	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In4-l	1103	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE4.Reset Latch-l	1103	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1104	1	3	Struct			
	LE5.Gate Out	1104	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE5.Timer Out	1104	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE5.Out	1104	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE5.Out inverted	1104	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE5.Gate In1-l	1104	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In2-l	1104	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In3-l	1104	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In4-l	1104	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE5.Reset Latch-l	1104	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1105	1	3	Struct			
	LE6.Gate Out	1105	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE6.Timer Out	1105	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE6.Out	1105	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE6.Out inverted	1105	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE6.Gate In1-l	1105	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In2-l	1105	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In3-l	1105	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In4-l	1105	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE6.Reset Latch-l	1105	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1106	1	3	Struct			
	LE7.Gate Out	1106	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE7.Timer Out	1106	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE7.Out	1106	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE7.Out inverted	1106	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE7.Gate In1-l	1106	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE7.Gate In2-l	1106	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In3-l	1106	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In4-l	1106	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE7.Reset Latch-l	1106	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1107	1	3	Struct			
	LE8.Gate Out	1107	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE8.Timer Out	1107	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE8.Out	1107	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE8.Out inverted	1107	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE8.Gate In1-l	1107	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In2-l	1107	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In3-l	1107	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE8.Gate In4-l	1107	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE8.Reset Latch-l	1107	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1108	1	3	Struct			
	LE9.Gate Out	1108	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE9.Timer Out	1108	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE9.Out	1108	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE9.Out inverted	1108	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE9.Gate In1-l	1108	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In2-l	1108	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In3-l	1108	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In4-l	1108	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE9.Reset Latch-l	1108	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Logics		1109	1	3	Struct			
	LE10.Gate Out	1109	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE10.Timer Out	1109	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE10.Out	1109	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE10.Out inverted	1109	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE10.Gate In1-l	1109	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In2-l	1109	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In3-l	1109	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In4-l	1109	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE10.Reset Latch-l	1109	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1110	1	3	Struct			
	LE11.Gate Out	1110	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE11.Timer Out	1110	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE11.Out	1110	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE11.Out inverted	1110	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE11.Gate In1-l	1110	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In2-l	1110	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In3-l	1110	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In4-l	1110	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE11.Reset Latch-l	1110	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1111	1	3	Struct			
	LE12.Gate Out	1111	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE12.Timer Out	1111	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE12.Out	1111	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE12.Out inverted	1111	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE12.Gate In1-l	1111	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In2-l	1111	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In3-l	1111	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In4-l	1111	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE12.Reset Latch-l	1111	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1112	1	3	Struct			
	LE13.Gate Out	1112	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE13.Timer Out	1112	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE13.Out	1112	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE13.Out inverted	1112	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE13.Gate In1-l	1112	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE13.Gate In2-l	1112	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In3-l	1112	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In4-l	1112	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE13.Reset Latch-l	1112	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1113	1	3	Struct			
	LE14.Gate Out	1113	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE14.Timer Out	1113	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE14.Out	1113	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE14.Out inverted	1113	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE14.Gate In1-l	1113	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In2-l	1113	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In3-l	1113	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE14.Gate In4-l	1113	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE14.Reset Latch-l	1113	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1114	1	3	Struct			
	LE15.Gate Out	1114	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE15.Timer Out	1114	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE15.Out	1114	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE15.Out inverted	1114	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE15.Gate In1-l	1114	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In2-l	1114	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In3-l	1114	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In4-l	1114	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE15.Reset Latch-l	1114	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Logics		1115	1	3	Struct			
	LE16.Gate Out	1115	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE16.Timer Out	1115	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE16.Out	1115	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE16.Out inverted	1115	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE16.Gate In1-l	1115	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In2-l	1115	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In3-l	1115	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In4-l	1115	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE16.Reset Latch-l	1115	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1116	1	3	Struct			
	LE17.Gate Out	1116	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE17.Timer Out	1116	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE17.Out	1116	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE17.Out inverted	1116	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE17.Gate In1-l	1116	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In2-l	1116	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In3-l	1116	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In4-l	1116	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE17.Reset Latch-l	1116	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1117	1	3	Struct			
	LE18.Gate Out	1117	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE18.Timer Out	1117	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE18.Out	1117	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE18.Out inverted	1117	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE18.Gate In1-l	1117	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In2-l	1117	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In3-l	1117	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In4-l	1117	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE18.Reset Latch-l	1117	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1118	1	3	Struct			
	LE19.Gate Out	1118	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE19.Timer Out	1118	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE19.Out	1118	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE19.Out inverted	1118	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE19.Gate In1-l	1118	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE19.Gate In2-l	1118	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In3-l	1118	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In4-l	1118	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE19.Reset Latch-l	1118	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1119	1	3	Struct			
	LE20.Gate Out	1119	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE20.Timer Out	1119	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE20.Out	1119	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE20.Out inverted	1119	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE20.Gate In1-l	1119	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In2-l	1119	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In3-l	1119	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	LE20.Gate In4-I	1119	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE20.Reset Latch-I	1119	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Modbus		1005	1	3	Struct			
	Scada Cmd 1	1005	1	3	Bit	0x1 (1)	-	Scada Command
	Scada Cmd 2	1005	1	3	Bit	0x2 (2)	-	Scada Command
	Scada Cmd 3	1005	1	3	Bit	0x4 (3)	-	Scada Command
	Scada Cmd 4	1005	1	3	Bit	0x8 (4)	-	Scada Command
	Scada Cmd 5	1005	1	3	Bit	0x10 (5)	-	Scada Command
	Scada Cmd 6	1005	1	3	Bit	0x20 (6)	-	Scada Command
	Scada Cmd 7	1005	1	3	Bit	0x40 (7)	-	Scada Command
	Scada Cmd 8	1005	1	3	Bit	0x80 (8)	-	Scada Command
	Scada Cmd 9	1005	1	3	Bit	0x100 (9)	-	Scada Command

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Scada Cmd 10	1005	1	3	Bit	0x200 (10)	-	Scada Command
	Scada Cmd 11	1005	1	3	Bit	0x400 (11)	-	Scada Command
	Scada Cmd 12	1005	1	3	Bit	0x800 (12)	-	Scada Command
	Scada Cmd 13	1005	1	3	Bit	0x1000 (13)	-	Scada Command
	Scada Cmd 14	1005	1	3	Bit	0x2000 (14)	-	Scada Command
	Scada Cmd 15	1005	1	3	Bit	0x4000 (15)	-	Scada Command
	Scada Cmd 16	1005	1	3	Bit	0x8000 (16)	-	Scada Command
Modbus		1006	1	3	Struct			
	Transmission	1006	1	3	Bit	0x1 (1)	-	Signal: SCADA active
PSet-Switch		59	1	3	Struct			
	PS 1	59	1	3	Bit	0x1 (1)	-	Signal: Parameter Set 1
	PS 2	59	1	3	Bit	0x2 (2)	-	Signal: Parameter Set 2

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	PS 3	59	1	3	Bit	0x4 (3)	-	Signal: Parameter Set 3
	PS 4	59	1	3	Bit	0x8 (4)	-	Signal: Parameter Set 4
	PSS manual	59	1	3	Bit	0x10 (5)	-	Signal: Manual Switch over of a Parameter Set
	PSS via Scada	59	1	3	Bit	0x20 (6)	-	Signal: Parameter Set Switch via Scada
	PSS via Inp fct	59	1	3	Bit	0x40 (7)	-	Signal: Parameter Set Switch via input function
	PS1-I	59	1	3	Bit	0x80 (8)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS2-I	59	1	3	Bit	0x100 (9)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS3-I	59	1	3	Bit	0x200 (10)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS4-I	59	1	3	Bit	0x400 (11)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	min 1 param changed (*)	59	1	3	Bit	0x800 (12)	-	Signal: At least one parameter has been changed
Prot		1	1	3	Struct			
	ExBlo1-I	1	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	1	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	1	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	1	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm L1	1	1	3	Bit	0x10 (5)	-	Signal: General-Alarm L1
	Alarm L2	1	1	3	Bit	0x20 (6)	-	Signal: General-Alarm L2
	Alarm L3	1	1	3	Bit	0x40 (7)	-	Signal: General-Alarm L3
	Alarm G	1	1	3	Bit	0x80 (8)	-	Signal: General-Alarm - Earth fault
	Alarm	1	1	3	Bit	0x100 (9)	-	Signal: General Alarm
	Trip L1 (*)	1	1	3	Bit	0x200 (10)	-	Signal: General Trip L1

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trip L2 (*)	1	1	3	Bit	0x400 (11)	-	Signal: General Trip L2
	Trip L3 (*)	1	1	3	Bit	0x800 (12)	-	Signal: General Trip L3
	Trip G (*)	1	1	3	Bit	0x1000 (13)	-	Signal: General Trip Ground fault
	Trip (*)	1	1	3	Bit	0x2000 (14)	-	Signal: General Trip
Prot		2	1	3	Struct			
	Blo TripCmd	2	1	3	Bit	0x1 (1)	-	Signal: Trip Command blocked
	ExBlo TripCmd-I	2	1	3	Bit	0x2 (2)	-	Module input state: External Blocking of the Trip Command
	ExBlo TripCmd	2	1	3	Bit	0x4 (3)	-	Signal: External Blocking of the Trip Command
Prot		57	1	3	Struct			
	FaultNo	57	1	3	Bit	0xffff (1)	-	Disturbance No
Prot		58	1	3	Struct			

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	No of grid faults	58	1	3	Bit	0xffff (1)	-	Number of grid faults: A grid fault, e.g. a short circuit, might cause several faults with trip and autoreclosing, each fault being identified by an increased fault number. In this case, the grid fault number remains the same.
RTD		143	1	3	Struct			
	ExBlo1-I	143	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	143	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	143	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	143	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	143	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	143	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	143	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	143	1	3	Bit	0x80 (8)	-	Alarm RTD Temperature Protection

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trip (*)	143	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	143	1	3	Bit	0x200 (10)	-	Signal: Trip Command
RTD		144	1	3	Struct			
	W1L1 Alarm	144	1	3	Bit	0x1 (1)	-	Winding1 Phase L1 Alarm RTD Temperature Protection
	W1L1 Timeout Alarm	144	1	3	Bit	0x2 (2)	-	Winding1 Phase L1 Timeout Alarm
	W1L1 Trip (*)	144	1	3	Bit	0x4 (3)	-	Winding1 Phase L1 Signal: Trip
	W1L1 Invalid	144	1	3	Bit	0x8 (4)	-	Winding1 Phase L1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	W1L2 Alarm	144	1	3	Bit	0x10 (5)	-	Winding1 Phase L2 Alarm RTD Temperature Protection
	W1L2 Timeout Alarm	144	1	3	Bit	0x20 (6)	-	Winding1 Phase L2 Timeout Alarm
	W1L2 Trip (*)	144	1	3	Bit	0x40 (7)	-	Winding1 Phase L2 Signal: Trip

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	W1L2 Invalid	144	1	3	Bit	0x80 (8)	-	Winding1 Phase L2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	W1L3 Alarm	144	1	3	Bit	0x100 (9)	-	Winding1 Phase L3 Alarm RTD Temperature Protection
	W1L3 Timeout Alarm	144	1	3	Bit	0x200 (10)	-	Winding1 Phase L3 Timeout Alarm
	W1L3 Trip (*)	144	1	3	Bit	0x400 (11)	-	Winding1 Phase L3 Signal: Trip
	W1L3 Invalid	144	1	3	Bit	0x800 (12)	-	Winding1 Phase L3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	W2L1 Alarm	144	1	3	Bit	0x1000 (13)	-	Winding2 Phase L1 Alarm RTD Temperature Protection
	W2L1 Timeout Alarm	144	1	3	Bit	0x2000 (14)	-	Winding2 Phase L1 Timeout Alarm
	W2L1 Trip (*)	144	1	3	Bit	0x4000 (15)	-	Winding2 Phase L1 Signal: Trip

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	W2L1 Invalid	144	1	3	Bit	0x8000 (16)	-	Winding2 Phase L1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD		145	1	3	Struct			
	W2L2 Alarm	145	1	3	Bit	0x1 (1)	-	Winding2 Phase L2 Alarm RTD Temperature Protection
	W2L2 Timeout Alarm	145	1	3	Bit	0x2 (2)	-	Winding2 Phase L2 Timeout Alarm
	W2L2 Trip (*)	145	1	3	Bit	0x4 (3)	-	Winding2 Phase L2 Signal: Trip
	W2L2 Invalid	145	1	3	Bit	0x8 (4)	-	Winding2 Phase L2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	W2L3 Alarm	145	1	3	Bit	0x10 (5)	-	Winding2 Phase L3 Alarm RTD Temperature Protection
	W2L3 Timeout Alarm	145	1	3	Bit	0x20 (6)	-	Winding2 Phase L3 Timeout Alarm
	W2L3 Trip (*)	145	1	3	Bit	0x40 (7)	-	Winding2 Phase L3 Signal: Trip

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	W2L3 Invalid	145	1	3	Bit	0x80 (8)	-	Winding2 Phase L3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Amb 1 Alarm	145	1	3	Bit	0x100 (9)	-	Ambient 1 Alarm RTD Temperature Protection
	Amb 1 Timeout Alarm	145	1	3	Bit	0x200 (10)	-	Ambient 1 Timeout Alarm
	Amb 1 Trip (*)	145	1	3	Bit	0x400 (11)	-	Ambient 1 Signal: Trip
	Amb 1 Invalid	145	1	3	Bit	0x800 (12)	-	Ambient 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Amb 2 Alarm	145	1	3	Bit	0x1000 (13)	-	Ambient 2 Alarm RTD Temperature Protection
	Amb 2 Timeout Alarm	145	1	3	Bit	0x2000 (14)	-	Ambient 2 Timeout Alarm
	Amb 2 Trip (*)	145	1	3	Bit	0x4000 (15)	-	Ambient 2 Signal: Trip

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Amb 2 Invalid	145	1	3	Bit	0x8000 (16)	-	Ambient 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD		146	1	3	Struct			
	Aux 1 Alarm	146	1	3	Bit	0x1 (1)	-	Auxiliary 1 Alarm RTD Temperature Protection
	Aux 1 Timeout Alarm	146	1	3	Bit	0x2 (2)	-	Auxiliary 1 Timeout Alarm
	Aux 1 Trip (*)	146	1	3	Bit	0x4 (3)	-	Auxiliary 1 Signal: Trip
	Aux 1 Invalid	146	1	3	Bit	0x8 (4)	-	Auxiliary 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Aux 2 Alarm	146	1	3	Bit	0x10 (5)	-	Auxiliary 2 Alarm RTD Temperature Protection
	Aux 2 Timeout Alarm	146	1	3	Bit	0x20 (6)	-	Auxiliary 2 Timeout Alarm
	Aux 2 Trip (*)	146	1	3	Bit	0x40 (7)	-	Auxiliary 2 Signal: Trip

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Aux 2 Invalid	146	1	3	Bit	0x80 (8)	-	Auxiliary 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Aux 3 Alarm	146	1	3	Bit	0x100 (9)	-	Auxiliary 3 Alarm RTD Temperature Protection
	Aux 3 Timeout Alarm	146	1	3	Bit	0x200 (10)	-	Auxiliary 3 Timeout Alarm
	Aux 3 Trip (*)	146	1	3	Bit	0x400 (11)	-	Auxiliary 3 Signal: Trip
	Aux 3 Invalid	146	1	3	Bit	0x800 (12)	-	Auxiliary 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Windg W1 Group Invalid	146	1	3	Bit	0x1000 (13)	-	Winding W1 Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Windg W2 Group Invalid	146	1	3	Bit	0x2000 (14)	-	Winding W2 Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
RTD		147	1	3	Struct			

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Amb Group Invalid	147	1	3	Bit	0x1 (1)	-	Ambient Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Alarm Amb Group	147	1	3	Bit	0x2 (2)	-	Alarm all Windings of group Ambient
	TimeoutAlmAmbGrp	147	1	3	Bit	0x4 (3)	-	Timeout Alarm of group Ambient
	Trip Amb Group (*)	147	1	3	Bit	0x8 (4)	-	Trip all Windings of group Ambient
	Alarm WD W2 Group	147	1	3	Bit	0x10 (5)	-	Alarm all Windings of group W2
	TimeoutAlmWDW2Grp	147	1	3	Bit	0x20 (6)	-	Timeout Alarm of group W2
	Trip WD W2 Group (*)	147	1	3	Bit	0x40 (7)	-	Trip all Windings of group W2
	Alarm WD W1 Group	147	1	3	Bit	0x80 (8)	-	Alarm all Windings of group W1
	TimeoutAlmWDW1Grp	147	1	3	Bit	0x100 (9)	-	Timeout Alarm of group W1
	Trip WD W1 Group (*)	147	1	3	Bit	0x200 (10)	-	Trip all Windings of group W1

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Trip Group 1 (*)	147	1	3	Bit	0x2000 (14)	-	Trip Group 1
	Trip Group 2 (*)	147	1	3	Bit	0x4000 (15)	-	Trip Group 2
RTD		205	1	3	Struct			
	Alarm Any Group	205	1	3	Bit	0x1 (1)	-	Alarm Any Group
	Trip Any Group (*)	205	1	3	Bit	0x2 (2)	-	Trip Any Group
	TimeoutAlmAnyGrp	205	1	3	Bit	0x4 (3)	-	Timeout Alarm Any Group
	Aux4 Alarm	205	1	3	Bit	0x8 (4)	-	Auxiliary 4 Alarm RTD Temperature Protection
	Aux4 Timeout Alarm	205	1	3	Bit	0x10 (5)	-	Auxiliary 4 Timeout Alarm
	Aux4 Invalid	205	1	3	Bit	0x20 (6)	-	Auxiliary 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)
	Aux4 Trip (*)	205	1	3	Bit	0x40 (7)	-	Auxiliary 4 Signal: Trip
	AuxGrpInvalid	205	1	3	Bit	0x80 (8)	-	Invalid Auxiliary Group

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Alarm Aux Group	205	1	3	Bit	0x100 (9)	-	Alarm Auxiliary Group
	TimeoutAlmAuxGrp	205	1	3	Bit	0x200 (10)	-	Timeout Alarm Auxiliary Group
	Trip Aux Group (*)	205	1	3	Bit	0x400 (11)	-	Trip Auxiliary Group
SG[1]		177	1	3	Struct			
	Aux OFF-I	177	1	3	Bit	0x1 (1)	-	Module input state: Position indicator/check-back signal of the CB (52b)
	Aux ON-I	177	1	3	Bit	0x2 (2)	-	Module Input State: Position indicator/check-back signal of the CB (52a)
	Ready-I	177	1	3	Bit	0x4 (3)	-	Module input state: CB ready
	Sys-in-Sync-I	177	1	3	Bit	0x8 (4)	-	State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.
	Interl OFF1-I	177	1	3	Bit	0x10 (5)	-	State of the module input: Interlocking of the OFF command
	Interl OFF2-I	177	1	3	Bit	0x20 (6)	-	State of the module input: Interlocking of the OFF command

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Interl OFF3-I	177	1	3	Bit	0x40 (7)	-	State of the module input: Interlocking of the OFF command
	Interl ON1-I	177	1	3	Bit	0x80 (8)	-	State of the module input: Interlocking of the ON command
	Interl ON2-I	177	1	3	Bit	0x100 (9)	-	State of the module input: Interlocking of the ON command
	Interl ON3-I	177	1	3	Bit	0x200 (10)	-	State of the module input: Interlocking of the ON command
	SCmd OFF-I	177	1	3	Bit	0x800 (12)	-	State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input
	SCmd ON-I	177	1	3	Bit	0x1000 (13)	-	State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input
	TripCmd (*)	177	1	3	Bit	0x2000 (14)	-	Signal: Trip Command
	OFF Cmd	177	1	3	Bit	0x4000 (15)	-	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
	OFF Cmd manual	177	1	3	Bit	0x8000 (16)	-	Signal: OFF Cmd manual

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
SG[1]		178	1	3	Struct			
	ON Cmd	178	1	3	Bit	0x1 (1)	-	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
	ON Cmd manual	178	1	3	Bit	0x2 (2)	-	Signal: ON Cmd manual
	Sync ON request	178	1	3	Bit	0x4 (3)	-	Signal: Synchronous ON request
	SGwear Slow SG	178	1	3	Bit	0x8 (4)	-	Signal: Alarm, the circuit breaker (load-break switch) becomes slower
	Res SGwear SI SG	178	1	3	Bit	0x10 (5)	-	Signal: Resetting the slow Switchgear Alarm
	CES Disturbed	178	1	3	Bit	0x40 (7)	-	Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.
	CES Fiel Interl	178	1	3	Bit	0x80 (8)	-	Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.
	CES ON d OFF	178	1	3	Bit	0x400 (11)	-	Signal: Command Execution Supervision: On Command during a pending OFF Command.

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	CES SwitchDir	178	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.
	CES SG not ready	178	1	3	Bit	0x1000 (13)	-	Signal: Command Execution Supervision: Switchgear not ready
	CES SyncTimeout	178	1	3	Bit	0x2000 (14)	-	Signal: Command Execution Supervision: Switching Command not executed. No Synchronization signal while t-sync was running.
	CES succesf	178	1	3	Bit	0x4000 (15)	-	Signal: Command Execution Supervision: Switching command executed successfully.
	Prot ON	178	1	3	Bit	0x8000 (16)	-	Signal: ON Command issued by the Prot module
SG[1]		179	1	3	Struct			

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Pos Disturb	179	1	3	Bit	0x1 (1)	-	Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
	t-Dwell	179	1	3	Bit	0x2 (2)	-	Signal: Dwell time
	Pos Indeterm	179	1	3	Bit	0x4 (3)	-	Signal: Circuit Breaker is in Indeterminate Position
	Pos OFF	179	1	3	Bit	0x8 (4)	-	Signal: Circuit Breaker is in OFF-Position
	Pos ON	179	1	3	Bit	0x10 (5)	-	Signal: Circuit Breaker is in ON-Position
	Ready	179	1	3	Bit	0x20 (6)	-	Signal: Circuit breaker is ready for operation.
	Pos not ON	179	1	3	Bit	0x40 (7)	-	Signal: Pos not ON
	SI SingleContactInd	179	1	3	Bit	0x80 (8)	-	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Position Ind manipul	179	1	3	Bit	0x100 (9)	-	Signal: Position Indicators faked
	OFF incl TripCmd	179	1	3	Bit	0x200 (10)	-	Signal: The OFF Command includes the OFF Command issued by the Protection module.
	ON incl Prot ON	179	1	3	Bit	0x400 (11)	-	Signal: The ON Command includes the ON Command issued by the Protection module.
	CES Fail TripCmd	179	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision: Command execution failed because trip command is pending.
	Interl OFF	179	1	3	Bit	0x1000 (13)	-	Signal: One or more IL_Off inputs are active.
	Interl ON	179	1	3	Bit	0x2000 (14)	-	Signal: One or more IL_On inputs are active.
SG[1]		195	1	3	Struct			
	Isum Intr trip	195	1	3	Bit	0x10 (5)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.
	Isum Intr trip: IL1	195	1	3	Bit	0x20 (6)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Isum Intr trip: IL2	195	1	3	Bit	0x40 (7)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2
	Isum Intr trip: IL3	195	1	3	Bit	0x80 (8)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3
	Operations Alarm	195	1	3	Bit	0x100 (9)	-	Signal: Service Alarm, too many Operations
	WearLevel Alarm	195	1	3	Bit	0x200 (10)	-	Signal: Threshold for the Alarm
	WearLevel Lockout	195	1	3	Bit	0x400 (11)	-	Signal: Threshold for the Lockout Level
	Isum Intr ph Alm	195	1	3	Bit	0x800 (12)	-	Signal: Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.
SG[1]		256	1	3	Struct			
	Removed-I	256	1	3	Bit	0x1 (1)	-	State of the module input: The withdrawable circuit breaker is Removed
	CES SG removed	256	1	3	Bit	0x2 (2)	-	Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.
	Removed	256	1	3	Bit	0x4 (3)	-	Signal: The withdrawable circuit breaker is Removed

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
SG[2]		180	1	3	Struct			
	Aux OFF-I	180	1	3	Bit	0x1 (1)	-	Module input state: Position indicator/check-back signal of the CB (52b)
	Aux ON-I	180	1	3	Bit	0x2 (2)	-	Module Input State: Position indicator/check-back signal of the CB (52a)
	Ready-I	180	1	3	Bit	0x4 (3)	-	Module input state: CB ready
	Sys-in-Sync-I	180	1	3	Bit	0x8 (4)	-	State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.
	Interl OFF1-I	180	1	3	Bit	0x10 (5)	-	State of the module input: Interlocking of the OFF command
	Interl OFF2-I	180	1	3	Bit	0x20 (6)	-	State of the module input: Interlocking of the OFF command
	Interl OFF3-I	180	1	3	Bit	0x40 (7)	-	State of the module input: Interlocking of the OFF command
	Interl ON1-I	180	1	3	Bit	0x80 (8)	-	State of the module input: Interlocking of the ON command
	Interl ON2-I	180	1	3	Bit	0x100 (9)	-	State of the module input: Interlocking of the ON command

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Interl ON3-I	180	1	3	Bit	0x200 (10)	-	State of the module input: Interlocking of the ON command
	SCmd OFF-I	180	1	3	Bit	0x800 (12)	-	State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input
	SCmd ON-I	180	1	3	Bit	0x1000 (13)	-	State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input
	TripCmd (*)	180	1	3	Bit	0x2000 (14)	-	Signal: Trip Command
	OFF Cmd	180	1	3	Bit	0x4000 (15)	-	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
	OFF Cmd manual	180	1	3	Bit	0x8000 (16)	-	Signal: OFF Cmd manual
SG[2]		181	1	3	Struct			
	ON Cmd	181	1	3	Bit	0x1 (1)	-	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
	ON Cmd manual	181	1	3	Bit	0x2 (2)	-	Signal: ON Cmd manual

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Sync ON request	181	1	3	Bit	0x4 (3)	-	Signal: Synchronous ON request
	SGwear Slow SG	181	1	3	Bit	0x8 (4)	-	Signal: Alarm, the circuit breaker (load-break switch) becomes slower
	Res SGwear SI SG	181	1	3	Bit	0x10 (5)	-	Signal: Resetting the slow Switchgear Alarm
	CES Disturbed	181	1	3	Bit	0x40 (7)	-	Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.
	CES Fiel Interl	181	1	3	Bit	0x80 (8)	-	Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.
	CES ON d OFF	181	1	3	Bit	0x400 (11)	-	Signal: Command Execution Supervision: On Command during a pending OFF Command.

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	CES SwitchDir	181	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.
	CES SG not ready	181	1	3	Bit	0x1000 (13)	-	Signal: Command Execution Supervision: Switchgear not ready
	CES SyncTimeout	181	1	3	Bit	0x2000 (14)	-	Signal: Command Execution Supervision: Switching Command not executed. No Synchronization signal while t-sync was running.
	CES succesf	181	1	3	Bit	0x4000 (15)	-	Signal: Command Execution Supervision: Switching command executed successfully.
	Prot ON	181	1	3	Bit	0x8000 (16)	-	Signal: ON Command issued by the Prot module
SG[2]		182	1	3	Struct			

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Pos Disturb	182	1	3	Bit	0x1 (1)	-	Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
	t-Dwell	182	1	3	Bit	0x2 (2)	-	Signal: Dwell time
	Pos Indeterm	182	1	3	Bit	0x4 (3)	-	Signal: Circuit Breaker is in Indeterminate Position
	Pos OFF	182	1	3	Bit	0x8 (4)	-	Signal: Circuit Breaker is in OFF-Position
	Pos ON	182	1	3	Bit	0x10 (5)	-	Signal: Circuit Breaker is in ON-Position
	Ready	182	1	3	Bit	0x20 (6)	-	Signal: Circuit breaker is ready for operation.
	Pos not ON	182	1	3	Bit	0x40 (7)	-	Signal: Pos not ON
	SI SingleContactInd	182	1	3	Bit	0x80 (8)	-	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Position Ind manipul	182	1	3	Bit	0x100 (9)	-	Signal: Position Indicators faked
	OFF incl TripCmd	182	1	3	Bit	0x200 (10)	-	Signal: The OFF Command includes the OFF Command issued by the Protection module.
	ON incl Prot ON	182	1	3	Bit	0x400 (11)	-	Signal: The ON Command includes the ON Command issued by the Protection module.
	CES Fail TripCmd	182	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision: Command execution failed because trip command is pending.
	Interl OFF	182	1	3	Bit	0x1000 (13)	-	Signal: One or more IL_Off inputs are active.
	Interl ON	182	1	3	Bit	0x2000 (14)	-	Signal: One or more IL_On inputs are active.
SG[2]		196	1	3	Struct			
	Isum Intr trip	196	1	3	Bit	0x10 (5)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.
	Isum Intr trip: IL1	196	1	3	Bit	0x20 (6)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Isum Intr trip: IL2	196	1	3	Bit	0x40 (7)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2
	Isum Intr trip: IL3	196	1	3	Bit	0x80 (8)	-	Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3
	Operations Alarm	196	1	3	Bit	0x100 (9)	-	Signal: Service Alarm, too many Operations
	WearLevel Alarm	196	1	3	Bit	0x200 (10)	-	Signal: Threshold for the Alarm
	WearLevel Lockout	196	1	3	Bit	0x400 (11)	-	Signal: Threshold for the Lockout Level
	Isum Intr ph Alm	196	1	3	Bit	0x800 (12)	-	Signal: Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.
SG[2]		257	1	3	Struct			
	Removed-I	257	1	3	Bit	0x1 (1)	-	State of the module input: The withdrawable circuit breaker is Removed
	CES SG removed	257	1	3	Bit	0x2 (2)	-	Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.
	Removed	257	1	3	Bit	0x4 (3)	-	Signal: The withdrawable circuit breaker is Removed

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
SOTF		65	1	3	Struct			
	ExBlo1-I	65	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	ExBlo2-I	65	1	3	Bit	0x2 (2)	-	Module input state: External blocking
	Ext SOTF-I	65	1	3	Bit	0x4 (3)	-	Module input state: External Switch Onto Fault Alarm
	Ex rev Interl-I	65	1	3	Bit	0x8 (4)	-	Module input state: External reverse interlocking
	active	65	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	65	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Ex rev Interl	65	1	3	Bit	0x40 (7)	-	Signal: External reverse Interlocking
	enabled	65	1	3	Bit	0x800 (12)	-	Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.
	I<	65	1	3	Bit	0x2000 (14)	-	Signal: No Load Current.
Sgen		1012	1	3	Struct			
	ExBlo	1012	1	3	Bit	0x1 (1)	-	Module input state: External blocking

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Ex ForcePost-I	1012	1	3	Bit	0x2 (2)	-	State of the module input: Force Post state. Abort simulation.
	Running	1012	1	3	Bit	0x10 (5)	-	Signal; Measuring value simulation is running
	State	1012	1	3	Bit	0xe0 (6)	-	Signal: Wave generation states: 0=Off, 1=PreFault, 2=Fault, 3=PostFault, 4=InitReset
	Ex Start Simulation-I	1012	1	3	Bit	0x100 (9)	-	State of the module input: External Start of Fault Simulation (Using the test parameters)
Sys		154	1	3	Struct			
	Lock Settings-I	154	1	3	Bit	0x40 (7)	-	State of the module input: No parameters can be changed as long as this input is true. The parameter settings are locked.
	SNTP active	154	1	3	Bit	0x80 (8)	-	Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.
	Setting Lock Bypass	154	1	3	Bit	0x100 (9)	-	Signal: Short-period unlock of the Setting Lock
SysA		173	1	3	Struct			
	ExBlo-I	173	1	3	Bit	0x1 (1)	-	Module input state: External blocking

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo	173	1	3	Bit	0x2 (2)	-	Signal: External Blocking
	Alm Current Demd	173	1	3	Bit	0x4 (3)	-	Signal: Alarm averaged demand current
	active	173	1	3	Bit	0x8 (4)	-	Signal: active
	Alarm I THD	173	1	3	Bit	0x10 (5)	-	Signal: Alarm Total Harmonic Distortion Current
	Trip Current Demand (*)	173	1	3	Bit	0x1000 (13)	-	Signal: Trip averaged demand current
	Trip I THD (*)	173	1	3	Bit	0x2000 (14)	-	Signal: Trip Total Harmonic Distortion Current
TCS[1]		150	1	3	Struct			
	ExBlo1-I	150	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	150	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	150	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	150	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	150	1	3	Bit	0x10 (5)	-	Signal: Alarm Trip Circuit Supervision

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Not Possible	150	1	3	Bit	0x20 (6)	-	Not possible because no state indicator assigned to the breaker.
	CB Pos Detect-I	150	1	3	Bit	0xc0 (7)	-	Module input state: Criterion by which the Circuit Breaker Switch Position is to be detected.
	Aux ON-I	150	1	3	Bit	0x100 (9)	-	Module Input State: Position indicator/check-back signal of the CB (52a)
	Aux OFF-I	150	1	3	Bit	0x200 (10)	-	Module input state: Position indicator/check-back signal of the CB (52b)
TCS[2]		151	1	3	Struct			
	ExBlo1-I	151	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	151	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	active	151	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	151	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	151	1	3	Bit	0x10 (5)	-	Signal: Alarm Trip Circuit Supervision

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Not Possible	151	1	3	Bit	0x20 (6)	-	Not possible because no state indicator assigned to the breaker.
	CB Pos Detect-I	151	1	3	Bit	0xc0 (7)	-	Module input state: Criterion by which the Circuit Breaker Switch Position is to be detected.
	Aux ON-I	151	1	3	Bit	0x100 (9)	-	Module Input State: Position indicator/check-back signal of the CB (52a)
	Aux OFF-I	151	1	3	Bit	0x200 (10)	-	Module input state: Position indicator/check-back signal of the CB (52b)
ThR		19	1	3	Struct			
	ExBlo1-I	19	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	19	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	19	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	active	19	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	19	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	19	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	ExBlo TripCmd	19	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	19	1	3	Bit	0x80 (8)	-	Signal: Alarm Thermal Overload
	Trip (*)	19	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	19	1	3	Bit	0x200 (10)	-	Signal: Trip Command
URTD		1007	1	3	Struct			
	W1L1 Superv	1007	1	3	Bit	0x1 (1)	-	Signal: Supervision Channel Winding1 Phase L1
	W1L2 Superv	1007	1	3	Bit	0x2 (2)	-	Signal: Supervision Channel Winding1 Phase L2
	W1L3 Superv	1007	1	3	Bit	0x4 (3)	-	Signal: Supervision Channel Winding1 Phase L3
	W2L1 Superv	1007	1	3	Bit	0x8 (4)	-	Signal: Supervision Channel Winding2 Phase L1
	W2L2 Superv	1007	1	3	Bit	0x10 (5)	-	Signal: Supervision Channel Winding2 Phase L2
	W2L3 Superv	1007	1	3	Bit	0x20 (6)	-	Signal: Supervision Channel Winding2 Phase L3
	Amb1 Superv	1007	1	3	Bit	0x40 (7)	-	Signal: Supervision Channel Ambient1

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	Amb2 Superv	1007	1	3	Bit	0x80 (8)	-	Signal: Supervision Channel Ambient2
	Aux1 Superv	1007	1	3	Bit	0x100 (9)	-	Signal: Supervision Channel Auxiliary1
	Aux2 Superv	1007	1	3	Bit	0x200 (10)	-	Signal: Supervision Channel Auxiliary2
	Aux3 Superv	1007	1	3	Bit	0x400 (11)	-	Signal: Supervision Channel Auxiliary3
	Superv	1007	1	3	Bit	0x800 (12)	-	Signal: URTD Supervision Channel
	Aux4 Superv	1007	1	3	Bit	0x1000 (13)	-	Signal: Supervision Channel Auxiliary4

Legend * = These Signals have to be acknowledged by the Scada System.

Measuring values

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
CT W1	IL1	20100	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
CT W1	IL2	20102	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
CT W1	IL3	20104	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
CT W1	IG meas	20106	2	4	Float IEE754		A	Measured value (measured): IG (fundamental)
CT W1	I0	20114	2	4	Float IEE754		A	Measured value (calculated): Zero current (fundamental)
CT W1	I1	20116	2	4	Float IEE754		A	Measured value (calculated): Positive phase sequence current (fundamental)
CT W1	I2	20118	2	4	Float IEE754		A	Measured value (calculated): Unbalanced load current (fundamental)
CT W1	IL1 H2	20120	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL1
CT W1	IL2 H2	20122	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL2
CT W1	IL3 H2	20124	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL3

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W1	IG H2 meas	20126	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IG (measured)
CT W1	IG calc	20160	2	4	Float IEE754		A	Measured value (calculated): IG (fundamental)
CT W1	phi IG calc	20200	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IG calc
CT W1	phi IG meas	20202	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IG meas
CT W1	phi IL1	20204	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL1
CT W1	phi IL2	20206	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL2
CT W1	phi IL3	20208	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL3
CT W1	IL1 THD	20210	2	4	Float IEE754		A	Measured value (calculated): IL1 Total Harmonic Current
CT W1	IL2 THD	20212	2	4	Float IEE754		A	Measured value (calculated): IL2 Total Harmonic Current
CT W1	IL3 THD	20214	2	4	Float IEE754		A	Measured value (calculated): IL3 Total Harmonic Current
CT W1	%IL1 THD	20216	2	4	Float IEE754		%	Measured value (calculated): IL1 Total Harmonic Distortion

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W1	%IL2 THD	20218	2	4	Float IEE754		%	Measured value (calculated): IL2 Total Harmonic Distortion
CT W1	%IL3 THD	20220	2	4	Float IEE754		%	Measured value (calculated): IL3 Total Harmonic Distortion
CT W1	IL1 RMS	20316	2	4	Float IEE754		A	Measured value: Phase current (RMS)
CT W1	IL2 RMS	20318	2	4	Float IEE754		A	Measured value: Phase current (RMS)
CT W1	IL3 RMS	20320	2	4	Float IEE754		A	Measured value: Phase current (RMS)
CT W1	IG meas RMS	20322	2	4	Float IEE754		A	Measured value (measured): IG (RMS)
CT W1	IG calc RMS	20324	2	4	Float IEE754		A	Measured value (calculated): IG (RMS)
CT W1	%(I2/I1)	20376	2	4	Float IEE754		%	Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.
CT W1	phi I0	20378	2	4	Float IEE754		°	Measured value (calculated): Angle Zero Sequence System
CT W1	phi I1	20380	2	4	Float IEE754		°	Measured value (calculated): Angle of Positive Sequence System

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W1	phi I2	20382	2	4	Float IEE754		°	Measured Value (calculated): Angle of Negative Sequence System
CT W1	IG H2 calc	20500	2	4	Float IEE754		%	Measured value (calculated): 2nd harmonic/1st harmonic of IG (calculated)
CT W1	I1 max	21074	2	4	Float IEE754		A	Maximum value positive phase sequence current (fundamental)
CT W1	I1 min	21076	2	4	Float IEE754		A	Minimum value positive phase sequence current (fundamental)
CT W1	I2 max	21080	2	4	Float IEE754		A	Maximum value unbalanced load (fundamental)
CT W1	I2 min	21082	2	4	Float IEE754		A	Minimum value unbalanced load current (fundamental)
CT W1	IL1 avg RMS	21130	2	4	Float IEE754		A	IL1 average value (RMS)
CT W1	IL2 avg RMS	21132	2	4	Float IEE754		A	IL2 average value (RMS)
CT W1	IL3 avg RMS	21134	2	4	Float IEE754		A	IL3 average value (RMS)
CT W1	IL1 max RMS	21136	2	4	Float IEE754		A	IL1 maximum value (RMS)

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W1	IL2 max RMS	21138	2	4	Float IEE754		A	IL2 maximum value (RMS)
CT W1	IL3 max RMS	21140	2	4	Float IEE754		A	IL3 maximum value (RMS)
CT W1	IL1 min RMS	21142	2	4	Float IEE754		A	IL1 minimum value (RMS)
CT W1	IL2 min RMS	21144	2	4	Float IEE754		A	IL2 minimum value (RMS)
CT W1	IL3 min RMS	21146	2	4	Float IEE754		A	IL3 minimum value (RMS)
CT W1	IG H2 meas max	21222	2	4	Float IEE754		%	Measured value: Maximum ratio of 2nd harmonic over fundamental of IG (measured)
CT W1	IG H2 meas min	21224	2	4	Float IEE754		%	Measured value: Minimum ratio of 2nd harmonic over fundamental of IG (measured)
CT W1	IL1 H2 max	21228	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL1
CT W1	IL1 H2 min	21230	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic over fundamental of IL1
CT W1	IL2 H2 max	21234	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL2

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W1	IL2 H2 min	21236	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic over fundamental of IL2
CT W1	IL3 H2 max	21240	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL3
CT W1	IL3 H2 min	21242	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic/1st harmonic minimum value of IL3
CT W1	IG calc max RMS	21456	2	4	Float IEE754		A	Measured value (calculated):IG maximum value (RMS)
CT W1	IG calc min RMS	21458	2	4	Float IEE754		A	Measured value (calculated):IG minimum value (RMS)
CT W1	IG meas max RMS	21462	2	4	Float IEE754		A	Measured value: IG maximum value (RMS)
CT W1	IG meas min RMS	21464	2	4	Float IEE754		A	Measured value: IG minimum value (RMS)
CT W1	%(I2/I1) max	21468	2	4	Float IEE754		%	Measured value (calculated): I2/I1 maximum value, phase sequence will be taken into account automatically
CT W1	%(I2/I1) min	21470	2	4	Float IEE754		%	Measured value (calculated): I2/I1 minimum value, phase sequence will be taken into account automatically

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W1	IG H2 calc max	21774	2	4	Float IEE754		%	Measured value (calculated): Maximum ratio of 2nd harmonic over fundamental of IG (calculated)
CT W1	IG H2 calc min	21776	2	4	Float IEE754		%	IG H2 calc min
CT W1	IL1 Peak demand	21784	2	4	Float IEE754		A	IL1 Peak value, RMS value
CT W1	IL2 Peak demand	21786	2	4	Float IEE754		A	IL2 Peak value, RMS value
CT W1	IL3 Peak demand	21788	2	4	Float IEE754		A	IL3 Peak value, RMS value
CT W2	I0	20222	2	4	Float IEE754		A	Measured value (calculated): Zero current (fundamental)
CT W2	I1	20224	2	4	Float IEE754		A	Measured value (calculated): Positive phase sequence current (fundamental)
CT W2	I2	20226	2	4	Float IEE754		A	Measured value (calculated): Unbalanced load current (fundamental)
CT W2	IG calc	20228	2	4	Float IEE754		A	Measured value (calculated): IG (fundamental)
CT W2	IG meas	20230	2	4	Float IEE754		A	Measured value (measured): IG (fundamental)

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W2	IL1	20232	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
CT W2	IL2	20234	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
CT W2	IL3	20236	2	4	Float IEE754		A	Measured value: Phase current (fundamental)
CT W2	IG H2 meas	20238	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IG (measured)
CT W2	IL1 H2	20240	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL1
CT W2	IL2 H2	20242	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL2
CT W2	IL3 H2	20244	2	4	Float IEE754		%	Measured value: 2nd harmonic/1st harmonic of IL3
CT W2	IG calc RMS	20248	2	4	Float IEE754		A	Measured value (calculated): IG (RMS)
CT W2	IG meas RMS	20250	2	4	Float IEE754		A	Measured value (measured): IG (RMS)
CT W2	IL1 RMS	20252	2	4	Float IEE754		A	Measured value: Phase current (RMS)
CT W2	IL2 RMS	20254	2	4	Float IEE754		A	Measured value: Phase current (RMS)

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W2	IL3 RMS	20256	2	4	Float IEE754		A	Measured value: Phase current (RMS)
CT W2	phi IG calc	20258	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IG calc
CT W2	phi IG meas	20260	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IG meas
CT W2	phi IL1	20262	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL1
CT W2	phi IL2	20264	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL2
CT W2	phi IL3	20266	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor IL3
CT W2	IL1 THD	20268	2	4	Float IEE754		A	Measured value (calculated): IL1 Total Harmonic Current
CT W2	IL2 THD	20270	2	4	Float IEE754		A	Measured value (calculated): IL2 Total Harmonic Current
CT W2	IL3 THD	20272	2	4	Float IEE754		A	Measured value (calculated): IL3 Total Harmonic Current
CT W2	%IL1 THD	20274	2	4	Float IEE754		%	Measured value (calculated): IL1 Total Harmonic Distortion
CT W2	%IL2 THD	20276	2	4	Float IEE754		%	Measured value (calculated): IL2 Total Harmonic Distortion

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W2	%IL3 THD	20278	2	4	Float IEE754		%	Measured value (calculated): IL3 Total Harmonic Distortion
CT W2	%(I2/I1)	20488	2	4	Float IEE754		%	Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.
CT W2	phi I0	20490	2	4	Float IEE754		°	Measured value (calculated): Angle Zero Sequence System
CT W2	phi I1	20492	2	4	Float IEE754		°	Measured value (calculated): Angle of Positive Sequence System
CT W2	phi I2	20494	2	4	Float IEE754		°	Measured Value (calculated): Angle of Negative Sequence System
CT W2	IG H2 calc	20502	2	4	Float IEE754		%	Measured value (calculated): 2nd harmonic/1st harmonic of IG (calculated)
CT W2	IL1 avg RMS	21256	2	4	Float IEE754		A	IL1 average value (RMS)
CT W2	IL1 max RMS	21258	2	4	Float IEE754		A	IL1 maximum value (RMS)
CT W2	IL1 min RMS	21260	2	4	Float IEE754		A	IL1 minimum value (RMS)
CT W2	IL2 avg RMS	21262	2	4	Float IEE754		A	IL2 average value (RMS)

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W2	IL2 max RMS	21264	2	4	Float IEE754		A	IL2 maximum value (RMS)
CT W2	IL2 min RMS	21266	2	4	Float IEE754		A	IL2 minimum value (RMS)
CT W2	IL3 avg RMS	21268	2	4	Float IEE754		A	IL3 average value (RMS)
CT W2	IL3 max RMS	21270	2	4	Float IEE754		A	IL3 maximum value (RMS)
CT W2	IL3 min RMS	21272	2	4	Float IEE754		A	IL3 minimum value (RMS)
CT W2	I1 max	21276	2	4	Float IEE754		A	Maximum value positive phase sequence current (fundamental)
CT W2	I1 min	21278	2	4	Float IEE754		A	Minimum value positive phase sequence current (fundamental)
CT W2	I2 max	21282	2	4	Float IEE754		A	Maximum value unbalanced load (fundamental)
CT W2	I2 min	21284	2	4	Float IEE754		A	Minimum value unbalanced load current (fundamental)
CT W2	IG H2 meas max	21306	2	4	Float IEE754		%	Measured value: Maximum ratio of 2nd harmonic over fundamental of IG (measured)

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W2	IG H2 meas min	21308	2	4	Float IEE754		%	Measured value: Minimum ratio of 2nd harmonic over fundamental of IG (measured)
CT W2	IL1 H2 max	21312	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL1
CT W2	IL1 H2 min	21314	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic over fundamental of IL1
CT W2	IL2 H2 max	21318	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL2
CT W2	IL2 H2 min	21320	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic over fundamental of IL2
CT W2	IL3 H2 max	21324	2	4	Float IEE754		%	Maximum ratio of 2nd harmonic over fundamental of IL3
CT W2	IL3 H2 min	21326	2	4	Float IEE754		%	Minimum ratio of 2nd harmonic/1st harmonic minimum value of IL3
CT W2	IG calc max RMS	21756	2	4	Float IEE754		A	Measured value (calculated):IG maximum value (RMS)
CT W2	IG calc min RMS	21758	2	4	Float IEE754		A	Measured value (calculated):IG minimum value (RMS)
CT W2	IG meas max RMS	21762	2	4	Float IEE754		A	Measured value: IG maximum value (RMS)

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
CT W2	IG meas min RMS	21764	2	4	Float IEE754		A	Measured value: IG minimum value (RMS)
CT W2	%(I2/I1) max	21768	2	4	Float IEE754		%	Measured value (calculated): I2/I1 maximum value, phase sequence will be taken into account automatically
CT W2	%(I2/I1) min	21770	2	4	Float IEE754		%	Measured value (calculated): I2/I1 minimum value, phase sequence will be taken into account automatically
CT W2	IG H2 calc max	21780	2	4	Float IEE754		%	Measured value (calculated): Maximum ratio of 2nd harmonic over fundamental of IG (calculated)
CT W2	IG H2 calc min	21782	2	4	Float IEE754		%	IG H2 calc min
CT W2	IL1 Peak demand	21930	2	4	Float IEE754		A	IL1 Peak value, RMS value
CT W2	IL2 Peak demand	21932	2	4	Float IEE754		A	IL2 Peak value, RMS value
CT W2	IL3 Peak demand	21934	2	4	Float IEE754		A	IL3 Peak value, RMS value
Date and Time		20000	6	4	Struct			
	y	20000	6	4	Short	Word 0 (1)	-	year

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
	m	20000	6	4	Short	Word 1 (17)	-	month
	d	20000	6	4	Short	Word 2 (33)	-	days
	h	20000	6	4	Short	Word 3 (49)	-	hours
	min	20000	6	4	Short	Word 4 (65)	-	minute
	ms	20000	6	4	Short	Word 5 (81)	-	milliseconds
IRIG-B	Edges	20298	2	4	Float IEE754		-	Edges
IRIG-B	NoOfFrameErrors	20300	2	4	Float IEE754		-	Total Number of Frame Errors. Physically corrupted Frame.
IRIG-B	NoOfFramesOK	20302	2	4	Float IEE754		-	Total Number valid Frames.
Id	Id L1 H2	20280	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L1 Harmonic:2
Id	Id L2 H2	20282	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L2 Harmonic:2
Id	Id L3 H2	20284	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L3 Harmonic:2

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Id	Id L1 H4	20286	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L1 Harmonic:4
Id	Id L2 H4	20288	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L2 Harmonic:4
Id	Id L3 H4	20290	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L3 Harmonic:4
Id	Id L1 H5	20292	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L1 Harmonic:5
Id	Id L2 H5	20294	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L2 Harmonic:5
Id	Id L3 H5	20296	2	4	Float IEE754		%	Measured value (calculated): Differential Current Phase L3 Harmonic:5
Id	Id L1	20352	2	4	Float IEE754		lb	Measured value (calculated): Differential Current Phase L1
Id	Id L2	20354	2	4	Float IEE754		lb	Measured value (calculated): Differential Current Phase L2
Id	Id L3	20356	2	4	Float IEE754		lb	Measured value (calculated): Differential Current Phase L3
Id	IS L1	20358	2	4	Float IEE754		lb	Measured value (calculated): Restraint Current Phase L1

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Id	IS L2	20360	2	4	Float IEE754		lb	Measured value (calculated): Restraint Current Phase L2
Id	IS L3	20362	2	4	Float IEE754		lb	Measured value (calculated): Restraint Current Phase L3
Id	Id L1H2max	21342	2	4	Float IEE754		%	Maximum Value Id L1H2
Id	Id L2H2max	21348	2	4	Float IEE754		%	Maximum Value Id L2H2
Id	Id L3H2max	21354	2	4	Float IEE754		%	Maximum Value Id L3H2
Id	Id L1H4max	21360	2	4	Float IEE754		%	Maximum Value Id L1H4
Id	Id L2H4max	21366	2	4	Float IEE754		%	Maximum Value Id L2H4
Id	Id L3H4max	21372	2	4	Float IEE754		%	Maximum Value Id L3H4
Id	Id L1H5max	21378	2	4	Float IEE754		%	Maximum Value Id L1H5
Id	Id L2H5max	21384	2	4	Float IEE754		%	Maximum Value Id L2H5
Id	Id L3H5max	21390	2	4	Float IEE754		%	Maximum Value Id L3H5

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
IdG	Idg W1	20364	2	4	Float IEE754		A	Measured value (calculated): Ground Differential Current Winding 1
IdG	ISG W1	20366	2	4	Float IEE754		A	Measured value (calculated): Ground Stabilizing Current Winding 1
IdG	Idg W2	20368	2	4	Float IEE754		A	Measured value (calculated): Ground Differential Current Winding 2
IdG	ISG W2	20370	2	4	Float IEE754		A	Measured value (calculated): Ground Stabilizing Current Winding 2
RTD	Hottest WD W1	20504	2	4	Float IEE754		°C	Hottest winding on side W1
RTD	Hottest WD W2	20506	2	4	Float IEE754		°C	Hottest winding on side W2
RTD	Hottest Amb	20508	2	4	Float IEE754		°C	Hottest Ambient Temperature
RTD	Hottest Aux Temp	21820	2	4	Float IEE754		°C	Hottest Auxiliary temperature in degrees C.
SG[1]	Sum trip IL1	20800	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[1]	Sum trip IL2	20802	2	4	Float IEE754		A	Summation of the tripping currents phase

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
SG[1]	Sum trip IL3	20804	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[1]	Isum Intr per hour	20806	2	4	Float IEE754		kA	Sum per hour of interrupting currents.
SG[1]	SG OPEN capacity	20808	2	4	Float IEE754		%	Used capacity. 100% means, that the switchgear is to be maintained.
SG[1]	TripCmd Cr	20810	2	4	Float IEE754		-	Counter: Total number of trips of the switchgear (circuit breaker, load break switch...). Resettable with Total or All.
SG[2]	Sum trip IL1	20812	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[2]	Sum trip IL2	20814	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[2]	Sum trip IL3	20816	2	4	Float IEE754		A	Summation of the tripping currents phase
SG[2]	Isum Intr per hour	20818	2	4	Float IEE754		kA	Sum per hour of interrupting currents.
SG[2]	SG OPEN capacity	20820	2	4	Float IEE754		%	Used capacity. 100% means, that the switchgear is to be maintained.
SG[2]	TripCmd Cr	20822	2	4	Float IEE754		-	Counter: Total number of trips of the switchgear (circuit breaker, load break switch...). Resettable with Total or All.

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Statistics	Idg W1 max	21938	2	4	Float IEE754		A	Measured value (calculated): Ground Differential Current Winding 1 Maximum Value
Statistics	ISG W1 max	21944	2	4	Float IEE754		A	Measured value (calculated): Ground Stabilizing Current Winding 1 Maximum Value
Statistics	Idg W2 max	21950	2	4	Float IEE754		A	Measured value (calculated): Ground Differential Current Winding 2 Maximum Value
Statistics	ISG W2 max	21956	2	4	Float IEE754		A	Measured value (calculated): Ground Stabilizing Current Winding 2 Maximum Value
Statistics	Id L1 max	21962	2	4	Float IEE754		lb	Measured value (calculated): Differential Current Phase L1 Maximum Value
Statistics	Id L2 max	21968	2	4	Float IEE754		lb	Measured value (calculated): Differential Current Phase L2 Maximum Value
Statistics	Id L3 max	21974	2	4	Float IEE754		lb	Measured value (calculated): Differential Current Phase L3 Maximum Value
Statistics	IS L1 max	21980	2	4	Float IEE754		lb	Measured value (calculated): Restraint Current Phase L1 Maximum Value
Statistics	IS L2 max	21986	2	4	Float IEE754		lb	Measured value (calculated): Restraint Current Phase L2 Maximum Value

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Statistics	IS L3 max	21992	2	4	Float IEE754		lb	Measured value (calculated): Restraint Current Phase L3 Maximum Value
ThR	Thermal Cap Used	20110	2	4	Float IEE754		%	Measured value: Thermal Capacity Used
ThR	Time To Trip	20112	2	4	Float IEE754		s	Measured value (calculated/measured): Remaining time until the thermal overload module will trip
ThR	Thermal Cap max	21086	2	4	Float IEE754		%	Thermal Capacity maximum value
ThR	Thermal Cap min	21088	2	4	Float IEE754		%	Thermal Capacity minimum value
URTD	Aux4	20328	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature
URTD	W1 L1	20330	2	4	Float IEE754		°C	Measured Value: Winding Temperature
URTD	W1 L2	20332	2	4	Float IEE754		°C	Measured Value: Winding Temperature
URTD	W1 L2	20334	2	4	Float IEE754		°C	Measured Value: Winding Temperature
URTD	W2 L1	20336	2	4	Float IEE754		°C	Measured Value: Winding Temperature

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
URTD	W2 L2	20338	2	4	Float IEE754		°C	Measured Value: Winding Temperature
URTD	W2 L2	20340	2	4	Float IEE754		°C	Measured Value: Winding Temperature
URTD	Amb1	20342	2	4	Float IEE754		°C	Measured Value: Ambient Temperature
URTD	Amb2	20344	2	4	Float IEE754		°C	Measured Value: Ambient Temperature
URTD	Aux1	20346	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature
URTD	Aux2	20348	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature
URTD	Aux3	20350	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature
URTD	RTD Max	20486	2	4	Float IEE754		°C	Maximum temperature of all channels.
URTD	W1 L1 max	21194	2	4	Float IEE754		°C	Measured Value: Winding Temperature Maximum Value
URTD	W1 L2 max	21196	2	4	Float IEE754		°C	Measured Value: Winding Temperature Maximum Value
URTD	W1 L2 max	21198	2	4	Float IEE754		°C	Measured Value: Winding Temperature Maximum Value

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
URTD	W2 L1 max	21200	2	4	Float IEE754		°C	Measured Value: Winding Temperature Maximum Value
URTD	W2 L2 max	21202	2	4	Float IEE754		°C	Measured Value: Winding Temperature Maximum Value
URTD	W2 L2 max	21204	2	4	Float IEE754		°C	Measured Value: Winding Temperature Maximum Value
URTD	Amb1 max	21206	2	4	Float IEE754		°C	Measured Value: Ambient Temperature Maximum Value
URTD	Amb2 max	21208	2	4	Float IEE754		°C	Measured Value: Ambient Temperature Maximum Value
URTD	Aux1 max	21210	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature Maximum Value
URTD	Aux2 max	21212	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature Maximum Value
URTD	Aux3 max	21214	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature Maximum Value
URTD	Aux4 max	21800	2	4	Float IEE754		°C	Measured Value: Auxiliary Temperature Maximum Value
Values	Build	20008	2	4	Float IEE754		-	Build
Values	Operating hours Cr	20010	2	4	Float IEE754		h	Operating hours counter of the protective device

Commands

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
Acknowledge	LEDs	22000	1	5	0xFF00		-	LEDs
Acknowledge	Binary Outputs	22001	1	5	0xFF00		-	Binary Outputs
Acknowledge	Scada	22002	1	5	0xFF00		-	Scada
Acknowledge	Device	22003	1	5	0xFF00		-	Device
Acknowledge	Ack TripCmd	22005	1	5	0xFF00		-	Signal: Acknowledge Trip Command
Reset	Modbus diagnosis counter	22006	1	5	0xFF00		-	Modbus diagnosis counter
Scada Cmd	Assbl Scada Cmd 1	22020	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 2	22021	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Scada Cmd	Assbl Scada Cmd 3	22022	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 4	22023	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 5	22024	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 6	22025	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 7	22026	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 8	22027	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Scada Cmd	Assbl Scada Cmd 9	22028	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 10	22029	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 11	22030	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 12	22031	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 13	22032	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 14	22033	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
Scada Cmd	Assbl Scada Cmd 15	22034	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 16	22035	1	5	0xFF00= On 0x0000= Off		-	Assignable Scada Command
PSet-Switch	Scada PS1	22050	1	5	0xFF00		-	Scada Setting Group1
PSet-Switch	Scada PS2	22051	1	5	0xFF00		-	Scada Setting Group2
PSet-Switch	Scada PS3	22052	1	5	0xFF00		-	Scada Setting Group3
PSet-Switch	Scada PS4	22053	1	5	0xFF00		-	Scada Setting Group4
AFRMS Mode	AFRMS SCADA	22054	1	5	0xFF00= On 0x0000= Off		-	Signal: Arcflash Reduction Maintenance SCADA Mode
SG	SG ControlCmd1	22100	1	5	0xFF00= On 0x0000= Off		-	Control Command Switchgear

Appendix - Data Point Lists

Module	Subgroup Names Functions	Start Register Address	No. of Modbus Registers	Function Code	Format	Bit Mask / (Bit Position)	Unit	Description
SG	SG ControlCmd2	22101	1	5	0xFF00= On 0x0000= Off		-	Control Command Switchgear

Settings

<i>Module</i>	<i>Subgroup Names Functions</i>	<i>Start Register Address</i>	<i>No. of Modbus Registers</i>	<i>Function Code</i>	<i>Format</i>	<i>Bit Mask / (Bit Position)</i>	<i>Unit</i>	<i>Description</i>
Date and Time		32500	6	3 16	Struct			
	y	32500	6	3 16	Short	Word 0 (1)	-	year
	m	32500	6	3 16	Short	Word 1 (17)	-	month
	d	32500	6	3 16	Short	Word 2 (33)	-	days
	h	32500	6	3 16	Short	Word 3 (49)	-	hours
	min	32500	6	3 16	Short	Word 4 (65)	-	minute
	ms	32500	6	3 16	Short	Word 5 (81)	-	milliseconds

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Woodward Kempen GmbH

Krefelder Weg 47 · D – 47906 Kempen (Germany)
Postfach 10 07 55 (P.O.Box) · D – 47884 Kempen (Germany)
Phone: +49 (0) 21 52 145 1

Internet

www.woodward.com

Sales

Phone: +49 (0) 21 52 145 216 or 342
Fax: +49 (0) 21 52 145 354
e-mail: salesEMEA_PGD@woodward.com

Service

Phone: +49 (0) 21 52 145 614
Fax: +49 (0) 21 52 145 455
e-mail: supportEMEA_PGD@woodward.com