

High**PROTEC**

MRU4

MODBUS Data Point List



HighPROTEC Version: 3.11

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English

REFERENCE MANUAL MRU4-3.11-EN-Modbus-Datapoints

Build 62598

Revision A

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SEG Electronics GmbH

Krefelder Weg 47 • D-47906 Kempen (Germany)

Telephone: +49 (0) 21 52 145 0

Internet: www.SEGelectronics.de

Sales

Telephone: +49 (0) 21 52 145 331

Fax: +49 (0) 21 52 145 354

E-mail: sales@SEGelectronics.de

Service

Telephone: +49 (0) 21 52 145 600

Fax: +49 (0) 21 52 145 354

E-mail: support@SEGelectronics.de

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1 Modbus Parameters

For the Modbus Protocol several parameters have to be set which are relevant for the communication between the control system (SCADA) and the device. The parameters and their setting possibilities or value ranges are shown in the tables below.

NOTICE!



The Parameters are described within the Reference Manual of the device (separate document).

1.1 Notes for the SCADA-System

When using Modbus RTU the following times have to be considered by the control system and are fixed within the device:

The dwell times (t_D) before start of a telegram must be set at least to 3.5 characters.

Examples:

- 3.5 characters 9600 Baud = 4 ms
- 3.5 characters 19200 Baud = 2 ms
- 3.5 characters 38400 Baud = 1 ms

Start of a new telegram is expected when the dwell time (t_D) is > 3.5 characters.

The fact that the probability of disruptions during transmission of a telegram increases with its length has to be taken into duly consideration and thus a query to the Slave should be possibly such that the response telegram is not much longer than 32 Bytes.

2 Specific Modbus Function Codes

For reading out data from the device or to carry out commands, the services listed in the table, also called »Function Codes«, are supported.

Function Code	Designation	Description
3	Read Holding Registers	There are single or several data words read as from a specific data word address. Only status addresses and parameter addresses can be read.
4	Read Input Registers	There are single or several data words read as from a specific data word address. Only measuring values can be read.
5	Write single Output (Bit)	All other values are illegal and will not affect the output. Via this function code acknowledgments can be executed as well as counters reseted or blockings set.
8	Loopback Test	Test function for the communication system.
16	Load Multiple Registers	There are single or several data words written as from a specific data word address.

On the following pages the Modbus functions are described in detail.

2.1 Function Code 3/4

Query

Slave address	3/4	Register address	Register address	Register number	Register number	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Response

Slave address	3/4	Byte number	Register 0	Register 0	...	Check-sum	Check-sum
			HI	LO		HI	LO

Register address (HI · 256 + LO) — The data word address from where reading should start.

Register number (HI · 256 + LO) — Number of data words to be read. Valid range: 1...125

Byte number — Number of subsequent Bytes containing data words.

Register — Data words read out of the device (Highbyte and Lowbyte).

2.2 Float Values IEEE 754

	Sign	Exponent	Mantissa
Value:	+1	2^{13}	1.34199857711792
Encoded as:	0	140	2868892
Binary:	☐	☑☐☐☑☑☐☐	☐☑☐☑☐☑☑☑☑☐☐☑☑☐☑☐☑☑☑☐☐
Decimal Representation:	10993.652		
Binary Representation:	01000110001010111100011010011100		
Hexadecimal Representation:	0x462bc69c		

For displaying a float value, it is important to save received bytes in a correct order. A float value in Modbus will be transmitted in “Big Endian” format (Motorola Format), that means the most significant byte is transmitted first.

For saving received bytes in Modbus master it must be considered which architecture is used. Is Modbus Master is a “Little Endian” architecture, the received frame needs to be swapped to corresponding memory addresses. If it is not saved in correct order it is possible that the displayed value is useless.

*

Example:

The following value is transmitted:

Modbus transmit value			
0x46	0x2b	0xc6	0x9c

Then the representation in the receiving device's internal memory has to be as follows:

Memory Addresses		Big Endian		Little Endian	
Address	Hex	10993.65		Hex	10993.65
1000	0x46			0x9c	
1001	0x2b			0xc6	
1002	0xc6			0x2b	
1003	0x9c			0x46	

2.3 Function Code 5

Query

Slave address	5	Register address	Register address	Register data	Register data	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Response

Slave address	5	Register address	Register address	Register data	Register data	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Register address (HI · 256 + LO) — Data word address to be written

Register data — Value of the data word to be written (High-byte and Low-byte).

Permitted value range:

- FF00 hex request for a single bit to be on: This often means to reset a counter, execute acknowledgments or set blockings signals.
- 0000 hex request for a single bit to be off: This often means to deactivate blocking signals or to reset single bits.

2.4 Function Code 8

Query

Slave address	8	Data Diag Code HI 0x00	Data Diag Code LO 0x00	Test data	Test data	Check-sum HI	Check-sum LO
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Response

Slave address	8	Data Diag Code HI	Data Diag Code LO	Test data	Test data	Check-sum HI	Check-sum LO
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Data Diag Code HI (high), Data Diag Code LO (Low) — Diagnostic Code (subfunction code of function code 8) for testing the communication system. The Diagnostic Code “Return Query Data” (0x00, 0x00) is being supported.

Test Data — By using the Diagnostic Code 0x00 0x00, the transmitted data is sent back to the Master unchanged.

2.5 Function Code 16

Query

Slave address	16	Register address	Register address	Register number	Register number	Byte number	Register 0	Register 0	...	Check-sum	Check-sum
		HI	LO	HI	LO		HI	LO		HI	LO

Response

Slave address	16	Register address	Register address	Register number	Register number	Check-sum	Check-sum
		HI	LO	HI	LO	HI	LO

Register address (HI · 256 + LO) — Data word address as from where writing should start.

Register number (HI · 256 + LO):

- Query: Number of data words to be written. Valid range: 1...123.
- Response: Number of data words written.

Byte number — Number of subsequent Bytes to contain data words.

Register — Data words read out of the device (High-byte and Low-byte).

2.6 Setting Date and Time

Date and time can be set by means of function code 16 and read with function code 3. If the device address 0 (broadcast address) is selected, the times of all devices connected to this bus are simultaneously reset.

NOTICE!



The devices do not respond to a broadcast command.

2.7 Supported MODBUS Error Messages

Exception Response Telegrams are described within the general "Modbus Application Protocol Specification". An exception response table with examples is shown there. The table below contains just the actually used codes. In case the device has recognized an error it will react in the following way:

Exception Code	Designation	Description
1	Illegal Function	The message received includes a function code which is not supported by the Slave.
2	Illegal Data Address	Access was sought on a data word address not included in the data module.
3	Illegal Data Value	The received message contains an invalid data structure (e. g. wrong number of data bytes).
4	Slave Device Failure	An unrecoverable error occurred while the server (or slave) was attempting to perform the requested action.

The response given by the *device* in case of a failure has the following format:

Slave Address	0x80 + Function Code	Exception Code	Check-sum HI	Check-sum LO
---------------	-------------------------	----------------	-----------------	-----------------

In the second Byte of the response the Function Code is sent with the highest Bit set to 1. This is equivalent to an addition by 0x80. The third Byte holds the Exception Code of the error message.

3 Appendix - Data Point Lists

3.1 Signals

Legend: (*) = These signals have to be acknowledged by the Scada System.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
BO Slot X2		1003	1	3	Struct			
	BO 1	1003	1	3	Bit	0x1 (1)	-	Signal: Binary Output Relay
	BO 2	1003	1	3	Bit	0x2 (2)	-	Signal: Binary Output Relay
	BO 3	1003	1	3	Bit	0x4 (3)	-	Signal: Binary Output Relay
	BO 4	1003	1	3	Bit	0x8 (4)	-	Signal: Binary Output Relay
	BO 5	1003	1	3	Bit	0x10 (5)	-	Signal: Binary Output Relay
	DISARMED!	1003	1	3	Bit	0x40 (7)	-	Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance
	Outs forced	1003	1	3	Bit	0x80 (8)	-	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
								forced and hence does not show the state of the assigned signals.
CBF - 62BF		53	1	3	Struct			
	ExBlo1-l	53	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	53	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	Active	53	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	53	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Trigger1-l	53	1	3	Bit	0x10 (5)	-	Module Input: Trigger that will start the CBF
	Trigger2-l	53	1	3	Bit	0x20 (6)	-	Module Input: Trigger that will start the CBF
	Trigger3-l	53	1	3	Bit	0x40 (7)	-	Module Input: Trigger that will start the CBF
	running	53	1	3	Bit	0x80 (8)	-	Signal: CBF-Module started
	Alarm (*)	53	1	3	Bit	0x100 (9)	-	Signal: Circuit Breaker Failure
	Lockout (*)	53	1	3	Bit	0x200	-	Signal: Lockout

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(10)		
	Waiting for Trigger (*)	53	1	3	Bit	0x400 (11)	-	Waiting for Trigger
Ctrl		176	1	3	Struct			
	Local	176	1	3	Bit	0x1 (1)	-	Switching Authority: Local
	Remote	176	1	3	Bit	0x2 (2)	-	Switching Authority: Remote
	NonInterl	176	1	3	Bit	0x4 (3)	-	Non-Interlocking is active
	SG Disturb	176	1	3	Bit	0x8 (4)	-	(At least one) Switchgear is disturbed.
	SG Indeterm	176	1	3	Bit	0x10 (5)	-	(At least one) Switchgear is moving (Position cannot be determined).
DI Slot X1		1000	1	3	Struct			
	DI 1	1000	1	3	Bit	0x1 (1)	-	Signal: Digital Input
	DI 2	1000	1	3	Bit	0x2 (2)	-	Signal: Digital Input
	DI 3	1000	1	3	Bit	0x4 (3)	-	Signal: Digital Input

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	DI 4	1000	1	3	Bit	0x8 (4)	-	Signal: Digital Input
	DI 5	1000	1	3	Bit	0x10 (5)	-	Signal: Digital Input
	DI 6	1000	1	3	Bit	0x20 (6)	-	Signal: Digital Input
	DI 7	1000	1	3	Bit	0x40 (7)	-	Signal: Digital Input
	DI 8	1000	1	3	Bit	0x80 (8)	-	Signal: Digital Input
ExP[1]		49	1	3	Struct			
	ExBlo1-I	49	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	49	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	49	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	49	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	49	1	3	Bit	0x10 (5)	-	Module input state: Trip

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Active	49	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	49	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	49	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	49	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	49	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	49	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	49	1	3	Bit	0x800 (12)	-	Signal: Trip Command
ExP[2]		50	1	3	Struct			
	ExBlo1-l	50	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	50	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-l	50	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Alarm-I	50	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	50	1	3	Bit	0x10 (5)	-	Module input state: Trip
	Active	50	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	50	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	50	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	50	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	50	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	50	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	50	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Exp[3]		51	1	3	Struct			
	ExBlo1-I	51	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo2-I	51	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	51	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	51	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	51	1	3	Bit	0x10 (5)	-	Module input state: Trip
	Active	51	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	51	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	51	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	51	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	51	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	51	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	51	1	3	Bit	0x800	-	Signal: Trip Command

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(12)		
Exp[4]		52	1	3	Struct			
	ExBlo1-l	52	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	52	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-l	52	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-l	52	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-l	52	1	3	Bit	0x10 (5)	-	Module input state: Trip
	Active	52	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	52	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	52	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	52	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	52	1	3	Bit	0x200	-	Signal: Alarm

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(10)		
	Trip (*)	52	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	52	1	3	Bit	0x800 (12)	-	Signal: Trip Command
Fast Status Register		5000	1	3	Struct			
	Device Type	5000	1	3	Bit	0xffff (1)	-	Device type code for relationship between device name and its Modbus code. HighPROTEC: MRI4 - 1000 MRU4 - 1001 MRA4 - 1002 MCA4 - 1003 MRDT4 - 1005 MCDTV4 - 1006 MCDGV4 - 1007 MRM4 - 1009 MRMV4 - 1010 MCDLV4 - 1011
Fast Status Register		5001	1	3	Struct			
	Comm Version	5001	1	3	Bit	0xffff	-	Modbus Communication version. This version number changes if something

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		becomes incompatible between different Modbus releases.
Fast Status Register		5002	1	3	Struct			
	Config Bin Inp1-l	5002	1	3	Bit	0x1 (1)	-	State of the module input: Config Bin Inp
	Config Bin Inp2-l	5002	1	3	Bit	0x2 (2)	-	State of the module input: Config Bin Inp
	Config Bin Inp3-l	5002	1	3	Bit	0x4 (3)	-	State of the module input: Config Bin Inp
	Config Bin Inp4-l	5002	1	3	Bit	0x8 (4)	-	State of the module input: Config Bin Inp
	Config Bin Inp5-l	5002	1	3	Bit	0x10 (5)	-	State of the module input: Config Bin Inp
	Config Bin Inp6-l	5002	1	3	Bit	0x20 (6)	-	State of the module input: Config Bin Inp
	Config Bin Inp7-l	5002	1	3	Bit	0x40 (7)	-	State of the module input: Config Bin Inp
	Config Bin Inp8-l	5002	1	3	Bit	0x80 (8)	-	State of the module input: Config Bin Inp
	Config Bin Inp9-l	5002	1	3	Bit	0x100 (9)	-	State of the module input: Config Bin Inp

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Config Bin Inp10-I	5002	1	3	Bit	0x200 (10)	-	State of the module input: Config Bin Inp
	Config Bin Inp11-I	5002	1	3	Bit	0x400 (11)	-	State of the module input: Config Bin Inp
	Config Bin Inp12-I	5002	1	3	Bit	0x800 (12)	-	State of the module input: Config Bin Inp
	Config Bin Inp13-I	5002	1	3	Bit	0x1000 (13)	-	State of the module input: Config Bin Inp
	Config Bin Inp14-I	5002	1	3	Bit	0x2000 (14)	-	State of the module input: Config Bin Inp
	Config Bin Inp15-I	5002	1	3	Bit	0x4000 (15)	-	State of the module input: Config Bin Inp
	Config Bin Inp16-I	5002	1	3	Bit	0x8000 (16)	-	State of the module input: Config Bin Inp
Fast Status Register		5003	1	3	Struct			
	Config Bin Inp17-I	5003	1	3	Bit	0x1 (1)	-	State of the module input: Config Bin Inp
	Config Bin Inp18-I	5003	1	3	Bit	0x2 (2)	-	State of the module input: Config Bin Inp
	Config Bin Inp19-I	5003	1	3	Bit	0x4	-	State of the module input: Config Bin Inp

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(3)		
	Config Bin Inp20-I	5003	1	3	Bit	0x8 (4)	-	State of the module input: Config Bin Inp
	Config Bin Inp21-I	5003	1	3	Bit	0x10 (5)	-	State of the module input: Config Bin Inp
	Config Bin Inp22-I	5003	1	3	Bit	0x20 (6)	-	State of the module input: Config Bin Inp
	Config Bin Inp23-I	5003	1	3	Bit	0x40 (7)	-	State of the module input: Config Bin Inp
	Config Bin Inp24-I	5003	1	3	Bit	0x80 (8)	-	State of the module input: Config Bin Inp
	Config Bin Inp25-I	5003	1	3	Bit	0x100 (9)	-	State of the module input: Config Bin Inp
	Config Bin Inp26-I	5003	1	3	Bit	0x200 (10)	-	State of the module input: Config Bin Inp
	Config Bin Inp27-I	5003	1	3	Bit	0x400 (11)	-	State of the module input: Config Bin Inp
	Config Bin Inp28-I	5003	1	3	Bit	0x800 (12)	-	State of the module input: Config Bin Inp
	Config Bin Inp29-I	5003	1	3	Bit	0x1000 (13)	-	State of the module input: Config Bin Inp

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Config Bin Inp30-I	5003	1	3	Bit	0x2000 (14)	-	State of the module input: Config Bin Inp
	Config Bin Inp31-I	5003	1	3	Bit	0x4000 (15)	-	State of the module input: Config Bin Inp
	Config Bin Inp32-I	5003	1	3	Bit	0x8000 (16)	-	State of the module input: Config Bin Inp
Fast Status Register		5004	1	3	Struct			
	Trip Cause (*)	5004	1	3	Bit	0xffff (1)	-	Initial reason of trip. It is presented as an integer value and corresponds to the "Trip" entry in the fault record, which refers to the name of the protective module that tripped first. Look up the definition of these integer values (i. e. the mapping trip code number-->module name) in the "Cause of Trip" table within the SCADA documentation.
IRIG-B		148	1	3	Struct			
	IRIG-B active	148	1	3	Bit	0x1 (1)	-	Signal: If there is no valid IRIG-B signal for 60 sec, IRIG-B is regarded as inactive.
	High-Low Invert	148	1	3	Bit	0x2 (2)	-	Signal: The High and Low signals of the IRIG-B are inverted. This does NOT mean that the wiring is faulty. If the wiring is faulty no IRIG-B signal will be detected.
Intertripping		253	1	3	Struct			
	ExBlo1-I	253	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	253	1	3	Bit	0x2	-	Module input state: External blocking2

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	253	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Alarm-I	253	1	3	Bit	0x8 (4)	-	Module input state: Alarm
	Trip-I	253	1	3	Bit	0x10 (5)	-	Module input state: Trip
	Active	253	1	3	Bit	0x20 (6)	-	Signal: active
	ExBlo	253	1	3	Bit	0x40 (7)	-	Signal: External Blocking
	Blo TripCmd	253	1	3	Bit	0x80 (8)	-	Signal: Trip Command blocked
	ExBlo TripCmd	253	1	3	Bit	0x100 (9)	-	Signal: External Blocking of the Trip Command
	Alarm	253	1	3	Bit	0x200 (10)	-	Signal: Alarm
	Trip (*)	253	1	3	Bit	0x400 (11)	-	Signal: Trip
	TripCmd (*)	253	1	3	Bit	0x800 (12)	-	Signal: Trip Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
LVRT[1] - 27		254	1	3	Struct			
	ExBlo1-I	254	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	254	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	254	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	254	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	254	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	254	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	254	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
LVRT[1] - 27		255	1	3	Struct			
	Alarm L1	255	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	255	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	255	1	3	Bit	0x4	-	Signal: Alarm L3

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(3)		
	Alarm	255	1	3	Bit	0x8 (4)	-	Signal: Alarm voltage stage
	Trip L1 (*)	255	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	255	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	255	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	255	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	255	1	3	Bit	0x100 (9)	-	Signal: Trip Command
	t-LVRT is running (*)	255	1	3	Bit	0x200 (10)	-	Signal: t-LVRT is running
LVRT[2] - 27		270	1	3	Struct			
	ExBlo1-I	270	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	270	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo TripCmd-I	270	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	270	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	270	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	270	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	270	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
LVRT[2] - 27		271	1	3	Struct			
	Alarm L1	271	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	271	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	271	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	271	1	3	Bit	0x8 (4)	-	Signal: Alarm voltage stage
	Trip L1 (*)	271	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Trip L2 (*)	271	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	271	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	271	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	271	1	3	Bit	0x100 (9)	-	Signal: Trip Command
	t-LVRT is running (*)	271	1	3	Bit	0x200 (10)	-	Signal: t-LVRT is running
Logics		1100	1	3	Struct			
	LE1.Gate Out	1100	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE1.Timer Out	1100	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE1.Out	1100	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE1.Out inverted	1100	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE1.Gate In1-I	1100	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE1.Gate In2-I	1100	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In3-I	1100	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE1.Gate In4-I	1100	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE1.Reset Latch-I	1100	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1101	1	3	Struct			
	LE2.Gate Out	1101	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE2.Timer Out	1101	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE2.Out	1101	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE2.Out inverted	1101	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE2.Gate In1-I	1101	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In2-I	1101	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE2.Gate In3-I	1101	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE2.Gate In4-I	1101	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE2.Reset Latch-I	1101	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1102	1	3	Struct			
	LE3.Gate Out	1102	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE3.Timer Out	1102	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE3.Out	1102	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE3.Out inverted	1102	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE3.Gate In1-I	1102	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In2-I	1102	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE3.Gate In3-I	1102	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE3.Gate In4-I	1102	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE3.Reset Latch-I	1102	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1103	1	3	Struct			
	LE4.Gate Out	1103	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE4.Timer Out	1103	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE4.Out	1103	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE4.Out inverted	1103	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE4.Gate In1-I	1103	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In2-I	1103	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In3-I	1103	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE4.Gate In4-I	1103	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE4.Reset Latch-I	1103	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1104	1	3	Struct			
	LE5.Gate Out	1104	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE5.Timer Out	1104	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE5.Out	1104	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE5.Out inverted	1104	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE5.Gate In1-I	1104	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In2-I	1104	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In3-I	1104	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE5.Gate In4-I	1104	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE5.Reset Latch-I	1104	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Logics		1105	1	3	Struct			
	LE6.Gate Out	1105	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE6.Timer Out	1105	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE6.Out	1105	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE6.Out inverted	1105	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE6.Gate In1-I	1105	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In2-I	1105	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In3-I	1105	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE6.Gate In4-I	1105	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE6.Reset Latch-I	1105	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1106	1	3	Struct			
	LE7.Gate Out	1106	1	3	Bit	0x1	-	Signal: Output of the logic gate

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	LE7.Timer Out	1106	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE7.Out	1106	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE7.Out inverted	1106	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE7.Gate In1-I	1106	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In2-I	1106	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In3-I	1106	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE7.Gate In4-I	1106	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE7.Reset Latch-I	1106	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1107	1	3	Struct			
	LE8.Gate Out	1107	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE8.Timer Out	1107	1	3	Bit	0x2	-	Signal: Timer Output

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	LE8.Out	1107	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE8.Out inverted	1107	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE8.Gate In1-I	1107	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In2-I	1107	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In3-I	1107	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE8.Gate In4-I	1107	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE8.Reset Latch-I	1107	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1108	1	3	Struct			
	LE9.Gate Out	1108	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE9.Timer Out	1108	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE9.Out	1108	1	3	Bit	0x4	-	Signal: Latched Output (Q)

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(3)		
	LE9.Out inverted	1108	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE9.Gate In1-I	1108	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In2-I	1108	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In3-I	1108	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE9.Gate In4-I	1108	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE9.Reset Latch-I	1108	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1109	1	3	Struct			
	LE10.Gate Out	1109	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE10.Timer Out	1109	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE10.Out	1109	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE10.Out inverted	1109	1	3	Bit	0x8	-	Signal: Negated Latched Output (Q NOT)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(4)		
	LE10.Gate In1-I	1109	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In2-I	1109	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In3-I	1109	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE10.Gate In4-I	1109	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE10.Reset Latch-I	1109	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1110	1	3	Struct			
	LE11.Gate Out	1110	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE11.Timer Out	1110	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE11.Out	1110	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE11.Out inverted	1110	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE11.Gate In1-I	1110	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In2-I	1110	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In3-I	1110	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE11.Gate In4-I	1110	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE11.Reset Latch-I	1110	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1111	1	3	Struct			
	LE12.Gate Out	1111	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE12.Timer Out	1111	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE12.Out	1111	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE12.Out inverted	1111	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE12.Gate In1-I	1111	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE12.Gate In2-I	1111	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In3-I	1111	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE12.Gate In4-I	1111	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE12.Reset Latch-I	1111	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1112	1	3	Struct			
	LE13.Gate Out	1112	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE13.Timer Out	1112	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE13.Out	1112	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE13.Out inverted	1112	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE13.Gate In1-I	1112	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In2-I	1112	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE13.Gate In3-I	1112	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE13.Gate In4-I	1112	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE13.Reset Latch-I	1112	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1113	1	3	Struct			
	LE14.Gate Out	1113	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE14.Timer Out	1113	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE14.Out	1113	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE14.Out inverted	1113	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE14.Gate In1-I	1113	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In2-I	1113	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE14.Gate In3-I	1113	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE14.Gate In4-I	1113	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE14.Reset Latch-I	1113	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1114	1	3	Struct			
	LE15.Gate Out	1114	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE15.Timer Out	1114	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE15.Out	1114	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE15.Out inverted	1114	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE15.Gate In1-I	1114	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In2-I	1114	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In3-I	1114	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE15.Gate In4-I	1114	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	LE15.Reset Latch-I	1114	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1115	1	3	Struct			
	LE16.Gate Out	1115	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE16.Timer Out	1115	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE16.Out	1115	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE16.Out inverted	1115	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE16.Gate In1-I	1115	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In2-I	1115	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In3-I	1115	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE16.Gate In4-I	1115	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE16.Reset Latch-I	1115	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Logics		1116	1	3	Struct			
	LE17.Gate Out	1116	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE17.Timer Out	1116	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE17.Out	1116	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE17.Out inverted	1116	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE17.Gate In1-I	1116	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In2-I	1116	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In3-I	1116	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE17.Gate In4-I	1116	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE17.Reset Latch-I	1116	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1117	1	3	Struct			
	LE18.Gate Out	1117	1	3	Bit	0x1	-	Signal: Output of the logic gate

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	LE18.Timer Out	1117	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE18.Out	1117	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE18.Out inverted	1117	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE18.Gate In1-I	1117	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In2-I	1117	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In3-I	1117	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE18.Gate In4-I	1117	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE18.Reset Latch-I	1117	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1118	1	3	Struct			
	LE19.Gate Out	1118	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE19.Timer Out	1118	1	3	Bit	0x2	-	Signal: Timer Output

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	LE19.Out	1118	1	3	Bit	0x4 (3)	-	Signal: Latched Output (Q)
	LE19.Out inverted	1118	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE19.Gate In1-I	1118	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In2-I	1118	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In3-I	1118	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE19.Gate In4-I	1118	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE19.Reset Latch-I	1118	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Logics		1119	1	3	Struct			
	LE20.Gate Out	1119	1	3	Bit	0x1 (1)	-	Signal: Output of the logic gate
	LE20.Timer Out	1119	1	3	Bit	0x2 (2)	-	Signal: Timer Output
	LE20.Out	1119	1	3	Bit	0x4	-	Signal: Latched Output (Q)

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(3)		
	LE20.Out inverted	1119	1	3	Bit	0x8 (4)	-	Signal: Negated Latched Output (Q NOT)
	LE20.Gate In1-I	1119	1	3	Bit	0x10 (5)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In2-I	1119	1	3	Bit	0x20 (6)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In3-I	1119	1	3	Bit	0x40 (7)	-	State of the module input: Assignment of the Input Signal
	LE20.Gate In4-I	1119	1	3	Bit	0x80 (8)	-	State of the module input: Assignment of the Input Signal
	LE20.Reset Latch-I	1119	1	3	Bit	0x100 (9)	-	State of the module input: Reset Signal for the Latching
Modbus		1005	1	3	Struct			
	Scada Cmd 1	1005	1	3	Bit	0x1 (1)	-	Scada Command
	Scada Cmd 2	1005	1	3	Bit	0x2 (2)	-	Scada Command
	Scada Cmd 3	1005	1	3	Bit	0x4 (3)	-	Scada Command
	Scada Cmd 4	1005	1	3	Bit	0x8	-	Scada Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(4)		
	Scada Cmd 5	1005	1	3	Bit	0x10 (5)	-	Scada Command
	Scada Cmd 6	1005	1	3	Bit	0x20 (6)	-	Scada Command
	Scada Cmd 7	1005	1	3	Bit	0x40 (7)	-	Scada Command
	Scada Cmd 8	1005	1	3	Bit	0x80 (8)	-	Scada Command
	Scada Cmd 9	1005	1	3	Bit	0x100 (9)	-	Scada Command
	Scada Cmd 10	1005	1	3	Bit	0x200 (10)	-	Scada Command
	Scada Cmd 11	1005	1	3	Bit	0x400 (11)	-	Scada Command
	Scada Cmd 12	1005	1	3	Bit	0x800 (12)	-	Scada Command
	Scada Cmd 13	1005	1	3	Bit	0x1000 (13)	-	Scada Command
	Scada Cmd 14	1005	1	3	Bit	0x2000 (14)	-	Scada Command

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Scada Cmd 15	1005	1	3	Bit	0x4000 (15)	-	Scada Command
	Scada Cmd 16	1005	1	3	Bit	0x8000 (16)	-	Scada Command
PSet-Switch		59	1	3	Struct			
	PS 1	59	1	3	Bit	0x1 (1)	-	Signal: The currently active Parameter Set is PS 1
	PS 2	59	1	3	Bit	0x2 (2)	-	Signal: The currently active Parameter Set is PS 2
	PS 3	59	1	3	Bit	0x4 (3)	-	Signal: The currently active Parameter Set is PS 3
	PS 4	59	1	3	Bit	0x8 (4)	-	Signal: The currently active Parameter Set is PS 4
	PSS manual	59	1	3	Bit	0x10 (5)	-	Signal: Manual Switch over of a Parameter Set
	PSS via Scada	59	1	3	Bit	0x20 (6)	-	Signal: Parameter Set Switch via Scada. Write into this output byte the integer of the parameter set that should become active (e.g. 4 => Switch onto parameter set 4).
	PSS via Inp fct	59	1	3	Bit	0x40 (7)	-	Signal: Parameter Set Switch via input function

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	PS1-I	59	1	3	Bit	0x80 (8)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS2-I	59	1	3	Bit	0x100 (9)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS3-I	59	1	3	Bit	0x200 (10)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	PS4-I	59	1	3	Bit	0x400 (11)	-	State of the module input respectively of the signal, that should activate this Parameter Setting Group.
	min 1 param changed (*)	59	1	3	Bit	0x800 (12)	-	Signal: At least one parameter has been changed
Prot		1	1	3	Struct			
	ExBlo1-I	1	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	1	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	Active	1	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	1	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm L1	1	1	3	Bit	0x10 (5)	-	Signal: General-Alarm L1

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Alarm L2	1	1	3	Bit	0x20 (6)	-	Signal: General-Alarm L2
	Alarm L3	1	1	3	Bit	0x40 (7)	-	Signal: General-Alarm L3
	Alarm G	1	1	3	Bit	0x80 (8)	-	Signal: General-Alarm - Earth fault
	Alarm	1	1	3	Bit	0x100 (9)	-	Signal: General Alarm
	Trip L1 (*)	1	1	3	Bit	0x200 (10)	-	Signal: General Trip L1
	Trip L2 (*)	1	1	3	Bit	0x400 (11)	-	Signal: General Trip L2
	Trip L3 (*)	1	1	3	Bit	0x800 (12)	-	Signal: General Trip L3
	Trip G (*)	1	1	3	Bit	0x1000 (13)	-	Signal: General Trip Ground fault
	Trip (*)	1	1	3	Bit	0x2000 (14)	-	Signal: General Trip
Prot		2	1	3	Struct			
	Blo TripCmd	2	1	3	Bit	0x1 (1)	-	Signal: Trip Command blocked

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo TripCmd-I	2	1	3	Bit	0x2 (2)	-	Module input state: External Blocking of the Trip Command
	ExBlo TripCmd	2	1	3	Bit	0x4 (3)	-	Signal: External Blocking of the Trip Command
Prot		57	1	3	Struct			
	Fault No.	57	1	3	Bit	0xffff (1)	-	Fault number
ReCon[1]		158	1	3	Struct			
	ExBlo1-I	158	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	158	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	Active	158	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	158	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Blo by Meas Circ Superv	158	1	3	Bit	0x10 (5)	-	Signal: Module blocked by measuring circuit supervision
	reconnected-I	158	1	3	Bit	0x20 (6)	-	This signal indicates the state "reconnected" (mains parallel).
	V Ext Release PCC-I	158	1	3	Bit	0x40	-	Module input state: Release signal is being generated by the PCC (External Release)

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(7)		
	PCC Fuse Fail VT-I	158	1	3	Bit	0x80 (8)	-	State of the module input: Blocking if the fuse of a voltage transformer has tripped at the PCC.
	Release Energy Res.	158	1	3	Bit	0x100 (9)	-	Signal: Signal: Release Energy Resource.
	Decoupling1-l	158	1	3	Bit	0x200 (10)	-	Decoupling function, that triggers the reconnection.
	Decoupling2-l	158	1	3	Bit	0x400 (11)	-	Decoupling function, that triggers the reconnection.
	Decoupling3-l	158	1	3	Bit	0x800 (12)	-	Decoupling function, that triggers the reconnection.
	Decoupling4-l	158	1	3	Bit	0x1000 (13)	-	Decoupling function, that triggers the reconnection.
	Decoupling5-l	158	1	3	Bit	0x2000 (14)	-	Decoupling function, that triggers the reconnection.
	Decoupling6-l	158	1	3	Bit	0x4000 (15)	-	Decoupling function, that triggers the reconnection.
ReCon[2]		159	1	3	Struct			
	ExBlo1-l	159	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	159	1	3	Bit	0x2	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	Active	159	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	159	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Blo by Meas Circ Superv	159	1	3	Bit	0x10 (5)	-	Signal: Module blocked by measuring circuit supervision
	reconnected-l	159	1	3	Bit	0x20 (6)	-	This signal indicates the state "reconnected" (mains parallel).
	V Ext Release PCC-l	159	1	3	Bit	0x40 (7)	-	Module input state: Release signal is being generated by the PCC (External Release)
	PCC Fuse Fail VT-l	159	1	3	Bit	0x80 (8)	-	State of the module input: Blocking if the fuse of a voltage transformer has tripped at the PCC.
	Release Energy Res.	159	1	3	Bit	0x100 (9)	-	Signal: Signal: Release Energy Resource.
	Decoupling1-l	159	1	3	Bit	0x200 (10)	-	Decoupling function, that triggers the reconnection.
	Decoupling2-l	159	1	3	Bit	0x400 (11)	-	Decoupling function, that triggers the reconnection.
	Decoupling3-l	159	1	3	Bit	0x800 (12)	-	Decoupling function, that triggers the reconnection.

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEI)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Decoupling4-I	159	1	3	Bit	0x1000 (13)	-	Decoupling function, that triggers the reconnection.
	Decoupling5-I	159	1	3	Bit	0x2000 (14)	-	Decoupling function, that triggers the reconnection.
	Decoupling6-I	159	1	3	Bit	0x4000 (15)	-	Decoupling function, that triggers the reconnection.
SG[1]		177	1	3	Struct			
	Aux OFF-I	177	1	3	Bit	0x1 (1)	-	Module input state: Position indicator/ check-back signal of the CB (52b)
	Aux ON-I	177	1	3	Bit	0x2 (2)	-	Module Input State: Position indicator/ check-back signal of the CB (52a)
	Ready-I	177	1	3	Bit	0x4 (3)	-	Module input state: CB ready
	Sys-in-Sync-I	177	1	3	Bit	0x8 (4)	-	State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.
	Interl OFF1-I	177	1	3	Bit	0x10 (5)	-	State of the module input: Interlocking of the OFF command
	Interl OFF2-I	177	1	3	Bit	0x20 (6)	-	State of the module input: Interlocking of the OFF command
	Interl OFF3-I	177	1	3	Bit	0x40 (7)	-	State of the module input: Interlocking of the OFF command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Interl ON1-I	177	1	3	Bit	0x80 (8)	-	State of the module input: Interlocking of the ON command
	Interl ON2-I	177	1	3	Bit	0x100 (9)	-	State of the module input: Interlocking of the ON command
	Interl ON3-I	177	1	3	Bit	0x200 (10)	-	State of the module input: Interlocking of the ON command
	SCmd OFF-I	177	1	3	Bit	0x800 (12)	-	State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input
	SCmd ON-I	177	1	3	Bit	0x1000 (13)	-	State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input
	TripCmd (*)	177	1	3	Bit	0x2000 (14)	-	Signal: Trip Command
	OFF Cmd	177	1	3	Bit	0x4000 (15)	-	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.
	OFF Cmd manual	177	1	3	Bit	0x8000 (16)	-	Signal: OFF Cmd manual
SG[1]		178	1	3	Struct			
	ON Cmd	178	1	3	Bit	0x1 (1)	-	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.
	ON Cmd manual	178	1	3	Bit	0x2	-	Signal: ON Cmd manual

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	Sync ON request	178	1	3	Bit	0x4 (3)	-	Signal: Synchronous ON request
	SGwear Slow SG	178	1	3	Bit	0x8 (4)	-	Signal: Alarm, the circuit breaker (load-break switch) becomes slower
	Res SGwear SI SG	178	1	3	Bit	0x10 (5)	-	Signal: Resetting the slow Switchgear Alarm
	CES Disturbed	178	1	3	Bit	0x40 (7)	-	Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.
	CES Fiel Interl	178	1	3	Bit	0x80 (8)	-	Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.
	CES ON d OFF	178	1	3	Bit	0x400 (11)	-	Signal: Command Execution Supervision: On Command during a pending OFF Command.
	CES SwitchDir	178	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.
	CES SG not ready	178	1	3	Bit	0x1000 (13)	-	Signal: Command Execution Supervision: Switchgear not ready
	CES SyncTimeout	178	1	3	Bit	0x2000	-	Signal: Command Execution Supervision: Switching Command not executed. No

Module (ANSI / IEEI)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(14)		Synchronization signal while t-sync was running.
	CES succesf	178	1	3	Bit	0x4000 (15)	-	Signal: Command Execution Supervision: Switching command executed successfully.
	Prot ON	178	1	3	Bit	0x8000 (16)	-	Signal: ON Command issued by the Prot module
SG[1]		179	1	3	Struct			
	Pos Disturb	179	1	3	Bit	0x1 (1)	-	Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.
	t-Dwell	179	1	3	Bit	0x2 (2)	-	Signal: Dwell time
	Pos Indeterm	179	1	3	Bit	0x4 (3)	-	Signal: Circuit Breaker is in Indeterminate Position
	Pos OFF	179	1	3	Bit	0x8 (4)	-	Signal: Circuit Breaker is in OFF-Position
	Pos ON	179	1	3	Bit	0x10 (5)	-	Signal: Circuit Breaker is in ON-Position
	Ready	179	1	3	Bit	0x20 (6)	-	Signal: Circuit breaker is ready for operation.
	Pos not ON	179	1	3	Bit	0x40 (7)	-	Signal: Pos not ON

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	SI SingleContactInd	179	1	3	Bit	0x80 (8)	-	Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.
	Position Ind manipul	179	1	3	Bit	0x100 (9)	-	Signal: Position Indicators faked
	OFF incl TripCmd	179	1	3	Bit	0x200 (10)	-	Signal: The OFF Command includes the OFF Command issued by the Protection module.
	ON incl Prot ON	179	1	3	Bit	0x400 (11)	-	Signal: The ON Command includes the ON Command issued by the Protection module.
	CES Fail TripCmd	179	1	3	Bit	0x800 (12)	-	Signal: Command Execution Supervision: Command execution failed because trip command is pending.
	Interl OFF	179	1	3	Bit	0x1000 (13)	-	Signal: One or more IL_Off inputs are active.
	Interl ON	179	1	3	Bit	0x2000 (14)	-	Signal: One or more IL_On inputs are active.
SG[1]		195	1	3	Struct			
	Operations Alarm	195	1	3	Bit	0x100 (9)	-	Signal: Too many Operations. (The operations counter »TripCmd Cr« has exceeded the limit set at »Operations Alarm«.)
SG[1]		256	1	3	Struct			
	Removed-I	256	1	3	Bit	0x1 (1)	-	State of the module input: The withdrawable circuit breaker is Removed

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	CES SG removed	256	1	3	Bit	0x2 (2)	-	Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.
	Removed	256	1	3	Bit	0x4 (3)	-	Signal: The withdrawable circuit breaker is Removed
SSV		273	1	3	Struct			
	System Error	273	1	3	Bit	0x1 (1)	-	Signal: Device Failure
	New error (*)	273	1	3	Bit	0x8 (4)	-	Signal: A new error message has been issued.
	New warning (*)	273	1	3	Bit	0x10 (5)	-	Signal: A new warning message has been issued.
	Active	273	1	3	Bit	0x20 (6)	-	Signal: active
Sgen		1012	1	3	Struct			
	ExBlo1-l	1012	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	Ex ForcePost-l	1012	1	3	Bit	0x2 (2)	-	State of the module input:Force Post state. Abort simulation.
	Running	1012	1	3	Bit	0x10 (5)	-	Signal: Measuring value simulation is running

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	State	1012	1	3	Bit	0xe0 (6)	-	Signal: Wave generation states: 0=Off, 1=PreFault, 2=Fault, 3=PostFault, 4=InitReset
	Ex Start Simulation- I	1012	1	3	Bit	0x100 (9)	-	State of the module input:External Start of Fault Simulation (Using the test parameters)
	ExBlo2-I	1012	1	3	Bit	0x200 (10)	-	Module input state: External blocking2
	Manual Start	1012	1	3	Bit	0x400 (11)	-	Fault Simulation has been started manually.
	Manual Stop	1012	1	3	Bit	0x800 (12)	-	Fault Simulation has been stopped manually.
	Started	1012	1	3	Bit	0x1000 (13)	-	Fault Simulation has been started
	Stopped	1012	1	3	Bit	0x2000 (14)	-	Fault Simulation has been stopped
Sync - 25		175	1	3	Struct			
	Active	175	1	3	Bit	0x1 (1)	-	Signal: active
	ExBlo1-I	175	1	3	Bit	0x2 (2)	-	Module input state: External blocking1
	ExBlo2-I	175	1	3	Bit	0x4 (3)	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo	175	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Bypass-I	175	1	3	Bit	0x10 (5)	-	State of the module input: The Synchrocheck will be bypassed if the state of the assigned signal (logic input) becomes true.
	CBCloseInitiate-I	175	1	3	Bit	0x20 (6)	-	State of the module input: Breaker Close Initiate with synchronism check from any control sources (e.g. HMI / SCADA). If the state of the assigned signal becomes true, a Breaker Close will be initiated (Trigger Source).
	AngleDiffTooHigh	175	1	3	Bit	0x40 (7)	-	Signal: Phase Angle difference between bus and line voltages too high.
	Sys-in-Sync	175	1	3	Bit	0x80 (8)	-	Signal: Bus and line voltages are in synchronism according to the system synchronism criteria.
	LiveBus	175	1	3	Bit	0x100 (9)	-	Signal: Live-Bus flag: 1=Live-Bus, 0=Voltage is below the LiveBus threshold
	LiveLine	175	1	3	Bit	0x200 (10)	-	Signal: Live Line flag: 1=Live-Line, 0=Voltage is below the LiveLine threshold
	SlipTooHigh	175	1	3	Bit	0x400 (11)	-	Signal: Frequency difference (slip frequency) between bus and line voltages too high.
	SyncOverridden	175	1	3	Bit	0x800 (12)	-	Signal: Synchronism Check is overridden because one of the Synchronism overriding conditions (DB/DL or ExtBypass) is met.
	Ready to Close	175	1	3	Bit	0x1000	-	Signal: Ready to Close

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3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(13)		
	SynchronFailed	175	1	3	Bit	0x2000 (14)	-	Signal: This signal indicates a failed synchronization. It is set for 5s when the circuit breaker is still open after the Synchron-Run-timer has timed out.
	SynchronRunTiming	175	1	3	Bit	0x4000 (15)	-	Signal: Synchron-Run-timer is timing (This timer starts when Close-Initiate is coming and stops if breaker is closed. Timeout means synchronizing failed.)
	VDiffTooHigh	175	1	3	Bit	0x8000 (16)	-	Signal: Voltage difference between bus and line too high.
Sys		154	1	3	Struct			
	Maint Mode Active	154	1	3	Bit	0x1 (1)	-	Signal: Arc Flash Reduction Maintenance Active
	MaintMode Manually	154	1	3	Bit	0x2 (2)	-	Signal: Arc Flash Reduction Maintenance Manual Mode
	Maint Mode DI	154	1	3	Bit	0x4 (3)	-	Signal: Arc Flash Reduction Maintenance Digital Input Mode
	Maint Mode SCADA	154	1	3	Bit	0x8 (4)	-	Signal: Arc Flash Reduction Maintenance SCADA Mode
	Maint Mode Inactive	154	1	3	Bit	0x10 (5)	-	Signal: Arc Flash Reduction Maintenance Inactive
	Maint Mode-I	154	1	3	Bit	0x20 (6)	-	Module Input State: Arc Flash Reduction Maintenance Switch

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Setting Lock-I	154	1	3	Bit	0x40 (7)	-	State of the module input: No parameters can be changed as long as this input is true. The parameter settings are locked.
	SNTP active	154	1	3	Bit	0x80 (8)	-	Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.
	Setting Lock Bypass	154	1	3	Bit	0x100 (9)	-	Signal: Short-period unlock of the Setting Lock
SysA		173	1	3	Struct			
	ExBlo-I	173	1	3	Bit	0x1 (1)	-	Module input state: External blocking
	ExBlo	173	1	3	Bit	0x2 (2)	-	Signal: External Blocking
	Active	173	1	3	Bit	0x8 (4)	-	Signal: active
	Alarm V THD	173	1	3	Bit	0x200 (10)	-	Signal: Alarm Total Harmonic Distortion Voltage
SysA		174	1	3	Struct			
	Trip V THD (*)	174	1	3	Bit	0x4 (3)	-	Signal: Trip Total Harmonic Distortion Voltage
TCS - 74TC		150	1	3	Struct			
	ExBlo1-I	150	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEI)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo2-I	150	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	Active	150	1	3	Bit	0x4 (3)	-	Signal: active
	ExBlo	150	1	3	Bit	0x8 (4)	-	Signal: External Blocking
	Alarm	150	1	3	Bit	0x10 (5)	-	Signal: Alarm Trip Circuit Supervision
	Not Possible	150	1	3	Bit	0x20 (6)	-	Not possible because no state indicator assigned to the breaker.
	Aux ON-I	150	1	3	Bit	0x100 (9)	-	Module Input State: Position indicator/ check-back signal of the CB (52a)
	Aux OFF-I	150	1	3	Bit	0x200 (10)	-	Module input state: Position indicator/ check-back signal of the CB (52b)
TimeSync		54	1	3	Struct			
	synchronized	54	1	3	Bit	0x1 (1)	-	Clock is synchronized.
V012[1] - 47		100	1	3	Struct			
	ExBlo1-I	100	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	100	1	3	Bit	0x2	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	100	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	100	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	100	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	100	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	100	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	100	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage asymmetry
	Trip (*)	100	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	100	1	3	Bit	0x200 (10)	-	Signal: Trip Command
V012[2] - 47		101	1	3	Struct			
	ExBlo1-I	101	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	101	1	3	Bit	0x2	-	Module input state: External blocking2

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	101	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	101	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	101	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	101	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	101	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	101	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage asymmetry
	Trip (*)	101	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	101	1	3	Bit	0x200 (10)	-	Signal: Trip Command
V012[3] - 47		102	1	3	Struct			
	ExBlo1-I	102	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	102	1	3	Bit	0x2	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	102	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	102	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	102	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	102	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	102	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	102	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage asymmetry
	Trip (*)	102	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	102	1	3	Bit	0x200 (10)	-	Signal: Trip Command
V012[4] - 47		103	1	3	Struct			
	ExBlo1-I	103	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	103	1	3	Bit	0x2	-	Module input state: External blocking2

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	103	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	103	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	103	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	103	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	103	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	103	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage asymmetry
	Trip (*)	103	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	103	1	3	Bit	0x200 (10)	-	Signal: Trip Command
V012[5] - 47		104	1	3	Struct			
	ExBlo1-I	104	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	104	1	3	Bit	0x2	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	104	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	104	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	104	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	104	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	104	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	104	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage asymmetry
	Trip (*)	104	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	104	1	3	Bit	0x200 (10)	-	Signal: Trip Command
V012[6] - 47		105	1	3	Struct			
	ExBlo1-I	105	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	105	1	3	Bit	0x2	-	Module input state: External blocking2

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	105	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	105	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	105	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	105	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	105	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	105	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage asymmetry
	Trip (*)	105	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	105	1	3	Bit	0x200 (10)	-	Signal: Trip Command
VG[1] - 27A, 59N,A		32	1	3	Struct			
	ExBlo1-I	32	1	3	Bit	0x1 (1)	-	Module input state: External blocking1

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo2-I	32	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	32	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	32	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	32	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	32	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	32	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	32	1	3	Bit	0x80 (8)	-	Signal: Alarm Residual Voltage Supervision-stage
	Trip (*)	32	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	32	1	3	Bit	0x200 (10)	-	Signal: Trip Command
VG[2] - 27A, 59N,A		33	1	3	Struct			
	ExBlo1-I	33	1	3	Bit	0x1	-	Module input state: External blocking1

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	ExBlo2-I	33	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	33	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	33	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	33	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	33	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	33	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	33	1	3	Bit	0x80 (8)	-	Signal: Alarm Residual Voltage Supervision-stage
	Trip (*)	33	1	3	Bit	0x100 (9)	-	Signal: Trip
	TripCmd (*)	33	1	3	Bit	0x200 (10)	-	Signal: Trip Command
VTS		56	1	3	Struct			

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Ex Fuse Fail VT-I	56	1	3	Bit	0x1 (1)	-	Module input state: External fuse failure voltage transformers
	Ex Fuse Fail EVT-I	56	1	3	Bit	0x2 (2)	-	Module input state: External fuse failure earth voltage transformer
	ExBlo1-I	56	1	3	Bit	0x4 (3)	-	Module input state: External blocking1
	ExBlo2-I	56	1	3	Bit	0x8 (4)	-	Module input state: External blocking2
	Active	56	1	3	Bit	0x10 (5)	-	Signal: active
	ExBlo	56	1	3	Bit	0x20 (6)	-	Signal: External Blocking
	Alarm ΔV	56	1	3	Bit	0x40 (7)	-	Signal: Alarm ΔV Voltage Transformer Measuring Circuit Supervision
	Alarm	56	1	3	Bit	0x80 (8)	-	Signal: Alarm Voltage Transformer Measuring Circuit Supervision
	Ex FF VT	56	1	3	Bit	0x100 (9)	-	Signal: Ex FF VT
	Ex FF EVT	56	1	3	Bit	0x200 (10)	-	Signal: Alarm Fuse Failure Earth Voltage Transformers
V[1] - 27, 59		24	1	3	Struct			

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo1-I	24	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	24	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	24	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	24	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	24	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	24	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	24	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
V[1] - 27, 59		25	1	3	Struct			
	Alarm L1	25	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	25	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	25	1	3	Bit	0x4 (3)	-	Signal: Alarm L3

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Alarm	25	1	3	Bit	0x8 (4)	-	Signal: Alarm voltage stage
	Trip L1 (*)	25	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	25	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	25	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	25	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	25	1	3	Bit	0x100 (9)	-	Signal: Trip Command
V[2] - 27, 59		26	1	3	Struct			
	ExBlo1-I	26	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	26	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	26	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	26	1	3	Bit	0x8 (4)	-	Signal: active

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo	26	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	26	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	26	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
V[2] - 27, 59		27	1	3	Struct			
	Alarm L1	27	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	27	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	27	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	27	1	3	Bit	0x8 (4)	-	Signal: Alarm voltage stage
	Trip L1 (*)	27	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	27	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	27	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Trip (*)	27	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	27	1	3	Bit	0x100 (9)	-	Signal: Trip Command
V[3] - 27, 59		28	1	3	Struct			
	ExBlo1-I	28	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	28	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	28	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	28	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	28	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	28	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	28	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
V[3] - 27, 59		29	1	3	Struct			
	Alarm L1	29	1	3	Bit	0x1	-	Signal: Alarm L1

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	Alarm L2	29	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	29	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	29	1	3	Bit	0x8 (4)	-	Signal: Alarm voltage stage
	Trip L1 (*)	29	1	3	Bit	0x10 (5)	-	Signal: General Trip Phase L1
	Trip L2 (*)	29	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	29	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	29	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	29	1	3	Bit	0x100 (9)	-	Signal: Trip Command
V[4] - 27, 59		30	1	3	Struct			
	ExBlo1-I	30	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	30	1	3	Bit	0x2	-	Module input state: External blocking2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(2)		
	ExBlo TripCmd-I	30	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	30	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	30	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	30	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	30	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
V[4] - 27, 59		31	1	3	Struct			
	Alarm L1	31	1	3	Bit	0x1 (1)	-	Signal: Alarm L1
	Alarm L2	31	1	3	Bit	0x2 (2)	-	Signal: Alarm L2
	Alarm L3	31	1	3	Bit	0x4 (3)	-	Signal: Alarm L3
	Alarm	31	1	3	Bit	0x8 (4)	-	Signal: Alarm voltage stage
	Trip L1 (*)	31	1	3	Bit	0x10	-	Signal: General Trip Phase L1

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(5)		
	Trip L2 (*)	31	1	3	Bit	0x20 (6)	-	Signal: General Trip Phase L2
	Trip L3 (*)	31	1	3	Bit	0x40 (7)	-	Signal: General Trip Phase L3
	Trip (*)	31	1	3	Bit	0x80 (8)	-	Signal: Trip
	TripCmd (*)	31	1	3	Bit	0x100 (9)	-	Signal: Trip Command
V[5] - 27, 59		92	1	3	Struct			
	ExBlo1-I	92	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	92	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	92	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	92	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	92	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	92	1	3	Bit	0x20	-	Signal: Trip Command blocked

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(6)		
	ExBlo TripCmd	92	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	92	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage stage
	Alarm L1	92	1	3	Bit	0x100 (9)	-	Signal: Alarm L1
	Alarm L2	92	1	3	Bit	0x200 (10)	-	Signal: Alarm L2
	Alarm L3	92	1	3	Bit	0x400 (11)	-	Signal: Alarm L3
	Trip (*)	92	1	3	Bit	0x800 (12)	-	Signal: Trip
	Trip L1 (*)	92	1	3	Bit	0x1000 (13)	-	Signal: General Trip Phase L1
	Trip L2 (*)	92	1	3	Bit	0x2000 (14)	-	Signal: General Trip Phase L2
	Trip L3 (*)	92	1	3	Bit	0x4000 (15)	-	Signal: General Trip Phase L3
	TripCmd (*)	92	1	3	Bit	0x8000 (16)	-	Signal: Trip Command

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
V[6] - 27, 59		93	1	3	Struct			
	ExBlo1-I	93	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	93	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	93	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	93	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	93	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	93	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	93	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	93	1	3	Bit	0x80 (8)	-	Signal: Alarm voltage stage
	Alarm L1	93	1	3	Bit	0x100 (9)	-	Signal: Alarm L1
	Alarm L2	93	1	3	Bit	0x200 (10)	-	Signal: Alarm L2

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Alarm L3	93	1	3	Bit	0x400 (11)	-	Signal: Alarm L3
	Trip (*)	93	1	3	Bit	0x800 (12)	-	Signal: Trip
	Trip L1 (*)	93	1	3	Bit	0x1000 (13)	-	Signal: General Trip Phase L1
	Trip L2 (*)	93	1	3	Bit	0x2000 (14)	-	Signal: General Trip Phase L2
	Trip L3 (*)	93	1	3	Bit	0x4000 (15)	-	Signal: General Trip Phase L3
	TripCmd (*)	93	1	3	Bit	0x8000 (16)	-	Signal: Trip Command
delta phi - 78V		249	1	3	Struct			
	ExBlo1-I	249	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	249	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	249	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	249	1	3	Bit	0x8 (4)	-	Signal: active

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo	249	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	249	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	249	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	249	1	3	Bit	0x80 (8)	-	Signal: Alarm Frequency Protection (collective signal)
	Trip (*)	249	1	3	Bit	0x100 (9)	-	Signal: Trip Frequency Protection (collective signal)
	TripCmd (*)	249	1	3	Bit	0x200 (10)	-	Signal: Trip Command
	Blo by V<	249	1	3	Bit	0x400 (11)	-	Signal: Module is blocked by undervoltage.
df/dt - 81R		250	1	3	Struct			
	ExBlo1-l	250	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	250	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-l	250	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Active	250	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	250	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo TripCmd	250	1	3	Bit	0x20 (6)	-	Signal: Trip Command blocked
	ExBlo TripCmd	250	1	3	Bit	0x40 (7)	-	Signal: External Blocking of the Trip Command
	Alarm	250	1	3	Bit	0x80 (8)	-	Signal: Alarm Frequency Protection (collective signal)
	Trip (*)	250	1	3	Bit	0x100 (9)	-	Signal: Trip Frequency Protection (collective signal)
	TripCmd (*)	250	1	3	Bit	0x200 (10)	-	Signal: Trip Command
	Blo by V<	250	1	3	Bit	0x400 (11)	-	Signal: Module is blocked by undervoltage.
f[1] - 81		34	1	3	Struct			
	ExBlo1-l	34	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	34	1	3	Bit	0x2 (2)	-	Module input state: External blocking2

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	ExBlo TripCmd-I	34	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	34	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	34	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo by V<	34	1	3	Bit	0x20 (6)	-	Signal: Module is blocked by undervoltage.
	Blo TripCmd	34	1	3	Bit	0x40 (7)	-	Signal: Trip Command blocked
	ExBlo TripCmd	34	1	3	Bit	0x80 (8)	-	Signal: External Blocking of the Trip Command
f[1] - 81		35	1	3	Struct			
	Alarm f	35	1	3	Bit	0x1 (1)	-	Signal: Alarm Frequency Protection
	Alarm df/dt DF/DT	35	1	3	Bit	0x2 (2)	-	Alarm instantaneous or average value of the rate-of-frequency-change
	Trip f (*)	35	1	3	Bit	0x4 (3)	-	Signal: Frequency has exceeded the limit.
	Trip df/dt DF/DT (*)	35	1	3	Bit	0x8 (4)	-	Signal: Trip df/dt or DF/DT

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Alarm	35	1	3	Bit	0x10 (5)	-	Signal: Alarm Frequency Protection (collective signal)
	Alarm delta phi	35	1	3	Bit	0x20 (6)	-	Signal: Alarm Vector Surge
	Trip (*)	35	1	3	Bit	0x40 (7)	-	Signal: Trip Frequency Protection (collective signal)
	Trip delta phi (*)	35	1	3	Bit	0x80 (8)	-	Signal: Trip Vector Surge
	TripCmd (*)	35	1	3	Bit	0x100 (9)	-	Signal: Trip Command
f[2] - 81		36	1	3	Struct			
	ExBlo1-I	36	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	36	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	36	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	36	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	36	1	3	Bit	0x10 (5)	-	Signal: External Blocking

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Blo by V<	36	1	3	Bit	0x20 (6)	-	Signal: Module is blocked by undervoltage.
	Blo TripCmd	36	1	3	Bit	0x40 (7)	-	Signal: Trip Command blocked
	ExBlo TripCmd	36	1	3	Bit	0x80 (8)	-	Signal: External Blocking of the Trip Command
f[2] - 81		37	1	3	Struct			
	Alarm f	37	1	3	Bit	0x1 (1)	-	Signal: Alarm Frequency Protection
	Alarm df/dt DF/DT	37	1	3	Bit	0x2 (2)	-	Alarm instantaneous or average value of the rate-of-frequency-change
	Trip f (*)	37	1	3	Bit	0x4 (3)	-	Signal: Frequency has exceeded the limit.
	Trip df/dt DF/DT (*)	37	1	3	Bit	0x8 (4)	-	Signal: Trip df/dt or DF/DT
	Alarm	37	1	3	Bit	0x10 (5)	-	Signal: Alarm Frequency Protection (collective signal)
	Alarm delta phi	37	1	3	Bit	0x20 (6)	-	Signal: Alarm Vector Surge
	Trip (*)	37	1	3	Bit	0x40 (7)	-	Signal: Trip Frequency Protection (collective signal)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Trip delta phi (*)	37	1	3	Bit	0x80 (8)	-	Signal: Trip Vector Surge
	TripCmd (*)	37	1	3	Bit	0x100 (9)	-	Signal: Trip Command
f[3] - 81		38	1	3	Struct			
	ExBlo1-I	38	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	38	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	38	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	38	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	38	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo by V<	38	1	3	Bit	0x20 (6)	-	Signal: Module is blocked by undervoltage.
	Blo TripCmd	38	1	3	Bit	0x40 (7)	-	Signal: Trip Command blocked
	ExBlo TripCmd	38	1	3	Bit	0x80 (8)	-	Signal: External Blocking of the Trip Command

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
f[3] - 81		39	1	3	Struct			
	Alarm f	39	1	3	Bit	0x1 (1)	-	Signal: Alarm Frequency Protection
	Alarm df/dt DF/DT	39	1	3	Bit	0x2 (2)	-	Alarm instantaneous or average value of the rate-of-frequency-change
	Trip f (*)	39	1	3	Bit	0x4 (3)	-	Signal: Frequency has exceeded the limit.
	Trip df/dt DF/DT (*)	39	1	3	Bit	0x8 (4)	-	Signal: Trip df/dt or DF/DT
	Alarm	39	1	3	Bit	0x10 (5)	-	Signal: Alarm Frequency Protection (collective signal)
	Alarm delta phi	39	1	3	Bit	0x20 (6)	-	Signal: Alarm Vector Surge
	Trip (*)	39	1	3	Bit	0x40 (7)	-	Signal: Trip Frequency Protection (collective signal)
	Trip delta phi (*)	39	1	3	Bit	0x80 (8)	-	Signal: Trip Vector Surge
	TripCmd (*)	39	1	3	Bit	0x100 (9)	-	Signal: Trip Command
f[4] - 81		40	1	3	Struct			
	ExBlo1-I	40	1	3	Bit	0x1	-	Module input state: External blocking1

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(1)		
	ExBlo2-I	40	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	40	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	40	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	40	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo by V<	40	1	3	Bit	0x20 (6)	-	Signal: Module is blocked by undervoltage.
	Blo TripCmd	40	1	3	Bit	0x40 (7)	-	Signal: Trip Command blocked
	ExBlo TripCmd	40	1	3	Bit	0x80 (8)	-	Signal: External Blocking of the Trip Command
f[4] - 81		41	1	3	Struct			
	Alarm f	41	1	3	Bit	0x1 (1)	-	Signal: Alarm Frequency Protection
	Alarm df/dt DF/DT	41	1	3	Bit	0x2 (2)	-	Alarm instantaneous or average value of the rate-of-frequency-change
	Trip f (*)	41	1	3	Bit	0x4	-	Signal: Frequency has exceeded the limit.

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(3)		
	Trip df/dt DF/DT (*)	41	1	3	Bit	0x8 (4)	-	Signal: Trip df/dt or DF/DT
	Alarm	41	1	3	Bit	0x10 (5)	-	Signal: Alarm Frequency Protection (collective signal)
	Alarm delta phi	41	1	3	Bit	0x20 (6)	-	Signal: Alarm Vector Surge
	Trip (*)	41	1	3	Bit	0x40 (7)	-	Signal: Trip Frequency Protection (collective signal)
	Trip delta phi (*)	41	1	3	Bit	0x80 (8)	-	Signal: Trip Vector Surge
	TripCmd (*)	41	1	3	Bit	0x100 (9)	-	Signal: Trip Command
f[5] - 81		42	1	3	Struct			
	ExBlo1-I	42	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-I	42	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-I	42	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	42	1	3	Bit	0x8	-	Signal: active

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(4)		
	ExBlo	42	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo by V<	42	1	3	Bit	0x20 (6)	-	Signal: Module is blocked by undervoltage.
	Blo TripCmd	42	1	3	Bit	0x40 (7)	-	Signal: Trip Command blocked
	ExBlo TripCmd	42	1	3	Bit	0x80 (8)	-	Signal: External Blocking of the Trip Command
f[5] - 81		43	1	3	Struct			
	Alarm f	43	1	3	Bit	0x1 (1)	-	Signal: Alarm Frequency Protection
	Alarm df/dt DF/DT	43	1	3	Bit	0x2 (2)	-	Alarm instantaneous or average value of the rate-of-frequency-change
	Trip f (*)	43	1	3	Bit	0x4 (3)	-	Signal: Frequency has exceeded the limit.
	Trip df/dt DF/DT (*)	43	1	3	Bit	0x8 (4)	-	Signal: Trip df/dt or DF/DT
	Alarm	43	1	3	Bit	0x10 (5)	-	Signal: Alarm Frequency Protection (collective signal)
	Alarm delta phi	43	1	3	Bit	0x20	-	Signal: Alarm Vector Surge

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(6)		
	Trip (*)	43	1	3	Bit	0x40 (7)	-	Signal: Trip Frequency Protection (collective signal)
	Trip delta phi (*)	43	1	3	Bit	0x80 (8)	-	Signal: Trip Vector Surge
	TripCmd (*)	43	1	3	Bit	0x100 (9)	-	Signal: Trip Command
f[6] - 81		44	1	3	Struct			
	ExBlo1-l	44	1	3	Bit	0x1 (1)	-	Module input state: External blocking1
	ExBlo2-l	44	1	3	Bit	0x2 (2)	-	Module input state: External blocking2
	ExBlo TripCmd-l	44	1	3	Bit	0x4 (3)	-	Module input state: External Blocking of the Trip Command
	Active	44	1	3	Bit	0x8 (4)	-	Signal: active
	ExBlo	44	1	3	Bit	0x10 (5)	-	Signal: External Blocking
	Blo by V<	44	1	3	Bit	0x20 (6)	-	Signal: Module is blocked by undervoltage.
	Blo TripCmd	44	1	3	Bit	0x40	-	Signal: Trip Command blocked

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(7)		
	ExBlo TripCmd	44	1	3	Bit	0x80 (8)	-	Signal: External Blocking of the Trip Command
f[6] - 81		45	1	3	Struct			
	Alarm f	45	1	3	Bit	0x1 (1)	-	Signal: Alarm Frequency Protection
	Alarm df/dt DF/DT	45	1	3	Bit	0x2 (2)	-	Alarm instantaneous or average value of the rate-of-frequency-change
	Trip f (*)	45	1	3	Bit	0x4 (3)	-	Signal: Frequency has exceeded the limit.
	Trip df/dt DF/DT (*)	45	1	3	Bit	0x8 (4)	-	Signal: Trip df/dt or DF/DT
	Alarm	45	1	3	Bit	0x10 (5)	-	Signal: Alarm Frequency Protection (collective signal)
	Alarm delta phi	45	1	3	Bit	0x20 (6)	-	Signal: Alarm Vector Surge
	Trip (*)	45	1	3	Bit	0x40 (7)	-	Signal: Trip Frequency Protection (collective signal)
	Trip delta phi (*)	45	1	3	Bit	0x80 (8)	-	Signal: Trip Vector Surge
	TripCmd (*)	45	1	3	Bit	0x100	-	Signal: Trip Command

3 Appendix - Data Point Lists

3.1 Signals

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
						(9)		

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Date and Time		20000	6	4	Struct			
	y	20000	6	4	Short	Word 0 (1)	-	year
	m	20000	6	4	Short	Word 1 (17)	-	month
	d	20000	6	4	Short	Word 2 (33)	-	days
	h	20000	6	4	Short	Word 3 (49)	-	hours
	min	20000	6	4	Short	Word 4 (65)	-	minute
	ms	20000	6	4	Short	Word 5 (81)	-	milliseconds
IRIG-B	Edges	20298	2	4	Float IEE754		-	Edges: Total number of rising and falling edges. This signal indicates if a signal is available at the IRIG-B input.
IRIG-B	NoOfFrameErrors	20300	2	4	Float IEE754		-	Total Number of Frame Errors. Physically corrupted Frame.
IRIG-B	NoOfFramesOK	20302	2	4	Float IEE754		-	Total Number valid Frames.
LVRT[1] – 27	Cr Tot Numb of Vdips	24092	2	4	Float IEE754		-	Counter Total number of voltage dips.
LVRT[1] – 27	Num Vdips in t-LVRT	24094	2	4	Float IEE754		-	Number of Voltage dips during t-LVRT

3 Appendix - Data Point Lists

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
LVRT[1] - 27	Cr Num Vdips to Trip	24096	2	4	Float IEE754		-	Counter Total number of voltage dips that caused a Trip
LVRT[2] - 27	Cr Tot Numb of Vdips	24138	2	4	Float IEE754		-	Counter Total number of voltage dips.
LVRT[2] - 27	Num Vdips in t-LVRT	24140	2	4	Float IEE754		-	Number of Voltage dips during t-LVRT
LVRT[2] - 27	Cr Num Vdips to Trip	24142	2	4	Float IEE754		-	Counter Total number of voltage dips that caused a Trip
Modbus	Mapped Meas 1	23000	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 2	23002	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 3	23004	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 4	23006	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 5	23008	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 6	23010	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 7	23012	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 8	23014	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 9	23016	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Modbus	Mapped Meas 10	23018	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 11	23020	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 12	23022	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 13	23024	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 14	23026	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 15	23028	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
Modbus	Mapped Meas 16	23030	2	4	Float IEE754		-	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.
SG[1]	TripCmd Cr	20810	2	4	Float IEE754		-	Counter: Total number of trips of the switchgear.
Sync – 25	f Bus	20520	2	4	Float IEE754		Hz	Bus frequency
Sync – 25	V Bus	20522	2	4	Float IEE754		V	Bus Voltage
Sync – 25	Angle Bus	20524	2	4	Float IEE754		°	Bus Angle (Reference)
Sync – 25	Angle Diff	20526	2	4	Float IEE754		°	Angle difference between bus and line voltages.
Sync – 25	Volt Diff	20528	2	4	Float IEE754		V	Voltage difference between bus and line.
Sync – 25	f Line	20530	2	4	Float IEE754		Hz	Line frequency
Sync – 25	V Line	20532	2	4	Float IEE754		V	Line Voltage

3 Appendix - Data Point Lists

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Sync - 25	Angle Line	20534	2	4	Float IEE754		°	Line Angle
Sync - 25	Slip Freq	20536	2	4	Float IEE754		Hz	Slip frequency
VT	f	20128	2	4	Float IEE754		Hz	Measured value: Frequency
VT	VL12	20130	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (fundamental)
VT	VL23	20132	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (fundamental)
VT	VL31	20134	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (fundamental)
VT	VL1	20136	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (fundamental)
VT	VL2	20138	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (fundamental)
VT	VL3	20140	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (fundamental)
VT	VX meas	20142	2	4	Float IEE754		V	Measured value (measured): VX measured (fundamental)
VT	V0	20146	2	4	Float IEE754		V	Measured value (calculated): Symmetrical components Zero voltage(fundamental)
VT	V1	20148	2	4	Float IEE754		V	Measured value (calculated): Symmetrical components positive phase sequence voltage(fundamental)
VT	V2	20150	2	4	Float IEE754		V	Measured value (calculated): Symmetrical components negative phase sequence voltage(fundamental)
VT	VG calc	20162	2	4	Float IEE754		V	Measured value (calculated): VG (fundamental)
VT	phi VG calc	20386	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VG calc Reference phasor is required to calculate the angle. This is the first measured

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
								voltage (or current) channel with sufficiently high amplitude.
VT	phi VX meas	20388	2	4	Float IEE754		°	Measured value: Angle of Phasor VX meas Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi VL12	20390	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL12 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi VL1	20392	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL1 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi VL23	20394	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL23 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi VL2	20396	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL2 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.

3 Appendix - Data Point Lists

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT	phi VL31	20398	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL31 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi VL3	20400	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL3 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi V0	20402	2	4	Float IEE754		°	Measured value (calculated): Angle Zero Sequence System Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi V1	20404	2	4	Float IEE754		°	Measured value (calculated): Angle of Positive Sequence System Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	phi V2	20406	2	4	Float IEE754		°	Measured Value (calculated): Angle of Negative Sequence System Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.
VT	VL1 THD	20408	2	4	Float IEE754		V	Measured value (calculated): VL1 Total Harmonic Distortion

Module (ANSI / IEEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT	VL12 THD	20410	2	4	Float IEE754		V	Measured value (calculated): V12 Total Harmonic Distortion
VT	VL2 THD	20412	2	4	Float IEE754		V	Measured value (calculated): VL2 Total Harmonic Distortion
VT	VL23 THD	20414	2	4	Float IEE754		V	Measured value (calculated): V23 Total Harmonic Distortion
VT	VL3 THD	20416	2	4	Float IEE754		V	Measured value (calculated): VL3 Total Harmonic Distortion
VT	VL31 THD	20418	2	4	Float IEE754		V	Measured value (calculated): V31 Total Harmonic Distortion
VT	%VL1 THD	20420	2	4	Float IEE754		%	Measured value (calculated): VL1 Total Harmonic Distortion / Ground wave
VT	%VL12 THD	20422	2	4	Float IEE754		%	Measured value (calculated): V12 Total Harmonic Distortion / Ground wave
VT	%VL2 THD	20424	2	4	Float IEE754		%	Measured value (calculated): VL2 Total Harmonic Distortion / Ground wave
VT	%VL23 THD	20426	2	4	Float IEE754		%	Measured value (calculated): V23 Total Harmonic Distortion / Ground wave
VT	%VL3 THD	20428	2	4	Float IEE754		%	Measured value (calculated): VL3 Total Harmonic Distortion / Ground wave
VT	%VL31 THD	20430	2	4	Float IEE754		%	Measured value (calculated): V31 Total Harmonic Distortion / Ground wave
VT	VG calc RMS	20432	2	4	Float IEE754		V	Measured value (calculated): VG (RMS)
VT	VX meas RMS	20434	2	4	Float IEE754		V	Measured value (measured): VX measured (RMS)
VT	VL1 RMS	20436	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (RMS)
VT	VL12 RMS	20438	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (RMS)
VT	VL2 RMS	20440	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (RMS)

3 Appendix - Data Point Lists

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT	VL23 RMS	20442	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (RMS)
VT	VL3 RMS	20444	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (RMS)
VT	VL31 RMS	20446	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (RMS)
VT	%(V2/V1)	20450	2	4	Float IEE754		%	Measured value (calculated): V2/V1, phase sequence will be taken into account automatically.
VT	f max	21002	2	4	Float IEE754		Hz	Max. frequency value
VT	f min	21004	2	4	Float IEE754		Hz	Min. frequency value
VT	V1 max	21044	2	4	Float IEE754		V	Maximum value: Symmetrical components positive phase sequence voltage(fundamental)
VT	V1 min	21046	2	4	Float IEE754		V	Minimum value: Symmetrical components positive phase sequence voltage(fundamental)
VT	V2 max	21050	2	4	Float IEE754		V	Maximum value: Symmetrical components negative phase sequence voltage(fundamental)
VT	V2 min	21052	2	4	Float IEE754		V	Minimum value: Symmetrical components negative phase sequence voltage(fundamental)
VT	delta phi	21126	2	4	Float IEE754		°	Measured value (calculated): Vector surge
VT	df/dt	21128	2	4	Float IEE754		Hz/s	Measured value (calculated): Rate-of-frequency-change.
VT	VG calc max RMS	21498	2	4	Float IEE754		V	Measured value (calculated):VX maximum value (RMS)
VT	VG calc min RMS	21500	2	4	Float IEE754		V	Measured value (calculated):VX minimum value (RMS)
VT	VX meas max RMS	21504	2	4	Float IEE754		V	Measured value: VX maximum value (RMS)

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT	VX meas min RMS	21506	2	4	Float IEE754		V	Measured value: VX minimum value (RMS)
VT	VL12 avg RMS	21508	2	4	Float IEE754		V	VL12 average value (RMS)
VT	VL12 max RMS	21510	2	4	Float IEE754		V	VL12 maximum value (RMS)
VT	VL12 min RMS	21512	2	4	Float IEE754		V	VL12 minimum value (RMS)
VT	VL1 avg RMS	21514	2	4	Float IEE754		V	VL1 average value (RMS)
VT	VL1 max RMS	21516	2	4	Float IEE754		V	VL1 maximum value (RMS)
VT	VL1 min RMS	21518	2	4	Float IEE754		V	VL1 minimum value (RMS)
VT	VL23 avg RMS	21520	2	4	Float IEE754		V	VL23 average value (RMS)
VT	VL23 max RMS	21522	2	4	Float IEE754		V	VL23 maximum value (RMS)
VT	VL23 min RMS	21524	2	4	Float IEE754		V	VL23 minimum value (RMS)
VT	VL2 avg RMS	21526	2	4	Float IEE754		V	VL2 average value (RMS)
VT	VL2 max RMS	21528	2	4	Float IEE754		V	VL2 maximum value (RMS)
VT	VL2 min RMS	21530	2	4	Float IEE754		V	VL2 minimum value (RMS)
VT	VL31 avg RMS	21532	2	4	Float IEE754		V	VL31 average value (RMS)
VT	VL31 max RMS	21534	2	4	Float IEE754		V	VL31 maximum value (RMS)
VT	VL31 min RMS	21536	2	4	Float IEE754		V	VL31 minimum value (RMS)
VT	VL3 avg RMS	21538	2	4	Float IEE754		V	VL3 average value (RMS)
VT	VL3 max RMS	21540	2	4	Float IEE754		V	VL3 maximum value (RMS)
VT	VL3 min RMS	21542	2	4	Float IEE754		V	VL3 minimum value (RMS)
VT	%(V2/V1) max	21552	2	4	Float IEE754		%	Measured value (calculated):V2/V1 maximum value, phase sequence will be taken into account automatically
VT	%(V2/V1) min	21554	2	4	Float IEE754		%	Measured value (calculated):V2/V1 minimum value , phase sequence will be taken into account automatically

3 Appendix - Data Point Lists

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT - fault value	f	50128	2	4	Float IEE754		Hz	Measured value: Frequency , as stored in the Fault Recorder
VT - fault value	VL12	50130	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (fundamental) , as stored in the Fault Recorder
VT - fault value	VL23	50132	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (fundamental) , as stored in the Fault Recorder
VT - fault value	VL31	50134	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (fundamental) , as stored in the Fault Recorder
VT - fault value	VL1	50136	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (fundamental) , as stored in the Fault Recorder
VT - fault value	VL2	50138	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (fundamental) , as stored in the Fault Recorder
VT - fault value	VL3	50140	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (fundamental) , as stored in the Fault Recorder
VT - fault value	VX meas	50142	2	4	Float IEE754		V	Measured value (measured): VX measured (fundamental) , as stored in the Fault Recorder
VT - fault value	V0	50146	2	4	Float IEE754		V	Measured value (calculated): Symmetrical components Zero voltage(fundamental) , as stored in the Fault Recorder
VT - fault value	V1	50148	2	4	Float IEE754		V	Measured value (calculated): Symmetrical components positive phase sequence voltage(fundamental) , as stored in the Fault Recorder
VT - fault value	V2	50150	2	4	Float IEE754		V	Measured value (calculated): Symmetrical components negative phase sequence voltage(fundamental) , as stored in the Fault Recorder

Module (ANSI / IEEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT - fault value	VG calc	50162	2	4	Float IEE754		V	Measured value (calculated): VG (fundamental) , as stored in the Fault Recorder
VT - fault value	phi VG calc	50386	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VG calc Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VX meas	50388	2	4	Float IEE754		°	Measured value: Angle of Phasor VX meas Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VL12	50390	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL12 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VL1	50392	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL1 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VL23	50394	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL23 Reference phasor is required to calculate the angle. This is the first measured

3 Appendix - Data Point Lists

3.2 Measuring Values

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
								voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VL2	50396	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL2 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VL31	50398	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL31 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	phi VL3	50400	2	4	Float IEE754		°	Measured value (calculated): Angle of Phasor VL3 Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude. , as stored in the Fault Recorder
VT - fault value	VG calc RMS	50432	2	4	Float IEE754		V	Measured value (calculated): VG (RMS) , as stored in the Fault Recorder
VT - fault value	VX meas RMS	50434	2	4	Float IEE754		V	Measured value (measured): VX measured (RMS) , as stored in the Fault Recorder
VT - fault value	VL1 RMS	50436	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (RMS) , as stored in the Fault Recorder
VT - fault value	VL12 RMS	50438	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (RMS) , as stored in the Fault Recorder

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
VT - fault value	VL2 RMS	50440	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (RMS) , as stored in the Fault Recorder
VT - fault value	VL23 RMS	50442	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (RMS) , as stored in the Fault Recorder
VT - fault value	VL3 RMS	50444	2	4	Float IEE754		V	Measured value: Phase-to-neutral voltage (RMS) , as stored in the Fault Recorder
VT - fault value	VL31 RMS	50446	2	4	Float IEE754		V	Measured value: Phase-to-phase voltage (RMS) , as stored in the Fault Recorder
VT - fault value	delta phi	51126	2	4	Float IEE754		°	Measured value (calculated): Vector surge , as stored in the Fault Recorder
VT - fault value	df/dt	51128	2	4	Float IEE754		Hz/s	Measured value (calculated): Rate-of-frequency-change. , as stored in the Fault Recorder
Values	Build	20008	2	4	Float IEE754		-	Build Number
Values	Operating hours Cr	20010	2	4	Float IEE754		h	Operating hours counter of the protective device

3.3 Commands

Module (ANSI / IEC)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Acknowledge	LEDs	22000	1	5	0xFF00		-	LEDs
Acknowledge	Binary Outputs	22001	1	5	0xFF00		-	Binary Outputs
Acknowledge	Scada	22002	1	5	0xFF00		-	Scada
Acknowledge	Device	22003	1	5	0xFF00		-	Device
Acknowledge	Ack TripCmd	22005	1	5	0xFF00		-	Signal: Acknowledge Trip Command
Reset	Modbus diagnosis counter	22006	1	5	0xFF00		-	Modbus diagnosis counter
Scada Cmd	Assbl Scada Cmd 1	22020	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 2	22021	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 3	22022	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 4	22023	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 5	22024	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 6	22025	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 7	22026	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 8	22027	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 9	22028	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 10	22029	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command

Module (ANSI / IEEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Scada Cmd	Assbl Scada Cmd 11	22030	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 12	22031	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 13	22032	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 14	22033	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 15	22034	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Scada Cmd	Assbl Scada Cmd 16	22035	1	5	0xFF00=On, 0x0000=Off		-	Assignable Scada Command
Fault rec	Res all rec	22040	1	5	0xFF00		-	Reset all records
PSet-Switch	Scada PS1	22050	1	5	0xFF00		-	Scada Setting Group1
PSet-Switch	Scada PS2	22051	1	5	0xFF00		-	Scada Setting Group2
PSet-Switch	Scada PS3	22052	1	5	0xFF00		-	Scada Setting Group3
PSet-Switch	Scada PS4	22053	1	5	0xFF00		-	Scada Setting Group4
Maint Mode	Maint Mode SCADA	22054	1	5	0xFF00=On, 0x0000=Off		-	Signal: Arc Flash Reduction Maintenance SCADA Mode
SG	SG ControlCmd1	22100	1	5	0xFF00=On, 0x0000=Off		-	Control Command Switchgear

3.4 Settings

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
Date and Time		32500	6	3 16	Struct			
	y	32500	6	3 16	Short	Word 0 (1)	-	year
	m	32500	6	3 16	Short	Word 1 (17)	-	month
	d	32500	6	3 16	Short	Word 2 (33)	-	days
	h	32500	6	3 16	Short	Word 3 (49)	-	hours
	min	32500	6	3 16	Short	Word 4 (65)	-	minute
	ms	32500	6	3 16	Short	Word 5 (81)	-	milliseconds
Fault rec		50000	9	3 16	Struct			
	RecordNo	50000	9	3 16	Short	Word 0 (1)	-	Record Number
	Trip Cause	50000	9	3 16	Short	Word 1 (17)	-	Code for the trip cause. In case of several simultaneous trip causes the primary cause is selected. If there is another trip later then the new trip cause overwrites the previous one. The codes for the trip cause are documented in the SCADA documentation.

Module (ANSI / IEEE)	Name Function	Start Register Address	No. of Modbus Registers	Function code	Format	Bit Mask (Bit position)	Unit	Description
	Pickup Cause	50000	9	3 16	Short	Word 2 (33)	-	Code for last Pickup cause corresponds to fault record: See scada doc for correlation between pickup reason and code
	Fault No.	50000	9	3 16	Short	Word 3 (49)	-	Fault number
	No. of Grid Faults	50000	9	3 16	Short	Word 4 (65)	-	Number of grid faults: This is a counter for all faults (i.e. General Alarms »Prot . Alarm«), but except faults during a running cycle of the Automatic Reclosure module (signal »AR . running«). (Remark: The »Fault No.« counts every new fault independent of AR cycles. This means that for protective devices without AR module these two counters are equivalent.)
	Time stamp:	50000	9	3 16	long long	Word 5- Word 8 (81)	-	Timestamp in milliseconds since 1970

3.5 Cause of Trip

Cause of trip reason is provided on two different Modbus addresses:

- At address 5004 the “last primary trip cause” is available. This means, in case of several simultaneous trip causes the primary cause is selected. If there is another trip later then the new trip cause overwrites the previous one. The trip cause can be read as long as a trip reason is present. In addition, the content of this register can be latched. The trip cause is latched in the same way as other trip signals, that means if the corresponding latching setting in Modbus is active, the content of the register is fixed until it is acknowledged by command.
- At address 50000 and up the last trip and alarm reason is available with related record, fault, net number and time stamp. Any saved record can be requested by sending the record number to the corresponding register. Be aware that the content of these registers can only be read entirely and that the content changes every time a new fault occurs in the fault recorder.

All required fault values must be defined within a SCADApt (HptSMap) mapping file. The default datapoint list cannot be used in this case. All data-points that are needed for communicating with the substation must be defined as a HptSMap mapping file. (See also the separate SCADApt User Manual.)

If not a specific fault is selected, last fault value is presented on these addresses.

The following table is showing the “trip cause code” and its relation to the “trip cause reason”.

Trip Cause	Description	Module
1	NORM	
1306		ExP[1]
1307		ExP[2]
1308		ExP[3]
1309		ExP[4]
1310		Intertripping
1401		f[1]
1402		f[2]
1403		f[3]

Trip Cause	Description	Module
1404		f[4]
1405		f[5]
1406		f[6]
1407		df/dt
1408		delta phi
2501		LVRT[1]
2502		LVRT[2]
3001		V012[1]
3002		V012[2]
3003		V012[3]
3004		V012[4]
3005		V012[5]
3006		V012[6]
4001		VG[1]
4002		VG[2]
4101		V[1]
4102		V[2]
4103		V[3]
4104		V[4]
4105		V[5]

Trip Cause	Description	Module
4106		V[6]

High **PROTEC**

MRU4

MODBUS - DATA POINT LIST



SEG Electronics GmbH

Krefelder Weg 47 • D-47906 Kempen (Germany)

Telephone: +49 (0) 21 52 145 0

Internet: www.SEGelectronics.de

Sales

Telephone: +49 (0) 21 52 145 331

Fax: +49 (0) 21 52 145 354

E-mail: sales@SEGelectronics.de

Service

Telephone: +49 (0) 21 52 145 600

Fax: +49 (0) 21 52 145 354

E-mail: support@SEGelectronics.de

docs.SEGelectronics.de/HighPROTEC



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