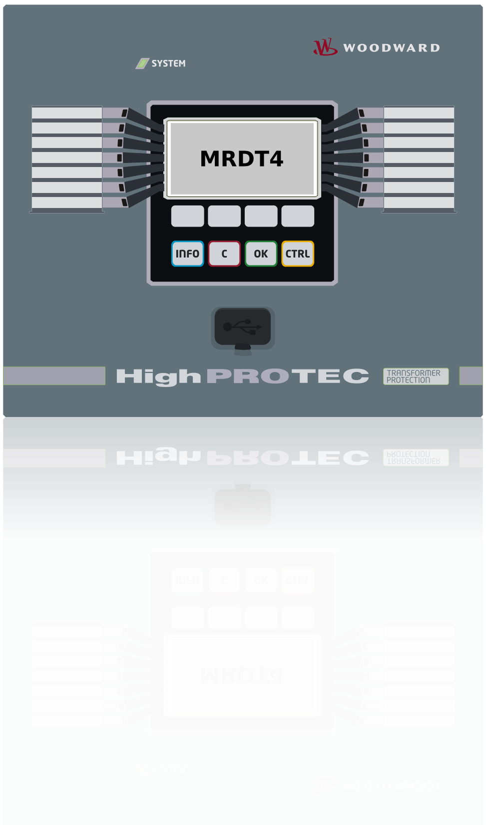
HighPROTEC

Reference manual MRDT4-3.6-EN-REF



MRDT4

Transformer Differential Protection

Version: 3.6.b (Build 41472)

Original document · English

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Original reference manual

Woodward Kempen GmbH

Krefelder Weg 47 • D–47906 Kempen (Germany)

Postfach 10 07 55 (P.O.Box) • D–47884 Kempen (Germany)

Telephone: +49 (0) 21 52 145 1

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1 About This Reference Manual

This document is a reference of all the Setting Values, Direct Commands and Signals of the MRDT4. In other words, it lists all parameters that are available (or can be made available) with the (optionally) full featured versions of the MRDT4 protection device.

CAUTION!

This document does not intend to give long and/or detailed description, nor does it intend to replace the full Technical Manual in any way. Only a quite short description is given for each parameter.

This document is a reference of all the Setting Values, Direct Commands and Signals of the MRDT4.

Every HighPROTEC protection device operates using a lot of digital values of various types. Throughout our Technical Documentation, we are talking of “settings” (or “parameters”) or “signals” or “(measured) values”, depending on the type.

Please consult the Technical Manual, in particular Chapter “Modules, Settings, Signals and Values”, for details of the existing data types.

Modules

The firmware of every HighPROTEC protection device can be thought of being sub-divided in several independent function blocks, the so-called “modules”. Every protection function, for example, is a module of its own. But one of the fundamental concepts of a HighPROTEC protection device is to implement this with great consequence: The functionality of calculating statistical data is a module (named »Statistics«), every communication protocol is a module, the control of switchgear devices is a module (named »Ctrl«), but the properties of the switchgear itself is part of another module. There is even a general protection module (named »Prot«) that interacts all specific protection modules.

Every parameter, every value and every signal is therefore a member of some module.

But note that the settings dialogs (on the panel (HMI) or in the *Smart view* operating software) often omit the module name whenever it is clear from the menu branch. This means the parameters are often displayed only with their individual parameter names, i. e. simply *»Function«* instead of the full-blown *»I[1] . Function«*. This increases the overview and simplifies all configuration and operation work; however, it is good to know that the writing *»Function«* is just an abbreviation. In fact, **every** parameter **always** belongs to a module, and therefore – to make this concept absolutely clear – the reference tables have always the module name added in front of every parameter name

Especially for protection functions it is often required to have several instances active. For example, overcurrent protection usually has several “stages”, and all of these are running at the same time (using their individual setting values). Therefore it is an important feature of every HighPROTEC protection device that a lot of modules exist in several “instances”, which are numbered (in brackets): For the overcurrent protection, for example: »I[1]«, I[2]«, …

In the reference tables, usually every module has its own dedicated chapter, which lists the available number of instances at the beginning. Then, however, in the sub-chapters listing the various parameter types, only the first instance (e. g. »I[1]«) is mentioned, because all the other instances are identical anyway.

Structure of a Reference Table

Since (almost) every module can be activated or deactivated independently of the other modules and all parameters of an inactive module disappear from the menu branch it would not be helpful if this Reference Manual would list parameters sorted according to the menu structure. Instead, we list categories of modules (e. g. “Protection Functions”) and all the modules within a category.

For each parameter, there is a table with its properties, looking like this:

Module . Parameter	[Menu Path to This Parameter]	
Default Value For some parameters: • Availability restrictions	Value Range	Perm.
Type	*Short descriptive text explaining the functionality of this parameter.*	

“Type” is the data type of the parameter, which is denoted by a small icon. The following types are possible:

- Setting Parameter
- Direct Control
- Input State
- Signal (Output State)
- Statistical Value
- Counter
- (Measuring) Value
- Dialog — Such a dialog can feature several data objects using a special representation and/or functionality.

“Perm.” means “permission”, i. e. the access level and password that is required to modify the parameter. (Please refer to the “Security” chapter in the full Technical Manual for details.)

“ Adapt. Param.” means that this parameter supports Adaptive Parameter Sets. (See the “Adaptive Parameter Sets” section in the User Manual.)

For some parameter types (e. g. Input and Output States), the second row (default, value range, permission) is useless and therefore omitted.

Example of a parameter:

I[1] . **Mode**	[Device planning]	
non directional	Selection List Mode: -, non directional, forward, reverse	S.3
	general operation mode	

This means that one can find the parameter in the menu [Device planning], and its values are picked from a selection list named “Mode”. The “╚▷” arrow indicates a cross-reference (hyperlink) into the “Selection Lists” chapter, and a click takes you to a table that lists all available choices. The access level “S.3” means the access level “Supervisor-Lv3”, which is required to modify the parameter.

Audience of This Manual

The manual serves as working basis for:

- Engineers in the protection field,
- commissioning engineers,
- people dealing with setting, testing and maintenance of protection and control devices,
- as well as trained personnel for electrical installations and power stations.

All functions concerning the MRDT4 are listed. Should there be a description of any functions, parameters or inputs/outputs which do not apply to the device in use, please ignore that information.

This manual describes the (optionally) full featured versions of the devices.

All technical information and data included in this manual reflect their state at the time this document was issued. We reserve the right to carry out technical modifications in line with further development without changing this manual and without previous notice. Hence no claim can be brought based on the information and descriptions this manual includes.

We do not accept any liability for damage and operational failures caused by operating errors or disregarding the directions of this manual.

No part of this manual is allowed to be reproduced or passed on to others in any form, unless *Woodward* have approved in writing.

This Reference Manual is part of the delivery scope when purchasing the device. In case the device is passed on (sold) to a third party, the manual has to be handed over as well.

Information Concerning Liability and Warranty

Woodward does not accept any liability for damage resulting from conversions or changes carried out on the device or planning (projecting) work, parameter setting or adjustment changes done by the customer.

The warranty expires after a device has been opened by others than *Woodward* specialists.

Warranty and liability conditions stated in *Woodward* General Terms and Conditions are not supplemented by the above mentioned explanations.

2 Hardware

2.1 Device Configuration

Transformer Differential Protection						
MRDT4	**-2**	**#**	**#**	**#**	**#**	**#**
Hardware Variant 1						
8 digital inputs | 7 binary output relays		**A**				
16 digital inputs | 13 binary output relays		**D**				
Hardware Variant 2						
W1: Default Ground Current - W2: Default Ground Current			**0**			
W1: Sensitive Ground Current - W2: Default Ground Current			**1**			
W1: Default Ground Current - W2: Sensitive Ground Current			**2**			
W1: Sensitive Ground Current - W2: Sensitive Ground Current			**3**			
Housing						
Flush mounting				**A**		
19 inch mounting (semi-flush)				**B**		
Customized Version 1				**H**		
Customized Version 2				**K**		
Communication						
Without					**A**	
RS 485: Modbus RTU | IEC 60870-5-103 | DNP3 RTU					**B**	
Ethernet: Modbus TCP | DNP3 UDP/TCP | IEC 60870-5-104					**C**	
Fiber Optics: Profibus-DP					**D**	
D-SUB: Profibus-DP					**E**	
Fiber Optics: Modbus RTU | IEC 60870-5-103 | DNP3 RTU					**F**	
RS 485/D-SUB: Modbus RTU | IEC 60870-5-103 | DNP3 RTU					**G**	
Ethernet: IEC 61850 communication | Modbus TCP | DNP3 UDP/TCP | IEC 60870-5-104					**H**	
RS 485, Ethernet: Modbus TCP/RTU | IEC 60870-5-103 | IEC 60870-5-104 | DNP3 UDP/TCP/RTU					**I**	
Ethernet/Fiber Optics: IEC 61850 communication | Modbus TCP | DNP3 UDP/TCP | IEC 60870-5-104					**K**	
Ethernet/Fiber Optics: Modbus TCP | DNP3 UDP/TCP | IEC 60870-5-104					**L**	

Transformer Differential Protection						
MRDT4	**-2**	**#**	**#**	**#**	**#**	**#**
RS 485, Ethernet: IEC 61850 | Modbus TCP/RTU | IEC 60870-5-103 | IEC 60870-5-104 | DNP3 UDP/TCP/RTU					**T**	
Printed Circuit Board						
Standard						**A**
printed circuit boards are conformal coated						**B**

2.2 Digital Inputs

2.2.1 DI Slot X1 (“DI8-X1”)

DI Slot X1 . **Nom voltage**	[Device Para / Digital Inputs / DI Slot X1 / Group 1] [Device Para / Digital Inputs / DI Slot X1 / Group 2] [Device Para / Digital Inputs / DI Slot X1 / Group 3]	
24 VDC	24 VDC, 48 VDC, 60 VDC, 110 VDC, 230 VDC, 110 VAC, 230 VAC └⇒ Nom voltage.	S.3
	Nominal voltage of the digital inputs	

DI Slot X1 . **Inverting 1** **...** DI Slot X1 . **Inverting 8**	[Device Para / Digital Inputs / DI Slot X1 / Group 1] [Device Para / Digital Inputs / DI Slot X1 / Group 2] [Device Para / Digital Inputs / DI Slot X1 / Group 3]	
inactive	inactive, active └⇒ Mode.	S.3
	Inverting the input signals.	

DI Slot X1 . **Debouncing time 1** **...** DI Slot X1 . **Debouncing time 8**	[Device Para / Digital Inputs / DI Slot X1 / Group 1] [Device Para / Digital Inputs / DI Slot X1 / Group 2] [Device Para / Digital Inputs / DI Slot X1 / Group 3]	
no debouncing time	no debouncing time, 20 ms, 50 ms, 100 ms └⇒ Debouncing time.	S.3
	A change of the state of a digital input will only be recognized after the debouncing time has expired (become effective). Thus, transient signals will not be misinterpreted.	

DI Slot X1 . **DI 1** **...** DI Slot X1 . **DI 8**	[Device Para / Digital Inputs / DI Slot X1 / Group 1] [Device Para / Digital Inputs / DI Slot X1 / Group 2] [Device Para / Digital Inputs / DI Slot X1 / Group 3]
	Signal: Digital Input

2.2.2 DI Slot X6

DI Slot X6 . **Nom voltage**	[Device Para / Digital Inputs / DI Slot X6 / Group 1]	
24 VDC	24 VDC, 48 VDC, 60 VDC, 110 VDC, 230 VDC, 110 VAC, 230 VAC ⮡ Nom voltage.	S.3
	Nominal voltage of the digital inputs	

DI Slot X6 . **Inverting 1** ... DI Slot X6 . **Inverting 8**	[Device Para / Digital Inputs / DI Slot X6 / Group 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting the input signals.	

DI Slot X6 . **Debouncing time 1** ... DI Slot X6 . **Debouncing time 8**	[Device Para / Digital Inputs / DI Slot X6 / Group 1]	
no debouncing time	no debouncing time, 20 ms, 50 ms, 100 ms ⮡ Debouncing time.	S.3
	A change of the state of a digital input will only be recognized after the debouncing time has expired (become effective). Thus, transient signals will not be misinterpreted.	

DI Slot X6 . **DI 1** ... DI Slot X6 . **DI 8**	[Device Para / Digital Inputs / DI Slot X6 / Group 1]
	Signal: Digital Input

2.3 Binary Outputs

2.3.1 BO Slot X2 (6 Binary Outputs) (6 Binary Outputs)

BO Slot X2 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ↳ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X2 . **t-hold**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X2 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X2 . **Latched**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
active	inactive, active ↳ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X2 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
“-” *Only available if:* • BO Slot X2 . Latched = active	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X2 . **Inverting**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
inactive	inactive, active ↳ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X2 . **Assignment 1**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
SG[1] . TripCmd	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Inverting 1** . . . BO Slot X2 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X2 . **Assignment 2** . . . BO Slot X2 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 1]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X2 . **t-hold**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X2 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X2 . **Latched**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
active	inactive, active ⮡ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X2 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
"-" *Only available if:* • BO Slot X2 . Latched = active	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X2 . **Inverting**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X2 . **Assignment 1**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
SG[2] . TripCmd	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Inverting 1** **. . .** BO Slot X2 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X2 . **Assignment 2** **. . .** BO Slot X2 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 2]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X2 . **t-hold**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X2 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X2 . **Latched**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
inactive	inactive, active ⮡ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X2 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
“-” *Only available if:* • BO Slot X2 . Latched = active	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X2 . **Inverting**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
inactive	inactive, active └⊳ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X2 . **Assignment 1**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
Prot . Alarm	“-” . . . Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Inverting 1** **. . .** BO Slot X2 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
inactive	inactive, active └⊳ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X2 . **Assignment 2** **. . .** BO Slot X2 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 3]	
“-”	“-” . . . Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
Normally open (NO)	Normally open (NO), Normally closed (NC) └⊳ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X2 . **t-hold**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
0.00s	0.00s ... 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X2 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
0.00s	0.00s ... 300.00s	S.3
	Switch Off Delay	

BO Slot X2 . **Latched**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X2 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
"-" *Only available if:* • BO Slot X2 . Latched = active	"-" ... Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X2 . **Inverting**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X2 . **Assignment 1** **...** BO Slot X2 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
"-"	"-" ... Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Inverting 1** . . . BO Slot X2 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 4]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X2 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X2 . **t-hold**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X2 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X2 . **Latched**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
inactive	inactive, active ⮡ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X2 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
“-” *Only available if:* • BO Slot X2 . Latched = active	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X2 . **Inverting**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X2 . **Assignment 1** ... BO Slot X2 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
“-”	“-” ... Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Inverting 1** ... BO Slot X2 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 5]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X2 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X2 . **t-hold**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
0.00s	0.00s ... 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X2 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
0.00s	0.00s ... 300.00s	S.3
	Switch Off Delay	

BO Slot X2 . **Latched**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
inactive	inactive, active ⮑ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X2 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
“-” *Only available if:* • BO Slot X2 . Latched = active	“-” . . . Sys . Internal test state ⮑ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X2 . **Inverting**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
inactive	inactive, active ⮑ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X2 . **Assignment 1** **...** BO Slot X2 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
“-”	“-” . . . Sys . Internal test state ⮑ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X2 . **Inverting 1** **...** BO Slot X2 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X2 / BO 6]	
inactive	inactive, active ⮑ Mode.	S.3
	Inverting of the state of the assigned signal.	

2.3.1.1 BO Slot X2: Service

BO Slot X2 . **DISARMED Ctrl**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X2]	
inactive	inactive, active ↳ active/inactive.	S.3
	Enables and disables the disarming of the relay outputs. This is the first step of a two step process, to inhibit the operation or the relay outputs. Please refer to "DISARMED" for the second step.	

BO Slot X2 . **Disarm Mode**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X2]	
permanent	permanent, timeout ↳ Mode.	S.3
	CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance.	

BO Slot X2 . **t-Timeout DISARM**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X2]	
0.03s *Only available if:* • BO Slot X2 . Disarm Mode = timeout	0.00s ... 300.00s	S.3
	The relays will be armed again after expiring of this time.	

BO Slot X2 . **DISARMED**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X2]	
inactive	inactive, active ↳ active/inactive.	S.3
	This is the second step, after the "DISARMED Ctrl" has been activated, that is required to DISARM the relay outputs. This will DISARM those output relays that are currently not latched and that are not on “hold” by a pending minimum hold time. CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: Zone Interlocking and Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance.	

BO Slot X2 . **Force Mode**	[Service / Test (Prot inhibit) / Force OR / BO Slot X2]	
permanent	permanent, timeout └─▷ Mode.	S.3
	By means of this function the normal Output Relay States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	

BO Slot X2 . **t-Timeout Force**	[Service / Test (Prot inhibit) / Force OR / BO Slot X2]	
0.03s *Only available if:* • BO Slot X2 . Force Mode = timeout	0.00s ... 300.00s	S.3
	The Output State will be set by force for the duration of this time. That means for the duration of this time the Output Relay does not show the state of the signals that are assigned on it.	

BO Slot X2 . **Force all Outs**	[Service / Test (Prot inhibit) / Force OR / BO Slot X2]	
Normal	Normal, De-Energized, Energized └─▷ Relay operating modes.	S.3
◉	*By means of this function the normal Output Relay State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state. Forcing all outputs relays of an entire assembly group is superior to forcing a single output relay.*	

BO Slot X2 . **Force OR1** ... BO Slot X2 . **Force OR6**	[Service / Test (Prot inhibit) / Force OR / BO Slot X2]	
Normal	Normal, De-Energized, Energized └─▷ Relay operating modes.	S.3
◉	*By means of this function the normal Output Relay State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.*	

2.3.2 BO Slot X5 (6 Binary Outputs) (6 Binary Outputs)

BO Slot X5 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
Operating Mode		

BO Slot X5 . **t-hold**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
0.00s	0.00s . . . 300.00s	S.3
To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.		

BO Slot X5 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
0.00s	0.00s . . . 300.00s	S.3
Switch Off Delay		

BO Slot X5 . **Latched**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
inactive	inactive, active ⮡ Mode.	S.3
Defines whether the Relay Output will be latched when it picks up.		

BO Slot X5 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
“-” *Only available if:* • BO Slot X5 . Latched = active	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.		

BO Slot X5 . **Inverting**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
inactive	inactive, active ⮡ Mode.	S.3
Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).		

BO Slot X5 . **Assignment 1** ... BO Slot X5 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X5 . **Inverting 1** ... BO Slot X5 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X5 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X5 . **t-hold**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X5 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X5 . **Latched**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
inactive	inactive, active ⮡ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X5 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
“-” *Only available if:* • BO Slot X5 . Latched = active	“-” . . . Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X5 . **Inverting**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
inactive	inactive, active └⊳ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X5 . **Assignment 1** **...** BO Slot X5 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
“-”	“-” . . . Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X5 . **Inverting 1** **...** BO Slot X5 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 2]	
inactive	inactive, active └⊳ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X5 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
Normally open (NO)	Normally open (NO), Normally closed (NC) └⊳ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X5 . **t-hold**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
0.00s	0.00s ... 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X5 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
0.00s	0.00s ... 300.00s	S.3
	Switch Off Delay	

BO Slot X5 . **Latched**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
inactive	inactive, active ↳ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X5 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
"-" *Only available if:* • BO Slot X5 . Latched = active	"-" ... Sys . Internal test state ↳ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X5 . **Inverting**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
inactive	inactive, active ↳ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X5 . **Assignment 1** **...** BO Slot X5 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
"-"	"-" ... Sys . Internal test state ↳ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X5 . **Inverting 1** ... BO Slot X5 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 3]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X5 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ⮡ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X5 . **t-hold**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X5 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X5 . **Latched**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
inactive	inactive, active ⮡ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X5 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
“-” *Only available if:* • BO Slot X5 . Latched = active	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X5 . **Inverting**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
inactive	inactive, active └▷ Mode.	S.3
✎	*Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).*	

BO Slot X5 . **Assignment 1** . . . BO Slot X5 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
“-”	“-” . . . Sys . Internal test state └▷ 1..n, Assignment List.	S.3
✎	*Assignment*	

BO Slot X5 . **Inverting 1** . . . BO Slot X5 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 4]	
inactive	inactive, active └▷ Mode.	S.3
✎	*Inverting of the state of the assigned signal.*	

BO Slot X5 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
Normally open (NO)	Normally open (NO), Normally closed (NC) └▷ 1...n Operating Modes.	S.3
✎	*Operating Mode*	

BO Slot X5 . **t-hold**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
0.00s	0.00s . . . 300.00s	S.3
✎	*To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.*	

BO Slot X5 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
0.00s	0.00s . . . 300.00s	S.3
✎	*Switch Off Delay*	

BO Slot X5 . **Latched**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
inactive	inactive, active └⊳ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X5 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
"-" *Only available if:* • BO Slot X5 . Latched = active	"-" . . . Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X5 . **Inverting**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
inactive	inactive, active └⊳ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X5 . **Assignment 1** **. . .** BO Slot X5 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
"-"	"-" . . . Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X5 . **Inverting 1** **. . .** BO Slot X5 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 5]	
inactive	inactive, active └⊳ Mode.	S.3
	Inverting of the state of the assigned signal.	

BO Slot X5 . **Operating Mode**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
Normally open (NO)	Normally open (NO), Normally closed (NC) ↳ 1...n Operating Modes.	S.3
	Operating Mode	

BO Slot X5 . **t-hold**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
0.00s	0.00s . . . 300.00s	S.3
	To clearly identify the state transition of a binary output relay, the "new state" is being hold, at least for the duration of the hold time.	

BO Slot X5 . **t-Off Delay**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
0.00s	0.00s . . . 300.00s	S.3
	Switch Off Delay	

BO Slot X5 . **Latched**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
inactive	inactive, active ↳ Mode.	S.3
	Defines whether the Relay Output will be latched when it picks up.	

BO Slot X5 . **Acknowledgement**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
“-” *Only available if:* • BO Slot X5 . Latched = active	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	S.3
	Acknowledgement Signal - An acknowledgement signal (that acknowledges the corresponding binary output relay) can be assigned to each output relay. The acknowledgement-signal is only effective if the parameter "Latched" is set to active.	

BO Slot X5 . **Inverting**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
inactive	inactive, active ↳ Mode.	S.3
	Inverting of the collective signal (OR-gate/disjunction). In combination with inverted input signals an AND-gate can be programmed (Conjunction).	

BO Slot X5 . **Assignment 1** **...** BO Slot X5 . **Assignment 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
“-”	“-” . . . Sys . Internal test state └⇨ 1..n, Assignment List.	S.3
	Assignment	

BO Slot X5 . **Inverting 1** **...** BO Slot X5 . **Inverting 7**	[Device Para / Binary Outputs / BO Slot X5 / BO 6]	
inactive	inactive, active └⇨ Mode.	S.3
	Inverting of the state of the assigned signal.	

2.3.2.1 BO Slot X5: Service

BO Slot X5 . **DISARMED Ctrl**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X5]	
inactive	inactive, active ↳ active/inactive.	S.3
	Enables and disables the disarming of the relay outputs. This is the first step of a two step process, to inhibit the operation or the relay outputs. Please refer to "DISARMED" for the second step.	

BO Slot X5 . **Disarm Mode**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X5]	
permanent	permanent, timeout ↳ Mode.	S.3
	CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance.	

BO Slot X5 . **t-Timeout DISARM**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X5]	
0.03s *Only available if:* • BO Slot X5 . Disarm Mode = timeout	0.00s ... 300.00s	S.3
	The relays will be armed again after expiring of this time.	

BO Slot X5 . **DISARMED**	[Service / Test (Prot inhibit) / DISARMED / BO Slot X5]	
inactive	inactive, active ↳ active/inactive.	S.3
	This is the second step, after the "DISARMED Ctrl" has been activated, that is required to DISARM the relay outputs. This will DISARM those output relays that are currently not latched and that are not on "hold" by a pending minimum hold time. CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: Zone Interlocking and Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance.	

BO Slot X5 . **Force Mode**	[Service / Test (Prot inhibit) / Force OR / BO Slot X5]	
permanent	permanent, timeout ↳ Mode.	S.3
	By means of this function the normal Output Relay States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	

BO Slot X5 . **t-Timeout Force**	[Service / Test (Prot inhibit) / Force OR / BO Slot X5]	
0.03s *Only available if:* • BO Slot X5 . Force Mode = timeout	0.00s ... 300.00s	S.3
	The Output State will be set by force for the duration of this time. That means for the duration of this time the Output Relay does not show the state of the signals that are assigned on it.	

BO Slot X5 . **Force all Outs**	[Service / Test (Prot inhibit) / Force OR / BO Slot X5]	
Normal	Normal, De-Energized, Energized ↳ Relay operating modes.	S.3
	By means of this function the normal Output Relay State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state. Forcing all outputs relays of an entire assembly group is superior to forcing a single output relay.	

BO Slot X5 . **Force OR1** ... BO Slot X5 . **Force OR6**	[Service / Test (Prot inhibit) / Force OR / BO Slot X5]	
Normal	Normal, De-Energized, Energized ↳ Relay operating modes.	S.3
	By means of this function the normal Output Relay State can be overwritten (forced). The relay can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	

2.4 LEDs

2.4.1 LEDs group A

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 1]	
inactive	inactive, active, active, ack. by alarm ╘▷ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 1]	
“-”	“-” . . . Sys . Internal test state ╘▷ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 1]	
green	green, red, red flash, green flash, “-” ╘▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 1]	
“-”	green, red, red flash, green flash, “-” ╘▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1**	[Device Para / LEDs / LEDs group A / LED 1]	
Prot . active	“-” . . . Sys . Internal test state ╘▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** . . . LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group A . **Assignment 2** . . . LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 1]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 2]	
active	inactive, active, active, ack. by alarm ⮡ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 2]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 2]	
red	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 2]	
“-”	green, red, red flash, green flash, “-” ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1**	[Device Para / LEDs / LEDs group A / LED 2]	
SG[1] . TripCmd	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** **. . .** LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 2]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group A . **Assignment 2**	[Device Para / LEDs / LEDs group A / LED 2]	
SG[2] . TripCmd	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Assignment 3** LEDs group A . **Assignment 4** LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 2]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 3]	
inactive	inactive, active, active, ack. by alarm └⇒ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 3]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 3]	
red flash	green, red, red flash, green flash, “-” └⇒ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 3]	
“-”	green, red, red flash, green flash, “-” └⇒ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1**	[Device Para / LEDs / LEDs group A / LED 3]	
Prot . Alarm	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** . . . LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 3]	
inactive	inactive, active └⇒ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group A . **Assignment 2** ... LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 3]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 4]	
inactive	inactive, active, active, ack. by alarm └⇒ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 4]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 4]	
red	green, red, red flash, green flash, “-” └⇒ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 4]	
“-”	green, red, red flash, green flash, “-” └⇒ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1** **. . .** LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 4]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** **. . .** LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 4]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 5]	
inactive	inactive, active, active, ack. by alarm ⮡ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 5]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 5]	
red	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 5]	
“-”	green, red, red flash, green flash, “-” ⮱ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1** . . . LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 5]	
“-”	“-” . . . Sys . Internal test state ⮱ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** . . . LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 5]	
inactive	inactive, active ⮱ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 6]	
inactive	inactive, active, active, ack. by alarm ⮱ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 6]	
“-”	“-” . . . Sys . Internal test state ⮱ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 6]	
red	green, red, red flash, green flash, “-” ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 6]	
“-”	green, red, red flash, green flash, “-” ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1** . . . LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 6]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** . . . LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 6]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group A . **Latched**	[Device Para / LEDs / LEDs group A / LED 7]	
inactive	inactive, active, active, ack. by alarm ╚═▷ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group A . **Ack signal**	[Device Para / LEDs / LEDs group A / LED 7]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group A . **LED active color**	[Device Para / LEDs / LEDs group A / LED 7]	
red	green, red, red flash, green flash, “-” ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group A . **LED inactive color**	[Device Para / LEDs / LEDs group A / LED 7]	
“-”	green, red, red flash, green flash, “-” ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group A . **Assignment 1** **. . .** LEDs group A . **Assignment 5**	[Device Para / LEDs / LEDs group A / LED 7]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group A . **Inverting 1** **. . .** LEDs group A . **Inverting 5**	[Device Para / LEDs / LEDs group A / LED 7]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Inverting of the state of the assigned signal.	

2.4.2 LEDs group B

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 1]	
inactive	inactive, active, active, ack. by alarm ⮡ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 1]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 1]	
red	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 1]	
“-”	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group B . **Assignment 1** **. . .** LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 1]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

LEDs group B . **Inverting 1** ... LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 2]	
inactive	inactive, active, active, ack. by alarm ⮡ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 2]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 2]	
red	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 2]	
“-”	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group B . **Assignment 1** **. . .** LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 2]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Assignment	

LEDs group B . **Inverting 1** **. . .** LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 2]	
inactive	inactive, active └⇒ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 3]	
inactive	inactive, active, active, ack. by alarm └⇒ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 3]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 3]	
red	green, red, red flash, green flash, “-” └⇒ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 3]	
“-”	green, red, red flash, green flash, “-” └─▷ LED active color.	S.3
The LED lights up in this color if the state of the OR-assignment of the signals is untrue.		

LEDs group B . **Assignment 1** ... LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 3]	
“-”	“-” . . . Sys . Internal test state └─▷ 1..n, Assignment List.	S.3
Assignment		

LEDs group B . **Inverting 1** ... LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 3]	
inactive	inactive, active └─▷ Mode.	S.3
Inverting of the state of the assigned signal.		

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 4]	
inactive	inactive, active, active, ack. by alarm └─▷ Mode.	S.3
Defines whether the LED will be latched when it picks up.		

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 4]	
“-”	“-” . . . Sys . Internal test state └─▷ 1..n, Assignment List.	S.3
Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.		

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 4]	
red	green, red, red flash, green flash, “-” └▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 4]	
“-”	green, red, red flash, green flash, “-” └▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group B . **Assignment 1** . . . LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 4]	
“-”	“-” . . . Sys . Internal test state └▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group B . **Inverting 1** . . . LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 4]	
inactive	inactive, active └▷ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 5]	
inactive	inactive, active, active, ack. by alarm └▷ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 5]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 5]	
red	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 5]	
“-”	green, red, red flash, green flash, “-” ⮡ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group B . **Assignment 1** **. . .** LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 5]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Assignment	

LEDs group B . **Inverting 1** **. . .** LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 5]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 6]	
inactive	inactive, active, active, ack. by alarm ╚═▷ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 6]	
"-"	"-" . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 6]	
red	green, red, red flash, green flash, "-" ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 6]	
"-"	green, red, red flash, green flash, "-" ╚═▷ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group B . **Assignment 1** . . . LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 6]	
"-"	"-" . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
	Assignment	

LEDs group B . **Inverting 1** **. . .** LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 6]	
inactive	inactive, active ↳ Mode.	S.3
	Inverting of the state of the assigned signal.	

LEDs group B . **Latched**	[Device Para / LEDs / LEDs group B / LED 7]	
inactive	inactive, active, active, ack. by alarm ↳ Mode.	S.3
	Defines whether the LED will be latched when it picks up.	

LEDs group B . **Ack signal**	[Device Para / LEDs / LEDs group B / LED 7]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	S.3
	Acknowledgement signal for the LED. If latching is set to active the LED can only be acknowledged if those signals that initiated the setting are no longer present.	

LEDs group B . **LED active color**	[Device Para / LEDs / LEDs group B / LED 7]	
red	green, red, red flash, green flash, “-” ↳ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is true.	

LEDs group B . **LED inactive color**	[Device Para / LEDs / LEDs group B / LED 7]	
“-”	green, red, red flash, green flash, “-” ↳ LED active color.	S.3
	The LED lights up in this color if the state of the OR-assignment of the signals is untrue.	

LEDs group B . **Assignment 1** **. . .** LEDs group B . **Assignment 5**	[Device Para / LEDs / LEDs group B / LED 7]	
“-”	“-” . . . Sys . Internal test state 1..n, Assignment List.	S.3
	Assignment	

LEDs group B . **Inverting 1** **. . .** LEDs group B . **Inverting 5**	[Device Para / LEDs / LEDs group B / LED 7]	
inactive	inactive, active Mode.	S.3
	Inverting of the state of the assigned signal.	

2.5 HMI

front-panel

Password	[Device Para / Security / Password]
🗎	This item represents a special dialog. (See the Technical Manual for details.) *Changing the password*

Access Level	[Device Para / Security / Access Level]
🗎	This item represents a special dialog. (See the Technical Manual for details.) *Access Level*

2.5.1 HMI: Global Parameters

HMI . **Display Off**	[Device Para / HMI]	
180s	20s . . . 3600s	S.3
	The display back light will be turned off when this timer has expired.	

HMI . **Menu language**	[Device Para / HMI]	
English	English . . . Romanian ⮡ Selection.	S.3
	Selection of the language	

HMI . **Display ANSI Device No.**	[Device Para / HMI]	
active	inactive, active ⮡ Mode.	S.3
	Display ANSI Device Numbers	

HMI . **t-max Edit/Access**	[Device Para / Security / General Settings]	
180s	20s . . . 3600s	S.3
	If no other key(s) is pressed at the panel, after expiration of this time, all cached (changed) parameters are canceled. The device access will be locked by falling back into Read-only level Lv0.	

2.5.2 HMI: Direct Controls

HMI . **Contrast**	[Device Para / HMI]	
50%	0% ... 100%	S.3
◉	*Contrast*	

HMI . **Config. Device Reset**	[Device Para / Security / General Settings]	
"Fact.def.", "PW rst"	"Fact.def.", "PW rst", Only "Fact.defaults", Reset deact. ╚═▷ Config. Device Reset.	S.3
◉	*If the »C« key is pressed while the device is performing a cold restart a general Reset Dialog appears on the screen. Select which options shall be available with this dialog.*	

2.5.3 HMI: Values

HMI . **Config. Device Reset**	[Operation / Security / Security States]	
"Fact.def.", "PW rst"	"Fact.def.", "PW rst", Only "Fact.defaults", Reset deact. ╚═▷ Config. Device Reset.	
✎	*If the »C« key is pressed while the device is performing a cold restart a general Reset Dialog appears on the screen. Select which options shall be available with this dialog.*	

3 Security

- Ctrl . Switching Authority: ⮡ Tab.
- HMI . Config. Device Reset: ⮡ Tab.
- HMI . t-max Edit/Access: ⮡ Tab.
- HMI . Config. Device Reset: ⮡ Tab.
- Password: ⮡ Tab.
- Access Level: ⮡ Tab.

Sys . **Smart view via USB**	[Operation / Security / Security States]
active	inactive, active ⮡ Mode.
✎	*Information whether or not the Smart view access via the USB interface is activated (allowed).*

Sys . **Smart view via Eth**	[Operation / Security / Security States]
active *Avail. depends on HW*	inactive, active ⮡ Mode.
✎	*Information whether or not the Smart view access via the Ethernet interface is activated (allowed).*

Sys . **Passw. for USB conn.**	[Operation / Security / Security States]
disabled	disabled, default, def. by user ⮡ Type of passw. def..
✎	*Type / Security-level of the connection password that is used for a USB connection.*

Sys . **Passw.remote net.conn.**	[Operation / Security / Security States]
disabled *Avail. depends on HW*	disabled, default, def. by user ⮡ Type of passw. def..
✎	*Type / Security-level of the connection password that is used for a Smart view connection via some network interface.*

Sys . **TLS Certificate**	[Operation / Security / Security States]	
Device-specific	Device-specific, Basic, Corrupt └⇒ TLS Certificate.	
✎	*Type of certificate that the device uses for the encrypted communication. This value is directly related to the security-level of the communication.*	

Security Logger	[Operation / Security / Security Logger]
🗎	This item represents a special dialog. (See the Technical Manual for details.) *Security-related messages*

Sys . **Smart view via USB**	[Device Para / Security / Communication]	
active	inactive, active └⇒ Mode.	S.3
◉	*Activate (allow) or inactivate (disallow) the Smart view access via the USB interface.*	

Sys . **Smart view via Eth**	[Device Para / Security / Communication]	
active *Avail. depends on HW*	inactive, active └⇒ Mode.	S.3
◉	*Activate (allow) or inactivate (disallow) the Smart view access via the Ethernet interface.*	

4 Field settings

Field settings

4.1 Field Para: Global Parameters

Field Para . **Phase Sequence**	[Field Para / General Settings]	
ABC	ABC, ACB └⇒ Phase Sequence.	S.3
🔧	*Phase Sequence direction*	

Field Para . **f**	[Field Para / General Settings]	
50Hz	50Hz, 60Hz └⇒ fN.	S.3
🔧	*Nominal frequency*	

4.2 CT W1

Current Transformer Winding 1

4.2.1 CT W1: Global Parameters

CT W1 . **IL1, IL2, IL3 Cutoff Level**	[Device Para / Measurem Display / CT W1]	
0.005In	0.0In . . . 0.100In	S.3
	The Current shown in the Display or within the PC Software will be displayed as zero, if the Current falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W1 . **IG meas Cutoff Level**	[Device Para / Measurem Display / CT W1]	
0.005In	0.0In . . . 0.100In	S.3
	The measured Earth Current shown in the Display or within the PC Software will be displayed as zero, if the measured Earth Current falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W1 . **IG calc Cutoff Level**	[Device Para / Measurem Display / CT W1]	
0.005In	0.0In . . . 0.100In	S.3
	The calculated Earth Current shown in the Display or within the PC Software will be displayed as zero, if the calculated Earth Current falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W1 . **I012 Cutoff Level**	[Device Para / Measurem Display / CT W1]	
0.005In	0.0In . . . 0.100In	S.3
	The Symmetrical Component shown in the Display or within the PC Software will be displayed as zero, if the Symmetrical Component falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W1 . **CT pri**	[Field Para / CT W1]	
1000A	1A . . . 50000A	S.3
	Nominal current of the primary side of the current transformers.	

CT W1 . **CT sec**	[Field Para / CT W1]	
1A	1A, 5A ↳ Ratio prim/sec.	S.3
	Nominal current of the secondary side of the current transformers.	

CT W1 . **CT dir**	[Field Para / CT W1]	
0°	0°, 180° └⇒ Polarity.	S.3

Protection functions with directional feature can only work properly if the connection of the current transformers is free of wiring errors. If all current transformers are connected to the device with an incorrect polarity, the wiring error can be compensated by this parameter. This parameter turns the current vectors by 180 degrees.

CT W1 . **ECT pri**	[Field Para / CT W1]	
1000A	1A . . . 50000A	S.3

This parameter defines the primary nominal current of the connected earth current transformer. If the earth current is measured via the Holmgreen connection, the primary value of the phase current transformer must be entered here.

CT W1 . **ECT sec**	[Field Para / CT W1]	
1A	1A, 5A └⇒ Ratio prim/sec.	S.3

This parameter defines the secondary nominal current of the connected earth current transformer. If the earth current is done via the Holmgreen connection, the primary value of the phase current transformer must be entered here.

CT W1 . **ECT dir**	[Field Para / CT W1]	
0°	0°, 180° └⇒ Polarity.	S.3

Earth fault protection with directional feature depends also on the correct wiring of the earth current transformer. An incorrect polarity/wiring can be corrected by means of the settings "0°" or "180°". The operator has the possibility of turning the current vector by 180 degrees (change of sign) without modification of the wiring. This means, that - in terms of figures - the determined current indicator was turned by 180° by the device.

4.2.2 CT W1: Signals (Output States)

CT W1 . **Phase seq. wrong**	[Operation / Status Display / Supervision / Phase Sequence]

Signal that the device has detected a phase sequence (L1-L2-L3 / L1-L3-L2) that is different from the one that had been set at [Field settings / General Settings] »Phase Sequence«.

4.2.3 CT W1: Values

CT W1 . **IL1**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: Phase current (fundamental)*

CT W1 . **IL2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: Phase current (fundamental)*

CT W1 . **IL3**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: Phase current (fundamental)*

CT W1 . **IG meas**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (measured): IG (fundamental)*

CT W1 . **IG calc**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): IG (fundamental)*

CT W1 . **I0**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Zero current (fundamental)*

CT W1 . **I1**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Positive phase sequence current (fundamental)*

CT W1 . **I2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Unbalanced load current (fundamental)*

CT W1 . **IL1 H2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: 2nd harmonic/1st harmonic of IL1*

CT W1 . **IL2 H2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: 2nd harmonic/1st harmonic of IL2*

CT W1 . **IL3 H2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: 2nd harmonic/1st harmonic of IL3*

CT W1 . **IG H2 meas**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value: 2nd harmonic/1st harmonic of IG (measured)*

CT W1 . **IG H2 calc**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): 2nd harmonic/1st harmonic of IG (calculated)*

CT W1 . **%(I2/I1)**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.*

CT W1 . **phi IL1**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle of Phasor IL1* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi IL2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle of Phasor IL2* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi IL3**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle of Phasor IL3* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi IG meas**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle of Phasor IG meas* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi IG calc**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle of Phasor IG calc* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi I0**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle Zero Sequence System* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi I1**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured value (calculated): Angle of Positive Sequence System* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **phi I2**	[Operation / Measured Values / CT W1 / Current]
✎	*Measured Value (calculated): Angle of Negative Sequence System* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W1 . **IL1 RMS**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value: Phase current (RMS)*

CT W1 . **IL2 RMS**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value: Phase current (RMS)*

CT W1 . **IL3 RMS**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value: Phase current (RMS)*

CT W1 . **IG meas RMS**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (measured): IG (RMS)*

CT W1 . **IG calc RMS**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IG (RMS)*

CT W1 . **%IL1 THD**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IL1 Total Harmonic Distortion*

CT W1 . **%IL2 THD**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IL2 Total Harmonic Distortion*

CT W1 . **%IL3 THD**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IL3 Total Harmonic Distortion*

CT W1 . **IL1 THD**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IL1 Total Harmonic Current*

CT W1 . **IL2 THD**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IL2 Total Harmonic Current*

CT W1 . **IL3 THD**	[Operation / Measured Values / CT W1 / Current RMS]
✎	*Measured value (calculated): IL3 Total Harmonic Current*

4.2.4 CT W1: Statistical Values

CT W1 . **IL1 avg RMS**	[Operation / Statistics / Demand / CT W1]
☑	*IL1 average value (RMS)*

CT W1 . **IL2 avg RMS**	[Operation / Statistics / Demand / CT W1]
☑	*IL2 average value (RMS)*

CT W1 . **IL3 avg RMS**	[Operation / Statistics / Demand / CT W1]
☑	*IL3 average value (RMS)*

CT W1 . **IL1 Peak demand**	[Operation / Statistics / Demand / CT W1]
☑	*IL1 Peak value, RMS value*

CT W1 . **IL2 Peak demand**	[Operation / Statistics / Demand / CT W1]
☑	*IL2 Peak value, RMS value*

CT W1 . **IL3 Peak demand**	[Operation / Statistics / Demand / CT W1]
☑	*IL3 Peak value, RMS value*

CT W1 . **IL1 max RMS**	[Operation / Statistics / Max / CT W1]
☑	*IL1 maximum value (RMS)*

CT W1 . **IL2 max RMS**	[Operation / Statistics / Max / CT W1]
☑	*IL2 maximum value (RMS)*

CT W1 . **IL3 max RMS**	[Operation / Statistics / Max / CT W1]
☑	*IL3 maximum value (RMS)*

CT W1 . **IG meas max RMS**	[Operation / Statistics / Max / CT W1]
☑	*Measured value: IG maximum value (RMS)*

CT W1 . **IG calc max RMS**	[Operation / Statistics / Max / CT W1]
☑	*Measured value (calculated):IG maximum value (RMS)*

CT W1 . **I1 max**	[Operation / Statistics / Max / CT W1]
☑	*Maximum value positive phase sequence current (fundamental)*

CT W1 . **I2 max**	[Operation / Statistics / Max / CT W1]
☑	*Maximum value negative sequence current (fundamental)*

CT W1 . **%(I2/I1) max**	[Operation / Statistics / Max / CT W1]
☑	*Measured value (calculated): I2/I1 maximum value, phase sequence will be taken into account automatically*

CT W1 . **IL1 H2 max**	[Operation / Statistics / Max / CT W1]
☑	*Maximum ratio of 2nd harmonic over fundamental of IL1*

CT W1 . **IL2 H2 max**	[Operation / Statistics / Max / CT W1]
☑	*Maximum ratio of 2nd harmonic over fundamental of IL2*

CT W1 . **IL3 H2 max**	[Operation / Statistics / Max / CT W1]
☑	*Maximum ratio of 2nd harmonic over fundamental of IL3*

CT W1 . **IG H2 meas max**	[Operation / Statistics / Max / CT W1]
☑	*Measured value: Maximum ratio of 2nd harmonic over fundamental of IG (measured)*

CT W1 . **IG H2 calc max**	[Operation / Statistics / Max / CT W1]
☑	*Measured value (calculated): Maximum ratio of 2nd harmonic over fundamental of IG (calculated)*

CT W1 . **IL1 min RMS**	[Operation / Statistics / Min / CT W1]
☑	*IL1 minimum value (RMS)*

CT W1 . **IL2 min RMS**	[Operation / Statistics / Min / CT W1]
☑	*IL2 minimum value (RMS)*

CT W1 . **IL3 min RMS**	[Operation / Statistics / Min / CT W1]
☑	*IL3 minimum value (RMS)*

CT W1 . **IG meas min RMS**	[Operation / Statistics / Min / CT W1]
☑	*Measured value: IG minimum value (RMS)*

CT W1 . **IG calc min RMS**	[Operation / Statistics / Min / CT W1]
☑	*Measured value (calculated):IG minimum value (RMS)*

CT W1 . **I1 min**	[Operation / Statistics / Min / CT W1]
☑	*Minimum value positive phase sequence current (fundamental)*

CT W1 . **I2 min**	[Operation / Statistics / Min / CT W1]
☑	*Minimum value unbalanced load current (fundamental)*

CT W1 . **%(I2/I1) min**	[Operation / Statistics / Min / CT W1]
☑	*Measured value (calculated): I2/I1 minimum value, phase sequence will be taken into account automatically*

CT W1 . **IL1 H2 min**	[Operation / Statistics / Min / CT W1]
☑	*Minimum ratio of 2nd harmonic over fundamental of IL1*

CT W1 . **IL2 H2 min**	[Operation / Statistics / Min / CT W1]
☑	*Minimum ratio of 2nd harmonic over fundamental of IL2*

CT W1 . **IL3 H2 min**	[Operation / Statistics / Min / CT W1]
☑	*Minimum ratio of 2nd harmonic/1st harmonic minimum value of IL3*

CT W1 . **IG H2 meas min**	[Operation / Statistics / Min / CT W1]
☑	*Measured value: Minimum ratio of 2nd harmonic over fundamental of IG (measured)*

CT W1 . **IG H2 calc min**	[Operation / Statistics / Min / CT W1]
☑	*IG H2 calc min*

4.3 CT W2

Current Transformer Winding 2

4.3.1 CT W2: Global Parameters

CT W2 . **IL1, IL2, IL3 Cutoff Level**	[Device Para / Measurem Display / CT W2]	
0.005In	0.0In ... 0.100In	S.3
	The Current shown in the Display or within the PC Software will be displayed as zero, if the Current falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W2 . **IG meas Cutoff Level**	[Device Para / Measurem Display / CT W2]	
0.005In	0.0In ... 0.100In	S.3
	The measured Earth Current shown in the Display or within the PC Software will be displayed as zero, if the measured Earth Current falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W2 . **IG calc Cutoff Level**	[Device Para / Measurem Display / CT W2]	
0.005In	0.0In ... 0.100In	S.3
	The calculated Earth Current shown in the Display or within the PC Software will be displayed as zero, if the calculated Earth Current falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W2 . **I012 Cutoff Level**	[Device Para / Measurem Display / CT W2]	
0.005In	0.0In ... 0.100In	S.3
	The Symmetrical Component shown in the Display or within the PC Software will be displayed as zero, if the Symmetrical Component falls below this Cutoff Level. This parameter has no impact on recorders.	

CT W2 . **CT pri**	[Field Para / CT W2]	
1000A	1A ... 50000A	S.3
	Nominal current of the primary side of the current transformers.	

CT W2 . **CT sec**	[Field Para / CT W2]	
1A	1A, 5A └▷ Ratio prim/sec.	S.3
	Nominal current of the secondary side of the current transformers.	

CT W2 . **CT dir**	[Field Para / CT W2]	
0°	0°, 180° └⇒ Polarity.	S.3
	Protection functions with directional feature can only work properly if the connection of the current transformers is free of wiring errors. If all current transformers are connected to the device with an incorrect polarity, the wiring error can be compensated by this parameter. This parameter turns the current vectors by 180 degrees.	

CT W2 . **ECT pri**	[Field Para / CT W2]	
1000A	1A . . . 50000A	S.3
	This parameter defines the primary nominal current of the connected earth current transformer. If the earth current is measured via the Holmgreen connection, the primary value of the phase current transformer must be entered here.	

CT W2 . **ECT sec**	[Field Para / CT W2]	
1A	1A, 5A └⇒ Ratio prim/sec.	S.3
	This parameter defines the secondary nominal current of the connected earth current transformer. If the earth current is done via the Holmgreen connection, the primary value of the phase current transformer must be entered here.	

CT W2 . **ECT dir**	[Field Para / CT W2]	
0°	0°, 180° └⇒ Polarity.	S.3
	Earth fault protection with directional feature depends also on the correct wiring of the earth current transformer. An incorrect polarity/wiring can be corrected by means of the settings "0°" or "180°". The operator has the possibility of turning the current vector by 180 degrees (change of sign) without modification of the wiring. This means, that - in terms of figures - the determined current indicator was turned by 180° by the device.	

4.3.2 CT W2: Signals (Output States)

CT W2 . **Phase seq. wrong**	[Operation / Status Display / Supervision / Phase Sequence]
	Signal that the device has detected a phase sequence (L1-L2-L3 / L1-L3-L2) that is different from the one that had been set at [Field settings / General Settings] »Phase Sequence«.

4.3.3 CT W2: Values

CT W2 . **IL1**	[Operation / Measured Values / CT W2 / Current]
	Measured value: Phase current (fundamental)

CT W2 . **IL2**	[Operation / Measured Values / CT W2 / Current]
	Measured value: Phase current (fundamental)

CT W2 . **IL3**	[Operation / Measured Values / CT W2 / Current]
	Measured value: Phase current (fundamental)

CT W2 . **IG meas**	[Operation / Measured Values / CT W2 / Current]
	Measured value (measured): IG (fundamental)

CT W2 . **IG calc**	[Operation / Measured Values / CT W2 / Current]
	Measured value (calculated): IG (fundamental)

CT W2 . **I0**	[Operation / Measured Values / CT W2 / Current]
	Measured value (calculated): Zero current (fundamental)

CT W2 . **I1**	[Operation / Measured Values / CT W2 / Current]
	Measured value (calculated): Positive phase sequence current (fundamental)

CT W2 . **I2**	[Operation / Measured Values / CT W2 / Current]
	Measured value (calculated): Unbalanced load current (fundamental)

CT W2 . **IL1 H2**	[Operation / Measured Values / CT W2 / Current]
	Measured value: 2nd harmonic/1st harmonic of IL1

CT W2 . **IL2 H2**	[Operation / Measured Values / CT W2 / Current]
	Measured value: 2nd harmonic/1st harmonic of IL2

CT W2 . **IL3 H2**	[Operation / Measured Values / CT W2 / Current]
	Measured value: 2nd harmonic/1st harmonic of IL3

CT W2 . **IG H2 meas**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value: 2nd harmonic/1st harmonic of IG (measured)*

CT W2 . **IG H2 calc**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): 2nd harmonic/1st harmonic of IG (calculated)*

CT W2 . **%(I2/I1)**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.*

CT W2 . **phi IL1**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): Angle of Phasor IL1* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi IL2**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): Angle of Phasor IL2* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi IL3**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): Angle of Phasor IL3* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi IG meas**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): Angle of Phasor IG meas* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi IG calc**	[Operation / Measured Values / CT W2 / Current]
✎	*Measured value (calculated): Angle of Phasor IG calc* *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi I0**	[Operation / Measured Values / CT W2 / Current]
	Measured value (calculated): Angle Zero Sequence System *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi I1**	[Operation / Measured Values / CT W2 / Current]
	Measured value (calculated): Angle of Positive Sequence System *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **phi I2**	[Operation / Measured Values / CT W2 / Current]
	Measured Value (calculated): Angle of Negative Sequence System *Reference phasor is required to calculate the angle. This is the first measured voltage (or current) channel with sufficiently high amplitude.*

CT W2 . **IL1 RMS**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value: Phase current (RMS)

CT W2 . **IL2 RMS**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value: Phase current (RMS)

CT W2 . **IL3 RMS**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value: Phase current (RMS)

CT W2 . **IG meas RMS**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value (measured): IG (RMS)

CT W2 . **IG calc RMS**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value (calculated): IG (RMS)

CT W2 . **%IL1 THD**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value (calculated): IL1 Total Harmonic Distortion

CT W2 . **%IL2 THD**	[Operation / Measured Values / CT W2 / Current RMS]
	Measured value (calculated): IL2 Total Harmonic Distortion

CT W2 . **%IL3 THD**	[Operation / Measured Values / CT W2 / Current RMS]
✎	*Measured value (calculated): IL3 Total Harmonic Distortion*

CT W2 . **IL1 THD**	[Operation / Measured Values / CT W2 / Current RMS]
✎	*Measured value (calculated): IL1 Total Harmonic Current*

CT W2 . **IL2 THD**	[Operation / Measured Values / CT W2 / Current RMS]
✎	*Measured value (calculated): IL2 Total Harmonic Current*

CT W2 . **IL3 THD**	[Operation / Measured Values / CT W2 / Current RMS]
✎	*Measured value (calculated): IL3 Total Harmonic Current*

4.3.4 CT W2: Statistical Values

CT W2 . **IL1 avg RMS**	[Operation / Statistics / Demand / CT W2]
☑	*IL1 average value (RMS)*

CT W2 . **IL2 avg RMS**	[Operation / Statistics / Demand / CT W2]
☑	*IL2 average value (RMS)*

CT W2 . **IL3 avg RMS**	[Operation / Statistics / Demand / CT W2]
☑	*IL3 average value (RMS)*

CT W2 . **IL1 Peak demand**	[Operation / Statistics / Demand / CT W2]
☑	*IL1 Peak value, RMS value*

CT W2 . **IL2 Peak demand**	[Operation / Statistics / Demand / CT W2]
☑	*IL2 Peak value, RMS value*

CT W2 . **IL3 Peak demand**	[Operation / Statistics / Demand / CT W2]
☑	*IL3 Peak value, RMS value*

CT W2 . **IL1 max RMS**	[Operation / Statistics / Max / CT W2]
☑	*IL1 maximum value (RMS)*

CT W2 . **IL2 max RMS**	[Operation / Statistics / Max / CT W2]
☑	*IL2 maximum value (RMS)*

CT W2 . **IL3 max RMS**	[Operation / Statistics / Max / CT W2]
☑	*IL3 maximum value (RMS)*

CT W2 . **IG meas max RMS**	[Operation / Statistics / Max / CT W2]
☑	*Measured value: IG maximum value (RMS)*

CT W2 . **IG calc max RMS**	[Operation / Statistics / Max / CT W2]
☑	*Measured value (calculated):IG maximum value (RMS)*

CT W2 . **I1 max**	[Operation / Statistics / Max / CT W2]
☑	*Maximum value positive phase sequence current (fundamental)*

CT W2 . **I2 max**	[Operation / Statistics / Max / CT W2]
☑	*Maximum value negative sequence current (fundamental)*

CT W2 . **%(I2/I1) max**	[Operation / Statistics / Max / CT W2]
☑	*Measured value (calculated): I2/I1 maximum value, phase sequence will be taken into account automatically*

CT W2 . **IL1 H2 max**	[Operation / Statistics / Max / CT W2]
☑	*Maximum ratio of 2nd harmonic over fundamental of IL1*

CT W2 . **IL2 H2 max**	[Operation / Statistics / Max / CT W2]
☑	*Maximum ratio of 2nd harmonic over fundamental of IL2*

CT W2 . **IL3 H2 max**	[Operation / Statistics / Max / CT W2]
☑	*Maximum ratio of 2nd harmonic over fundamental of IL3*

CT W2 . **IG H2 meas max**	[Operation / Statistics / Max / CT W2]
☑	*Measured value: Maximum ratio of 2nd harmonic over fundamental of IG (measured)*

CT W2 . **IG H2 calc max**	[Operation / Statistics / Max / CT W2]
☑	*Measured value (calculated): Maximum ratio of 2nd harmonic over fundamental of IG (calculated)*

CT W2 . **IL1 min RMS**	[Operation / Statistics / Min / CT W2]
☑	*IL1 minimum value (RMS)*

CT W2 . **IL2 min RMS**	[Operation / Statistics / Min / CT W2]
☑	*IL2 minimum value (RMS)*

CT W2 . **IL3 min RMS**	[Operation / Statistics / Min / CT W2]
☑	*IL3 minimum value (RMS)*

CT W2 . **IG meas min RMS**	[Operation / Statistics / Min / CT W2]
☑	*Measured value: IG minimum value (RMS)*

CT W2 . **IG calc min RMS**	[Operation / Statistics / Min / CT W2]
☑	*Measured value (calculated):IG minimum value (RMS)*

CT W2 . **I1 min**	[Operation / Statistics / Min / CT W2]
☑	*Minimum value positive phase sequence current (fundamental)*

CT W2 . **I2 min**	[Operation / Statistics / Min / CT W2]
☑	*Minimum value unbalanced load current (fundamental)*

CT W2 . **%(I2/I1) min**	[Operation / Statistics / Min / CT W2]
☑	*Measured value (calculated): I2/I1 minimum value, phase sequence will be taken into account automatically*

CT W2 . **IL1 H2 min**	[Operation / Statistics / Min / CT W2]
☑	*Minimum ratio of 2nd harmonic over fundamental of IL1*

CT W2 . **IL2 H2 min**	[Operation / Statistics / Min / CT W2]
☑	*Minimum ratio of 2nd harmonic over fundamental of IL2*

CT W2 . **IL3 H2 min**	[Operation / Statistics / Min / CT W2]
☑	*Minimum ratio of 2nd harmonic/1st harmonic minimum value of IL3*

CT W2 . **IG H2 meas min**	[Operation / Statistics / Min / CT W2]
☑	*Measured value: Minimum ratio of 2nd harmonic over fundamental of IG (measured)*

CT W2 . **IG H2 calc min**	[Operation / Statistics / Min / CT W2]
☑	*IG H2 calc min*

4.4 Transformer

Transformer

4.4.1 Transformer: Global Parameters

Transformer . **SN**	[Field Para / Transformer]	
11MVA	0.001MVA . . . 2000.000MVA	P.2
	Rated Power of the Transformer in MVA	

Transformer . **Rated W1 (HV)**	[Field Para / Transformer]	
10500V	60V . . . 500000V	P.2
	Transformer rated voltage (phase to phase) of the HV side. At the protective device, this is connected to the current measuring input W1 (slot X3).	

Transformer . **Rated W2 (LV)**	[Field Para / Transformer]	
10000V	60V . . . 500000V	P.2
	Transformer rated voltage (phase to phase) of the LV side. At the protective device, this is connected to the current measuring input W2 (slot X4).	

Transformer . **W1 Connection/ Grounding**	[Field Para / Transformer]	
D	Y, D, Z, YN, ZN ⮡ W1 Connection/Grounding.	P.2
	Note: The zero current will be removed in order to prevent faulty tripping of the differential protection. If a star point is connected to ground according to the winding connection, the zero current (symmetrical components) will be removed.	

Transformer . **W2 Connection/ Grounding**	[Field Para / Transformer]	
yn	y, d, z, yn, zn ⮡ W2 Connection/Grounding.	P.2
	Note: The zero current will be removed in order to prevent faulty tripping of the differential protection. If a star point is connected to ground according to the winding connection, the zero current (symmetrical components) will be removed.	

Transformer . **Phase Shift**	[Field Para / Transformer]	
1	0 . . . 11	P.2
	Phase Shift between W1 side and W2 side. The phase shift angle is a factor (1, 2, 3,..., 11) multiplied with 30 degrees.	

Transformer . **Tap changer**	[Field Para / Transformer]	
0%	-15% . . . 15%	P.2
	Tap changer (referring to the W1 side)	

5 System

System

5.1 Sys: Global Parameters

Sys . **Scaling**	[Device Para / Measurem Display / General Settings]	
Per unit values	Per unit values, Primary values, Secondary values └⇒ Scaling.	S.3
	Display of the measured values as primary, secondary or per unit values	

Sys . **Ack via »C« key**	[Device Para / Acknowledge]	
Ack LEDs w/o passw.	Nothing, Ack LEDs w/o passw., Ack LEDs, Ack LEDs and relays, Ack Everything └⇒ Ack via »C« key.	P.2
	Select which acknowledgeable elements can be reset via pressing the »C« key.	

Sys . **Remote Reset**	[Device Para / Acknowledge]	
active	inactive, active └⇒ Mode.	P.2
	Enables or disables the option to acknowledge from external/remote via signals (assignments) and SCADA.	

Sys . **Ack LED**	[Device Para / Acknowledge]	
"-" *Only available if:* • Sys . Remote Reset = active	"-" . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	All acknowledgeable LEDs will be acknowledged if the state of the assigned signal becomes true.	

Sys . **Ack BO**	[Device Para / Acknowledge]	
“-” *Only available if:* • Sys . Remote Reset = active	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	All acknowledgeable binary output relays will be acknowledged if the state of the assigned signal becomes true.	

Sys . **Ack Scada**	[Device Para / Acknowledge]	
“-” *Only available if:* • Sys . Remote Reset = active	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Latched SCADA signals are acknowledged if the state of the assigned signal becomes true.	

Sys . **Setting Lock**	[Field Para / General Settings]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	No parameters can be changed as long as this input is true. The parameter settings are locked.	

Sys . **PSet-Switch**	[Protection Para / PSet-Switch]	
PS1	PS1, PS2, PS3, PS4, PSS via Inp fct, PSS via Scada └⇒ PSet-Switch.	P.2
	Switching Parameter Set	

Sys . **PS1: activated by** ... Sys . **PS4: activated by**	[Protection Para / PSet-Switch]	
“-”	“-” ... Logics . LE80.Out inverted 1..n, PSS.	P.2
	This Setting Group will be the active one if: The Parameter Setting Group Switch is set to "Switch via Input" and the other three input functions are inactive at the same time. In case that there is more than one input function active, no Parameter Setting Group Switch will be executed. In case all input functions are inactive, the device will keep working with the Setting Group that was activated lastly.	

5.2 Sys: Direct Controls

Sys . **Ack BO LED Scd TCmd**	[Operation / Acknowledge]	
inactive	inactive, active Mode.	P.1
	Reset the binary output relays, LEDs, SCADA and the Trip Command.	

Sys . **Ack LED**	[Operation / Acknowledge]	
inactive	inactive, active Mode.	P.1
	All acknowledgeable LEDs will be acknowledged.	

Sys . **Ack BO**	[Operation / Acknowledge]	
inactive	inactive, active Mode.	P.1
	All acknowledgeable binary output relays will be acknowledged.	

Sys . **Ack Scada**	[Operation / Acknowledge]	
inactive	inactive, active Mode.	P.1
	Latched SCADA signals are acknowledged.	

Sys . **Setting Lock Bypass**	[Field Para / General Settings]	
inactive	inactive, active └▷ Mode.	P.1
◉	*Short-period unlock of the Setting Lock*	

Sys . **Reboot**	[Service / General]	
no	no, yes └▷ yes/no.	S.3
◉	*Rebooting the device.*	

5.3 Sys: Input States

Sys . **Ack LED-I**	[Operation / Status Display / Sys]
⇩	*Module input state: LEDs acknowledgement by digital input*

Sys . **Ack BO-I**	[Operation / Status Display / Sys]
⇩	*Module input state: Acknowledgement of the binary Output Relays*

Sys . **Ack Scada-I**	[Operation / Status Display / Sys]
⇩	*Module input state: Acknowledge latched SCADA signals.*

Sys . **PS1-I** **. . .** Sys . **PS4-I**	[Operation / Status Display / Sys]
⇩	*State of the module input respectively of the signal, that should activate this Parameter Setting Group.*

Sys . **Setting Lock-I**	[Operation / Status Display / Sys]
⇩	*State of the module input: No parameters can be changed as long as this input is true. The parameter settings are locked.*

5.4 Sys: Signals (Output States)

Sys . **Reboot**	[Operation / Status Display / Sys]
⇧	*Signal: Rebooting the device.* *Device Start-up Codes: 1=Normal Start-up; 2=Reboot by the Operator; 3=Reboot by means of Super Reset; 4=outdated; 5=outdated; 6=Unknown Error Source; 7=Forced Reboot (initiated by the main processor); 8=Exceeded Time Limit of the Protection Cycle; 9= Forced Reboot (initiated by the digital signal processor); 10=Exceeded Time Limit of the Measured Value Processing; 11=Sags of the Supply Voltage; 12=Illegal Memory Access.*

Sys . **Act Set**	[Operation / Status Display / Sys] [Protection Para / PSet-Switch]
⇧	*Signal: Active Parameter Set*

Sys . **PS 1**	[Operation / Status Display / Sys]
⇧	*Signal: The currently active Parameter Set is PS 1*

Sys . **PS 2**	[Operation / Status Display / Sys]
⇧	*Signal: The currently active Parameter Set is PS 2*

Sys . **PS 3**	[Operation / Status Display / Sys]
⇧	*Signal: The currently active Parameter Set is PS 3*

Sys . **PS 4**	[Operation / Status Display / Sys]
⇧	*Signal: The currently active Parameter Set is PS 4*

Sys . **PSS manual**	[Operation / Status Display / Sys]
⇧	*Signal: Manual Switch over of a Parameter Set*

Sys . **PSS via Scada**	[Operation / Status Display / Sys]
⇧	*Signal: Parameter Set Switch via Scada. Write into this output byte the integer of the parameter set that should become active (e.g. 4 => Switch onto parameter set 4).*

Sys . **PSS via Inp fct**	[Operation / Status Display / Sys]
⇧	*Signal: Parameter Set Switch via input function*

Sys . **min 1 param changed**	[Operation / Status Display / Sys]
⇧	*Signal: At least one parameter has been changed*

Sys . **Setting Lock Bypass**	[Operation / Status Display / Sys]
⇧	*Signal: Short-period unlock of the Setting Lock*

Sys . **Ack LED**	[Operation / Status Display / Sys]
⇧	*Signal: LEDs acknowledgement*

Sys . **Ack BO**	[Operation / Status Display / Sys]
⇧	*Signal: Acknowledgement of the Binary Outputs*

Sys . **Ack Scada**	[Operation / Status Display / Sys]
⇧	*Signal: Acknowledge latched SCADA signals*

Sys . **Ack TripCmd**	[Operation / Status Display / Sys]
⇧	*Signal: Reset Trip Command*

Sys . **Ack LED-HMI**	[Operation / Status Display / Sys]
⇧	*Signal: LEDs acknowledgement :HMI*

Sys . **Ack BO-HMI**	[Operation / Status Display / Sys]
⇧	*Signal: Acknowledgement of the Binary Outputs :HMI*

Sys . **Ack Scada-HMI**	[Operation / Status Display / Sys]
⇧	*Signal: Acknowledge latched SCADA signals :HMI*

Sys . **Ack TripCmd-HMI**	[Operation / Status Display / Sys]
⇧	*Signal: Reset Trip Command :HMI*

Sys . **Ack LED-Sca**	[Operation / Status Display / Sys]
⇧	*Signal: LEDs acknowledgement :SCADA*

Sys . **Ack BO-Sca**	[Operation / Status Display / Sys]
⇧	*Signal: Acknowledgement of the Binary Outputs :SCADA*

Sys . **Ack Counter-Sca**	[Operation / Status Display / Sys]
	Signal: Reset of all Counters :SCADA

Sys . **Ack Scada-Sca**	[Operation / Status Display / Sys]
	Signal: Acknowledge latched SCADA signals :SCADA

Sys . **Ack TripCmd-Sca**	[Operation / Status Display / Sys]
	Signal: Reset Trip Command :SCADA

Sys . **Res OperationsCr**	[Operation / Status Display / Sys]
	Signal:: Res OperationsCr

Sys . **Res AlarmCr**	[Operation / Status Display / Sys]
	Signal:: Res AlarmCr

Sys . **Res TripCmdCr**	[Operation / Status Display / Sys]
	Signal:: Res TripCmdCr

Sys . **Res TotalCr**	[Operation / Status Display / Sys]
	Signal:: Res TotalCr

5.5 Sys: Values

Sys . **Operating hours Cr**	[Operation / Count and RevData / Sys]
	Operating hours counter of the protective device

Sys . **DM version**	[Device Para / Version]
3.6.b	3.6.b .
	Version of the device model

Sys . **SW version**	[Device Para / Version]
	Version of the device firmware

Sys . **Build**	[Device Para / Version]
✎	*Build Number*

Sys . **CAT No**	[Device Para / Version]
✎	*»CAT No.«, Order Code as printed on the nameplate of the device.*

Sys . **REV.**	[Device Para / Version]
✎	*Revision (as printed on the nameplate of the device).*

Sys . **S/N**	[Device Para / Version]
✎	*The serial number of the device.*

Sys . **Bootloader Build**	[Device Para / Version]
✎	*Build number of the bootloader*

6 Measured Values

- HMI: ╚═▷ "HMI: Values"
- CT W1: ╚═▷ "CT W1: Values"
- CT W2: ╚═▷ "CT W2: Values"
- System: ╚═▷ "Sys: Values"
- Id: ╚═▷ "Id: Values"
- IdG: ╚═▷ "IdG: Values"
- Modbus: ╚═▷ "Modbus: Values"
- IEC 61850: ╚═▷ "IEC 61850: Values"
- IEC104: ╚═▷ "IEC104: Values"
- Profibus: ╚═▷ "Profibus: Values"
- SNTP: ╚═▷ "SNTP: Values"
- Id: ╚═▷ "Id: Values"
- ThR: ╚═▷ "ThR: Values"
- URTD: ╚═▷ "URTD: Values"
- Control: ╚═▷ "Ctrl: Values"
- Breaker Wear: ╚═▷ "SG[1]: Values"
- Disturb rec: ╚═▷ "Disturb rec: Values"
- Sgen: ╚═▷ "Sgen: Values"

6.1 Id

Motor Differential Protection Module

6.1.1 Id: Global Parameters

Id . **Id Cutoff Level**	[Device Para / Measurem Display / Diff]	
0.005In	0.0In ... 0.100In	S.3
	The Differential Current shown in the Display or within the PC Software will be displayed as zero, if the Differential Current falls below this Cutoff Level. This parameter has no impact on recorders.	

Id . **IS Cutoff Level**	[Device Para / Measurem Display / Diff]	
0.005In	0.0In ... 0.100In	S.3
	The Restraint Current shown in the Display or within the PC Software will be displayed as zero, if the Restraint Current falls below this Cutoff Level. This parameter has no impact on recorders.	

6.1.2 Id: Values

Id . **Is L1**	[Operation / Measured Values / Id]
	Measured value (calculated): Restraint Current Phase L1

Id . **Is L2**	[Operation / Measured Values / Id]
	Measured value (calculated): Restraint Current Phase L2

Id . **Is L3**	[Operation / Measured Values / Id]
	Measured value (calculated): Restraint Current Phase L3

Id . **Id L1**	[Operation / Measured Values / Id]
	Measured value (calculated): Differential Current Phase L1

Id . **Id L2**	[Operation / Measured Values / Id]
	Measured value (calculated): Differential Current Phase L2

Id . **Id L3**	[Operation / Measured Values / Id]
	Measured value (calculated): Differential Current Phase L3

6.1.3 Id: Statistical Values

Id . **Is L1 max**	[Operation / Statistics / Max / Id]
☑	*Measured value (calculated): Restraint Current Phase L1 Maximum Value*

Id . **Is L2 max**	[Operation / Statistics / Max / Id]
☑	*Measured value (calculated): Restraint Current Phase L2 Maximum Value*

Id . **Is L3 max**	[Operation / Statistics / Max / Id]
☑	*Measured value (calculated): Restraint Current Phase L3 Maximum Value*

Id . **Id L1 max**	[Operation / Statistics / Max / Id]
☑	*Measured value (calculated): Differential Current Phase L1 Maximum Value*

Id . **Id L2 max**	[Operation / Statistics / Max / Id]
☑	*Measured value (calculated): Differential Current Phase L2 Maximum Value*

Id . **Id L3 max**	[Operation / Statistics / Max / Id]
☑	*Measured value (calculated): Differential Current Phase L3 Maximum Value*

6.2 IdG

Restricted Ground Fault Differential Protection Module

6.2.1 IdG: Global Parameters

IdG . **IdG Cutoff Level**	[Device Para / Measurem Display / Diff]	
0.005In	0.0In . . . 0.100In	S.3
	The Ground Differential Current shown in the Display or within the PC Software will be displayed as zero, if the Ground Differential Current falls below this Cutoff Level. This parameter has no impact on recorders.	

IdG . **ISG Cutoff Level**	[Device Para / Measurem Display / Diff]	
0.005In	0.0In . . . 0.100In	S.3
	The GroundRestraint Current shown in the Display or within the PC Software will be displayed as zero, if the Ground Restraint Current falls below this Cutoff Level. This parameter has no impact on recorders.	

6.2.2 IdG: Values

IdG . **IsG W1**	[Operation / Measured Values / IdG W1]
	Measured value (calculated): Ground Stabilizing Current Winding 1

IdG . **IdG W1**	[Operation / Measured Values / IdG W1]
	Measured value (calculated): Ground Differential Current IdG Winding 1

IdG . **IsG W2**	[Operation / Measured Values / IdG W2]
	Measured value (calculated): Ground Stabilizing Current Winding 2

IdG . **IdG W2**	[Operation / Measured Values / IdG W2]
	Measured value (calculated): Ground Differential Current IdG Winding 2

6.2.3 IdG: Statistical Values

IdG . **IsG W1 max**	[Operation / Statistics / Max / IdG W1]
	Measured value (calculated): Ground Stabilizing Current Winding 1 Maximum Value

IdG . **IdG W1 max**	[Operation / Statistics / Max / IdG W1]
☑	*Measured value (calculated): Ground Differential Current IdG Winding 1 Maximum Value*

IdG . **IsG W2 max**	[Operation / Statistics / Max / IdG W2]
☑	*Measured value (calculated): Ground Stabilizing Current Winding 2 Maximum Value*

IdG . **IdG W2 max**	[Operation / Statistics / Max / IdG W2]
☑	*Measured value (calculated): Ground Differential Current IdG Winding 2 Maximum Value*

7 Statistics

- CT W1: ⮡ “CT W1: Statistical Values”
- CT W2: ⮡ “CT W2: Statistical Values”
- Id: ⮡ “Id: Statistical Values”
- IdG: ⮡ “IdG: Statistical Values”
- Id: ⮡ “Id: Statistical Values”
- ThR: ⮡ “ThR: Statistical Values”
- URTD: ⮡ “URTD: Statistical Values”

7.1 Statistics: Global Parameters

Statistics . **Start I Demand via:**	[Device Para / Statistics / Demand / Current Demand]	
Duration	Duration, StartFct ⮡ Duration.	S.3
	Start Current demand by:	

Statistics . **Start I Demand Fc**	[Device Para / Statistics / Demand / Current Demand]	
“-” *Only available if:* • Statistics . Start I Demand via: = StartFct	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Start of the calculation, if the assigned signal becomes true.	

Statistics . **ResFc I Demand**	[Device Para / Statistics / Demand / Current Demand]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Resetting of Statistics - Current Demand (avg, peak avg)	

Statistics . **Duration I Demand**	[Device Para / Statistics / Demand / Current Demand]	
15 s *Only available if:* • Statistics . Start I Demand via: = Duration	2 s ... 30 d ╚═▷ Duration.	S.3
Recording time		

Statistics . **Window I Demand**	[Device Para / Statistics / Demand / Current Demand]	
sliding	sliding, fixed ╚═▷ Window configuration.	S.3
Window configuration		

Statistics . **ResFc Max**	[Device Para / Statistics / Min / Max]	
"-"	"-" ... Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
Resetting of all Maximum values		

Statistics . **ResFc Min**	[Device Para / Statistics / Min / Max]	
"-"	"-" ... Sys . Internal test state ╚═▷ 1..n, Assignment List.	S.3
Resetting of all Minimum values		

7.2 Statistics: Direct Controls

Statistics . **ResFc all**	[Operation / Reset]	
inactive	inactive, active ╚═▷ Mode.	P.1
Resetting of all Statistic values (Current Demand, Power Demand, Min, Max)		

Statistics . **ResFc Max**	[Operation / Reset]	
inactive	inactive, active └⊳ Mode.	P.1
◉	*Resetting of all Maximum values*	

Statistics . **ResFc Min**	[Operation / Reset]	
inactive	inactive, active └⊳ Mode.	P.1
◉	*Resetting of all Minimum values*	

Statistics . **ResFc I Demand**	[Operation / Reset]	
inactive	inactive, active └⊳ Mode.	P.1
◉	*Resetting of Statistics - Current Demand (avg, peak avg)*	

7.3 Statistics: Input States

Statistics . **StartFc I Demand-I**	[Operation / Status Display / Statistics]
⇩	*State of the module input: Start of the Statistics of the Current Demand*

7.4 Statistics: Signals (Output States)

Statistics . **ResFc all**	[Operation / Status Display / Statistics]
⇧	*Signal: Resetting of all Statistic values (Current Demand, Power Demand, Min, Max)*

Statistics . **ResFc I Demand**	[Operation / Status Display / Statistics]
⇧	*Signal: Resetting of Statistics - Current Demand (avg, peak avg)*

Statistics . **ResFc Max**	[Operation / Status Display / Statistics]
⇧	*Signal: Resetting of all Maximum values*

Statistics . **ResFc Min**	[Operation / Status Display / Statistics]
⇧	*Signal: Resetting of all Minimum values*

7.5 Statistics: Counters

Statistics . **Res Cr I Demand**	[Operation / Statistics / Demand / CT W1] [Operation / Statistics / Demand / CT W2]
#	*Number of resets since last booting. The timestamp shows date and time of the last reset.*

Statistics . **Res Cr Max values**	[Operation / Statistics / Max / CT W1] ... [Operation / Statistics / Max / URTD]
#	*Number of resets since last booting. The timestamp shows date and time of the last reset.*

Statistics . **Res Cr Min values**	[Operation / Statistics / Min / CT W1] [Operation / Statistics / Min / CT W2]
#	*Number of resets since last booting. The timestamp shows date and time of the last reset.*

8 Communication

Scada

8.1 Scada: Device Planning Parameters

Scada . **Protocol**	[Device planning]	
"-"	"-" . . . Profibus Used Protocol.	S.3
	Select the SCADA protocol to be used.	

8.2 Scada: Signals (Output States)

Scada . **SCADA connected**	[Operation / Status Display / Scada]
	At least one SCADA System is connected to the device.

Scada . **SCADA not connected**	[Operation / Status Display / Scada]
	No SCADA System is connected to the device

8.3 TcpIp

TcpIp

TCP/IP config	[Device Para / TCP/IP / TCP/IP config]
This item represents a special dialog. (See the Technical Manual for details.) *configuration of the TCP/IP protocol*	

8.3.1 TcpIp: Global Parameters

TcpIp . **Keep Alive Time**	[Device Para / TCP/IP / Advanced Settings]	
720s	1s ... 7200s	S.3
Keep Alive Time is the duration between two keep alive transmissions in idle condition		

TcpIp . **Keep Alive Interval**	[Device Para / TCP/IP / Advanced Settings]	
15s	1s ... 60s	S.3
Keep Alive Interval is the duration between two successive keep alive retransmissions, if the acknowledgement to the previous keepalive transmission was not received.		

TcpIp . **Keep Alive Retry**	[Device Para / TCP/IP / Advanced Settings]	
3	3 ... 3	S.3
Keep alive retry is the number of retransmissions to be carried out before declaring that the remote end is not available.		

8.4 DNP3

Distributed Network Protocol

8.4.1 DNP3: Global Parameters

DNP3 . **Function**	[Device Para / DNP3 / Communication]	
inactive	inactive, active ↳ Mode.	S.3
	Permanent activation or deactivation of module/stage.	

DNP3 . **IP Port Number**	[Device Para / DNP3 / Communication]	
20000	0 ... 65535	S.3
	IP Port Number. *In general it is recommended to keep the default value. If this is not possible then select a number out of the private range 49152-52151 or 52164-65535 that is not yet in use within your network.*	

DNP3 . **Baud rate**	[Device Para / DNP3 / Communication]	
19200	1200 ... 115200 ↳ Baud rate.	S.3
	Baud rate for communication	

DNP3 . **Frame Layout**	[Device Para / DNP3 / Communication]	
8E1	8E1, 8O1, 8N1, 8N2 ↳ Byte Frame.	S.3
	Frame Layout	

DNP3 . **Optical rest position**	[Device Para / DNP3 / Communication]	
Light on *Avail. depends on HW*	Light off, Light on ↳ Optical rest position.	S.3
	Optical rest position	

DNP3 . **SelfAddress**	[Device Para / DNP3 / Communication]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Support of self (automatic) addresses	

DNP3 . **DataLink confirm**	[Device Para / DNP3 / Communication]	
Never	Never, Always, On_Large ╚═▷ Communication Start Variants.	S.3
	Enables or disables the data layer confirmation (ack).	

DNP3 . **t-DataLink confirm**	[Device Para / DNP3 / Communication]	
1s	0.1s ... 10.0s	S.3
	Data layer confirmation timeout	

DNP3 . **DataLink num retries**	[Device Para / DNP3 / Communication]	
3	0 ... 255	S.3
	Number of repetition of data link packet sending after failing	

DNP3 . **Direction Bit**	[Device Para / DNP3 / Communication]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Enables Direction Bit functionality. The Direction Bit is 0 for SlaveStation and 1 for MasterStation	

DNP3 . **Max Frame Size**	[Device Para / DNP3 / Communication]	
255	64 ... 255	S.3
	This value is used to limit the net Frame Size	

DNP3 . **Test Link Period**	[Device Para / DNP3 / Communication]	
0s	0.0s ... 120.0s	S.3
	This value specifies the time period when to send a Test Link-Frame	

DNP3 . **AppLink confirm**	[Device Para / DNP3 / Communication]	
Always	Never, Always, Event ↳ _AL_ResponseType_k.	S.3
	Determines if the device will request that the Application Layer response be confirmed or not	

DNP3 . **t-AppLink confirm**	[Device Para / DNP3 / Communication]	
5s	0.1s . . . 10.0s	S.3
	Application layer response timeout	

DNP3 . **AppLink num retries**	[Device Para / DNP3 / Communication]	
0	0 . . . 255	S.3
	The number of times the device will retransmit an Application Layer fragment	

DNP3 . **Unsol Reporting**	[Device Para / DNP3 / Communication]	
inactive	inactive, active ↳ Mode.	S.3
	Enables unsolicited reporting. This is available only for DNP3 TCP connections, and for DNP3 RTU in case of a peer-to-peer connection.	

DNP3 . **Unsol Reporting Timeout**	[Device Para / DNP3 / Communication]	
10s	1.0s . . . 60.0s	S.3
	Set the amount of time that the outstation will wait for an Application Layer confirmation back from the master indicating that the master received the unsolicited response message.	

DNP3 . **Unsol Reporting Retry**	[Device Para / DNP3 / Communication]	
2	0 . . . 255	S.3
	Set the number of retries that an outstation transmits in each unsolicited response series if it does not receive confirmation back from the master.	

DNP3 . **TestSeqNo**	[Device Para / DNP3 / Communication]	
inactive	inactive, active ↳ Mode.	S.3
	Test if sequence number of request is incremented. If it is not correctly incremented the request will be ignored. It is recommended to have it inactive but some older DNP implementations need it activated.	

DNP3 . **TestSBO**	[Device Para / DNP3 / Communication]	
active	inactive, active └⊳ Mode.	S.3
	It enables a stricter comparing of SBO and operate command. For older DNP versions it is recommanded to deactivated it.	

DNP3 . **Timeout SBO**	[Device Para / DNP3 / Communication]	
30s	1.0s ... 60.0s	S.3
	DNP Outputs can be controlled in a two stage procedure (SBO: Select Before Operate). These outputs are to be selected first by a Select command. After this the bit is reserved for this Operate request. This setting defines the timer for this reservation: After the timer has elapsed the bit is released.	

DNP3 . **ColdRestart**	[Device Para / DNP3 / Communication]	
inactive	inactive, active └⊳ Mode.	S.3
	Enables support for Cold Restart function.	

DNP3 . **Deadb integr time**	[Device Para / DNP3 / Communication]	
1	0 ... 300	S.3
	Deadband integration time.	

DNP3 . **BinaryInput 0** ... DNP3 . **BinaryInput 63**	[Device Para / DNP3 / Point map / Binary Inputs]	
“-”	“-” ... Sys . Internal test state └⊳ 1..n, Assignment List.	S.3
	Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.	

DNP3 . **DoubleBitInput 0** **...** DNP3 . **DoubleBitInput 5**	[Device Para / DNP3 / Point map / Double Bit Inputs]	
“-”	“-”, SG[1] . Pos, SG[2] . Pos ↳ 1..n, Assignment List.	S.3
	Double Bit Digital Input (DNP). This corresponds to a double bit binary output of the protective device.	

DNP3 . **BinaryCounter 0** **...** DNP3 . **BinaryCounter 7**	[Device Para / DNP3 / Point map / BinaryCounter]	
“-”	“-”, Prot . FaultNo, Prot . No. of Grid Fault, SG[1] . TripCmd Cr, SG[2] . TripCmd Cr, Sys . Operating hours Cr ↳ 1..n, Assignment List.	S.3
	Counter can be used to report counter values to the DNP master.	

DNP3 . **Analog value 0** **...** DNP3 . **Analog value 31**	[Device Para / DNP3 / Point map / Analog Input]	
“-”	“-” ... RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
	Analog value can be used to report values to the master (DNP)	

DNP3 . **Scale Factor 0** **...** DNP3 . **Scale Factor 31**	[Device Para / DNP3 / Point map / Analog Input]	
1	0.001 ... 1000000 ↳ Scale Factor.	S.3
	The scale factor is used to convert the measured value in an integer format	

DNP3 . **Dead Band 0** . . . DNP3 . **Dead Band 31**	[Device Para / DNP3 / Point map / Analog Input]	
1%	0.01% . . . 100.00%	S.3
	If a change of measured value is greater than the deadband value it will be reported to the master.	

8.4.2 DNP3: Direct Controls

DNP3 . **Res all Diag Cr**	[Operation / Count and RevData / DNP3] [Operation / Reset]	
inactive	inactive, active Mode.	S.3
	Reset all diagnosis counters	

DNP3 . **Slave Id**	[Device Para / DNP3 / Communication]	
1	0 . . . 65519	S.3
	SlaveId defines the DNP3 address of this device (Outstation)	

DNP3 . **Master Id**	[Device Para / DNP3 / Communication]	
65500	0 . . . 65519	S.3
	MasterId defines the DNP3 address of master (SCADA)	

8.4.3 DNP3: Input States

DNP3 . **BinaryInput0-I** . . . DNP3 . **BinaryInput63-I**	[Operation / Status Display / DNP3 / Binary Inputs]
	Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.

DNP3 . **DoubleBitInput0-I** ... DNP3 . **DoubleBitInput5-I**	[Operation / Status Display / DNP3 / Double Bit Inputs]
	Double Bit Digital Input (DNP). This corresponds to a double bit binary output of the protective device.

8.4.4 DNP3: Signals (Output States)

DNP3 . **busy**	[Operation / Status Display / DNP3 / State]
	This message is set if the protocol is started. It will be reset if the protocol is shut down.

DNP3 . **ready**	[Operation / Status Display / DNP3 / State]
	The message will be set if the protocol is successfully started and ready for data exchange.

DNP3 . **active**	[Operation / Status Display / DNP3 / State]
	The communication with the Master (SCADA) is active. *Note that for TCP/UDP, this state is permanently “Low” unless »DataLink confirm« is set to “Always”.*

8.4.5 DNP3: Counters

DNP3 . **NReceived**	[Operation / Count and RevData / DNP3]
#	*Diagnostic counter: Number of received characters*

DNP3 . **NSent**	[Operation / Count and RevData / DNP3]
#	*Diagnostic counter: Number of sent characters*

DNP3 . **NBadFramings**	[Operation / Count and RevData / DNP3]
#	*Diagnostic counter: Number of bad framings. A large number indicates a disturbed serial connection.*

DNP3 . **NBadParities**	[Operation / Count and RevData / DNP3]
#	*Diagnostic counter: Number of parity errrors. A large number indicates a disturbed serial connection.*

DNP3 . **NBreakSignals**	[Operation / Count and RevData / DNP3]
#	*Diagnostic counter: Number of break signals. A large number indicates a disturbed serial connection.*

DNP3 . **NBadChecksum**	[Operation / Count and RevData / DNP3]
#	*Diagnostic counter: Number of frames received with bad checksum.*

8.5 Modbus

Modbus

8.5.1 Modbus: Global Parameters

Modbus . **t-call**	[Device Para / Modbus / Communication / General Settings]	
10s	1s ... 3600s	S.3
	If there is no request telegram sent from Scada to the device after expiry of this time - the device concludes a communication failure within the Scada system.	

Modbus . **Scada CmdBlo**	[Device Para / Modbus / Communication / General Settings]	
inactive	inactive, active ↳ Mode.	S.3
	Activating (allowing)/ Deactivating (disallowing) the blocking of the Scada Commands	

Modbus . **Disable Latching**	[Device Para / Modbus / Communication / General Settings]	
inactive	inactive, active ↳ Mode.	S.3
	Disable Latching: If this parameter is active (true), none of the Modbus states will be latched. That means that trip signals wont be latched by Modbus.	

Modbus . **AllowGap**	[Device Para / Modbus / Communication / General Settings]	
inactive	inactive, active ↳ Mode.	S.3
	If this parameter is active (True), the user can request a set of modbus register without getting an exception, because of invalid address in the requested array. The invalid addresses have a special value 0xFAFA, but the user is responsible for ignoring invalid addresses. Attention: This special value can be valid, if address is valid.	

Modbus . **Optical rest position**	[Device Para / Modbus / Communication / General Settings]	
Light on *Avail. depends on HW*	Light off, Light on ↳ Optical rest position.	S.3
	Optical rest position	

Modbus . **TCP Port Config**	[Device Para / Modbus / Communication / TCP]	
Default	Default, Private ↳ Port selection.	S.3
	TCP Port Configuration. This parameter needs to be set to “Private” only if another TCP Port than the default one shall be used.	

Modbus . **Port**	[Device Para / Modbus / Communication / TCP]	
502	If: Modbus . TCP Port Config = Default • 502 ... 502 If: Modbus . TCP Port Config = Private • 49152 ... 65535	S.3
	IP Port Number. *In general it is recommended to keep the default value. if this is not possible then select a number out of the private range 49152-52151 or 52164-65535 that is not yet in use within your network.*	

Modbus . **t-timeout**	[Device Para / Modbus / Communication / RTU]	
1s	0.01s ... 10.00s	S.3
	Within this time the answer has to be received by the SCADA system, otherwise the request will be disregarded. In that case the Scada system detects a communication failure and the Scada System has to send a new request.	

Modbus . **Baud rate**	[Device Para / Modbus / Communication / RTU]	
19200	1200, 2400, 4800, 9600, 19200, 38400 ↳ Baud rate.	S.3
	Baud rate	

Modbus . **Physical Settings**	[Device Para / Modbus / Communication / RTU]	
8E1	8E1, 8O1, 8N1, 8N2 ↳ Byte Frame.	S.3
	Digit 1: Number of bits. Digit 2: E=even parity, O=odd parity, N=no parity. Digit 3: Number of stop bits. More information on the parity: It is possible that the last data bit is followed by a parity bit which is used for recognition of communication errors. The parity bit ensures that with even parity ("EVEN") always an even number of bits with valence "1" or with odd parity ("ODD") an odd number of "1" valence bits are transmitted. But it is also possible to transmit no parity bits (here the setting is "Parity = None"). More information on the stop-bits: The end of a data byte is terminated by the stop-bits.	

Modbus . **Config Bin Inp1** **. . .** Modbus . **Config Bin Inp32**	[Device Para / Modbus / Configb Registers / States]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	Virtual Digital Input. This corresponds to a virtual binary output of the protective device.	

Modbus . **Latched Config Bin Inp1** **. . .** Modbus . **Latched Config Bin Inp32**	[Device Para / Modbus / Configb Registers / States]	
inactive	inactive, active └⇒ Mode.	S.3
	Latched Configurable Binary Input	

Modbus . **Mapped Meas 1** **. . .** Modbus . **Mapped Meas 16**	[Device Para / Modbus / Configb Registers / Measured Values]	
“-”	“-” . . . RTD . Hottest Aux Temp └⇒ 1..n, TrendRecList.	S.3
	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.	

Modbus . **Type of SCADA mapping**	[Device Para / Modbus / Config. Data Obj.]	
Standard	Standard, User-defined └⇒ Type of SCADA mapping.	S.3
	*This setting decides whether the communication protocol shall use the default mapping of data objects, or some user-defined mapping that has been loaded from a *.HptSMap file.*	

8.5.2 Modbus: Direct Controls

Modbus . **Res Diagn Cr**	[Operation / Reset]	
inactive	inactive, active └▷ Mode.	P.1
◉	*All Modbus Diagnosis Counters will be reset.*	

Modbus . **Unit ID**	[Device Para / Modbus / Communication / TCP]	
255	1 . . . 255	P.1
◉	*The Unit Identifier is used for routing. This parameter is to be set, if a Modbus RTU and a Modbus TCP network should be coupled.*	

Modbus . **Slave ID**	[Device Para / Modbus / Communication / RTU]	
1	1 . . . 247	P.1
◉	*Device address (Slave ID) within the bus system. Each device address has to be unique within a bus system.*	

8.5.3 Modbus: Input States

Modbus . **Config Bin Inp1-I** . . . Modbus . **Config Bin Inp32-I**	[Operation / Status Display / Modbus / Configb Registers]
⇩	*State of the module input: Config Bin Inp*

8.5.4 Modbus: Signals (Output States)

Modbus . **Transmission RTU**	[Operation / Status Display / Modbus / State]
⇧	*Signal: SCADA active*

Modbus . **Transmission TCP**	[Operation / Status Display / Modbus / State]
⇧	*Signal: SCADA active*

Modbus . **Device Type**	[Operation / Status Display / Modbus / State]
	Device Type: Device type code for relationship between device name and its Modbus code. *Woodward:* *MRI4 - 1000* *MRU4 - 1001* *MRA4 - 1002* *MCA4 - 1003* *MRDT4 - 1005* *MCDTV4 - 1006* *MCDGV4 - 1007* *MRM4 - 1009* *MRMV4 - 1010* *MCDLV4 - 1011*

Modbus . **Comm Version**	[Operation / Status Display / Modbus / State]
	Modbus Communication version. This version number changes if something becomes incompatible between different Modbus releases.

Modbus . **Scada Cmd 1** ... Modbus . **Scada Cmd 16**	[Operation / Status Display / Modbus / Commands]
	Scada Command

8.5.5 Modbus: Values

Modbus . **Mapped Meas 1** ... Modbus . **Mapped Meas 16**	[Operation / Count and RevData / Modbus / Measured Values]
	Mapped Measured Values. They can be used to provide measured values to the Modbus Master.

Modbus . **Config info**	[Device Para / Modbus / Config. Data Obj.]
	Configuration comment (entered by the user during SCADA configuration)

Modbus . **Config version**	[Device Para / Modbus / Config. Data Obj.]
✎	*Version of the user-defined SCADA configuration*

Modbus . **Config status**	[Device Para / Modbus / Config. Data Obj.]	
Changing	Changing, OK, Config. not avail., Error └▷ Config status.	
✎	*Status of the user-defined SCADA configuration.* *Possible values:* *- New SCADA configuration is being loaded, but not active yet.* *- The SCADA configuration is active.* *- The user-defined SCADA configuration is not available (e.g. has not been loaded into the device).* *- Unexpected error. Please contact our service-team.*	

8.5.6 Modbus: Counters

Modbus . **NoOfRequestsTotal**	[Operation / Count and RevData / Modbus / TCP] [Operation / Count and RevData / Modbus / RTU]
#	*Total number of requests. Includes requests for other slaves.*

Modbus . **NoOfRequestsForMe**	[Operation / Count and RevData / Modbus / TCP] [Operation / Count and RevData / Modbus / RTU]
#	*Total Number of requests for this slave.*

Modbus . **NoOfResponse**	[Operation / Count and RevData / Modbus / TCP] [Operation / Count and RevData / Modbus / RTU]
#	*Total number of requests having been responded.*

Modbus . **NoOfQueryInvalid**	[Operation / Count and RevData / Modbus / TCP]
#	*Total number of Request errors. Request could not be interpreted*

Modbus . **NoOfInternalError**	[Operation / Count and RevData / Modbus / TCP]
#	*Total Number of Internal errors while interpreting the request.*

Modbus . **NoOfFrameErrors**	[Operation / Count and RevData / Modbus / RTU]
#	*Total Number of Frame Errors. Physically corrupted Frame.*

Modbus . **NoOfParityErrors**	[Operation / Count and RevData / Modbus / RTU]
#	*Total number of parity errors. Physically corrupted Frame.*

Modbus . **NoOfResponsTimeOverruns**	[Operation / Count and RevData / Modbus / RTU]
#	*Total number of requests with exceeded response time. Physically corrupted Frame.*

Modbus . **NoOfOverrunErros**	[Operation / Count and RevData / Modbus / RTU]
#	*Total Number of Overrun Failures. Physically corrupted Frame.*

Modbus . **NoOfBreaks**	[Operation / Count and RevData / Modbus / RTU]
#	*Number of detected communication aborts*

8.6 IEC 61850

IEC 61850 communication

8.6.1 IEC 61850: Global Parameters

IEC 61850 . **Function**	[Device Para / IEC 61850 / Communication]	
inactive	inactive, active ↳ 1..n, OnOffList.	S.3
	Permanent activation or deactivation of module/stage.	

IEC 61850 . **Deadb integr time**	[Device Para / IEC 61850 / Communication]	
0	0 . . . 300	S.3
	Deadband integration time.	

8.6.2 IEC 61850: Direct Controls

IEC 61850 . **ResetStatistic**	[Operation / Reset]	
inactive	inactive, active ↳ Mode.	P.1
	Reset of all IEC61850 diagnostic counters	

8.6.3 IEC 61850: Signals (Output States)

IEC 61850 . **MMS Client connected**	[Operation / Status Display / IEC 61850 / State]
	At least one MMS client is connected to the device

IEC 61850 . **All Goose Subscriber active**	[Operation / Status Display / IEC 61850 / State]
	All Goose subscriber in the device are working

IEC 61850 . **SPCSO1** . . . IEC 61850 . **SPCSO32**	[Operation / Status Display / IEC 61850 / ControlInputs]
	Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).

IEC 61850 . **GOSINGGIO1.Ind1.stVal** **...** IEC 61850 . **GOSINGGIO2.Ind32.stVal**	[Operation / Status Display / IEC 61850 / Virtual Inputs 1] [Operation / Status Display / IEC 61850 / Virtual Inputs 2]
⇫	*Signal: Virtual Input (IEC61850 GGIO Ind): State*

IEC 61850 . **GOSINGGIO1.Ind1.q** **...** IEC 61850 . **GOSINGGIO2.Ind32.q**	[Operation / Status Display / IEC 61850 / Virtual Inputs 1] [Operation / Status Display / IEC 61850 / Virtual Inputs 2]
⇫	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*

8.6.4 IEC 61850: Values

IEC 61850 . **GoosePublisherState**	[Operation / Status Display / IEC 61850 / State]
Off	Off, On, Error ⮡ State.
✎	*State of the GOOSE Publisher (on or off)*

IEC 61850 . **GooseSubscriberState**	[Operation / Status Display / IEC 61850 / State]
Off	Off, On, Error ⮡ State.
✎	*State of the GOOSE Subscriber (on or off)*

IEC 61850 . **MmsServerState**	[Operation / Status Display / IEC 61850 / State]
Off	Off, On, Error ⮡ State.
✎	*State of MMS Server (on or off)*

8.6.5 IEC 61850: Counters

IEC 61850 . **NoOfGooseRxAll**	[Operation / Count and RevData / IEC 61850]
#	*Total number of received GOOSE messages including messages for other devices (subscribed and not subscribed messages).*

IEC 61850 . **NoOfGooseRxSubscribed**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of subscribed GOOSE messages including messages with incorrect content.*

IEC 61850 . **NoOfGooseRxCorrect**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of subscribed and correctly received GOOSE messages.*

IEC 61850 . **NoOfGooseRxNew**	[Operation / Count and RevData / IEC 61850]
#	*Number of subscribed and correctly received GOOSE messages with new content.*

IEC 61850 . **NoOfGooseTxAll**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of GOOSE messages that have been published by this device.*

IEC 61850 . **NoOfGooseTxNew**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of new GOOSE messages (modified content) that have been published by this device.*

IEC 61850 . **NoOfServerRequestsAll**	[Operation / Count and RevData / IEC 61850]
#	*Total number of MMS Server requests including incorrect requests.*

IEC 61850 . **NoOfDataReadAll**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of values read from this device including incorrect requests.*

IEC 61850 . **NoOfDataReadCorrect**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of correctly read values from this device.*

IEC 61850 . **NoOfDataWrittenAll**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of values written by this device including incorrect ones.*

IEC 61850 . **NoOfDataWrittenCorrect**	[Operation / Count and RevData / IEC 61850]
#	*Total Number of correctly written values by this device.*

IEC 61850 . **NoOfDataChangeNotification**	[Operation / Count and RevData / IEC 61850]
#	*Number of detected changes within the datasets that are published with GOOSE messages.*

IEC 61850 . **No of Client Connections**	[Operation / Count and RevData / IEC 61850]
#	*Number of active MMS client connections*

8.6.6 IEC 61850 – Virt.Outp.

IEC 61850 communication

8.6.6.1 IEC 61850: Global Parameters

IEC 61850 . **COUTGGIO1.Ind1.stVal** **...** IEC 61850 . **COUTGGIO1.Ind32.stVal**	[Device Para / IEC 61850 / Virtual Outputs 1]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	S.3
	Virtual Output. This signal can be assigned or visualized via the SCD file to other devices within the IEC61850 substation.	

8.6.6.2 IEC 61850: Input States

IEC 61850 . **COUTGGIO1.Ind1.stVal-I** **...** IEC 61850 . **COUTGGIO1.Ind32.stVal-I**	[Operation / Status Display / IEC 61850 / Virtual Outputs 1]
	Module input state: Binary state of the Virtual Output (GGIO)

8.7 IEC103

IEC 60870-5-103 communication

8.7.1 IEC103: Global Parameters

IEC103 . **Function**	[Device Para / IEC103]	
inactive	inactive, active ⮡ Mode.	S.3
	Activation or deactivation of the IEC103 communication.	

IEC103 . **Slave ID**	[Device Para / IEC103]	
1	1 ... 247	S.3
	Device address (Slave ID) within the bus system. Each device address has to be unique within a bus system.	

IEC103 . **Baud rate**	[Device Para / IEC103]	
19200	1200, 2400, 4800, 9600, 19200, 38400, 57600 ⮡ Baud rate.	S.3
	Baud rate	

IEC103 . **Physical Settings**	[Device Para / IEC103]	
8E1	8E1, 8O1, 8N1, 8N2 ⮡ Byte Frame.	S.3
	Digit 1: Number of bits. Digit 2: E=even parity, O=odd parity, N=no parity. Digit 3: Number of stop bits. More information on the parity: It is possible that the last data bit is followed by a parity bit which is used for recognition of communication errors. The parity bit ensures that with even parity ("EVEN") always an even number of bits with valence "1" or with odd parity ("ODD") an odd number of "1" valence bits are transmitted. But it is also possible to transmit no parity bits (here the setting is "Parity = None"). More information on the stop-bits: The end of a data byte is terminated by the stop-bits.	

IEC103 . **t-call**	[Device Para / IEC103]	
60s	1s ... 3600s	S.3
	If there is no request telegram sent from Scada to the device after expiry of this time - the device concludes a communication failure within the Scada system.	

IEC103 . **Transm priv meas val**	[Device Para / IEC103]	
inactive	inactive, active ↳ Mode.	S.3
	Transmit additional (private) measuring values	

IEC103 . **Transfer Disturb Rec**	[Device Para / IEC103]	
inactive	inactive, active ↳ Mode.	S.3
	Activates the transmission of disturbance records	

IEC103 . **Timezone**	[Device Para / IEC103]	
UTC	UTC, Local Time ↳ Timezone.	S.3
	Selection whether the timestamps in IEC103 messages shall be given as UTC or local time. (“Local time” always includes the actual daylight saving settings.)	

IEC103 . **Energy Pulse Rate**	[Device Para / IEC103]	
0	0 . . . 100	S.3
	The energy values are always transmitted as counter values (i.e. as integer numbers). This setting defines the unit: If “1” is set then each counter increment is 1 kWh, if “2” is set then each counter increment is 2 kWh,etc. The setting “0” has the effect that no energy values are transmitted.	

IEC103 . **DFC-Compat.**	[Device Para / IEC103]	
inactive	inactive, active ↳ Mode.	S.3
	This setting is only required for certain substation implementations. If there should be communication problems related to the Command Response Queue this setting switches the device over to a different behavior.	

IEC103 . **Optical rest position**	[Device Para / IEC103]	
Light on *Avail. depends on HW*	Light off, Light on ↳ Optical rest position.	S.3
	Optical rest position	

IEC103 . **Ex activate test mode**	[Service / Test (Prot inhibit) / Scada / IEC103]	
Sgen . Running	"-" . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	The signal assigned to this parameter switches the IEC103 communication into Test Mode.	

IEC103 . **Ex activate Block MD**	[Service / Test (Prot inhibit) / Scada / IEC103]	
"-"	"-" . . . Sys . Internal test state └⇒ 1..n, Assignment List.	S.3
	The signal assigned to this parameter activates the blocking of IEC103 transmission in monitor direction.	

8.7.2 IEC103: Direct Controls

IEC103 . **Res all Diag Cr**	[Operation / Reset]	
inactive	inactive, active └⇒ Mode.	S.3
◉	*Reset all diagnosis counters*	

IEC103 . **Activate test mode**	[Service / Test (Prot inhibit) / Scada / IEC103]	
inactive	inactive, active └⇒ Mode.	S.3
◉	*This Direct Control parameter switches the IEC103 communication into Test Mode (or back to nomal mode).*	

IEC103 . **Activate Block MD**	[Service / Test (Prot inhibit) / Scada / IEC103]	
inactive	inactive, active └⇒ Mode.	S.3
◉	*This Direct Control parameter activates (or deactivates) the blocking of IEC103 transmission in monitor direction.*	

8.7.3 IEC103: Signals (Output States)

IEC103 . **Scada Cmd 1** **. . .** IEC103 . **Scada Cmd 10**	[Operation / Status Display / IEC103]
⇫	*Scada Command*

IEC103 . **Transmission**	[Operation / Status Display / IEC103]
⇫	*Signal: SCADA active*

IEC103 . **Failure Event lost**	[Operation / Status Display / IEC103]
⇫	*Failure event lost*

IEC103 . **Test mode active**	[Operation / Status Display / IEC103]
⇫	*Signal: IEC103 communication has been switched over into Test Mode.*

IEC103 . **Block MD active**	[Operation / Status Display / IEC103]
⇫	*Signal: The blocking of IEC103 transmission in monitor direction has been activated.*

8.7.4 IEC103: Counters

IEC103 . **NReceived**	[Operation / Count and RevData / IEC103]
#	*Total Number of received Messages*

IEC103 . **NSent**	[Operation / Count and RevData / IEC103]
#	*Total Number of sent Messages*

IEC103 . **NBadFramings**	[Operation / Count and RevData / IEC103]
#	*Number of bad Messages*

IEC103 . **NBadParities**	[Operation / Count and RevData / IEC103]
#	*Number of Parity Errors*

IEC103 . **NBreakSignals**	[Operation / Count and RevData / IEC103]
#	*Number of Communication Interrupts*

IEC103 . **NInternalError**	[Operation / Count and RevData / IEC103]
#	*Number of Internal Errors*

IEC103 . **NBadCharChecksum**	[Operation / Count and RevData / IEC103]
#	*Number of Checksum Errors*

8.8 IEC104

IEC 60870-5-104 communication

8.8.1 IEC104: Global Parameters

IEC104 . **Function**	[Device Para / IEC104 / General Settings]	
inactive	inactive, active ↳ Mode.	S.3
	Activation or deactivation of the IEC104 communication.	

IEC104 . **TCP Port Config**	[Device Para / IEC104 / General Settings]	
Default	Default, Private ↳ Port selection.	S.3
	TCP Port Configuration. This parameter needs to be set to “Private” only if another TCP Port than the default one shall be used.	

IEC104 . **Port**	[Device Para / IEC104 / General Settings]	
2404	If: IEC104 . TCP Port Config = Default • 2404 ... 2404 If: IEC104 . TCP Port Config = Private • 49152 ... 65535	S.3
	IP Port Number. *In general it is recommended to keep the default value. if this is not possible then select a number out of the private range 49152-52151 or 52164-65535 that is not yet in use within your network.*	

IEC104 . **Timezone**	[Device Para / IEC104 / General Settings]	
UTC	UTC, Local Time ↳ Timezone.	S.3
	Selection whether the timestamps in the transmitted communication telegrams shall be given as UTC or local time. (“Local time” always includes the actual daylight saving settings.)	

IEC104 . **Deadb integr time**	[Device Para / IEC104 / General Settings]	
1s	0s ... 1000s	S.3
	Deadband integration time.	

IEC104 . **Timeout SBE**	[Device Para / IEC104 / General Settings]	
30s	1s ... 60s	S.3
	The communication outputs can be controlled in a two-stage procedure (SBE: Select Before Execute). These outputs have to be selected first by a Select command. After this the bit is reserved for this Execute request. This setting defines the timer for this reservation: After the timer has elapsed the bit is released.	

IEC104 . **Timeout t0**	[Device Para / IEC104 / Advanced]	
30s	30s ... 30s	S.3
	Timeout of connection establishment	

IEC104 . **Timeout t1**	[Device Para / IEC104 / Advanced]	
15s	15s ... 15s	S.3
	Timeout of send or test APDUs	

IEC104 . **Timeout t2**	[Device Para / IEC104 / Advanced]	
10s	10s ... 10s	S.3
	Timeout for acknowledges in case of no data messages	

IEC104 . **Timeout t3**	[Device Para / IEC104 / Advanced]	
20s	20s ... 20s	S.3
	Timeout for sending test frames in case of a long idle state	

IEC104 . **Param k**	[Device Para / IEC104 / Advanced]	
12	12 ... 12	S.3
	Protocol parameter k	

IEC104 . **Param w**	[Device Para / IEC104 / Advanced]	
8	8 ... 8	S.3
	Protocol parameter w	

IEC104 . **Length of address**	[Device Para / IEC104 / Advanced]	
2	2 ... 2	S.3
	Number of bytes of the Common Address of the ASDU	

IEC104 . **Length of CoT**	[Device Para / IEC104 / Advanced]	
2	2 ... 2	S.3
	Number of bytes of the Cause of Transmission	

IEC104 . **Length of Inf Obj addr**	[Device Para / IEC104 / Advanced]	
3	3 ... 3	S.3
	Number of bytes of the address of the Information Object	

IEC104 . **Update time**	[Device Para / IEC104 / Advanced]	
1s	1s ... 60s	S.3
	This setting specifies the time after which measurement values are refreshed. If cyclic transmission is selected new values are reported after this time has elapsed.	

IEC104 . **Transmit Int. State**	[Device Para / IEC104 / Advanced]	
active	inactive, active └⇒ Mode.	S.3
	If this parameter is set to “active” (default) then the intermediate position of a switchgear, too, is transmitted. This needs to be changed to “inactive” only in the rare case that the substation communication does not support the reporting of intermediate positions.	

IEC104 . **Trans. Cmd. State**	[Device Para / IEC104 / Advanced]	
active	inactive, active └⇒ Mode.	S.3
	_ If false it suppress change events for command states (Same address as cmd)	

IEC104 . **Type of SCADA mapping**	[Device Para / IEC104 / Config. Data Obj.]	
Standard	Standard, User-defined └⇒ Type of SCADA mapping.	S.3
	*This setting decides whether the communication protocol shall use the default mapping of data objects, or some user-defined mapping that has been loaded from a *.HptSMap file.*	

8.8.2 IEC104: Direct Controls

IEC104 . **Res all Diag Cr**	[Operation / Reset]	
inactive	inactive, active ╚═▷ Mode.	S.3
	Reset all diagnosis counters	

IEC104 . **Common address**	[Device Para / IEC104 / General Settings]	
1	1 . . . 65535	S.3
	Common Address of the ASDU	

8.8.3 IEC104: Signals (Output States)

IEC104 . **Scada Cmd 1** . . . IEC104 . **Scada Cmd 16**	[Operation / Status Display / IEC104]
	Scada Command

IEC104 . **busy**	[Operation / Status Display / IEC104]
	This message is set if the protocol is started. It will be reset if the protocol is shut down.

IEC104 . **ready**	[Operation / Status Display / IEC104]
	The message will be set if the protocol is successfully started and ready for data exchange.

IEC104 . **Transmission**	[Operation / Status Display / IEC104]
	Signal: SCADA active

IEC104 . **Failure Event lost**	[Operation / Status Display / IEC104]
	Failure event lost

8.8.4 IEC104: Values

IEC104 . **Config info**	[Device Para / IEC104 / Config. Data Obj.]
	Configuration comment (entered by the user during SCADA configuration)

IEC104 . **Config version**	[Device Para / IEC104 / Config. Data Obj.]
	Version of the user-defined SCADA configuration

IEC104 . **Config status**	[Device Para / IEC104 / Config. Data Obj.]	
Changing	Changing, OK, Config. not avail., Error Config status.	
	Status of the user-defined SCADA configuration. *Possible values:* *- Changing: New SCADA configuration is being loaded, but not active yet.* *- OK: The SCADA configuration is active.* *- Config. not avail.: The user-defined SCADA configuration is not available (e.g. has not been loaded into the device).* *- Error: Unexpected error. Please contact our service-team.*	

8.8.5 IEC104: Counters

IEC104 . **NReceived**	[Operation / Count and RevData / IEC104]
#	*Diagnostic counter: Number of received characters*

IEC104 . **NSent**	[Operation / Count and RevData / IEC104]
#	*Diagnostic counter: Number of sent characters*

IEC104 . **Num. of lost conn.**	[Operation / Count and RevData / IEC104]
#	*Diagnostic counter: Number of lost connections*

IEC104 . **NBadChecksum**	[Operation / Count and RevData / IEC104]
#	*Diagnostic counter: Number of frames received with bad checksum.*

8.9 Profibus

Profibus Module

8.9.1 Profibus: Global Parameters

Profibus . **Config Bin Inp 1** **. . .** Profibus . **Config Bin Inp 32**	[Device Para / Profibus / Config Bin Inp 1-16] [Device Para / Profibus / Config Bin Inp 17-32]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Virtual Digital Input. This corresponds to a virtual binary output of the protective device.	

Profibus . **Latched 1** **. . .** Profibus . **Latched 32**	[Device Para / Profibus / Config Bin Inp 1-16] [Device Para / Profibus / Config Bin Inp 17-32]	
inactive	inactive, active ⮡ Mode.	S.3
	Defines whether the Input is latched.	

Profibus . **Type of SCADA mapping**	[Device Para / Profibus / Config. Data Obj.]	
Standard	Standard, User-defined ⮡ Type of SCADA mapping.	S.3
	*This setting decides whether the communication protocol shall use the default mapping of data objects, or some user-defined mapping that has been loaded from a *.HptSMap file.*	

8.9.2 Profibus: Direct Controls

Profibus . **Slave ID**	[Operation / Status Display / Profibus / State] [Device Para / Profibus / Bus parameters]	
2	2 . . . 125	P.1
◉	*Device address (Slave ID) within the bus system. Each device address has to be unique within a bus system.*	

Profibus . **Reset Comds**	[Operation / Reset]	
inactive	inactive, active └─▷ Mode.	P.1
◉	*All Profibus Commands will be reset.*	

8.9.3 Profibus: Input States

Profibus . **Assignment 1-I** **...** Profibus . **Assignment 32-I**	[Operation / Status Display / Profibus / Config Bin Inp 1-16] [Operation / Status Display / Profibus / Config Bin Inp 17-32]
⇩	*Module input state: Scada Assignment*

8.9.4 Profibus: Signals (Output States)

Profibus . **Data OK**	[Operation / Status Display / Profibus / State]
⇧	*Data within the Input field are OK (Yes=1)*

Profibus . **SubModul Err**	[Operation / Status Display / Profibus / State]
⇧	*Assignable Signal, Failure in Sub-Module, Communication Failure.*

Profibus . **Connection active**	[Operation / Status Display / Profibus / State]
⇧	*Connection active*

Profibus . **Scada Cmd 1** **...** Profibus . **Scada Cmd 16**	[Operation / Status Display / Profibus / Commands]
⇧	*Scada Command*

8.9.5 Profibus: Values

Profibus . **Slave State**	[Operation / Status Display / Profibus / State]
Baud Search	Baud Search . . . Data exchange ╚═▷ State.
✎	*Communication State between Slave and Master.*

Profibus . **Baud rate**	[Operation / Status Display / Profibus / State]
-.-	12 Mb/s . . . -.- ╚═▷ Baud rate.
✎	*The baud rate that has been detected lastly, will still be shown after a connection issue.*

Profibus . **PNO Id**	[Operation / Status Display / Profibus / State]
0C50h	0C50h ╚═▷ PNO Id.
✎	*PNO Identification Number. GSD Identification Number.*

Profibus . **Config info**	[Operation / Status Display / Profibus / State] [Device Para / Profibus / Config. Data Obj.]
✎	*Configuration comment (entered by the user during SCADA configuration)*

Profibus . **Config version**	[Operation / Status Display / Profibus / State] [Device Para / Profibus / Config. Data Obj.]
✎	*Version of the user-defined SCADA configuration*

Profibus . **Config status**	[Operation / Status Display / Profibus / State] [Device Para / Profibus / Config. Data Obj.]
Changing	Changing, OK, Config. not avail., Error ╚═▷ Config status.
✎	*Status of the user-defined SCADA configuration.* *Possible values:*

8.9.6 Profibus: Counters

Profibus . **Master ID**	[Operation / Status Display / Profibus / State]
#	*Device address (Master ID) within the bus system. Each device address has to be unique within a bus system.*

Profibus . **HO Id PSub**	[Operation / Status Display / Profibus / State]
#	*Handoff Id of PbSub*

Profibus . **t-WatchDog**	[Operation / Status Display / Profibus / State]
#	*The Profibus Chip detects a communication issue if this timer is expired without any communication (Parameterising telegram).*

Profibus . **Fr Sync Err**	[Operation / Count and RevData / Profibus]
#	*Frames, that were sent from the Master to the Slave are faulty.*

Profibus . **Num. CRC err.**	[Operation / Count and RevData / Profibus]
#	*Number of CRC errors that the subsystem manager has recognized in the received response frames from the subsystem. (Each error caused a subsystem reset.)*

Profibus . **Num. frame loss err.**	[Operation / Count and RevData / Profibus]
#	*Number of frame loss errors that the subsystem manager has recognized in the received response frames from the subsystem. (Each error caused a subsystem reset.)*

Profibus . **Num. trig. CRC err.**	[Operation / Count and RevData / Profibus]
#	*Number of CRC errors that the subsystem has recognized in the received trigger frames from the host.*

Profibus . **Num. subsys. res.**	[Operation / Count and RevData / Profibus]
#	*Number of subsystem restarts or resets that the subsystem manager has caused.*

8.10 IRIG-B

IRIG-B-Module

8.10.1 IRIG-B: Device Planning Parameters

IRIG-B . **Mode**	[Device planning]	
"-"	"-", use ↳ Mode.	S.3
	IRIG-B-Module, general operation mode	

8.10.2 IRIG-B: Global Parameters

IRIG-B . **Function**	[Device Para / Time / TimeSync / IRIG-B]	
inactive	inactive, active ↳ Mode.	S.3
	Permanent activation or deactivation of module/stage.	

IRIG-B . **IRIG-B00X**	[Device Para / Time / TimeSync / IRIG-B]	
IRIGB-000	IRIGB-000 . . . IRIGB-007 ↳ IRIG-B00X.	S.3
	Determination of the Type: IRIG-B00X. IRIG-B types differ in types of included "Coded Expressions" (year, control-functions, straight-binary-seconds).	

8.10.3 IRIG-B: Direct Controls

IRIG-B . **Res IRIG-B Cr**	[Operation / Reset]	
inactive	inactive, active ↳ Mode.	P.1
◉	*Resetting of the Diagnosis Counters: IRIG-B*	

8.10.4 IRIG-B: Signals (Output States)

IRIG-B . **IRIG-B active**	[Operation / Status Display / TimeSync / IRIG-B]
⇪	*Signal: If there is no valid IRIG-B signal for 60 sec, IRIG-B is regarded as inactive.*

IRIG-B . **High-Low Invert**	[Operation / Status Display / TimeSync / IRIG-B]
⇪	*Signal: The High and Low signals of the IRIG-B are inverted. This does NOT mean that the wiring is faulty. If the wiring is faulty no IRIG-B signal will be detected.*

IRIG-B . **Control Signal1** **. . .** IRIG-B . **Control Signal18**	[Operation / Status Display / TimeSync / IRIG-B]
⇪	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*

8.10.5 IRIG-B: Counters

IRIG-B . **NoOfFramesOK**	[Operation / Count and RevData / TimeSync / IRIG-B]
#	*Total Number valid Frames.*

IRIG-B . **NoOfFrameErrors**	[Operation / Count and RevData / TimeSync / IRIG-B]
#	*Total Number of Frame Errors. Physically corrupted Frame.*

IRIG-B . **Edges**	[Operation / Count and RevData / TimeSync / IRIG-B]
#	*Edges: Total number of rising and falling edges. This signal indicates if a signal is available at the IRIG-B input.*

8.11 SNTP

SNTP-Module

8.11.1 SNTP: Device Planning Parameters

SNTP . **Mode**	[Device planning]	
“-”	“-”, use ⮑ Mode.	S.3
	SNTP-Module, general operation mode	

8.11.2 SNTP: Global Parameters

SNTP . **Server1**	[Device Para / Time / TimeSync / SNTP]	
inactive	inactive, active ⮑ Mode.	S.3
	Server 1	

SNTP . **IP Byte1** . . . SNTP . **IP Byte4**	[Device Para / Time / TimeSync / SNTP]	
0	0 . . . 255	S.3
	IP1.IP2.IP3.IP4	

SNTP . **Server2**	[Device Para / Time / TimeSync / SNTP]	
inactive	inactive, active ⮑ Mode.	S.3
	Server 2	

8.11.3 SNTP: Direct Controls

SNTP . **Res Counter**	[Operation / Reset]	
inactive	inactive, active ↳ Mode.	P.1
◉	*Reset all Counters.*	

8.11.4 SNTP: Signals (Output States)

SNTP . **SNTP active**	[Operation / Status Display / TimeSync / SNTP]
⇫	*Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.*

8.11.5 SNTP: Values

SNTP . **Used Server**	[Operation / Status Display / TimeSync / SNTP]	
None	Server1, Server2, None ↳ Server State.	
✎	*Which Server is used for SNTP synchronization.*	

SNTP . **PrecServer1**	[Operation / Status Display / TimeSync / SNTP]
✎	*Precision of Server 1*

SNTP . **PrecServer2**	[Operation / Status Display / TimeSync / SNTP]
✎	*Precision of Server 2*

SNTP . **ServerQlty**	[Operation / Status Display / TimeSync / SNTP]	
“-”	GOOD, SUFFICIENT, BAD, “-” ↳ State.	
✎	*Quality of Server used for Synchronization (GOOD, SUFFICIENT, BAD)*	

SNTP . **NetConn**	[Operation / Status Display / TimeSync / SNTP]
“-”	GOOD, SUFFICIENT, BAD, “-” └▷ State.
✎	*Quality of Network Connection (GOOD, SUFFICIENT, BAD).*

8.11.6 SNTP: Counters

SNTP . **StratumServer1**	[Operation / Status Display / TimeSync / SNTP]
#	*Stratum of Server 1*

SNTP . **StratumServer2**	[Operation / Status Display / TimeSync / SNTP]
#	*Stratum of Server 2*

SNTP . **NoOfSyncs**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Total Number of Synchronizations.*

SNTP . **NoOfConnectLost**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Total Number of lost SNTP Connections (no sync for 120 sec).*

SNTP . **NoOfSmallSyncs**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of very small Time Corrections.*

SNTP . **NoOfNormSyncs**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of normal Time Corrections*

SNTP . **NoOfBigSyncs**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of big Time Corrections*

SNTP . **NoOfFiltSyncs**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of filtered Time Corrections*

SNTP . **NoOfSlowTrans**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of slow Transfers.*

SNTP . **NoOfHighOffs**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of high Offsets.*

SNTP . **NoOfIntTimeouts**	[Operation / Count and RevData / TimeSync / SNTP]
#	*Service counter: Total Number of internal timeouts.*

8.12 TimeSync

Time synchronisation

Date and Time	[Device Para / Time / Date and Time]
	This item represents a special dialog. (See the Technical Manual for details.) *(Re-)setting Date and Time*

8.12.1 TimeSync: Global Parameters

TimeSync . **Time Zones**	[Device Para / Time / Timezone]	
UTC+0 London	UTC+14 Kiritimati ... UTC-11 Midway Islands ⮡ Time Zones.	S.3
	Time Zones	

TimeSync . **DST offset**	[Device Para / Time / Timezone]	
60min	-180min ... 180min	S.3
	Difference to wintertime	

TimeSync . **DST manual**	[Device Para / Time / Timezone]	
active	inactive, active ⮡ Mode.	S.3
	Manual setting of the Daylight Saving Time	

TimeSync . **Summertime**	[Device Para / Time / Timezone]	
inactive	inactive, active ⮡ Mode.	S.3
	Daylight Saving Time	

TimeSync . **Summertime m**	[Device Para / Time / Timezone]	
March	January ... December ⮡ Month of clock change.	S.3
	Month of clock change summertime	

TimeSync . **Summertime d**	[Device Para / Time / Timezone]	
Sunday	Sunday . . . General day └⇒ Date.	S.3
Day of clock change summertime		

TimeSync . **Summertime w**	[Device Para / Time / Timezone]	
Last	First, Second, Third, Fourth, Last └⇒ Day of clock change.	S.3
Place of selected day in month (for clock change summertime)		

TimeSync . **Summertime h**	[Device Para / Time / Timezone]	
2h	0h . . . 23h	S.3
Hour of clock change summertime		

TimeSync . **Summertime min**	[Device Para / Time / Timezone]	
0min	0min . . . 59min	S.3
Minute of clock change summertime		

TimeSync . **Wintertime m**	[Device Para / Time / Timezone]	
October	January . . . December └⇒ Month of clock change.	S.3
Month of clock change wintertime		

TimeSync . **Wintertime d**	[Device Para / Time / Timezone]	
Sunday	Sunday . . . General day └⇒ Date.	S.3
Day of clock change wintertime		

TimeSync . **Wintertime w**	[Device Para / Time / Timezone]	
Last	First, Second, Third, Fourth, Last └⇒ Day of clock change.	S.3
Place of selected day in month (for clock change wintertime)		

TimeSync . **Wintertime h**	[Device Para / Time / Timezone]	
3h	0h . . . 23h	S.3
	Hour of clock change wintertime	

TimeSync . **Wintertime min**	[Device Para / Time / Timezone]	
0min	0min . . . 59min	S.3
	Minute of clock change wintertime	

TimeSync . **TimeSync**	[Device Para / Time / TimeSync / TimeSync]	
"-"	"-", IRIG-B . IRIG-B, SNTP . SNTP, Modbus . Modbus, IEC103 . IEC 60870-5-103, IEC104 . IEC104, DNP3 . DNP3 └═▷ Used Protocol.	S.3
	Time synchronisation	

8.12.2 TimeSync: Signals (Output States)

TimeSync . **synchronized**	[Operation / Status Display / TimeSync / TimeSync]
	Clock is synchronized.

9 Protection Parameter

Module General Protection

9.1 Prot: Global Parameters

Prot . **Function**	[Protection Para / Global Prot Para / Prot]	
active	inactive, active └─▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

Prot . **ExBlo Fc**	[Protection Para / Global Prot Para / Prot]	
inactive	inactive, active └─▷ active/inactive.	P.2
	Activate (allow) the external blocking of the global protection functionality of the device.	

Prot . **ExBlo1** Prot . **ExBlo2**	[Protection Para / Global Prot Para / Prot]	
“-”	“-” . . . Sys . Internal test state └─▷ 1..n, Assignment List.	P.2
	If external blocking of this module is activated (allowed), the global protection functionality of the device will be blocked if the state of the assigned signal becomes true.	

Prot . **Blo TripCmd**	[Protection Para / Global Prot Para / Prot]	
inactive	inactive, active └─▷ Mode.	P.2
	Permanent blocking of the Trip Command of the entire Protection.	

Prot . **ExBlo TripCmd Fc**	[Protection Para / Global Prot Para / Prot]	
inactive	inactive, active └─▷ active/inactive.	P.2
	Activate (allow) the external blocking of the trip command of the entire device.	

Prot . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Prot]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	If external blocking of the tripping command is activated (allowed), the tripping command of the entire device will be blocked if the state of the assigned signal becomes true.	

9.2 Prot: Direct Controls

Prot . **Res FaultNo a GridFaultNo**	[Operation / Reset]	
inactive	inactive, active ↳ Mode.	P.1
	Resetting of fault number and grid fault number.	

9.3 Prot: Input States

Prot . **ExBlo1-I**	[Operation / Status Display / Prot]
	Module input state: External blocking1

Prot . **ExBlo2-I**	[Operation / Status Display / Prot]
	Module input state: External blocking2

Prot . **ExBlo TripCmd-I**	[Operation / Status Display / Prot]
	Module input state: External Blocking of the Trip Command

9.4 Prot: Signals (Output States)

Prot . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Prot]
	Signal: active

Prot . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Prot]
	Signal: General Alarm

Prot . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Prot]
Signal: General Trip	

Prot . **available**	[Operation / Status Display / Prot]
Signal: Protection is available	

Prot . **ExBlo**	[Operation / Status Display / Prot]
Signal: External Blocking	

Prot . **Blo TripCmd**	[Operation / Status Display / Prot]
Signal: Trip Command blocked	

Prot . **ExBlo TripCmd**	[Operation / Status Display / Prot]
Signal: External Blocking of the Trip Command	

Prot . **Alarm L1**	[Operation / Status Display / Prot]
Signal: General-Alarm L1	

Prot . **Alarm L2**	[Operation / Status Display / Prot]
Signal: General-Alarm L2	

Prot . **Alarm L3**	[Operation / Status Display / Prot]
Signal: General-Alarm L3	

Prot . **Alarm G**	[Operation / Status Display / Prot]
Signal: General-Alarm - Earth fault	

Prot . **Trip L1**	[Operation / Status Display / Prot]
Signal: General Trip L1	

Prot . **Trip L2**	[Operation / Status Display / Prot]
Signal: General Trip L2	

Prot . **Trip L3**	[Operation / Status Display / Prot]
⇧	*Signal: General Trip L3*

Prot . **Trip G**	[Operation / Status Display / Prot]
⇧	*Signal: General Trip Ground fault*

Prot . **Res FaultNo a GridFaultNo**	[Operation / Status Display / Prot]
⇧	*Signal: Resetting of fault number and grid fault number.*

Prot . **FaultNo**	[Operation / Count and RevData / Prot]
⇧	*Fault number*

Prot . **No. of Grid Fault**	[Operation / Count and RevData / Prot]
⇧	*Number of grid fault: A grid fault, e.g. a short circuit, might cause several faults with trip and autoreclosing; in this case, the fault number counts each fault, but the grid fault number remains the same.*

9.5 Id

Differential Protection Module

9.5.1 Id: Device Planning Parameters

Id . **Mode**	[Device planning]	
use	"-", use ↳ Mode.	S.3
	general operation mode	

9.5.2 Id: Global Parameters

Id . **ExBlo1** Id . **ExBlo2**	[Protection Para / Global Prot Para / Diff-Prot / Id]	
"-"	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Id . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Diff-Prot / Id]	
"-"	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.5.3 Id: Setting Group Parameters

Id . **Function**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
active	inactive, active ↳ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

Id . **ExBlo Fc**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

Id . **Blo TripCmd**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

Id . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

Id . **Id min**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
0.2Ib	0.05Ib ... 1.00Ib	P.2
	Constant minimum pickup current (differential current). Pickup value of the differential current based on the rated current Ib of the protection object.	

Id . **Id(Is0)**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
0.0Ib	0.0Ib ... 1.00Ib	P.2
	Starting point of the static tripping characteristic at Is0	

Id . **Id(Is1)**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
0.6Ib	0.2Ib ... 2.00Ib	P.2
	Breaking point of the static tripping characteristic at Is1	

Id . **Id(Is2)**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
6.2Ib	1.0Ib ... 8.0Ib	P.2
	Value of the static tripping characteristic at Is2	

Id . **Is1**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
2.0Ib	0.5Ib ... 4.0Ib	P.2
	Breaking point of the static tripping characteristic when Is1	

Id . **Is2**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
10.0Ib	5.0Ib ... 10.0Ib	P.2
	Value of the static tripping characteristic at Is2	

Id . **Char Reset%**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
95%	90% ... 98%	P.2
	Drop Out (in percent of the setting). Settable Drop Out works only on the gradients. Id min uses a fix Drop Out.	

Id . **d(H,m)**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
8Ib	0.0Ib ... 30.0Ib	P.2
	Restraining factor for rising the static tripping characteristic in case of stationary or transient harmonic components, which are ascertained by Fourier analysis (H) or transients monitor (m).	

Id . **Stab H2**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
inactive	inactive, active ⮡ Mode.	P.2
	Restraining of differential protection function against stationary or transient components of the 2nd harmonic at the phase current (e.g. rush-effect).	

Id . **H2 Sta**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
25%	10% ... 60%	P.2
	Threshold (2nd harmonic - basic wave ratio) for restraining the differential protection function against stationary 2nd harmonic.	

Id . **H2 Tra**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
10%	10% ... 60%	P.2
	Threshold (2nd harmonic - basic wave ratio) for temporary stabilisation of the differential protection function against transient 2nd harmonic.	

Id . **Stab H4**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
inactive	inactive, active ↳ Mode.	P.2
	Restraining of differential protection function against stationary components of the 4th harmonic at the phase current.	

Id . **H4 Sta**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
20%	10% ... 60%	P.2
	Threshold (4th harmonic - basic wave ratio) for restraining the differential protection function against stationary 4th harmonic.	

Id . **Stab H5**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
inactive	inactive, active ↳ Mode.	P.2
	Stabilisation of differential protection function against stationary or transient components of the 5th harmonic at the phase current (e.g. transformer overexcitation).	

Id . **H5 Sta**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
30%	10% ... 60%	P.2
	Threshold (5thd harmonic - basic wave ratio) for stabilising the differential protection function against stationary 5th harmonic.	

Id . **H5 Tra**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
15%	10% ... 60%	P.2
	Threshold (5th harmonic - basic wave ratio) for temporary restraining of the differential protection function against transient 5th harmonic.	

Id . **t-Trans**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
2s	0.05s ... 100.00s	P.2
	Time of temporary stabilisation of the differential protection function when thresholds for „H2 Tra“ and „H5 Tra“ (transient harmonic) are exceeded.	

Id . **Crossbl**	[Protection Para / Set 1...4 / Diff-Prot / Id]	
inactive	inactive, active ↳ Mode.	P.2
	Active = Phase overlapping stabilisation of the differential protection function. Inactive = Phase selective stabilisation of the differential protection function.	

Id . **CT Satur. Stab.**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
inactive	inactive, active └▷ Mode.	P.2
	Enable (or disable) temporary restraining of the differential protection triggered by the detection of an external fault in case of CT saturation.	

Id . **CT Sat. Stab. tBlock**	[Protection Para / Set 1…4 / Diff-Prot / Id]	
0.30s	0.01s . . . 10.00s	P.2
	Maximum stabilization duration time for temporary restraining via CT saturation stabilization. The setting value should be (among other dependencies) coordinated with the maximum fault clearing time for an external fault.	

9.5.4 Id: Input States

Id . **ExBlo1-I**	[Operation / Status Display / Diff-Prot / Id]
	Module input state: External blocking1

Id . **ExBlo2-I**	[Operation / Status Display / Diff-Prot / Id]
	Module input state: External blocking2

Id . **ExBlo TripCmd-I**	[Operation / Status Display / Diff-Prot / Id]
	Module input state: External Blocking of the Trip Command

9.5.5 Id: Signals (Output States)

Id . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Diff-Prot / Id]
	Signal: active

Id . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Diff-Prot / Id]
	Signal: Alarm

Id . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Trip*

Id . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Trip Command*

Id . **ExBlo**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: External Blocking*

Id . **Blo TripCmd**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Trip Command blocked*

Id . **ExBlo TripCmd**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: External Blocking of the Trip Command*

Id . **Alarm L1**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Alarm System Phase L1*

Id . **Alarm L2**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Alarm System Phase L2*

Id . **Alarm L3**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Alarm System L3*

Id . **Trip L1**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Trip System Phase L1*

Id . **Trip L2**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Trip System Phase L2*

Id . **Trip L3**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Trip System Phase L3*

Id . **Blo H2**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Blocked by Harmonic:2*

Id . **Blo H4**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Blocked by Harmonic:4*

Id . **Blo H5**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Blocked by Harmonic:5*

Id . **H2,H4,H5 Blo**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Blocked by Harmonics (Inhibit)*

Id . **CT Satur.Stab. triggered**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Temporary restraining of the Phase Differential Protection, triggered by the detection of an external fault in case of CT saturation.*

Id . **Transient**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Temporary stabilization of the differential protection afterwards the transformer is being engergized.*

Id . **Restraining**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Restraining of the differential protection by means of rising the tripping curve.*

Id . **CT Satur.Stab. L1 trig.**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Temporary restraining of the Phase Differential Protection in phase L1, triggered by the detection of an external phase L1 fault in case of CT saturation.*

Id . **CT Satur.Stab. L2 trig.**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Temporary restraining of the Phase Differential Protection in phase L2, triggered by the detection of an external phase L2 fault in case of CT saturation.*

Id . **CT Satur.Stab. L3 trig.**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Signal: Temporary restraining of the Phase Differential Protection in phase L3, triggered by the detection of an external phase L3 fault in case of CT saturation.*

Id . **Restraining: L1**	[Operation / Status Display / Diff-Prot / Id]
⇧	*Restraining: L1*

Id . **Restraining: L2**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Restraining: L2*

Id . **Restraining: L3**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Restraining: L3*

Id . **IH2 Blo L1**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L1: Blocking of the Phase Differential Protection because of second Harmonic.*

Id . **IH2 Blo L2**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L2: Blocking of the Phase Differential Protection because of second Harmonic.*

Id . **IH2 Blo L3**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L3: Blocking of the Phase Differential Protection because of second Harmonic.*

Id . **IH4 Blo L1**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L1: Blocking of the Phase Differential Protection because of fourth Harmonic.*

Id . **IH4 Blo L2**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L2: Blocking of the Phase Differential Protection because of fourth Harmonic.*

Id . **IH4 Blo L3**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L3: Blocking of the Phase Differential Protection because of fourth Harmonic.*

Id . **IH5 Blo L1**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L1: Blocking of the Phase Differential Protection because of fifth Harmonic.*

Id . **IH5 Blo L2**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L2: Blocking of the Phase Differential Protection because of fifth Harmonic.*

Id . **IH5 Blo L3**	[Operation / Status Display / Diff-Prot / Id]
⇫	*Signal:Phase L3: Blocking of the Phase Differential Protection because of fifth Harmonic.*

9.5.6 Id: Values

Id . **Id L1 H2**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L1 Harmonic:2*

Id . **Id L2 H2**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L2 Harmonic:2*

Id . **Id L3 H2**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L3 Harmonic:2*

Id . **Id L1 H4**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L1 Harmonic:4*

Id . **Id L2 H4**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L2 Harmonic:4*

Id . **Id L3 H4**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L3 Harmonic:4*

Id . **Id L1 H5**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L1 Harmonic:5*

Id . **Id L2 H5**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L2 Harmonic:5*

Id . **Id L3 H5**	[Operation / Measured Values / Id]
✎	*Measured value (calculated): Differential Current Phase L3 Harmonic:5*

9.5.7 Id: Statistical Values

Id . **Id L1H2max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L1H2*

Id . **Id L2H2max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L2H2*

Id . **Id L3H2max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L3H2*

Id . **Id L1H4max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L1H4*

Id . **Id L2H4max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L2H4*

Id . **Id L3H4max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L3H4*

Id . **Id L1H5max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L1H5*

Id . **Id L2H5max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L2H5*

Id . **Id L3H5max**	[Operation / Statistics / Max / Id]
☑	*Maximum Value Id L3H5*

9.6 IdH

High-Set Differential Protection Module

9.6.1 IdH: Device Planning Parameters

IdH . **Mode**	[Device planning]	
use	"-", use ⮡ Mode.	S.3
	general operation mode	

9.6.2 IdH: Global Parameters

IdH . **ExBlo1** IdH . **ExBlo2**	[Protection Para / Global Prot Para / Diff-Prot / IdH]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

IdH . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Diff-Prot / IdH]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.6.3 IdH: Setting Group Parameters

IdH . **Function**	[Protection Para / Set 1...4 / Diff-Prot / IdH]	
active	inactive, active ⮡ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

IdH . **ExBlo Fc**	[Protection Para / Set 1…4 / Diff-Prot / IdH]	
inactive	inactive, active └⊳ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

IdH . **Blo TripCmd**	[Protection Para / Set 1…4 / Diff-Prot / IdH]	
inactive	inactive, active └⊳ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

IdH . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / Diff-Prot / IdH]	
inactive	inactive, active └⊳ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

IdH . **Id>>**	[Protection Para / Set 1…4 / Diff-Prot / IdH]	
10.0Ib	0.5Ib . . . 30.0Ib	P.2
	Highset Differential Current Protection/Unstabilized high-phase fault: Pickup value of the differential current based on the rated current Ib of the protection object.	

9.6.4 IdH: Input States

IdH . **ExBlo1-I**	[Operation / Status Display / Diff-Prot / IdH]
	Module input state: External blocking1

IdH . **ExBlo2-I**	[Operation / Status Display / Diff-Prot / IdH]
	Module input state: External blocking2

IdH . **ExBlo TripCmd-I**	[Operation / Status Display / Diff-Prot / IdH]
	Module input state: External Blocking of the Trip Command

9.6.5 IdH: Signals (Output States)

IdH . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Diff-Prot / IdH]
	Signal: active

IdH . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Diff-Prot / IdH]
	Signal: Alarm

IdH . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Diff-Prot / IdH]
	Signal: Trip

IdH . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Diff-Prot / IdH]
	Signal: Trip Command

IdH . **ExBlo**	[Operation / Status Display / Diff-Prot / IdH]
	Signal: External Blocking

IdH . **Blo TripCmd**	[Operation / Status Display / Diff-Prot / IdH]
	Signal: Trip Command blocked

IdH . **ExBlo TripCmd**	[Operation / Status Display / Diff-Prot / IdH]
	Signal: External Blocking of the Trip Command

IdH . **Alarm L1**	[Operation / Status Display / Diff-Prot / IdH]
	Signal: Alarm System Phase L1

IdH . **Alarm L2**	[Operation / Status Display / Diff-Prot / IdH]
	Signal: Alarm System Phase L2

IdH . **Alarm L3**	[Operation / Status Display / Diff-Prot / IdH]
⇫	*Signal: Alarm System L3*

IdH . **Trip L1**	[Operation / Status Display / Diff-Prot / IdH]
⇫	*Signal: Trip System Phase L1*

IdH . **Trip L2**	[Operation / Status Display / Diff-Prot / IdH]
⇫	*Signal: Trip System Phase L2*

IdH . **Trip L3**	[Operation / Status Display / Diff-Prot / IdH]
⇫	*Signal: Trip System Phase L3*

9.7 IdG[1] . . . IdG[2]

Restricted Ground Fault Differential Protection Module

9.7.1 IdG[1]: Device Planning Parameters

IdG[1] . **Mode**	[Device planning]	
“-”	“-”, use ⮑ Mode.	S.3
	general operation mode	

9.7.2 IdG[1]: Global Parameters

IdG[1] . **CT Winding Side**	[Protection Para / Global Prot Para / Diff-Prot / IdG[1]]	
W1	W1, W2 ⮑ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

IdG[1] . **ExBlo1** IdG[1] . **ExBlo2**	[Protection Para / Global Prot Para / Diff-Prot / IdG[1]]	
“-”	“-” . . . Sys . Internal test state ⮑ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

IdG[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Diff-Prot / IdG[1]]	
“-”	“-” . . . Sys . Internal test state ⮑ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.7.3 IdG[1]: Setting Group Parameters

IdG[1] . **Function**	[Protection Para / Set 1…4 / Diff-Prot / IdG[1]]	
inactive	inactive, active ↳ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

IdG[1] . **ExBlo Fc**	[Protection Para / Set 1…4 / Diff-Prot / IdG[1]]	
inactive	inactive, active ↳ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

IdG[1] . **Blo TripCmd**	[Protection Para / Set 1…4 / Diff-Prot / IdG[1]]	
inactive	inactive, active ↳ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

IdG[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / Diff-Prot / IdG[1]]	
inactive	inactive, active ↳ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

IdG[1] . **IdG min**	[Protection Para / Set 1…4 / Diff-Prot / IdG[1]]	
0.05Ib	0.05Ib . . . 1.00Ib	P.2
	Constant minimum pickup current (earth differential current). Pickup value of the differential current based on the rated current Ib of the related protection object.	

IdG[1] . **IdG(Is0)**	[Protection Para / Set 1…4 / Diff-Prot / IdG[1]]	
0.1Ib	0.00Ib . . . 1.00Ib	P.2
	Starting point of the static tripping characteristic at Is0	

IdG[1] . **IdG(Is1)**	[Protection Para / Set 1...4 / Diff-Prot / IdG[1]]	
0.2Ib	0.2Ib ... 2.00Ib	P.2
	Breaking point of the static tripping characteristic at Is1	

IdG[1] . **IdG(Is2)**	[Protection Para / Set 1...4 / Diff-Prot / IdG[1]]	
2.0Ib	1.0Ib ... 8.0Ib	P.2
	Value of the static tripping characteristic at Is2	

IdG[1] . **Is1**	[Protection Para / Set 1...4 / Diff-Prot / IdG[1]]	
2.0Ib	0.5Ib ... 5.0Ib	P.2
	Breaking point of the static tripping characteristic when Is1	

IdG[1] . **Is2**	[Protection Para / Set 1...4 / Diff-Prot / IdG[1]]	
10.0Ib	5.0Ib ... 10.0Ib	P.2
	Value of the static tripping characteristic at Is2	

9.7.4 IdG[1]: Input States

IdG[1] . **ExBlo1-I**	[Operation / Status Display / Diff-Prot / IdG[1]]
	Module input state: External blocking1

IdG[1] . **ExBlo2-I**	[Operation / Status Display / Diff-Prot / IdG[1]]
	Module input state: External blocking2

IdG[1] . **ExBlo TripCmd-I**	[Operation / Status Display / Diff-Prot / IdG[1]]
	Module input state: External Blocking of the Trip Command

9.7.5 IdG[1]: Signals (Output States)

IdG[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Diff-Prot / IdG[1]]
	Signal: active

IdG[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Diff-Prot / IdG[1]]
⇧	*Signal: Alarm*

IdG[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Diff-Prot / IdG[1]]
⇧	*Signal: Trip*

IdG[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Diff-Prot / IdG[1]]
⇧	*Signal: Trip Command*

IdG[1] . **ExBlo**	[Operation / Status Display / Diff-Prot / IdG[1]]
⇧	*Signal: External Blocking*

IdG[1] . **Blo TripCmd**	[Operation / Status Display / Diff-Prot / IdG[1]]
⇧	*Signal: Trip Command blocked*

IdG[1] . **ExBlo TripCmd**	[Operation / Status Display / Diff-Prot / IdG[1]]
⇧	*Signal: External Blocking of the Trip Command*

9.8 IdGH[1] . . . IdGH[2]

Restricted Ground Fault Highset Protection Module

9.8.1 IdGH[1]: Device Planning Parameters

IdGH[1] . **Mode**	[Device planning]	
“-”	“-”, use └⇒ Mode.	S.3
	general operation mode	

9.8.2 IdGH[1]: Global Parameters

IdGH[1] . **CT Winding Side**	[Protection Para / Global Prot Para / Diff-Prot / IdGH[1]]	
W1	W1, W2 └⇒ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

IdGH[1] . **ExBlo1** IdGH[1] . **ExBlo2**	[Protection Para / Global Prot Para / Diff-Prot / IdGH[1]]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

IdGH[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Diff-Prot / IdGH[1]]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.8.3 IdGH[1]: Setting Group Parameters

IdGH[1] . **Function**	[Protection Para / Set 1...4 / Diff-Prot / IdGH[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

IdGH[1] . **ExBlo Fc**	[Protection Para / Set 1...4 / Diff-Prot / IdGH[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

IdGH[1] . **Blo TripCmd**	[Protection Para / Set 1...4 / Diff-Prot / IdGH[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

IdGH[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1...4 / Diff-Prot / IdGH[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

IdGH[1] . **IdG>>**	[Protection Para / Set 1...4 / Diff-Prot / IdGH[1]]	
2.00Ib	0.50Ib ... 20.00Ib	P.2
	Highset Ground Differential Current Protection/Unstabilized high-phase restricted earth fault: Pickup value of the earth differential current based on the rated current Ib of the related protection object.	

9.8.4 IdGH[1]: Input States

IdGH[1] . **ExBlo1-I**	[Operation / Status Display / Diff-Prot / IdGH[1]]
⇩	*Module input state: External blocking1*

IdGH[1] . **ExBlo2-I**	[Operation / Status Display / Diff-Prot / IdGH[1]]
⇩	*Module input state: External blocking2*

IdGH[1] . **ExBlo TripCmd-I**	[Operation / Status Display / Diff-Prot / IdGH[1]]
⇩	*Module input state: External Blocking of the Trip Command*

9.8.5 IdGH[1]: Signals (Output States)

IdGH[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: active*

IdGH[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: Alarm*

IdGH[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: Trip*

IdGH[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: Trip Command*

IdGH[1] . **ExBlo**	[Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: External Blocking*

IdGH[1] . **Blo TripCmd**	[Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: Trip Command blocked*

IdGH[1] . **ExBlo TripCmd**	[Operation / Status Display / Diff-Prot / IdGH[1]]
⇧	*Signal: External Blocking of the Trip Command*

9.9 IH2[1] ... IH2[2]

Module Inrush

9.9.1 IH2[1]: Device Planning Parameters

IH2[1] . **Mode**	[Device planning]	
use	“-”, use ↳ Device planning.	S.3
	Module Inrush, general operation mode	

9.9.2 IH2[1]: Global Parameters

IH2[1] . **CT Winding Side**	[Protection Para / Global Prot Para / I-Prot / IH2[1]]	
W1	W1 ↳ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

IH2[1] . **ExBlo1** IH2[1] . **ExBlo2**	[Protection Para / Global Prot Para / I-Prot / IH2[1]]	
“-”	“-” ... Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.9.3 IH2[1]: Setting Group Parameters

IH2[1] . **Function**	[Protection Para / Set 1...4 / I-Prot / IH2[1]]	
inactive	inactive, active ↳ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

IH2[1] . **ExBlo Fc**	[Protection Para / Set 1...4 / I-Prot / IH2[1]]	
inactive	inactive, active └▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

IH2[1] . **IH2 / IH1**	[Protection Para / Set 1...4 / I-Prot / IH2[1]]	
15%	10% ... 40%	P.2
	Maximum permissible percentage of the 2nd harmonic of the 1st harmonic.	

IH2[1] . **block mode**	[Protection Para / Set 1...4 / I-Prot / IH2[1]]	
1-ph Blo	1-ph Blo, 3-ph Blo └▷ block mode.	P.2
	1-ph Blo: If an inrush is detected in one phase, the corresponding phase of those modules will be blocked, where inrush blocking is set to active./3-ph Blo: If an inrush is detected in at least one phase, all three phases of those modules where inrush blocking is set to active will be blocked (cross blocking).	

9.9.4 IH2[1]: Input States

IH2[1] . **ExBlo1-I**	[Operation / Status Display / I-Prot / IH2[1]]
	Module input state: External blocking1

IH2[1] . **ExBlo2-I**	[Operation / Status Display / I-Prot / IH2[1]]
	Module input state: External blocking2

9.9.5 IH2[1]: Signals (Output States)

IH2[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / I-Prot / IH2[1]]
	Signal: active

IH2[1] . **ExBlo**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: External Blocking

IH2[1] . **Blo L1**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: Blocked L1

IH2[1] . **Blo L2**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: Blocked L2

IH2[1] . **Blo L3**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: Blocked L3

IH2[1] . **Blo IG meas**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: Blocking of the ground (earth) protection module (measured ground current)

IH2[1] . **Blo IG calc**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: Blocking of the ground (earth) protection module (calculated ground current)

IH2[1] . **3-ph Blo**	[Operation / Status Display / I-Prot / IH2[1]]
	Signal: Inrush was detected in at least one phase - trip command blocked.

9.10 I[1] . . . I[6]

Phase Overcurrent Stage

9.10.1 I[1]: Device Planning Parameters

I[1] . **Mode**	[Device planning]	
non directional	"-", non directional ⮡ I>.	S.3
	Phase Overcurrent Stage, general operation mode	

9.10.2 I[1]: Global Parameters

I[1] . **CT Winding Side**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
W1	W1, W2 ⮡ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

I[1] . **ExBlo1** I[1] . **ExBlo2**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

I[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

I[1] . **Ex rev Interl**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Sys . Internal test state ⮑ 1..n, Assignment List.	P.2
	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

I[1] . **AdaptSet 1**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ AdaptSet.	P.2
	Assignment Adaptive Parameter 1	

I[1] . **AdaptSet 2**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ AdaptSet.	P.2
	Assignment Adaptive Parameter 2	

I[1] . **AdaptSet 3**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ AdaptSet.	P.2
	Assignment Adaptive Parameter 3	

I[1] . **AdaptSet 4**	[Protection Para / Global Prot Para / I-Prot / I[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ AdaptSet.	P.2
	Assignment Adaptive Parameter 4	

9.10.3 I[1]: Setting Group Parameters

I[1] . **Function**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
active	inactive, active ⮑ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

I[1] . **ExBlo Fc**	[Protection Para / Set 1…4 / I-Prot / I[1]]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

I[1] . **Ex rev Interl Fc**	[Protection Para / Set 1…4 / I-Prot / I[1]]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "Ex rev Interl Fc = active".	

I[1] . **Blo TripCmd**	[Protection Para / Set 1…4 / I-Prot / I[1]]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

I[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / I-Prot / I[1]]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

I[1] . **Measuring method**	[Protection Para / Set 1…4 / I-Prot / I[1]]	
Fundamental	Fundamental, True RMS, I2 └⇒ Measuring method.	P.2
	Measuring method: fundamental or rms or 3rd harmonic (only generator protection relays)	

I[1] . **I>**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
1.00In ⟴ Adapt. Param.	0.02In . . . 40.00In	P.2
If the pickup value is exceeded, the module/element starts to time out to trip.		

I[1] . **Char**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
DEFT ⟴ Adapt. Param.	DEFT . . . I4T ╚▷ Char.	P.2
Characteristic		

I[1] . **t**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
1.00s ⟴ Adapt. Param.	0.00s . . . 300.00s	P.2
Tripping delay		

I[1] . **tchar**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
1 ⟴ Adapt. Param.	0.02 . . . 20.00	P.2
Time multiplier/tripping characteristic factor. The setting range depends on the selected tripping curve.		

I[1] . **Reset Mode**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
instantaneous ⟴ Adapt. Param.	instantaneous, delayed, calculated ╚▷ Reset Mode.	P.2
Reset Mode		

I[1] . **t-reset delay**	[Protection Para / Set 1...4 / I-Prot / I[1]]	
0s *Only available if:* • I[1] . Reset Mode = delayed ⟴ Adapt. Param.	0.00s . . . 60.00s	P.2
Reset delay for intermittent phase failures (INV characteristics only)		

I[1] . **IH2 Blo**	[Protection Para / Set 1…4 / I-Prot / I[1]]	
Sys . inactive Adapt. Param.	Sys . inactive, Sys . active IH2 Blo.	P.2
Blocking the trip command, if an inrush is detected.		

9.10.4 I[1]: Input States

I[1] . **ExBlo1-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: External blocking1	

I[1] . **ExBlo2-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: External blocking2	

I[1] . **ExBlo TripCmd-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: External Blocking of the Trip Command	

I[1] . **Ex rev Interl-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: External reverse interlocking	

I[1] . **AdaptSet1-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: Adaptive Parameter1	

I[1] . **AdaptSet2-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: Adaptive Parameter2	

I[1] . **AdaptSet3-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: Adaptive Parameter3	

I[1] . **AdaptSet4-I**	[Operation / Status Display / I-Prot / I[1]]
Module input state: Adaptive Parameter4	

9.10.5 I[1]: Signals (Output States)

I[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / I-Prot / I[1]]
	Signal: active

I[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / I-Prot / I[1]]
	Signal: Alarm

I[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / I-Prot / I[1]]
	Signal: Trip

I[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / I-Prot / I[1]]
	Signal: Trip Command

I[1] . **ExBlo**	[Operation / Status Display / I-Prot / I[1]]
	Signal: External Blocking

I[1] . **Ex rev Interl**	[Operation / Status Display / I-Prot / I[1]]
	Signal: External reverse Interlocking

I[1] . **Blo TripCmd**	[Operation / Status Display / I-Prot / I[1]]
	Signal: Trip Command blocked

I[1] . **ExBlo TripCmd**	[Operation / Status Display / I-Prot / I[1]]
	Signal: External Blocking of the Trip Command

I[1] . **IH2 Blo**	[Operation / Status Display / I-Prot / I[1]]
	Signal: Blocking the trip command by an inrush

I[1] . **Alarm L1**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Alarm L1*

I[1] . **Alarm L2**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Alarm L2*

I[1] . **Alarm L3**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Alarm L3*

I[1] . **Trip L1**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: General Trip Phase L1*

I[1] . **Trip L2**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: General Trip Phase L2*

I[1] . **Trip L3**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: General Trip Phase L3*

I[1] . **DefaultSet**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Default Parameter Set*

I[1] . **AdaptSet 1**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Adaptive Parameter 1*

I[1] . **AdaptSet 2**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Adaptive Parameter 2*

I[1] . **AdaptSet 3**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Adaptive Parameter 3*

I[1] . **AdaptSet 4**	[Operation / Status Display / I-Prot / I[1]]
⇧	*Signal: Adaptive Parameter 4*

9.11 IG[1] . . . IG[4]

Earth current protection - Stage

9.11.1 IG[1]: Device Planning Parameters

IG[1] . **Mode**	[Device planning]	
"-"	"-", non directional └⇒ Earth overcurrent.	S.3
	Earth current protection - Stage, general operation mode	

IG[1] . **Superv. only**	[Device planning]	
no	no, yes └⇒ yes/no.	S.3
	Earth current protection - Stage, if set to "Yes": Restriction of the function to a supervision functionality, i.e. there is no general alarm, no general trip and no trip command.	

9.11.2 IG[1]: Global Parameters

IG[1] . **CT Winding Side**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
W1	W1, W2 └⇒ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

IG[1] . **ExBlo1** IG[1] . **ExBlo2**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-"	"-" . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

IG[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-" *Only available if:* • IG[1] . Superv. only = no	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

IG[1] . **Ex rev Interl**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-"	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

IG[1] . **AdaptSet 1**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ↳ AdaptSet.	P.2
	Assignment Adaptive Parameter 1	

IG[1] . **AdaptSet 2**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ↳ AdaptSet.	P.2
	Assignment Adaptive Parameter 2	

IG[1] . **AdaptSet 3**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ↳ AdaptSet.	P.2
	Assignment Adaptive Parameter 3	

IG[1] . **AdaptSet 4**	[Protection Para / Global Prot Para / I-Prot / IG[1]]	
"-"	"-" . . . Logics . LE80.Out inverted ↳ AdaptSet.	P.2
	Assignment Adaptive Parameter 4	

9.11.3 IG[1]: Setting Group Parameters

IG[1] . **Function**	[Protection Para / Set 1…4 / I-Prot / IG[1]]	
inactive	inactive, active ⮡ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

IG[1] . **ExBlo Fc**	[Protection Para / Set 1…4 / I-Prot / IG[1]]	
inactive	inactive, active ⮡ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

IG[1] . **Ex rev Interl Fc**	[Protection Para / Set 1…4 / I-Prot / IG[1]]	
inactive	inactive, active ⮡ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "Ex rev Interl Fc = active".	

IG[1] . **Blo TripCmd**	[Protection Para / Set 1…4 / I-Prot / IG[1]]	
inactive *Only available if:* • IG[1] . Superv. only = no	inactive, active ⮡ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

IG[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / I-Prot / IG[1]]	
inactive *Only available if:* • IG[1] . Superv. only = no	inactive, active ⮡ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

IG[1] . **IG Source**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
CT W1 . calculated	If: IG[1] . CT Winding Side = W1 • CT W1 . sensitive measurement, CT W1 . measured, CT W1 . calculated If: IG[1] . CT Winding Side = W2 • CT W1 . calculated, CT W2 . measured (X4), CT W2 . sensitive measurement (X4) Measuring Channel.	P.2
	Selection if measured or calculated ground current should be used.	

IG[1] . **Measuring method**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
Fundamental	Fundamental, True RMS Measuring method.	P.2
	Measuring method: fundamental or rms or 3rd harmonic (only generator protection relays)	

IG[1] . **Meas Circuit Superv**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
Sys . inactive	Sys . inactive VTS Block.	P.2
	Activates the use of the measuring circuit supervision. In this case the module will be blocked if a measuring circuit supervision module (e.g. LOP, VTS) signals a disturbed measuring circuit (e.g. caused by a fuse failure).	

IG[1] . **IG>**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
0.02In Adapt. Param.	0.02In ... 20.00In	P.2
	If the pickup value is exceeded, the module/stage will be started.	

IG[1] . **IGs>**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
0.02In Adapt. Param.	0.002In ... 2.000In	P.2
	If the pickup value is exceeded, the module/stage will be started.	

IG[1] . **Char**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
DEFT ⟴ Adapt. Param.	DEFT . . . RXIDG ⮡ Char.	P.2
Characteristic		

IG[1] . **t**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
0.00s ⟴ Adapt. Param.	0.00s . . . 300.00s	P.2
Tripping delay		

IG[1] . **tchar**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
1 ⟴ Adapt. Param.	0.02 . . . 20.00	P.2
Time multiplier/tripping characteristic factor. The setting range depends on the selected tripping curve.		

IG[1] . **Reset Mode**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
instantaneous ⟴ Adapt. Param.	instantaneous, delayed, calculated ⮡ Reset Mode.	P.2
Reset Mode		

IG[1] . **t-reset delay**	[Protection Para / Set 1...4 / I-Prot / IG[1]]	
0.00s *Only available if:* • IG[1] . Reset Mode = delayed ⟴ Adapt. Param.	0.00s . . . 60.00s	P.2
Reset delay for intermittent phase failures (INV characteristics only)		

IG[1] . **IH2 Blo**	[Protection Para / Set 1…4 / I-Prot / IG[1]]	
Sys . inactive Adapt. Param.	Sys . inactive, Sys . active IH2 Blo.	P.2
Blocking the trip command, if an inrush is detected.		

9.11.4 IG[1]: Input States

IG[1] . **ExBlo1-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: External blocking1	

IG[1] . **ExBlo2-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: External blocking2	

IG[1] . **ExBlo TripCmd-I**	[Operation / Status Display / I-Prot / IG[1]]
Only available if: • IG[1] . Superv. only = no *Module input state: External Blocking of the Trip Command*	

IG[1] . **Ex rev Interl-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: External reverse interlocking	

IG[1] . **AdaptSet1-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: Adaptive Parameter1	

IG[1] . **AdaptSet2-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: Adaptive Parameter2	

IG[1] . **AdaptSet3-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: Adaptive Parameter3	

IG[1] . **AdaptSet4-I**	[Operation / Status Display / I-Prot / IG[1]]
Module input state: Adaptive Parameter4	

9.11.5 IG[1]: Signals (Output States)

IG[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / I-Prot / IG[1]]
⇪	*Signal: active*

IG[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / I-Prot / IG[1]]
⇪	*Signal: Alarm IG*

IG[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / I-Prot / IG[1]]
⇪	*Signal: Trip*

IG[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / I-Prot / IG[1]]
⇪	*Only available if:* • IG[1] . Superv. only = no *Signal: Trip Command*

IG[1] . **ExBlo**	[Operation / Status Display / I-Prot / IG[1]]
⇪	*Signal: External Blocking*

IG[1] . **Ex rev Interl**	[Operation / Status Display / I-Prot / IG[1]]
⇪	*Signal: External reverse Interlocking*

IG[1] . **Blo TripCmd**	[Operation / Status Display / I-Prot / IG[1]]
⇪	*Only available if:* • IG[1] . Superv. only = no *Signal: Trip Command blocked*

IG[1] . **ExBlo TripCmd**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Only available if:* • IG[1] . Superv. only = no *Signal: External Blocking of the Trip Command*

IG[1] . **IGH2 Blo**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Signal: blocked by an inrush*

IG[1] . **DefaultSet**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Signal: Default Parameter Set*

IG[1] . **AdaptSet 1**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Signal: Adaptive Parameter 1*

IG[1] . **AdaptSet 2**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Signal: Adaptive Parameter 2*

IG[1] . **AdaptSet 3**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Signal: Adaptive Parameter 3*

IG[1] . **AdaptSet 4**	[Operation / Status Display / I-Prot / IG[1]]
⇧	*Signal: Adaptive Parameter 4*

9.12 ThR

Thermal replica module

9.12.1 ThR: Device Planning Parameters

ThR . **Mode**	[Device planning]	
“-”	“-”, use ╚═▷ Device planning.	S.3
	Thermal replica module, general operation mode	

9.12.2 ThR: Global Parameters

ThR . **CT Winding Side**	[Protection Para / Global Prot Para / I-Prot / ThR]	
W1	W1, W2 ╚═▷ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

ThR . **ExBlo1** ThR . **ExBlo2**	[Protection Para / Global Prot Para / I-Prot / ThR]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

ThR . **ExBlo TripCmd**	[Protection Para / Global Prot Para / I-Prot / ThR]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.12.3 ThR: Setting Group Parameters

ThR . **Function**	[Protection Para / Set 1…4 / I-Prot / ThR]	
inactive	inactive, active └═▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

ThR . **ExBlo Fc**	[Protection Para / Set 1…4 / I-Prot / ThR]	
inactive	inactive, active └═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

ThR . **Blo TripCmd**	[Protection Para / Set 1…4 / I-Prot / ThR]	
inactive	inactive, active └═▷ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

ThR . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / I-Prot / ThR]	
inactive	inactive, active └═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

ThR . **Ib**	[Protection Para / Set 1…4 / I-Prot / ThR]	
1.00In	0.01In . . . 4.00In	P.2
	Base current: Maximum permissible thermal continuous current.	

ThR . **K**	[Protection Para / Set 1…4 / I-Prot / ThR]	
1.00	0.80 . . . 1.50	P.2
	*Overload Factor: The maximum thermal limit is defined as k*IB, the product of the overload factor and the base current.*	

ThR . **Alarm Theta**	[Protection Para / Set 1…4 / I-Prot / ThR]	
80%	50% . . . 100%	P.2
	Pickup value	

ThR . **τ-warm**	[Protection Para / Set 1…4 / I-Prot / ThR]	
10s	1s . . . 60000s	P.2
	Warming-up time constant	

ThR . **τ-cool**	[Protection Para / Set 1…4 / I-Prot / ThR]	
10s	1s . . . 60000s	P.2
	Cooling time constant	

9.12.4 ThR: Direct Controls

ThR . **Reset**	[Operation / Reset]	
inactive	inactive, active Mode.	P.1
	Reset the Thermal Replica	

9.12.5 ThR: Input States

ThR . **ExBlo1-I**	[Operation / Status Display / I-Prot / ThR]
	Module input state: External blocking1

ThR . **ExBlo2-I**	[Operation / Status Display / I-Prot / ThR]
	Module input state: External blocking2

ThR . **ExBlo TripCmd-I**	[Operation / Status Display / I-Prot / ThR]
	Module input state: External Blocking of the Trip Command

9.12.6 ThR: Signals (Output States)

ThR . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / I-Prot / ThR]
⇧	*Signal: active*

ThR . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / I-Prot / ThR]
⇧	*Signal: Alarm Thermal Overload*

ThR . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / I-Prot / ThR]
⇧	*Signal: Trip*

ThR . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / I-Prot / ThR]
⇧	*Signal: Trip Command*

ThR . **ExBlo**	[Operation / Status Display / I-Prot / ThR]
⇧	*Signal: External Blocking*

ThR . **Blo TripCmd**	[Operation / Status Display / I-Prot / ThR]
⇧	*Signal: Trip Command blocked*

ThR . **ExBlo TripCmd**	[Operation / Status Display / I-Prot / ThR]
⇧	*Signal: External Blocking of the Trip Command*

ThR . **Res Thermal Cap**	[Operation / Status Display / I-Prot / ThR]
⇧	*Signal: Resetting Thermal Replica*

9.12.7 ThR: Values

ThR . **Thermal Cap Used**	[Operation / Measured Values / ThR]
✎	*Measured value: Thermal Capacity Used*

ThR . **Time To Trip**	[Operation / Measured Values / ThR]
✎	*Measured value (calculated/measured): Remaining time until the thermal overload module will trip*

9.12.8 ThR: Statistical Values

ThR . **Thermal Cap max**	[Operation / Statistics / Max / ThR]
☑	*Thermal Capacity maximum value*

9.13 I2>[1] . . . I2>[2]

Unbalanced Load-Stage

9.13.1 I2>[1]: Device Planning Parameters

I2>[1] . **Mode**	[Device planning]	
“-”	“-”, use ⮡ Device planning.	S.3
	Unbalanced Load-Stage, general operation mode	

9.13.2 I2>[1]: Global Parameters

I2>[1] . **CT Winding Side**	[Protection Para / Global Prot Para / I-Prot / I2>[1]]	
W1	W1, W2 ⮡ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

I2>[1] . **ExBlo1** I2>[1] . **ExBlo2**	[Protection Para / Global Prot Para / I-Prot / I2>[1]]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

I2>[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / I-Prot / I2>[1]]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

I2>[1] . **CurrentBase**	[Protection Para / Global Prot Para / I-Prot / I2>[1]]	
Device Rating	Device Rating, Protected Object Rating ╚═▷ CurrentBase.	P.2
	Base Current Selection (based on Device Rating (1A/5A)/Protected Object Rating).	

9.13.3 I2>[1]: Setting Group Parameters

I2>[1] . **Function**	[Protection Para / Set 1…4 / I-Prot / I2>[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

I2>[1] . **ExBlo Fc**	[Protection Para / Set 1…4 / I-Prot / I2>[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

I2>[1] . **Blo TripCmd**	[Protection Para / Set 1…4 / I-Prot / I2>[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

I2>[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / I-Prot / I2>[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

I2>[1] . **I2>**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
0.01In *Only available if:* • I2>[1] . CurrentBase = Device Rating	0.01In ... 4.00In	P.2
	The Threshold setting defines a minimum operating current magnitude of I2 for the 46 function to operate, which ensures that the relay has a solid basis for initiating a current unbalance trip. This is a supervisory function and not a trip level.	

I2>[1] . **I2/FLA**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
0.08Ib *Only available if:* • I2>[1] . CurrentBase = Protected Object Rating	0.000Ib ... 1.000Ib	P.2
	Generator/motor unbalance current pickup value based on the full load current(FLA) (Setting from Continuous Unbalance Current Capability)	

I2>[1] . **%(I2/I1)**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
inactive	inactive, active ⮡ Mode.	P.2
	The %(I2/I1) setting is the unbalance trip pickup setting. It is defined by the ratio of negative sequence current to positive sequence current (% Unbalance=I2/I1). Phase sequence will be taken into account automatically.	

I2>[1] . **%(I2/I1)**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
20%	2% ... 40%	P.2
	The %(I2/I1) setting is the unbalance trip pickup setting. It is defined by the ratio of negative sequence current to positive sequence current (% Unbalance=I2/I1). Phase sequence will be taken into account automatically.	

I2>[1] . **Char**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
DEFT	DEFT, INV ⮡ Char.	P.2
	Characteristic	

I2>[1] . **t**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
0.00s	0.00s . . . 300.00s	P.2
	Tripping delay	

I2>[1] . **K**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
10.0s	1.00s . . . 200.00s	P.2
	This setting is the negative sequence capability constant. This value is normally provided by the generator manufacturer.	

I2>[1] . **τ-cool**	[Protection Para / Set 1...4 / I-Prot / I2>[1]]	
0.0s	0.0s . . . 60000.0s	P.2
	If the unbalanced load current falls below the pickup value, the cooling-off time is taken into account. If the unbalanced load exceeds the pickup value again, than the saved heat within the electrical equipment will lead to an accelerated trip.	

9.13.4 I2>[1]: Input States

I2>[1] . **ExBlo1-I**	[Operation / Status Display / I-Prot / I2>[1]]
	Module input state: External blocking1

I2>[1] . **ExBlo2-I**	[Operation / Status Display / I-Prot / I2>[1]]
	Module input state: External blocking2

I2>[1] . **ExBlo TripCmd-I**	[Operation / Status Display / I-Prot / I2>[1]]
	Module input state: External Blocking of the Trip Command

9.13.5 I2>[1]: Signals (Output States)

I2>[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / I-Prot / I2>[1]]
	Signal: active

I2>[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / I-Prot / I2>[1]]
	Signal: Alarm Negative Sequence

I2>[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / I-Prot / I2>[1]]
⇪	*Signal: Trip*

I2>[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / I-Prot / I2>[1]]
⇪	*Signal: Trip Command*

I2>[1] . **ExBlo**	[Operation / Status Display / I-Prot / I2>[1]]
⇪	*Signal: External Blocking*

I2>[1] . **Blo TripCmd**	[Operation / Status Display / I-Prot / I2>[1]]
⇪	*Signal: Trip Command blocked*

I2>[1] . **ExBlo TripCmd**	[Operation / Status Display / I-Prot / I2>[1]]
⇪	*Signal: External Blocking of the Trip Command*

9.14 SOTF

Switch Onto Fault - Module

9.14.1 SOTF: Device Planning Parameters

SOTF . **Mode**	[Device planning]	
“-”	“-”, use └⇒ Mode.	S.3
	general operation mode	

9.14.2 SOTF: Global Parameters

SOTF . **CT Winding Side**	[Protection Para / Global Prot Para / SOTF]	
W1	W1, W2 └⇒ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

SOTF . **Mode**	[Protection Para / Global Prot Para / SOTF]	
CB Pos	CB Pos, I<, CB Pos And I<, CB manual ON, Ext SOTF └⇒ Mode.	P.2
	general operation mode	

SOTF . **ExBlo1** SOTF . **ExBlo2**	[Protection Para / Global Prot Para / SOTF]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

SOTF . **Ex rev Interl**	[Protection Para / Global Prot Para / SOTF]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

SOTF . **Assigned SG**	[Protection Para / Global Prot Para / SOTF]	
. SG[1]	“-”, . SG[1], . SG[2] └⇒ CB List.	P.2
	Assigned Switchgear	

SOTF . **Ext SOTF**	[Protection Para / Global Prot Para / SOTF]	
“-”	“-” . . . Logics . LE80.Out inverted └⇒ 1..n, DI-LogicList.	P.2
	External Switch Onto Fault	

9.14.3 SOTF: Setting Group Parameters

SOTF . **Function**	[Protection Para / Set 1...4 / SOTF]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

SOTF . **ExBlo Fc**	[Protection Para / Set 1...4 / SOTF]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

SOTF . **Ex rev Interl Fc**	[Protection Para / Set 1…4 / SOTF]	
inactive	inactive, active └▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "Ex rev Interl Fc = active".	

SOTF . **I<**	[Protection Para / Set 1…4 / SOTF]	
0.01In	0.01In . . . 1.00In	P.2
	The CB is in the OFF Position, if the measured current is less than this parameter.	

SOTF . **t-enable**	[Protection Para / Set 1…4 / SOTF]	
2s	0.10s . . . 10.00s	P.2
	While this timer is running, and while the module is not blocked, the Switch Onto Fault Module is effective (SOTF is armed).	

9.14.4 SOTF: Input States

SOTF . **ExBlo1-I** SOTF . **ExBlo2-I**	[Operation / Status Display / SOTF]
	Module input state: External blocking

SOTF . **Ex rev Interl-I**	[Operation / Status Display / SOTF]
	Module input state: External reverse interlocking

SOTF . **Ext SOTF-I**	[Operation / Status Display / SOTF]
	Module input state: External Switch Onto Fault Alarm

9.14.5 SOTF: Signals (Output States)

SOTF . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / SOTF]
	Signal: active

SOTF . **ExBlo**	[Operation / Status Display / SOTF]
⇧	*Signal: External Blocking*

SOTF . **Ex rev Interl**	[Operation / Status Display / SOTF]
⇧	*Signal: External reverse Interlocking*

SOTF . **enabled**	[Operation / Status Display / SOTF]
⇧	*Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.*

SOTF . **I<**	[Operation / Status Display / SOTF]
⇧	*Signal: No Load Current.*

9.15 CLPU

Cold Load Pickup Module

9.15.1 CLPU: Device Planning Parameters

CLPU . **Mode**	[Device planning]	
“-”	“-”, use └⇒ Mode.	S.3
	general operation mode	

9.15.2 CLPU: Global Parameters

CLPU . **CT Winding Side**	[Protection Para / Global Prot Para / CLPU]	
W1	W1, W2 └⇒ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

CLPU . **Mode**	[Protection Para / Global Prot Para / CLPU]	
CB Pos	CB Pos, I<, CB Pos Or I<, CB Pos And I< └⇒ Mode.	P.2
	general operation mode	

CLPU . **ExBlo1** CLPU . **ExBlo2**	[Protection Para / Global Prot Para / CLPU]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

CLPU . **Ex rev Interl**	[Protection Para / Global Prot Para / CLPU]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External blocking of the module by external reverse interlocking, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

CLPU . **CB Pos Detect**	[Protection Para / Global Prot Para / CLPU]	
SG[1] . Pos	“-”, SG[1] . Pos, SG[2] . Pos └⇒ CB Manager.	P.2
	Criterion by which the Circuit Breaker Switch Position is to be detected.	

9.15.3 CLPU: Setting Group Parameters

CLPU . **Function**	[Protection Para / Set 1…4 / CLPU]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

CLPU . **ExBlo Fc**	[Protection Para / Set 1…4 / CLPU]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

CLPU . **Ex rev Interl Fc**	[Protection Para / Set 1…4 / CLPU]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "Ex rev Interl Fc = active".	

CLPU . **t-Load Off**	[Protection Para / Set 1...4 / CLPU]	
1.00s	0.00s ... 7200.00s	P.2
	Select the outage time required for a load to be considered cold. If the Pickup Timer (Delay) has run out, a Cold Load Signal will be issued.	

CLPU . **t-Max Block**	[Protection Para / Set 1...4 / CLPU]	
1.00s	0.00s ... 300.00s	P.2
	Select the amount of time for the cold load inrush. If the Release Time (Delay) has run out, a Warm Load Signal will be issued.	

CLPU . **I<**	[Protection Para / Set 1...4 / CLPU]	
0.01In	0.01In ... 1.00In	P.2
	The CB is in the OFF Position, if the measured current is less than this parameter.	

CLPU . **Threshold**	[Protection Para / Set 1...4 / CLPU]	
1.2In	0.10In ... 4.00In	P.2
	Set the load current inrush threshold.	

CLPU . **Settle Time**	[Protection Para / Set 1...4 / CLPU]	
1.00s	0.00s ... 300.00s	P.2
	Select the time for the cold load inrush	

9.15.4 CLPU: Input States

CLPU . **ExBlo1-I** CLPU . **ExBlo2-I**	[Operation / Status Display / CLPU]
	Module input state: External blocking

CLPU . **Ex rev Interl-I**	[Operation / Status Display / CLPU]
	Module input state: External reverse interlocking

9.15.5 CLPU: Signals (Output States)

CLPU . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / CLPU]
⇧	*Signal: active*

CLPU . **ExBlo**	[Operation / Status Display / CLPU]
⇧	*Signal: External Blocking*

CLPU . **Ex rev Interl**	[Operation / Status Display / CLPU]
⇧	*Signal: External reverse Interlocking*

CLPU . **enabled**	[Operation / Status Display / CLPU]
⇧	*Signal: Cold Load enabled*

CLPU . **detected**	[Operation / Status Display / CLPU]
⇧	*Signal: Cold Load detected*

CLPU . **I<**	[Operation / Status Display / CLPU]
⇧	*Signal: No Load Current.*

CLPU . **Load Inrush**	[Operation / Status Display / CLPU]
⇧	*Signal: Load Inrush*

CLPU . **Settle Time**	[Operation / Status Display / CLPU]
⇧	*Signal: Settle Time*

9.16 ExP[1] . . . ExP[4]

External Protection - Module

9.16.1 ExP[1]: Device Planning Parameters

ExP[1] . **Mode**	[Device planning]	
“-”	“-”, use ╚═▷ Device planning.	S.3
	External Protection - Module, general operation mode	

9.16.2 ExP[1]: Global Parameters

ExP[1] . **ExBlo1** ExP[1] . **ExBlo2**	[Protection Para / Global Prot Para / ExP / ExP[1]]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

ExP[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / ExP / ExP[1]]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

ExP[1] . **Alarm**	[Protection Para / Global Prot Para / ExP / ExP[1]]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	Assignment for External Alarm	

ExP[1] . **Trip**	[Protection Para / Global Prot Para / ExP / ExP[1]]	
“-”	“-” ... Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	External trip of the CB if the state of the assigned signal is true.	

9.16.3 ExP[1]: Setting Group Parameters

ExP[1] . **Function**	[Protection Para / Set 1...4 / ExP / ExP[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

ExP[1] . **ExBlo Fc**	[Protection Para / Set 1...4 / ExP / ExP[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

ExP[1] . **Blo TripCmd**	[Protection Para / Set 1...4 / ExP / ExP[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

ExP[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1...4 / ExP / ExP[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

9.16.4 ExP[1]: Input States

ExP[1] . **ExBlo1-I**	[Operation / Status Display / ExP / ExP[1]]
⇩	*Module input state: External blocking1*

ExP[1] . **ExBlo2-I**	[Operation / Status Display / ExP / ExP[1]]
⇩	*Module input state: External blocking2*

ExP[1] . **ExBlo TripCmd-I**	[Operation / Status Display / ExP / ExP[1]]
⇩	*Module input state: External Blocking of the Trip Command*

ExP[1] . **Alarm-I**	[Operation / Status Display / ExP / ExP[1]]
⇩	*Module input state: Alarm*

ExP[1] . **Trip-I**	[Operation / Status Display / ExP / ExP[1]]
⇩	*Module input state: Trip*

9.16.5 ExP[1]: Signals (Output States)

ExP[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / ExP / ExP[1]]
⇧	*Signal: active*

ExP[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / ExP / ExP[1]]
⇧	*Signal: Alarm*

ExP[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / ExP / ExP[1]]
⇧	*Signal: Trip*

ExP[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / ExP / ExP[1]]
⇧	*Signal: Trip Command*

ExP[1] . **ExBlo**	[Operation / Status Display / ExP / ExP[1]]
⇫	*Signal: External Blocking*

ExP[1] . **Blo TripCmd**	[Operation / Status Display / ExP / ExP[1]]
⇫	*Signal: Trip Command blocked*

ExP[1] . **ExBlo TripCmd**	[Operation / Status Display / ExP / ExP[1]]
⇫	*Signal: External Blocking of the Trip Command*

9.17 Ext Sudd Press

Sudden Pressure

9.17.1 Ext Sudd Press: Device Planning Parameters

Ext Sudd Press . **Mode**	[Device planning]	
“-”	“-”, use ↳ Device planning.	S.3
	External Protection - Module, general operation mode	

9.17.2 Ext Sudd Press: Global Parameters

Ext Sudd Press . **ExBlo1** Ext Sudd Press . **ExBlo2**	[Protection Para / Global Prot Para / Ext Sudd Press]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Ext Sudd Press . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Ext Sudd Press]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Ext Sudd Press . **Alarm**	[Protection Para / Global Prot Para / Ext Sudd Press]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	Assignment for External Alarm	

Ext Sudd Press . **Trip**	[Protection Para / Global Prot Para / Ext Sudd Press]	
"-"	"-" . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External trip of the CB if the state of the assigned signal is true.	

9.17.3 Ext Sudd Press: Setting Group Parameters

Ext Sudd Press . **Function**	[Protection Para / Set 1…4 / Ext Sudd Press]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

Ext Sudd Press . **ExBlo Fc**	[Protection Para / Set 1…4 / Ext Sudd Press]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

Ext Sudd Press . **Blo TripCmd**	[Protection Para / Set 1…4 / Ext Sudd Press]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

Ext Sudd Press . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / Ext Sudd Press]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

9.17.4 Ext Sudd Press: Input States

Ext Sudd Press . **ExBlo1-I**	[Operation / Status Display / Ext Sudd Press]
⇩ *Module input state: External blocking1*	

Ext Sudd Press . **ExBlo2-I**	[Operation / Status Display / Ext Sudd Press]
⇩ *Module input state: External blocking2*	

Ext Sudd Press . **ExBlo TripCmd-I**	[Operation / Status Display / Ext Sudd Press]
⇩ *Module input state: External Blocking of the Trip Command*	

Ext Sudd Press . **Alarm-I**	[Operation / Status Display / Ext Sudd Press]
⇩ *Module input state: Alarm*	

Ext Sudd Press . **Trip-I**	[Operation / Status Display / Ext Sudd Press]
⇩ *Module input state: Trip*	

9.17.5 Ext Sudd Press: Signals (Output States)

Ext Sudd Press . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Ext Sudd Press]
⇧ *Signal: active*	

Ext Sudd Press . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Ext Sudd Press]
⇧ *Signal: Alarm*	

Ext Sudd Press . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Ext Sudd Press]
⇧ *Signal: Trip*	

Ext Sudd Press . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Ext Sudd Press]
⇧ *Signal: Trip Command*	

Ext Sudd Press . **ExBlo**	[Operation / Status Display / Ext Sudd Press]
⇧	*Signal: External Blocking*

Ext Sudd Press . **Blo TripCmd**	[Operation / Status Display / Ext Sudd Press]
⇧	*Signal: Trip Command blocked*

Ext Sudd Press . **ExBlo TripCmd**	[Operation / Status Display / Ext Sudd Press]
⇧	*Signal: External Blocking of the Trip Command*

9.18 Ext Oil Temp

External Oil Temperature

9.18.1 Ext Oil Temp: Device Planning Parameters

Ext Oil Temp . **Mode**	[Device planning]	
"-"	"-", use ⮡ Device planning.	S.3
	External Protection - Module, general operation mode	

9.18.2 Ext Oil Temp: Global Parameters

Ext Oil Temp . **ExBlo1** Ext Oil Temp . **ExBlo2**	[Protection Para / Global Prot Para / Temp-Prot / Ext Oil Temp]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Ext Oil Temp . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Temp-Prot / Ext Oil Temp]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Ext Oil Temp . **Alarm**	[Protection Para / Global Prot Para / Temp-Prot / Ext Oil Temp]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	P.2
	Assignment for External Alarm	

Ext Oil Temp . **Trip**	[Protection Para / Global Prot Para / Temp-Prot / Ext Oil Temp]	
“-”	“-” . . . Sys . Internal test state └⇒ 1..n, Assignment List.	P.2
	External trip of the CB if the state of the assigned signal is true.	

9.18.3 Ext Oil Temp: Setting Group Parameters

Ext Oil Temp . **Function**	[Protection Para / Set 1…4 / Temp-Prot / Ext Oil Temp]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

Ext Oil Temp . **ExBlo Fc**	[Protection Para / Set 1…4 / Temp-Prot / Ext Oil Temp]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

Ext Oil Temp . **Blo TripCmd**	[Protection Para / Set 1…4 / Temp-Prot / Ext Oil Temp]	
inactive	inactive, active └⇒ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

Ext Oil Temp . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / Temp-Prot / Ext Oil Temp]	
inactive	inactive, active └⇒ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

9.18.4 Ext Oil Temp: Input States

Ext Oil Temp . **ExBlo1-I**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Module input state: External blocking1

Ext Oil Temp . **ExBlo2-I**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Module input state: External blocking2

Ext Oil Temp . **ExBlo TripCmd-I**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Module input state: External Blocking of the Trip Command

Ext Oil Temp . **Alarm-I**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Module input state: Alarm

Ext Oil Temp . **Trip-I**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Module input state: Trip

9.18.5 Ext Oil Temp: Signals (Output States)

Ext Oil Temp . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Signal: active

Ext Oil Temp . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Signal: Alarm

Ext Oil Temp . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Signal: Trip

Ext Oil Temp . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Temp-Prot / Ext Oil Temp]
	Signal: Trip Command

Ext Oil Temp . **ExBlo**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
⇪	*Signal: External Blocking*

Ext Oil Temp . **Blo TripCmd**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
⇪	*Signal: Trip Command blocked*

Ext Oil Temp . **ExBlo TripCmd**	[Operation / Status Display / Temp-Prot / Ext Oil Temp]
⇪	*Signal: External Blocking of the Trip Command*

9.19 Ext Temp Superv[1] . . . Ext Temp Superv[3]

External Temperature Supervision

9.19.1 Ext Temp Superv[1]: Device Planning Parameters

Ext Temp Superv[1] . **Mode**	[Device planning]	
"-"	"-", use ↳ Device planning.	S.3
	External Protection - Module, general operation mode	

9.19.2 Ext Temp Superv[1]: Global Parameters

Ext Temp Superv[1] . **ExBlo1** Ext Temp Superv[1] . **ExBlo2**	[Protection Para / Global Prot Para / Temp-Prot / Ext Temp Superv[1]]	
"-"	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Ext Temp Superv[1] . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Temp-Prot / Ext Temp Superv[1]]	
"-"	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

Ext Temp Superv[1] . **Alarm**	[Protection Para / Global Prot Para / Temp-Prot / Ext Temp Superv[1]]	
"-"	"-" . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	Assignment for External Alarm	

Ext Temp Superv[1] . **Trip**	[Protection Para / Global Prot Para / Temp-Prot / Ext Temp Superv[1]]	
“-”	“-” . . . Sys . Internal test state └─▷ 1..n, Assignment List.	P.2
	External trip of the CB if the state of the assigned signal is true.	

9.19.3 Ext Temp Superv[1]: Setting Group Parameters

Ext Temp Superv[1] . **Function**	[Protection Para / Set 1...4 / Temp-Prot / Ext Temp Superv[1]]	
inactive	inactive, active └─▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

Ext Temp Superv[1] . **ExBlo Fc**	[Protection Para / Set 1...4 / Temp-Prot / Ext Temp Superv[1]]	
inactive	inactive, active └─▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

Ext Temp Superv[1] . **Blo TripCmd**	[Protection Para / Set 1...4 / Temp-Prot / Ext Temp Superv[1]]	
inactive	inactive, active └─▷ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

Ext Temp Superv[1] . **ExBlo TripCmd Fc**	[Protection Para / Set 1...4 / Temp-Prot / Ext Temp Superv[1]]	
inactive	inactive, active └─▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

9.19.4 Ext Temp Superv[1]: Input States

Ext Temp Superv[1] . **ExBlo1-I**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Module input state: External blocking1	

Ext Temp Superv[1] . **ExBlo2-I**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Module input state: External blocking2	

Ext Temp Superv[1] . **ExBlo TripCmd-I**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Module input state: External Blocking of the Trip Command	

Ext Temp Superv[1] . **Alarm-I**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Module input state: Alarm	

Ext Temp Superv[1] . **Trip-I**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Module input state: Trip	

9.19.5 Ext Temp Superv[1]: Signals (Output States)

Ext Temp Superv[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Signal: active	

Ext Temp Superv[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Signal: Alarm	

Ext Temp Superv[1] . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
Signal: Trip	

Ext Temp Superv[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
⇫ *Signal: Trip Command*	

Ext Temp Superv[1] . **ExBlo**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
⇫ *Signal: External Blocking*	

Ext Temp Superv[1] . **Blo TripCmd**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
⇫ *Signal: Trip Command blocked*	

Ext Temp Superv[1] . **ExBlo TripCmd**	[Operation / Status Display / Temp-Prot / Ext Temp Superv[1]]
⇫ *Signal: External Blocking of the Trip Command*	

9.20 URTD

Universal Resistance Temperature Detector

9.20.1 URTD: Global Parameters

URTD . **Temperature Unit**	[Device Para / Measurem Display / General Settings]	
Celsius	Celsius, Fahrenheit ⮑ Units.	P.2
	Temperature Unit	

URTD . **Force Mode**	[Service / Test (Prot inhibit) / URTD]	
permanent	permanent, timeout ⮑ Mode.	P.2
	By means of this function the normal Output Relay States can be overwritten (forced) in case that the Relay is not in a disarmed state. The relays can be set from normal operation (relay works according to the assigned signals) to "force energized" or "force de-energized" state.	

URTD . **t-Timeout Force**	[Service / Test (Prot inhibit) / URTD]	
0.03s *Only available if:* • URTD . Force Mode = timeout	0.00s . . . 300.00s	P.2
	The Output State will be set by force for the duration of this time. That means for the duration of this time the Output Relay does not show the state of the signals that are assigned on it.	

9.20.2 URTD: Direct Controls

URTD . **Function**	[Service / Test (Prot inhibit) / URTD]	
inactive	inactive, active ⮑ active/inactive.	P.1
◉	*Permanent activation or deactivation of module/stage.*	

URTD . **Force W1 L1**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Winding Temperature*	

URTD . **Force W1 L2**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Winding Temperature*	

URTD . **Force W1 L3**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Winding Temperature*	

URTD . **Force W2 L1**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Winding Temperature*	

URTD . **Force W2 L2**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Winding Temperature*	

URTD . **Force W2 L3**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Winding Temperature*	

URTD . **Force Amb1**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Ambient Temperature*	

URTD . **Force Amb2**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Ambient Temperature*	

URTD . **Force Aux1**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Auxiliary Temperature*	

URTD . **Force Aux2**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Auxiliary Temperature*	

URTD . **Force Aux3**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Auxiliary Temperature*	

URTD . **Force Aux4**	[Service / Test (Prot inhibit) / URTD]	
0	If: URTD . Temperature Unit = Fahrenheit • 32 ... 392 If: URTD . Temperature Unit = Celsius • 0 ... 200	P.1
◉	*Force Measured Value: Auxiliary Temperature*	

9.20.3 URTD: Signals (Output States)

URTD . **W1L1 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Winding1 Phase L1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **W1L2 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Winding1 Phase L2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **W1L3 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Winding1 Phase L3, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **W2L1 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Winding2 Phase L1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **W2L2 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Winding2 Phase L2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **W2L3 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Winding2 Phase L3, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Amb1 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Ambient1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Amb2 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Ambient2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Aux1 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Auxiliary1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Aux2 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Auxiliary2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Aux3 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Auxiliary3, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Aux4 Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: Auxiliary4, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)

URTD . **Superv**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: URTD Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that all RTD channels are healthy.)

URTD . **Connection active**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: There is an active connection between the Temperature Detector (URTD) and the protective relay.

URTD . **Outs forced**	[Operation / Status Display / Temp-Prot / URTD]
	Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.

9.20.4 URTD: Values

URTD . **W1 L1**	[Operation / Measured Values / URTD]
	Measured Value: Winding Temperature

URTD . **W1 L2**	[Operation / Measured Values / URTD]
	Measured Value: Winding Temperature

URTD . **W1 L3**	[Operation / Measured Values / URTD]
	Measured Value: Winding Temperature

URTD . **W2 L1**	[Operation / Measured Values / URTD]
	Measured Value: Winding Temperature

URTD . **W2 L2**	[Operation / Measured Values / URTD]
✎	*Measured Value: Winding Temperature*

URTD . **W2 L3**	[Operation / Measured Values / URTD]
✎	*Measured Value: Winding Temperature*

URTD . **Amb1**	[Operation / Measured Values / URTD]
✎	*Measured Value: Ambient Temperature*

URTD . **Amb2**	[Operation / Measured Values / URTD]
✎	*Measured Value: Ambient Temperature*

URTD . **Aux1**	[Operation / Measured Values / URTD]
✎	*Measured Value: Auxiliary Temperature*

URTD . **Aux2**	[Operation / Measured Values / URTD]
✎	*Measured Value: Auxiliary Temperature*

URTD . **Aux3**	[Operation / Measured Values / URTD]
✎	*Measured Value: Auxiliary Temperature*

URTD . **Aux4**	[Operation / Measured Values / URTD]
✎	*Measured Value: Auxiliary Temperature*

URTD . **RTD Max**	[Operation / Measured Values / URTD]
✎	*Maximum temperature of all channels.*

9.20.5 URTD: Statistical Values

URTD . **W1 L1 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Winding Temperature Maximum Value*

URTD . **W1 L2 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Winding Temperature Maximum Value*

URTD . **W1 L3 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Winding Temperature Maximum Value*

URTD . **W2 L1 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Winding Temperature Maximum Value*

URTD . **W2 L2 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Winding Temperature Maximum Value*

URTD . **W2 L3 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Winding Temperature Maximum Value*

URTD . **Amb1 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Ambient Temperature Maximum Value*

URTD . **Amb2 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Ambient Temperature Maximum Value*

URTD . **Aux1 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Auxiliary Temperature Maximum Value*

URTD . **Aux2 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Auxiliary Temperature Maximum Value*

URTD . **Aux3 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Auxiliary Temperature Maximum Value*

URTD . **Aux4 max**	[Operation / Statistics / Max / URTD]
☑	*Measured Value: Auxiliary Temperature Maximum Value*

9.21 RTD

Temperature Protection Module

9.21.1 RTD: Device Planning Parameters

RTD . **Mode**	[Device planning]	
“-”	“-”, use ↳ Device planning.	S.3
	general operation mode	

9.21.2 RTD: Global Parameters

RTD . **ExBlo1** RTD . **ExBlo2**	[Protection Para / Global Prot Para / Temp-Prot / RTD]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

RTD . **ExBlo TripCmd**	[Protection Para / Global Prot Para / Temp-Prot / RTD]	
“-”	“-” . . . Sys . Internal test state ↳ 1..n, Assignment List.	P.2
	External blocking of the Trip Command of the module/the stage, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

RTD . **TripCmd Selection**	[Protection Para / Global Prot Para / Temp-Prot / RTD]	
Trip	Trip, Voting Trip ↳ TripCmd Selection.	P.2
	This parameter determines if the final trip of the RTD module is issued by the default way or by the voting groups.	

9.21.3 RTD: Setting Group Parameters

RTD . **Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / General Settings] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
inactive	inactive, active ↳ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

RTD . **ExBlo Fc**	[Protection Para / Set 1…4 / Temp-Prot / RTD / General Settings]	
inactive	inactive, active ↳ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

RTD . **Blo TripCmd**	[Protection Para / Set 1…4 / Temp-Prot / RTD / General Settings]	
inactive	inactive, active ↳ Mode.	P.2
	Permanent blocking of the Trip Command of the module/stage.	

RTD . **ExBlo TripCmd Fc**	[Protection Para / Set 1…4 / Temp-Prot / RTD / General Settings]	
inactive	inactive, active ↳ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo TripCmd Fc=active".	

RTD . **W1L1 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L1]	
active	inactive, active └⇒ Mode.	P.2
	Winding1 Phase L1 Alarm Function	

RTD . **W1L1 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L1]	
active	inactive, active └⇒ Mode.	P.2
	Winding1 Phase L1 Trip Function	

RTD . **W1L1 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L1]	
80°C	0°C . . . 200°C	P.2
	Winding1 Phase L1 Threshold for Temperature Alarm	

RTD . **W1L1 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L1]	
1min	0min . . . 360min	P.2
	Winding1 Phase L1 If this time is expired a Temperature Alarm will be generated.	

RTD . **W1L1 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L1]	
100°C	0°C . . . 200°C	P.2
	Winding1 Phase L1 Threshold for Temperature Trip	

RTD . **W1L2 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L2]	
active	inactive, active └⇒ Mode.	P.2
	Winding1 Phase L2 Alarm Function	

RTD . **W1L2 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L2]	
active	inactive, active └⇒ Mode.	P.2
	Winding1 Phase L2 Trip Function	

RTD . **W1L2 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L2]	
80°C	0°C ... 200°C	P.2
	Winding1 Phase L2 Threshold for Temperature Alarm	

RTD . **W1L2 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L2]	
1min	0min ... 360min	P.2
	Winding1 Phase L2 If this time is expired a Temperature Alarm will be generated.	

RTD . **W1L2 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L2]	
100°C	0°C ... 200°C	P.2
	Winding1 Phase L2 Threshold for Temperature Trip	

RTD . **W1L3 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L3]	
active	inactive, active ⮡ Mode.	P.2
	Winding1 Phase L3 Alarm Function	

RTD . **W1L3 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L3]	
active	inactive, active ⮡ Mode.	P.2
	Winding1 Phase L3 Trip Function	

RTD . **W1L3 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L3]	
80°C	0°C ... 200°C	P.2
	Winding1 Phase L3 Threshold for Temperature Alarm	

RTD . **W1L3 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L3]	
1min	0min ... 360min	P.2
	Winding1 Phase L3 If this time is expired a Temperature Alarm will be generated.	

RTD . **W1L3 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W1L3]	
100°C	0°C ... 200°C	P.2
	Winding1 Phase L3 Threshold for Temperature Trip	

RTD . **W2L1 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L1]	
active	inactive, active ╘═▷ Mode.	P.2
	Winding2 Phase L1 Alarm Function	

RTD . **W2L1 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L1]	
active	inactive, active ╘═▷ Mode.	P.2
	Winding2 Phase L1 Trip Function	

RTD . **W2L1 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L1]	
80°C	0°C ... 200°C	P.2
	Winding2 Phase L1 Threshold for Temperature Alarm	

RTD . **W2L1 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L1]	
1min	0min ... 360min	P.2
	Winding2 Phase L1 If this time is expired a Temperature Alarm will be generated.	

RTD . **W2L1 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L1]	
100°C	0°C ... 200°C	P.2
	Winding2 Phase L1 Threshold for Temperature Trip	

RTD . **W2L2 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L2]	
active	inactive, active ╘═▷ Mode.	P.2
	Winding2 Phase L2 Alarm Function	

RTD . **W2L2 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L2]	
active	inactive, active ╘═▷ Mode.	P.2
	Winding2 Phase L2 Trip Function	

RTD . **W2L2 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L2]	
80°C	0°C ... 200°C	P.2
	Winding2 Phase L2 Threshold for Temperature Alarm	

RTD . **W2L2 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L2]	
1min	0min ... 360min	P.2
	Winding2 Phase L2 If this time is expired a Temperature Alarm will be generated.	

RTD . **W2L2 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L2]	
100°C	0°C ... 200°C	P.2
	Winding2 Phase L2 Threshold for Temperature Trip	

RTD . **W2L3 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L3]	
active	inactive, active ⮡ Mode.	P.2
	Winding2 Phase L3 Alarm Function	

RTD . **W2L3 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L3]	
active	inactive, active ⮡ Mode.	P.2
	Winding2 Phase L3 Trip Function	

RTD . **W2L3 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L3]	
80°C	0°C ... 200°C	P.2
	Winding2 Phase L3 Threshold for Temperature Alarm	

RTD . **W2L3 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L3]	
1min	0min ... 360min	P.2
	Winding2 Phase L3 If this time is expired a Temperature Alarm will be generated.	

RTD . **W2L3 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / W2L3]	
100°C	0°C ... 200°C	P.2
	Winding2 Phase L3 Threshold for Temperature Trip	

RTD . **Amb1 Alarm Function** RTD . **Amb2 Alarm Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 2]	
active	inactive, active ↳ Mode.	P.2
	Ambient Alarm Function	

RTD . **Amb1 Trip Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 1]	
active	inactive, active ↳ Mode.	P.2
	Ambient Trip Function	

RTD . **Amb1 Alarm** RTD . **Amb2 Alarm** RTD . **Amb Alarm**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 2] [Protection Para / Set 1…4 / Temp-Prot / RTD / Amb Group]	
80°C	0°C . . . 200°C	P.2
	Ambient Threshold for Temperature Alarm	

RTD . **Amb1 t-Delay** RTD . **Amb2 t-Delay** RTD . **Amb t-Delay**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 2] [Protection Para / Set 1…4 / Temp-Prot / RTD / Amb Group]	
1min	0min . . . 360min	P.2
	Ambient If this time is expired a Temperature Alarm will be generated.	

RTD . **Amb1 Trip** RTD . **Amb2 Trip**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Amb 2]	
100°C	0°C . . . 200°C	P.2
	Ambient Threshold for Temperature Trip	

RTD . **Amb2 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Amb 2]	
active	inactive, active ╚▷ Mode.	P.2
Ambient Trip Function		

RTD . **Aux1Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Aux 1]	
active	inactive, active ╚▷ Mode.	P.2
Auxiliary Alarm Function		

RTD . **Aux1Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Aux 1]	
active	inactive, active ╚▷ Mode.	P.2
Auxiliary Trip Function		

RTD . **Aux1 Alarm** ... RTD . **Aux Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Aux 1] ... [Protection Para / Set 1...4 / Temp-Prot / RTD / Aux Group]	
80°C	0°C ... 200°C	P.2
Auxiliary Threshold for Temperature Alarm		

RTD . **Aux1 t-Delay** ... RTD . **Aux t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Aux 1] ... [Protection Para / Set 1...4 / Temp-Prot / RTD / Aux Group]	
1min	0min ... 360min	P.2
Auxiliary If this time is expired a Temperature Alarm will be generated.		

RTD . **Aux1 Trip** RTD . **Aux2 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Aux 1] [Protection Para / Set 1...4 / Temp-Prot / RTD / Aux 2]	
80°C	0°C ... 200°C	P.2
Auxiliary Threshold for Temperature Trip		

RTD . **Aux2 Alarm Function** RTD . **Aux3 Alarm Function** RTD . **Aux4 Alarm Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 2] [Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 3] [Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 4]	
active	inactive, active ⮡ Mode.	P.2
Auxiliary Alarm Function		

RTD . **Aux2 Trip Function** RTD . **Aux3 Trip Function** RTD . **Aux4 Trip Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 2] [Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 3] [Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 4]	
active	inactive, active ⮡ Mode.	P.2
Auxiliary Trip Function		

RTD . **Aux3 Trip** RTD . **Aux4 Trip** RTD . **Aux Trip**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 3] [Protection Para / Set 1…4 / Temp-Prot / RTD / Aux 4] [Protection Para / Set 1…4 / Temp-Prot / RTD / Aux Group]	
100°C	0°C . . . 200°C	P.2
Auxiliary Threshold for Temperature Trip		

RTD . **Windg W1 Alarm Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Windg W1 Group]	
inactive	inactive, active ⮡ Mode.	P.2
Winding W1 Alarm Function		

RTD . **Windg W1 Trip Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Windg W1 Group]	
inactive	inactive, active ⮡ Mode.	P.2
Winding W1 Trip Function		

RTD . **Windg W1 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W1 Group]	
80°C	0°C . . . 200°C	P.2
	Winding W1 Threshold for Temperature Alarm	

RTD . **Windg W1 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W1 Group]	
1min	0min . . . 360min	P.2
	Winding W1 If this time is expired a Temperature Alarm will be generated.	

RTD . **Windg W1 Trip**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W1 Group]	
100°C	0°C . . . 200°C	P.2
	Winding W1 Threshold for Temperature Trip	

RTD . **Windg W2 Alarm Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W2 Group]	
inactive	inactive, active ⮡ Mode.	P.2
	Winding W2 Alarm Function	

RTD . **Windg W2 Trip Function**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W2 Group]	
inactive	inactive, active ⮡ Mode.	P.2
	Winding W2 Trip Function	

RTD . **Windg W2 Alarm**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W2 Group]	
80°C	0°C . . . 200°C	P.2
	Winding W2 Threshold for Temperature Alarm	

RTD . **Windg W2 t-Delay**	[Protection Para / Set 1...4 / Temp-Prot / RTD / Windg W2 Group]	
1min	0min . . . 360min	P.2
	Winding W2 If this time is expired a Temperature Alarm will be generated.	

RTD . **Windg W2 Trip**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Windg W2 Group]	
100°C	0°C ... 200°C	P.2
	Winding W2 Threshold for Temperature Trip	

RTD . **Amb Alarm Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb Group]	
inactive	inactive, active ⮑ Mode.	P.2
	Ambient Alarm Function	

RTD . **Amb Trip Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb Group]	
inactive	inactive, active ⮑ Mode.	P.2
	Ambient Trip Function	

RTD . **Amb Trip**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Amb Group]	
80°C	0°C ... 200°C	P.2
	Ambient Threshold for Temperature Trip	

RTD . **Aux Alarm Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Aux Group]	
inactive	inactive, active ⮑ Mode.	P.2
	Auxiliary Alarm Function	

RTD . **Aux Trip Function**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Aux Group]	
inactive	inactive, active ⮑ Mode.	P.2
	Auxiliary Trip Function	

RTD . **Voting 1** RTD . **Voting 2**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
1	1 . . . 12	P.2
	Voting: This parameter defines how many of the selected channels must be over its threshold level for getting a voting trip	

RTD . **W1L1**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
	Winding1 Phase L1	

RTD . **W1L2**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
	Winding1 Phase L2	

RTD . **W1L3**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
	Winding1 Phase L3	

RTD . **W2L1**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
	Winding2 Phase L1	

RTD . **W2L2**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Winding2 Phase L2		

RTD . **W2L3**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Winding2 Phase L3		

RTD . **Amb 1**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Ambient 1		

RTD . **Amb 2**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Ambient 2		

RTD . **Aux 1**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Auxiliary 1		

RTD . **Aux 2**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Auxiliary 2		

RTD . **Aux 3**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Auxiliary 3		

RTD . **Aux 4**	[Protection Para / Set 1…4 / Temp-Prot / RTD / Voting1] [Protection Para / Set 1…4 / Temp-Prot / RTD / Voting2]	
no	no, yes ⮡ yes/no.	P.2
Auxiliary 4		

9.21.4 RTD: Input States

RTD . **ExBlo1-I**	[Operation / Status Display / Temp-Prot / RTD / General]
Module input state: External blocking1	

RTD . **ExBlo2-I**	[Operation / Status Display / Temp-Prot / RTD / General]
Module input state: External blocking2	

RTD . **ExBlo TripCmd-I**	[Operation / Status Display / Temp-Prot / RTD / General]
Module input state: External Blocking of the Trip Command	

9.21.5 RTD: Signals (Output States)

RTD . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Temp-Prot / RTD / General]
⇧	*Signal: active*

RTD . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / General]
⇧	*Alarm RTD Temperature Protection*

RTD . **W1L1 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W1L1]
⇧	*Winding1 Phase L1 Alarm RTD Temperature Protection*

RTD . **W1L1 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W1L1]
⇧	*Winding1 Phase L1 Timeout Alarm*

RTD . **W1L2 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W1L2]
⇧	*Winding1 Phase L2 Alarm RTD Temperature Protection*

RTD . **W1L2 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W1L2]
⇧	*Winding1 Phase L2 Timeout Alarm*

RTD . **W1L3 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W1L3]
⇧	*Winding1 Phase L3 Alarm RTD Temperature Protection*

RTD . **W1L3 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W1L3]
⇧	*Winding1 Phase L3 Timeout Alarm*

RTD . **W2L1 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W2L1]
⇧	*Winding2 Phase L1 Alarm RTD Temperature Protection*

RTD . **W2L1 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W2L1]
⇧	*Winding2 Phase L1 Timeout Alarm*

RTD . **W2L2 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W2L2]
⇧	*Winding2 Phase L2 Alarm RTD Temperature Protection*

RTD . **W2L2 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W2L2]
⇧	*Winding2 Phase L2 Timeout Alarm*

RTD . **W2L3 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W2L3]
⇧	*Winding2 Phase L3 Alarm RTD Temperature Protection*

RTD . **W2L3 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / W2L3]
⇧	*Winding2 Phase L3 Timeout Alarm*

RTD . **Amb 1 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Amb 1]
⇧	*Ambient 1 Alarm RTD Temperature Protection*

RTD . **Amb 1 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Amb 1]
⇧	*Ambient 1 Timeout Alarm*

RTD . **Amb 2 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Amb 2]
⇧	*Ambient 2 Alarm RTD Temperature Protection*

RTD . **Amb 2 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Amb 2]
⇧	*Ambient 2 Timeout Alarm*

RTD . **Aux 1 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 1]
⇧	*Auxiliary 1 Alarm RTD Temperature Protection*

RTD . **Aux 1 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 1]
⇧	*Auxiliary 1 Timeout Alarm*

RTD . **Aux 2 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 2]
⇧	*Auxiliary 2 Alarm RTD Temperature Protection*

RTD . **Aux 2 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 2]
⇧	*Auxiliary 2 Timeout Alarm*

RTD . **Aux 3 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 3]
⇧	*Auxiliary 3 Alarm RTD Temperature Protection*

RTD . **Aux 3 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 3]
⇧	*Auxiliary 3 Timeout Alarm*

RTD . **Aux4 Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 4]
⇧	*Auxiliary 4 Alarm RTD Temperature Protection*

RTD . **Aux4 Timeout Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux 4]
⇧	*Auxiliary 4 Timeout Alarm*

RTD . **Alarm WD W1 Group**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Windg W1 Group]
⇧	*Alarm all Windings of group W1*

RTD . **TimeoutAlmWDW1Grp**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Windg W1 Group]
⇧	*Timeout Alarm of group W1*

RTD . **Alarm WD W2 Group**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Windg W2 Group]
⇧	*Alarm all Windings of group W2*

RTD . **TimeoutAlmWDW2Grp**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Windg W2 Group]
⇧	*Timeout Alarm of group W2*

RTD . **Alarm Amb Group**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Amb Group]
⇫	*Alarm all Windings of group Ambient*

RTD . **TimeoutAlmAmbGrp**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Amb Group]
⇫	*Timeout Alarm of group Ambient*

RTD . **Alarm Aux Group**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux Group]
⇫	*Alarm Auxiliary Group*

RTD . **TimeoutAlmAuxGrp**	[Operation / Status Display / Alarms] [Operation / Status Display / Temp-Prot / RTD / Aux Group]
⇫	*Timeout Alarm Auxiliary Group*

RTD . **Trip**	[Operation / Status Display / Trips] [Operation / Status Display / Temp-Prot / RTD / General]
⇫	*Signal: Trip*

RTD . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Temp-Prot / RTD / General]
⇫	*Signal: Trip Command*

RTD . **ExBlo**	[Operation / Status Display / Temp-Prot / RTD / General]
⇫	*Signal: External Blocking*

RTD . **Blo TripCmd**	[Operation / Status Display / Temp-Prot / RTD / General]
⇫	*Signal: Trip Command blocked*

RTD . **ExBlo TripCmd**	[Operation / Status Display / Temp-Prot / RTD / General]
⇫	*Signal: External Blocking of the Trip Command*

RTD . **Timeout Alarm**	[Operation / Status Display / Temp-Prot / RTD / General]
	Alarm timeout expired

RTD . **W1L1 Trip**	[Operation / Status Display / Temp-Prot / RTD / W1L1]
	Winding1 Phase L1 Signal: Trip

RTD . **W1L1 Invalid**	[Operation / Status Display / Temp-Prot / RTD / W1L1]
	Winding1 Phase L1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)

RTD . **W1L2 Trip**	[Operation / Status Display / Temp-Prot / RTD / W1L2]
	Winding1 Phase L2 Signal: Trip

RTD . **W1L2 Invalid**	[Operation / Status Display / Temp-Prot / RTD / W1L2]
	Winding1 Phase L2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)

RTD . **W1L3 Trip**	[Operation / Status Display / Temp-Prot / RTD / W1L3]
	Winding1 Phase L3 Signal: Trip

RTD . **W1L3 Invalid**	[Operation / Status Display / Temp-Prot / RTD / W1L3]
	Winding1 Phase L3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)

RTD . **W2L1 Trip**	[Operation / Status Display / Temp-Prot / RTD / W2L1]
	Winding2 Phase L1 Signal: Trip

RTD . **W2L1 Invalid**	[Operation / Status Display / Temp-Prot / RTD / W2L1]
	Winding2 Phase L1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)

RTD . **W2L2 Trip**	[Operation / Status Display / Temp-Prot / RTD / W2L2]
	Winding2 Phase L2 Signal: Trip

RTD . **W2L2 Invalid**	[Operation / Status Display / Temp-Prot / RTD / W2L2]
	Winding2 Phase L2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)

RTD . **W2L3 Trip**	[Operation / Status Display / Temp-Prot / RTD / W2L3]
⇧	*Winding2 Phase L3 Signal: Trip*

RTD . **W2L3 Invalid**	[Operation / Status Display / Temp-Prot / RTD / W2L3]
⇧	*Winding2 Phase L3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Amb 1 Trip**	[Operation / Status Display / Temp-Prot / RTD / Amb 1]
⇧	*Ambient 1 Signal: Trip*

RTD . **Amb 1 Invalid**	[Operation / Status Display / Temp-Prot / RTD / Amb 1]
⇧	*Ambient 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Amb 2 Trip**	[Operation / Status Display / Temp-Prot / RTD / Amb 2]
⇧	*Ambient 2 Signal: Trip*

RTD . **Amb 2 Invalid**	[Operation / Status Display / Temp-Prot / RTD / Amb 2]
⇧	*Ambient 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Aux 1 Trip**	[Operation / Status Display / Temp-Prot / RTD / Aux 1]
⇧	*Auxiliary 1 Signal: Trip*

RTD . **Aux 1 Invalid**	[Operation / Status Display / Temp-Prot / RTD / Aux 1]
⇧	*Auxiliary 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Aux 2 Trip**	[Operation / Status Display / Temp-Prot / RTD / Aux 2]
⇧	*Auxiliary 2 Signal: Trip*

RTD . **Aux 2 Invalid**	[Operation / Status Display / Temp-Prot / RTD / Aux 2]
⇧	*Auxiliary 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Aux 3 Trip**	[Operation / Status Display / Temp-Prot / RTD / Aux 3]
⇧	*Auxiliary 3 Signal: Trip*

RTD . **Aux 3 Invalid**	[Operation / Status Display / Temp-Prot / RTD / Aux 3]
⇫	*Auxiliary 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Aux4 Trip**	[Operation / Status Display / Temp-Prot / RTD / Aux 4]
⇫	*Auxiliary 4 Signal: Trip*

RTD . **Aux4 Invalid**	[Operation / Status Display / Temp-Prot / RTD / Aux 4]
⇫	*Auxiliary 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Trip WD W1 Group**	[Operation / Status Display / Temp-Prot / RTD / Windg W1 Group]
⇫	*Trip all Windings of group W1*

RTD . **Windg W1 Group Invalid**	[Operation / Status Display / Temp-Prot / RTD / Windg W1 Group]
⇫	*Winding W1 Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Trip WD W2 Group**	[Operation / Status Display / Temp-Prot / RTD / Windg W2 Group]
⇫	*Trip all Windings of group W2*

RTD . **Windg W2 Group Invalid**	[Operation / Status Display / Temp-Prot / RTD / Windg W2 Group]
⇫	*Winding W2 Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Trip Amb Group**	[Operation / Status Display / Temp-Prot / RTD / Amb Group]
⇫	*Trip all Windings of group Ambient*

RTD . **Amb Group Invalid**	[Operation / Status Display / Temp-Prot / RTD / Amb Group]
⇫	*Ambient Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*

RTD . **Trip Aux Group**	[Operation / Status Display / Temp-Prot / RTD / Aux Group]
⇫	*Trip Auxiliary Group*

RTD . **AuxGrpInvalid**	[Operation / Status Display / Temp-Prot / RTD / Aux Group]
Invalid Auxiliary Group	

RTD . **Trip Any Group**	[Operation / Status Display / Temp-Prot / RTD / Any Group]
Trip Any Group	

RTD . **Alarm Any Group**	[Operation / Status Display / Temp-Prot / RTD / Any Group]
Alarm Any Group	

RTD . **TimeoutAlmAnyGrp**	[Operation / Status Display / Temp-Prot / RTD / Any Group]
Timeout Alarm Any Group	

RTD . **Trip Group 1**	[Operation / Status Display / Temp-Prot / RTD / Voting]
Trip Group 1	

RTD . **Trip Group 2**	[Operation / Status Display / Temp-Prot / RTD / Voting]
Trip Group 2	

9.21.6 RTD: Counters

RTD . **Hottest WD W1**	[Operation / Measured Values / URTD]
Hottest winding on side W1	

RTD . **Hottest WD W2**	[Operation / Measured Values / URTD]
Hottest winding on side W2	

RTD . **Hottest Amb**	[Operation / Measured Values / URTD]
Hottest Ambient Temperature	

RTD . **Hottest Aux Temp**	[Operation / Measured Values / URTD]
Hottest Auxiliary temperature in degrees C.	

9.22 Supervision

9.22.1 CBF[1] . . . CBF[2]

Circuit breaker failure protection module

9.22.1.1 CBF[1]: Device Planning Parameters

CBF[1] . **Mode**	[Device planning]	
“-”	“-”, use ⮡ Device planning.	S.3
	Module Circuit Breaker Failure protection, general operation mode	

9.22.1.2 CBF[1]: Global Parameters

CBF[1] . **Scheme**	[Protection Para / Global Prot Para / Supervision / CBF[1]]	
50BF	If: CBF[1] . CB = “-” • 50BF If: CBF[1] . CB ≠ “-” • 50BF, CB Pos, 50BF and CB Pos ⮡ Scheme.	P.2
	Scheme	

CBF[1] . **CT Winding Side**	[Protection Para / Global Prot Para / Supervision / CBF[1]]	
W1	W1, W2 ⮡ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

CBF[1] . **CB**	[Protection Para / Global Prot Para / Supervision / CBF[1]]	
SG[1] .	“-”, SG[1] . , SG[2] . ⮡ CB List.	P.2
	Selection of the Circuit Breaker to be monitored.	

CBF[1] . **ExBlo1** CBF[1] . **ExBlo2**	[Protection Para / Global Prot Para / Supervision / CBF[1]]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
✎	*External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.*	

CBF[1] . **Trigger**	[Protection Para / Global Prot Para / Supervision / CBF[1]]	
All Trips *Only available if:* • CBF[1] . CB ≠ “-”	- . -, All Trips, External Trips, Current Trips ╚═▷ Trigger.	P.2
✎	*Determining the trigger mode for the Breaker Failure.*	

CBF[1] . **Trigger1** CBF[1] . **Trigger2** CBF[1] . **Trigger3**	[Protection Para / Global Prot Para / Supervision / CBF[1]]	
“-”	“-” . . . Logics . LE80.Out inverted ╚═▷ Trigger.	P.2
✎	*Trigger that will start the CBF*	

9.22.1.3 CBF[1]: Setting Group Parameters

CBF[1] . **Function**	[Protection Para / Set 1…4 / Supervision / CBF[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
✎	*Permanent activation or deactivation of module/stage.*	

CBF[1] . **ExBlo Fc**	[Protection Para / Set 1…4 / Supervision / CBF[1]]	
inactive	inactive, active ╚═▷ active/inactive.	P.2
✎	*Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".*	

CBF[1] . **I-CBF >**	[Protection Para / Set 1...4 / Supervision / CBF[1]]	
0.02In	0.02In ... 4.00In	P.2
	Breaker Failure Alarm will be initiated if this threshold is still exceeded after the timer has expired (50 BF).	

CBF[1] . **t-CBF**	[Protection Para / Set 1...4 / Supervision / CBF[1]]	
0.20s	0.00s ... 10.00s	P.2
	If the delay time is expired, an CBF alarm is given out.	

9.22.1.4 CBF[1]: Direct Controls

CBF[1] . **Res Lockout**	[Operation / Reset]	
inactive	inactive, active Mode.	P.1
	Reset Lockout	

9.22.1.5 CBF[1]: Input States

CBF[1] . **ExBlo1-I**	[Operation / Status Display / Supervision / CBF[1]]
	Module input state: External blocking1

CBF[1] . **ExBlo2-I**	[Operation / Status Display / Supervision / CBF[1]]
	Module input state: External blocking2

CBF[1] . **Trigger1-I** CBF[1] . **Trigger2-I** CBF[1] . **Trigger3-I**	[Operation / Status Display / Supervision / CBF[1]]
	Module Input: Trigger that will start the CBF

9.22.1.6 CBF[1]: Signals (Output States)

CBF[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Supervision / CBF[1]]
	Signal: active

CBF[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Supervision / CBF[1]]
⇧ *Signal: Circuit Breaker Failure*	

CBF[1] . **ExBlo**	[Operation / Status Display / Supervision / CBF[1]]
⇧ *Signal: External Blocking*	

CBF[1] . **Waiting for Trigger**	[Operation / Status Display / Supervision / CBF[1]]
⇧ *Waiting for Trigger*	

CBF[1] . **running**	[Operation / Status Display / Supervision / CBF[1]]
⇧ *Signal: CBF-Module started*	

CBF[1] . **Lockout**	[Operation / Status Display / Supervision / CBF[1]]
⇧ *Signal: Lockout*	

CBF[1] . **Res Lockout**	[Operation / Status Display / Supervision / CBF[1]]
⇧ *Signal: Reset Lockout*	

9.22.2 TCS[1] . . . TCS[2]

Trip circuit supervision

9.22.2.1 TCS[1]: Device Planning Parameters

TCS[1] . **Mode**	[Device planning]	
"-"	"-", use ⮱▷ Device planning.	S.3
	Trip circuit supervision, general operation mode	

9.22.2.2 TCS[1]: Global Parameters

TCS[1] . **CB Pos Detect**	[Protection Para / Global Prot Para / Supervision / TCS[1]]	
SG[1] . Pos	"-", SG[1] . Pos, SG[2] . Pos ⮱▷ CB Manager.	P.2
	Criterion by which the Circuit Breaker Switch Position is to be detected.	

TCS[1] . **Mode**	[Protection Para / Global Prot Para / Supervision / TCS[1]]	
Closed *Only available if:* • TCS[1] . CB Pos Detect ≠ "-"	Closed, Either ⮱▷ Mode.	P.2
	Select if trip circuit is going to be monitored when the breaker is closed or when the breaker is either open or close.	

TCS[1] . **Input 1**	[Protection Para / Global Prot Para / Supervision / TCS[1]]	
"-" *Only available if:* • TCS[1] . CB Pos Detect ≠ "-"	"-" . . . DI Slot X6 . DI 8 ⮱▷ 1..n, Dig Inputs.	P.2
	Select the input configured to monitor the trip coil when the breaker is closed.	

TCS[1] . **Input 2**	[Protection Para / Global Prot Para / Supervision / TCS[1]]	
“-” *Only available if:* • TCS[1] . CB Pos Detect ≠ “-” • TCS[1] . Mode = Either	“-” . . . DI Slot X6 . DI 8 ╚▷ 1..n, Dig Inputs.	P.2
	Select the input configured to monitor the trip coil when the breaker is open. Only available if Mode set to “Either”.	

TCS[1] . **ExBlo1** TCS[1] . **ExBlo2**	[Protection Para / Global Prot Para / Supervision / TCS[1]]	
“-”	“-” . . . Sys . Internal test state ╚▷ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.22.2.3 TCS[1]: Setting Group Parameters

TCS[1] . **Function**	[Protection Para / Set 1...4 / Supervision / TCS[1]]	
inactive	inactive, active ╚▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

TCS[1] . **ExBlo Fc**	[Protection Para / Set 1...4 / Supervision / TCS[1]]	
inactive	inactive, active ╚▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

TCS[1] . **t-TCS**	[Protection Para / Set 1...4 / Supervision / TCS[1]]	
0.2s	0.10s . . . 10.00s	P.2
	Delay time of the Trip Circuit Supervision	

9.22.2.4 TCS[1]: Input States

TCS[1] . **Aux ON-I**	[Operation / Status Display / Supervision / TCS[1]]
⇩	*Module Input State: Position indicator/check-back signal of the CB (52a)*

TCS[1] . **Aux OFF-I**	[Operation / Status Display / Supervision / TCS[1]]
⇩	*Module input state: Position indicator/check-back signal of the CB (52b)*

TCS[1] . **ExBlo1-I**	[Operation / Status Display / Supervision / TCS[1]]
⇩	*Module input state: External blocking1*

TCS[1] . **ExBlo2-I**	[Operation / Status Display / Supervision / TCS[1]]
⇩	*Module input state: External blocking2*

9.22.2.5 TCS[1]: Signals (Output States)

TCS[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Supervision / TCS[1]]
⇧	*Signal: active*

TCS[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Supervision / TCS[1]]
⇧	*Signal: Alarm Trip Circuit Supervision*

TCS[1] . **ExBlo**	[Operation / Status Display / Supervision / TCS[1]]
⇧	*Signal: External Blocking*

TCS[1] . **Not Possible**	[Operation / Status Display / Supervision / TCS[1]]
⇧	*Not possible because no state indicator assigned to the breaker.*

9.22.3 CTS[1] . . . CTS[2]

CT Supervision

9.22.3.1 CTS[1]: Device Planning Parameters

CTS[1] . **Mode**	[Device planning]	
“-”	“-”, use ╚═▷ Device planning.	S.3
	CT Supervision, general operation mode	

9.22.3.2 CTS[1]: Global Parameters

CTS[1] . **CT Winding Side**	[Protection Para / Global Prot Para / Supervision / CTS[1]]	
W1	W1 ╚═▷ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

CTS[1] . **ExBlo1** CTS[1] . **ExBlo2**	[Protection Para / Global Prot Para / Supervision / CTS[1]]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.	

9.22.3.3 CTS[1]: Setting Group Parameters

CTS[1] . **Function**	[Protection Para / Set 1…4 / Supervision / CTS[1]]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

CTS[1] . **ExBlo Fc**	[Protection Para / Set 1...4 / Supervision / CTS[1]]	
inactive	inactive, active └═▷ active/inactive.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

CTS[1] . **ΔI**	[Protection Para / Set 1...4 / Supervision / CTS[1]]	
0.50In	0.10In ... 1.00In	P.2
	In order to prevent faulty tripping of phase selective protection functions that use the current as tripping criterion. If the difference of the measured earth current and the calculated value I0 is higher than the pick up value ΔI, an alarm event is generated after expiring of the excitation time. In such a case, a fuse failure, a broken wire or a faulty measuring circuit can be assumed.	

CTS[1] . **Alarm delay**	[Protection Para / Set 1...4 / Supervision / CTS[1]]	
1.0s	0.0s ... 9999.0s	P.2
	Alarm delay	

CTS[1] . **Kd**	[Protection Para / Set 1...4 / Supervision / CTS[1]]	
0.00	0.00 ... 0.99	P.2
	Dynamic correction factor for the evaluation of the difference between calculated and measured earth current. This correction factor allows transformer faults, caused by higher currents, to be compensated.	

9.22.3.4 CTS[1]: Input States

CTS[1] . **ExBlo1-I**	[Operation / Status Display / Supervision / CTS[1]]
	Module input state: External blocking1

CTS[1] . **ExBlo2-I**	[Operation / Status Display / Supervision / CTS[1]]
	Module input state: External blocking2

9.22.3.5 CTS[1]: Signals (Output States)

CTS[1] . **active**	[Operation / Status Display / All Actives] [Operation / Status Display / Supervision / CTS[1]]
⇧	*Signal: active*

CTS[1] . **Alarm**	[Operation / Status Display / Alarms] [Operation / Status Display / Supervision / CTS[1]]
⇧	*Signal: Alarm Current Transformer Measuring Circuit Supervision*

CTS[1] . **ExBlo**	[Operation / Status Display / Supervision / CTS[1]]
⇧	*Signal: External Blocking*

10 Control

Control

Control Page	[Control / Control Page]
🗎	This item represents a special dialog. (See the Technical Manual for details.) *Control Page*

10.1 Ctrl: Device Planning Parameters

10.2 Ctrl: Global Parameters

Ctrl . **Res NonIL**	[Control / General Settings]	
single Operation	single Operation, timeout, permanent ╚═▷ NonIL ResetMode.	C.2
	Resetmode Non-Interlocking	

Ctrl . **Timeout NonIL**	[Control / General Settings]	
60s	2s . . . 3600s	C.2
	Timeout Non-Interlocking	

Ctrl . **NonIL Assign**	[Control / General Settings]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	C.2
	Assignment Non-Interlocking	

10.3 Ctrl: Direct Controls

Ctrl . **Switching Authority**	[Control / General Settings]	
Local	None, Local, Remote, Local and Remote ╚═▷ Switching Authority.	C.2
◉	*Switching Authority*	

Ctrl . **NonInterl**	[Control / General Settings]	
inactive	inactive, active └▷ Mode.	C.2
◉	*DC for Non-Interlocking*	

10.4 Ctrl: Input States

Ctrl . **NonInterl-I**	[Operation / Status Display / Control / General Control]
⇩	*Non-Interlocking*

10.5 Ctrl: Signals (Output States)

Ctrl . **Local**	[Operation / Status Display / Control / General Control]
⇧	*Switching Authority: Local*

Ctrl . **Remote**	[Operation / Status Display / Control / General Control]
⇧	*Switching Authority: Remote*

Ctrl . **NonInterl**	[Operation / Status Display / Control / General Control]
⇧	*Non-Interlocking is active*

Ctrl . **SG Indeterm**	[Operation / Status Display / Control / General Control]
⇧	*(At least one) Switchgear is moving (Position cannot be determined).*

Ctrl . **SG Disturb**	[Operation / Status Display / Control / General Control]
⇧	*(At least one) Switchgear is disturbed.*

Ctrl . **CES SAuthority**	[Operation / Status Display / Control / General Control]
⇧	*Command Execution Supervision: Number of rejected Commands because of missing switching authority.*

Ctrl . **CES DoubleOperating**	[Operation / Status Display / Control / General Control]
⇧	*Command Execution Supervision: Number of rejected Commands because a second switch command is in conflict with a pending one.*

10.6 Ctrl: Values

Ctrl . **Switching Authority**	[Operation / Security / Security States]	
Local	None, Local, Remote, Local and Remote ╚═▷ Switching Authority.	
✎	*Switching Authority*	

10.7 SG[1] . . . SG[2]

Switchgear

10.7.1 SG[1]: Global Parameters

SG[1] . **ON incl Prot ON**	[Control / SG / SG[1] / General Settings]	
active	inactive, active └▷ Mode.	C.2
	The ON Command includes the ON Command issued by the Protection module.	

SG[1] . **OFF incl TripCmd**	[Control / SG / SG[1] / General Settings]	
active	inactive, active └▷ Mode.	C.2
	The OFF Command includes the OFF Command issued by the Protection module.	

SG[1] . **t-Move ON**	[Control / SG / SG[1] / General Settings]	
0.1s	0.01s . . . 100.00s	C.2
	Time to move to the ON Position	

SG[1] . **t-Move OFF**	[Control / SG / SG[1] / General Settings]	
0.1s	0.01s . . . 100.00s	C.2
	Time to move to the OFF Position	

SG[1] . **t-Dwell**	[Control / SG / SG[1] / General Settings]	
0s	0s . . . 100.00s	C.2
	Dwell time	

SG[1] . **t-TripCmd**	[Control / SG / SG[1] / Trip Manager]	
0.2s	0s . . . 300.00s	P.2
	Minimum hold time of the OFF-command (circuit breaker, load break switch)	

SG[1] . **Latched**	[Control / SG / SG[1] / Trip Manager]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Defines whether the Trip Command is latched.	

SG[1] . **Ack TripCmd**	[Control / SG / SG[1] / Trip Manager]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	Ack TripCmd	

SG[1] . **Off Cmd1**	[Control / SG / SG[1] / Trip Manager]	
Id . TripCmd	“-” . . . RTD . TripCmd ╚═▷ 1..n, Trip Cmds.	P.2
	Off Command to the Circuit Breaker if the state of the assigned signal becomes true.	

SG[1] . **Off Cmd2**	[Control / SG / SG[1] / Trip Manager]	
IdH . TripCmd	“-” . . . RTD . TripCmd ╚═▷ 1..n, Trip Cmds.	P.2
	Off Command to the Circuit Breaker if the state of the assigned signal becomes true.	

SG[1] . **Off Cmd3**	[Control / SG / SG[1] / Trip Manager]	
I[1] . TripCmd	“-” . . . RTD . TripCmd ╚═▷ 1..n, Trip Cmds.	P.2
	Off Command to the Circuit Breaker if the state of the assigned signal becomes true.	

SG[1] . **Off Cmd4** . . . SG[1] . **Off Cmd40**	[Control / SG / SG[1] / Trip Manager]	
“-”	“-” . . . RTD . TripCmd ╚═▷ 1..n, Trip Cmds.	P.2
	Off Command to the Circuit Breaker if the state of the assigned signal becomes true.	

SG[1] . **Aux ON**	[Control / SG / SG[1] / Pos Indicatrs Wirng]	
DI Slot X1 . DI 1	"-" . . . Logics . LE80.Out inverted ⮑ 1..n, DI-LogicList.	C.2
	The CB is in ON-position if the state of the assigned signal is true (52a).	

SG[1] . **Aux OFF**	[Control / SG / SG[1] / Pos Indicatrs Wirng]	
DI Slot X1 . DI 2	"-" . . . Logics . LE80.Out inverted ⮑ 1..n, DI-LogicList.	C.2
	The CB is in OFF-position if the state of the assigned signal is true (52b).	

SG[1] . **Ready**	[Control / SG / SG[1] / Pos Indicatrs Wirng]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ 1..n, DI-LogicList.	C.2
	Circuit breaker is ready for operation if the state of the assigned signal is true. This digital input can be used by some protective elements (if they are available within the device) like Auto Reclosure (AR), e.g. as a trigger signal.	

SG[1] . **Removed**	[Control / SG / SG[1] / Pos Indicatrs Wirng]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ 1..n, DI-LogicList.	C.2
	The withdrawable circuit breaker is Removed	

SG[1] . **SCmd ON**	[Control / SG / SG[1] / Ex ON/OFF Cmd]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ 1..n, DI-LogicList.	C.2
	Switching ON Command, e.g. the state of the Logics or the state of the digital input	

SG[1] . **SCmd OFF**	[Control / SG / SG[1] / Ex ON/OFF Cmd]	
"-"	"-" . . . Logics . LE80.Out inverted ⮑ 1..n, DI-LogicList.	C.2
	Switching OFF Command, e.g. the state of the Logics or the state of the digital input	

SG[1] . **Interl ON1** SG[1] . **Interl ON2** SG[1] . **Interl ON3**	[Control / SG / SG[1] / Interlockings]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	C.2
	Interlocking of the ON command	

SG[1] . **Interl OFF1** SG[1] . **Interl OFF2** SG[1] . **Interl OFF3**	[Control / SG / SG[1] / Interlockings]	
“-”	“-” . . . Sys . Internal test state ⮡ 1..n, Assignment List.	C.2
	Interlocking of the OFF command	

SG[1] . **Synchronism**	[Control / SG / SG[1] / Synchron Switchg]	
“-”	“-” . . . Logics . LE80.Out inverted ⮡ 1..n, In-SyncList.	C.2
	Synchronism	

SG[1] . **t-MaxSyncSuperv**	[Control / SG / SG[1] / Synchron Switchg]	
0.2s	0s . . . 3000.00s	C.2
	Synchron-Run timer: Max. time allowed for synchronizing process after a close initiate. Only used for GENERATOR2SYSTEM working mode.	

10.7.2 SG[1]: Direct Controls

SG[1] . **Ack TripCmd**	[Operation / Acknowledge]	
inactive	inactive, active ⮡ Mode.	P.1
	Acknowledge Trip Command	

SG[1] . **Res SGwear Sl SG**	[Operation / Reset]	
inactive	inactive, active └═▷ Mode.	P.1
◉	*Resetting the slow Switchgear Alarm*	

SG[1] . **Manipulate Position**	[Control / SG / SG[1] / General Settings]	
inactive	inactive, Pos OFF, Pos ON └═▷ Manipulate Position.	C.2
◉	*WARNING! Fake Position - Manual Position Manipulation*	

10.7.3 SG[1]: Input States

SG[1] . **Interl ON1-I** SG[1] . **Interl ON2-I** SG[1] . **Interl ON3-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: Interlocking of the ON command*

SG[1] . **Interl OFF1-I** SG[1] . **Interl OFF2-I** SG[1] . **Interl OFF3-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: Interlocking of the OFF command*

SG[1] . **SCmd ON-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input*

SG[1] . **SCmd OFF-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input*

SG[1] . **Aux ON-I**	[Operation / Status Display / Control / SG[1]]
⇩	*Module Input State: Position indicator/check-back signal of the CB (52a)*

SG[1] . **Aux OFF-I**	[Operation / Status Display / Control / SG[1]]
⇩	*Module input state: Position indicator/check-back signal of the CB (52b)*

SG[1] . **Ready-I**	[Operation / Status Display / Control / SG[1]]
⇩	*Module input state: CB ready*

SG[1] . **Sys-in-Sync-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.*

SG[1] . **Removed-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: The withdrawable circuit breaker is Removed*

SG[1] . **Ack TripCmd-I**	[Operation / Status Display / Control / SG[1]]
⇩	*State of the module input: Acknowledgement Signal (for the Trip Command) Module input signal*

10.7.4 SG[1]: Signals (Output States)

SG[1] . **TripCmd**	[Operation / Status Display / TripCmds] [Operation / Status Display / Control / SG[1]]
⇧	*Signal: Trip Command*

SG[1] . **SI SingleContactInd**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.*

SG[1] . **Pos not ON**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Pos not ON*

SG[1] . **Pos ON**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Circuit Breaker is in ON-Position*

SG[1] . **Pos OFF**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Circuit Breaker is in OFF-Position*

SG[1] . **Pos Indeterm**	[Operation / Status Display / Control / SG[1]]
	Signal: Circuit Breaker is in Indeterminate Position

SG[1] . **Pos Disturb**	[Operation / Status Display / Control / SG[1]]
	Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.

SG[1] . **Pos**	[Operation / Status Display / Control / SG[1]]
	Signal: Circuit Breaker Position (0 = Indeterminate, 1 = OFF, 2 = ON, 3 = Disturbed)

SG[1] . **Ready**	[Operation / Status Display / Control / SG[1]]
	Signal: Circuit breaker is ready for operation.

SG[1] . **t-Dwell**	[Operation / Status Display / Control / SG[1]]
	Signal: Dwell time

SG[1] . **Removed**	[Operation / Status Display / Control / SG[1]]
	Signal: The withdrawable circuit breaker is Removed

SG[1] . **Interl ON**	[Operation / Status Display / Control / SG[1]]
	Signal: One or more IL_On inputs are active.

SG[1] . **Interl OFF**	[Operation / Status Display / Control / SG[1]]
	Signal: One or more IL_Off inputs are active.

SG[1] . **CES succesf**	[Operation / Status Display / Control / SG[1]]
	Signal: Command Execution Supervision: Switching command executed successfully.

SG[1] . **CES Disturbed**	[Operation / Status Display / Control / SG[1]]
	Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.

SG[1] . **CES Fail TripCmd**	[Operation / Status Display / Control / SG[1]]
	Signal: Command Execution Supervision: Command execution failed because trip command is pending.

SG[1] . **CES SwitchDir**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.*

SG[1] . **CES ON d OFF**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Command Execution Supervision: On Command during a pending OFF Command.*

SG[1] . **CES SG not ready**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Command Execution Supervision: Switchgear not ready*

SG[1] . **CES Fiel Interl**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.*

SG[1] . **CES SyncTimeout**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Command Execution Supervision: Switching Command not executed. No Synchronization signal while t-sync was running.*

SG[1] . **CES SG removed**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.*

SG[1] . **Prot ON**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: ON Command issued by the Prot module*

SG[1] . **Ack TripCmd**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Acknowledge Trip Command*

SG[1] . **ON incl Prot ON**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: The ON Command includes the ON Command issued by the Protection module.*

SG[1] . **OFF incl TripCmd**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: The OFF Command includes the OFF Command issued by the Protection module.*

SG[1] . **Position Ind manipul**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Position Indicators faked*

SG[1] . **SGwear Slow SG**	[Operation / Status Display / Control / SG[1]]
	Signal: Alarm, the circuit breaker (load-break switch) becomes slower

SG[1] . **Res SGwear Sl SG**	[Operation / Status Display / Control / SG[1]]
	Signal: Resetting the slow Switchgear Alarm

SG[1] . **ON Cmd**	[Operation / Status Display / Control / SG[1]]
	Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.

SG[1] . **OFF Cmd**	[Operation / Status Display / Control / SG[1]]
	Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.

SG[1] . **ON Cmd manual**	[Operation / Status Display / Control / SG[1]]
	Signal: ON Cmd manual

SG[1] . **OFF Cmd manual**	[Operation / Status Display / Control / SG[1]]
	Signal: OFF Cmd manual

SG[1] . **Sync ON request**	[Operation / Status Display / Control / SG[1]]
	Signal: Synchronous ON request

10.7.5 Breaker Wear

Switchgear

10.7.5.1 SG[1]: Global Parameters

SG[1] . **CT Winding Side**	[Control / SG / SG[1] / SG Wear]	
W1	W1, W2 ╘▷ CT Winding Side.	C.2
	Measuring values will be used from this winding side	

SG[1] . **Operations Alarm**	[Control / SG / SG[1] / SG Wear]	
9999	1 . . . 100000	C.2
	Maximum number of operations. If the operations counter »TripCmd Cr« exceeds this limit then the signal »Operations Alarm« is set.	

SG[1] . **Isum Intr Alarm**	[Control / SG / SG[1] / SG Wear]	
100.00kA	0.00kA . . . 2000.00kA	C.2
	Alarm, the Sum (Limit) of interrupting currents has been exceeded.	

SG[1] . **Isum Intr ph Alm**	[Control / SG / SG[1] / SG Wear]	
100.00kA	0.00kA . . . 2000.00kA	C.2
	Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.	

SG[1] . **SGwear Curve Fc**	[Control / SG / SG[1] / SG Wear]	
inactive	inactive, active ╘▷ active/inactive.	C.2
	The Circuit Breaker (load-break switch) Wear Curve defines the maximum allowed CLOSE/OPEN cycles depending on the brake currents. If the circuit breaker maintenance curve is exceeded, an alarm will be issued. The breaker maintenance curve is to be taken from the technical data sheet of the breaker manufactor. By means of the available points this curve is to be replicated.	

SG[1] . **WearLevel Alarm**	[Control / SG / SG[1] / SG Wear]	
80.00%	0.00% . . . 100.00%	C.2
	Threshold for the Alarm	

SG[1] . **WearLevel Lockout**	[Control / SG / SG[1] / SG Wear]	
95.00%	0.00% . . . 100.00%	C.2
	Threshold for the Lockout Level	

SG[1] . **Current1**	[Control / SG / SG[1] / SG Wear]	
0.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #1	

SG[1] . **Count1**	[Control / SG / SG[1] / SG Wear]	
10000	1 . . . 32000	C.2
	Open Counts Allowed #1	

SG[1] . **Current2**	[Control / SG / SG[1] / SG Wear]	
1.20kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #2	

SG[1] . **Count2**	[Control / SG / SG[1] / SG Wear]	
10000	1 . . . 32000	C.2
	Open Counts Allowed #2	

SG[1] . **Current3**	[Control / SG / SG[1] / SG Wear]	
8.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #3	

SG[1] . **Count3**	[Control / SG / SG[1] / SG Wear]	
150	1 . . . 32000	C.2
	Open Counts Allowed #3	

SG[1] . **Current4**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #4	

SG[1] . **Count4**	[Control / SG / SG[1] / SG Wear]	
12	1 . . . 32000	C.2
	Open Counts Allowed #4	

SG[1] . **Current5**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #5	

SG[1] . **Count5**	[Control / SG / SG[1] / SG Wear]	
1	1 . . . 32000	C.2
	Open Counts Allowed #5	

SG[1] . **Current6**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #6	

SG[1] . **Count6**	[Control / SG / SG[1] / SG Wear]	
1	1 . . . 32000	C.2
	Open Counts Allowed #6	

SG[1] . **Current7**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #7	

SG[1] . **Count7**	[Control / SG / SG[1] / SG Wear]	
1	1 . . . 32000	C.2
	Open Counts Allowed #7	

SG[1] . **Current8**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #8	

SG[1] . **Count8**	[Control / SG / SG[1] / SG Wear]	
1	1 . . . 32000	C.2
	Open Counts Allowed #8	

SG[1] . **Current9**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA . . . 2000.00kA	C.2
	Interrupted Current Level #9	

SG[1] . **Count9**	[Control / SG / SG[1] / SG Wear]	
1	1 ... 32000	C.2
	Open Counts Allowed #9	

SG[1] . **Current10**	[Control / SG / SG[1] / SG Wear]	
20.00kA	0.00kA ... 2000.00kA	C.2
	Interrupted Current Level #10	

SG[1] . **Count10**	[Control / SG / SG[1] / SG Wear]	
1	1 ... 32000	C.2
	Open Counts Allowed #10	

10.7.5.2 SG[1]: Direct Controls

SG[1] . **Res TripCmd Cr**	[Operation / Reset]	
inactive	inactive, active ⮡ Mode.	P.1
◉	*Resetting of the Counter: Total number of trips of the switchgear*	

SG[1] . **Res Sum trip**	[Operation / Reset]	
inactive	inactive, active ⮡ Mode.	P.1
◉	*Reset summation of the tripping currents*	

SG[1] . **Res CB OPEN capacity**	[Operation / Reset]	
inactive	inactive, active ⮡ Mode.	P.1
◉	*Reset the CB OPEN capacity.* *(Remark: A »CB OPEN capacity« value of 100% means that the circuit breaker has to be maintained.)*	

SG[1] . **Res Isum Intr per hour**	[Operation / Reset]	
inactive	inactive, active └▷ Mode.	P.1
◉	*Reset of the Sum per hour of interrupting currents.*	

10.7.5.3 SG[1]: Signals (Output States)

SG[1] . **Operations Alarm**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Too many Operations. (The operations counter »TripCmd Cr« has exceeded the limit set at »Operations Alarm«.)*

SG[1] . **Isum Intr trip: IL1**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1*

SG[1] . **Isum Intr trip: IL2**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2*

SG[1] . **Isum Intr trip: IL3**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3*

SG[1] . **Isum Intr trip**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.*

SG[1] . **Res TripCmd Cr**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Resetting of the Counter: Total number of trips of the switchgear*

SG[1] . **Res Sum trip**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Reset summation of the tripping currents*

SG[1] . **WearLevel Alarm**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Threshold for the Alarm*

SG[1] . **WearLevel Lockout**	[Operation / Status Display / Control / SG[1]]
⇧	*Signal: Threshold for the Lockout Level*

SG[1] . **Res CB OPEN capacity**	[Operation / Status Display / Control / SG[1]]
	Signal: Reset of the wear maintenance curve (i. e. of the counter for the Circuit Breaker OPEN capacity.

SG[1] . **Isum Intr ph Alm**	[Operation / Status Display / Control / SG[1]]
	Signal: Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.

SG[1] . **Res Isum Intr ph Alm**	[Operation / Status Display / Control / SG[1]]
	Signal: Reset of the Alarm, "the per hour Sum (Limit) of interrupting currents has been exceeded".

10.7.5.4 SG[1]: Values

SG[1] . **Sum trip IL1** SG[1] . **Sum trip IL2** SG[1] . **Sum trip IL3**	[Operation / Count and RevData / Control / SG[1]]
	Summation of the tripping currents phase

SG[1] . **Isum Intr per hour**	[Operation / Count and RevData / Control / SG[1]]
	Sum per hour of interrupting currents.

SG[1] . **CB OPEN capacity**	[Operation / Count and RevData / Control / SG[1]]
	Used capacity of the circuit breaker. (100% means that the circuit breaker has to be maintained.)

10.7.5.5 SG[1]: Counters

SG[1] . **TripCmd Cr**	[Operation / Count and RevData / Control / SG[1]]
#	*Counter: Total number of trips of the switchgear.*

11 System Alarms

System Alarms

11.1 SysA: Device Planning Parameters

SysA . **Mode**	[Device planning]	
“-”	“-”, use ╚═▷ Mode.	S.3
	general operation mode	

11.2 SysA: Global Parameters

SysA . **Function**	[SysA / General Settings]	
inactive	inactive, active ╚═▷ Mode.	P.2
	Permanent activation or deactivation of module/stage.	

SysA . **ExBlo Fc**	[SysA / General Settings]	
“-”	“-” . . . Sys . Internal test state ╚═▷ 1..n, Assignment List.	P.2
	Activate (allow) or inactivate (disallow) blocking of the module/stage. This parameter is only effective if a signal is assigned to the corresponding global protection parameter. If the signal becomes true, those modules/stages are blocked that are parameterized "ExBlo Fc=active".	

SysA . **CT Winding Side**	[SysA / General Settings]	
W1	W1, W2 ╚═▷ CT Winding Side.	P.2
	Measuring values will be used from this winding side	

SysA . **Alarm**	[SysA / Demand / Current Demand] [SysA / THD / I THD]	
inactive	inactive, active active/inactive.	P.2
	Alarm	

SysA . **Threshold**	[SysA / Demand / Current Demand] [SysA / THD / I THD]	
500A	10A ... 500000A	P.2
	Threshold (to be entered as primary value)	

SysA . **t-Delay**	[SysA / Demand / Current Demand] [SysA / THD / I THD]	
0min	0min ... 60min	P.2
	Tripping Delay	

11.3 SysA: Input States

SysA . **ExBlo-I**	[Operation / Status Display / SysA]
	Module input state: External blocking

11.4 SysA: Signals (Output States)

SysA . **active**	[Operation / Status Display / SysA]
	Signal: active

SysA . **ExBlo**	[Operation / Status Display / SysA]
	Signal: External Blocking

SysA . **Alm Current Demd**	[Operation / Status Display / SysA]
	Signal: Alarm averaged demand current

SysA . **Alarm I THD**	[Operation / Status Display / SysA]
⇫	*Signal: Alarm Total Harmonic Distortion Current*

SysA . **Trip Current Demand**	[Operation / Status Display / SysA]
⇫	*Signal: Trip averaged demand current*

SysA . **Trip I THD**	[Operation / Status Display / SysA]
⇫	*Signal: Trip Total Harmonic Distortion Current*

12 Records

12.1 Event rec

The event recorder logs all events like switching operations, change of parameters, alarms, trips, operating mode selections, blockings and state transitions of inputs and outputs.

Event rec	[Operation / Recorders / Event rec]
🗎	This item represents a special dialog. (See the Technical Manual for details.) *The event recorder logs all events like switching operations, change of parameters, alarms, trips, operating mode selections, blockings and state transitions of inputs and outputs.*

12.1.1 Event rec: Direct Controls

Event rec . **Res all rec**	[Operation / Reset]	
inactive	inactive, active ⮡ Mode.	P.1
◉	*Reset all records*	

12.1.2 Event rec: Signals (Output States)

Event rec . **Res all records**	[Operation / Status Display / Recorders / Event rec]
⇧	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*

12.2 Disturb rec

After a trigger event has become true, the disturbance recorder writes analogue and digital tracks

Disturb rec	[Operation / Recorders / Disturb rec]
This item represents a special dialog. (See the Technical Manual for details.) *After a trigger event has become true, the disturbance recorder writes analogue and digital tracks*	

12.2.1 Disturb rec: Global Parameters

Disturb rec . **Start: 1**	[Device Para / Recorders / Disturb rec]	
Prot . Trip	"-" . . . Sys . Internal test state 1..n, Assignment List.	S.3
Start recording if the assigned signal is true.		

Disturb rec . **Start: 2** . . . Disturb rec . **Start: 8**	[Device Para / Recorders / Disturb rec]	
"-"	"-" . . . Sys . Internal test state 1..n, Assignment List.	S.3
Start recording if the assigned signal is true.		

Disturb rec . **Auto overwriting**	[Device Para / Recorders / Disturb rec]	
active	inactive, active Mode.	S.3
If there is no more free memory capacity left, the oldest file will be overwritten.		

Disturb rec . **Pre-trigger time**	[Device Para / Recorders / Disturb rec]	
20%	0% . . . 99%	S.3
The pre trigger time is set in percent of the »Max file size« value. It corresponds to the part of recording before the onset of the trigger event.		

Disturb rec . **Post-trigger time**	[Device Para / Recorders / Disturb rec]	
20%	0% ... 99%	S.3
	The post trigger time is set in percent of the »Max file size« value. It is the remaining time of the »Max file size«, depending on the »Pre-trigger time« setting and the duration of the trigger event, but at maximum the »Post-trigger time« set here.	

Disturb rec . **Max file size**	[Device Para / Recorders / Disturb rec]	
2s	0.1s ... 15.0s	S.3
	The maximum storage capacity per record, including pre-trigger and post-trigger time. The amount of records depends on the size of each record, on the max. file size (set here), and on the total storage capacity.	

12.2.2 Disturb rec: Direct Controls

Disturb rec . **Man Trigger**	[Operation / Recorders / Man Trigger]	
False	False, True ⮡ true or not true.	P.1
◉	*Manual Trigger*	

Disturb rec . **Res all rec**	[Operation / Reset]	
inactive	inactive, active ⮡ Mode.	P.1
◉	*Reset all records*	

12.2.3 Disturb rec: Input States

Disturb rec . **Start1-I** ... Disturb rec . **Start8-I**	[Operation / Status Display / Recorders / Disturb rec]
	State of the module input:: Trigger event / start recording

12.2.4 Disturb rec: Signals (Output States)

Disturb rec . **recording**	[Operation / Status Display / Recorders / Disturb rec]
	Signal: Recording

Disturb rec . **memory full**	[Operation / Status Display / Recorders / Disturb rec]
⇫	*Signal: Memory full*

Disturb rec . **Clear fail**	[Operation / Status Display / Recorders / Disturb rec]
⇫	*Signal: Clear failure in memory*

Disturb rec . **Res all records**	[Operation / Status Display / Recorders / Disturb rec]
⇫	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*

Disturb rec . **Res all records**	[Operation / Status Display / Recorders / Disturb rec]
⇫	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*

Disturb rec . **Man Trigger**	[Operation / Status Display / Recorders / Disturb rec]
⇫	*Signal: Manual Trigger*

12.2.5 Disturb rec: Values

Disturb rec . **Rec state**	[Operation / Status Display / Recorders / Disturb rec]
Ready	Ready, Recording, Writing file, Trigger Blo ╚═▷ Rec state.
✎	*Recording state*

Disturb rec . **Error code**	[Operation / Status Display / Recorders / Disturb rec]
OK	OK, Write err, Clear fail, Calculation err, File not found, Auto overwriting off ╚═▷ Fault.
✎	*Error code*

12.3 Fault rec

The values measured at the time of tripping are saved by the Fault Recorder.

Fault rec	[Operation / Recorders / Fault rec]
This item represents a special dialog. (See the Technical Manual for details.) *The values measured at the time of tripping are saved by the Fault Recorder.*	

12.3.1 Fault rec: Global Parameters

Fault rec . **Record-Mode**	[Device Para / Recorders / Fault rec]	
Trips only	Alarms and Trips, Trips only ⮡ Record-Mode.	S.3
Recorder Mode (Set the behaviour of the recorder)		

Fault rec . **t-meas-delay**	[Device Para / Recorders / Fault rec]	
0ms	0ms ... 60ms	S.3
After the Trip, the measurement will be delayed for this time.		

12.3.2 Fault rec: Direct Controls

Fault rec . **Res all rec**	[Operation / Reset]	
inactive	inactive, active ⮡ Mode.	P.1
Reset all records		

12.3.3 Fault rec: Signals (Output States)

Fault rec . **Res all records**	[Operation / Status Display / Recorders / Fault rec]
Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)	

12.4 Trend rec

Trend Recorder

Trend rec	[Operation / Recorders / Trend rec]
This item represents a special dialog. (See the Technical Manual for details.) *Trend Recorder*	

12.4.1 Trend rec: Global Parameters

Trend rec . **Resolution**	[Device Para / Recorders / Trend rec]	
15 min	60 min, 30 min, 15 min, 10 min, 5 min ⮡ Resolution.	S.3
Resolution (recording frequency)		

Trend rec . **Trend1**	[Device Para / Recorders / Trend rec]	
CT W1 . IL1 RMS	"-" . . . RTD . Hottest Aux Temp ⮡ 1..n, TrendRecList.	S.3
Observed Value1		

Trend rec . **Trend2**	[Device Para / Recorders / Trend rec]	
CT W1 . IL2 RMS	"-" . . . RTD . Hottest Aux Temp ⮡ 1..n, TrendRecList.	S.3
Observed Value2		

Trend rec . **Trend3**	[Device Para / Recorders / Trend rec]	
CT W1 . IL3 RMS	"-" . . . RTD . Hottest Aux Temp ⮡ 1..n, TrendRecList.	S.3
Observed Value3		

Trend rec . **Trend4**	[Device Para / Recorders / Trend rec]	
CT W1 . IG meas RMS	"-" . . . RTD . Hottest Aux Temp ⮡ 1..n, TrendRecList.	S.3
Observed Value4		

Trend rec . **Trend5**	[Device Para / Recorders / Trend rec]	
"-"	"-" . . . RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
Observed Value5		

Trend rec . **Trend6**	[Device Para / Recorders / Trend rec]	
"-"	"-" . . . RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
Observed Value6		

Trend rec . **Trend7**	[Device Para / Recorders / Trend rec]	
"-"	"-" . . . RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
Observed Value7		

Trend rec . **Trend8**	[Device Para / Recorders / Trend rec]	
"-"	"-" . . . RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
Observed Value8		

Trend rec . **Trend9**	[Device Para / Recorders / Trend rec]	
"-"	"-" . . . RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
Observed Value9		

Trend rec . **Trend10**	[Device Para / Recorders / Trend rec]	
"-"	"-" . . . RTD . Hottest Aux Temp ↳ 1..n, TrendRecList.	S.3
Observed Value10		

12.4.2 Trend rec: Direct Controls

Trend rec . **Res all rec**	[Operation / Reset]	
inactive	inactive, active └⇒ Mode.	P.1
◉	*Reset all records*	

12.4.3 Trend rec: Signals (Output States)

Trend rec . **Res all records**	[Operation / Status Display / Recorders / Trend rec]
⇫	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*

12.4.4 Trend rec: Counters

Trend rec . **Max avail Entries**	[Operation / Count and RevData / Trend rec]
#	*Maximum available entries in the current configuration*

13 Logic

13.1 Logics

Logic

13.1.1 Logics: Device Planning Parameters

Logics . **No of Equations:**	[Device planning]	
20	0, 5, 10, 20, 40, 80 ╚═▷ No of Equations:.	S.3
	Number of required Logic Equations:	

13.1.2 Logics . . . Logics

Logic

13.1.2.1 Logics: Global Parameters

Logics . **LE1.Gate**	[Logics / LE 1]	
AND	AND, OR, NAND, NOR ⮩ LE1.Gate.	S.3
	Logic gate	

Logics . **LE1.Input1** . . . Logics . **LE1.Input4**	[Logics / LE 1]	
“-”	“-” . . . Sys . Internal test state ⮩ 1..n, Assignment List.	S.3
	Assignment of the Input Signal	

Logics . **LE1.Inverting1** . . . Logics . **LE1.Inverting4**	[Logics / LE 1]	
inactive	inactive, active ⮩ Mode.	S.3
	Inverting the input signals.	

Logics . **LE1.t-On Delay**	[Logics / LE 1]	
0.00s	0.00s . . . 36000.00s	S.3
	Switch On Delay	

Logics . **LE1.t-Off Delay**	[Logics / LE 1]	
0.00s	0.00s . . . 36000.00s	S.3
	Switch Off Delay	

Logics . **LE1.Reset Latched**	[Logics / LE 1]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	Reset Signal for the Latching	

Logics . **LE1.Inverting Reset**	[Logics / LE 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting Reset Signal for the Latching	

Logics . **LE1.Inverting Set**	[Logics / LE 1]	
inactive	inactive, active ⮡ Mode.	S.3
	Inverting the Setting Signal for the Latching	

13.1.2.2 Logics: Input States

Logics . **LE1.Gate In1-I** . . . Logics . **LE1.Gate In4-I**	[Operation / Status Display / Logics]
	State of the module input: Assignment of the Input Signal

Logics . **LE1.Reset Latch-I**	[Operation / Status Display / Logics]
	State of the module input: Reset Signal for the Latching

13.1.2.3 Logics: Signals (Output States)

Logics . **LE1.Gate Out**	[Operation / Status Display / Logics]
	Signal: Output of the logic gate

Logics . **LE1.Timer Out**	[Operation / Status Display / Logics]
	Signal: Timer Output

Logics . **LE1.Out**	[Operation / Status Display / Logics]
⇧	*Signal: Latched Output (Q)*

Logics . **LE1.Out inverted**	[Operation / Status Display / Logics]
⇧	*Signal: Negated Latched Output (Q NOT)*

14 Self-Supervision

SelfSupervision

Messages	[Operation / Self-Supervision / Messages]
🗏	This item represents a special dialog. (See the Technical Manual for details.) *Internal messages*

14.1 SSV: Direct Controls

SSV . **Ack System LED**	[Operation / Acknowledge]	
False	False, True ↳ true or not true.	P.1
◉	*Acknowledge System LED (red/green flashing LED)*	

14.2 SSV: Signals (Output States)

SSV . **System Error**	[Operation / Self-Supervision / System State]
⇫	*Signal: Device Failure*

SSV . **SelfSuperVision Contact**	[Operation / Self-Supervision / System State]
⇫	*Signal: SelfSuperVision Contact*

SSV . **New error**	[Operation / Self-Supervision / System State]
⇫	*Signal: A new error message has been issued.*

SSV . **New warning**	[Operation / Self-Supervision / System State]
⇫	*Signal: A new warning message has been issued.*

14.3 SSV: Counters

SSV . **Cr No of free sockets**	[Operation / Self-Supervision / System State]
#	*Counter for network diagnosis. Number of free sockets.*

15 Service

- Sys . Reboot: ⮱ Tab.

15.1 Sgen

Sine wave generator

15.1.1 Sgen: Device Planning Parameters

Sgen . **Mode**	[Device planning]	
use	"-", use ⮡ Mode.	S.3
	Sine wave generator, general operation mode	

15.1.2 Sgen: Global Parameters

Sgen . **TripCmd Mode**	[Service / Test (Prot inhibit) / Sgen / Process]	
No TripCmd	No TripCmd, With TripCmd ⮡ TripCmd Mode.	S.3
	Trip Command Mode: Select between two operating modes for the Fault Simulator: "cold simulation" (without tripping the circuit breaker), or "hot simulation" (i.e. the simulation is authorized to trip the circuit breaker)	

Sgen . **Ex Start Simulation**	[Service / Test (Prot inhibit) / Sgen / Process]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	External Start of Fault Simulation (Using the test parameters)	

Sgen . **ExBlo1**	[Service / Test (Prot inhibit) / Sgen / Process]	
SG[1] . Pos ON	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.1	

Sgen . **ExBlo2**	[Service / Test (Prot inhibit) / Sgen / Process]	
"-"	"-" . . . Sys . Internal test state ⮡ 1..n, Assignment List.	S.3
	External blocking of the module, if blocking is activated (allowed) within a parameter set and if the state of the assigned signal is true.2	

Sgen . **Ex ForcePost**	[Service / Test (Prot inhibit) / Sgen / Process]	
“-”	“-” . . . Sys . Internal test state 1..n, Assignment List.	S.3
	Force Post state. Abort simulation.	

Sgen . **PreFault**	[Service / Test (Prot inhibit) / Sgen / Configuration / Times]	
0.0s	0.00s . . . 300.00s	S.3
	Pre Fault Duration	

Sgen . **FaultSimulation**	[Service / Test (Prot inhibit) / Sgen / Configuration / Times]	
0.0s	0.00s . . . 10800.00s	S.3
	Duration of Fault Simulation	

Sgen . **PostFault**	[Service / Test (Prot inhibit) / Sgen / Configuration / Times]	
0.0s	0.00s . . . 300.00s	S.3
	Post Fault Duration	

15.1.3 Sgen: Direct Controls

Sgen . **Start Simulation**	[Service / Test (Prot inhibit) / Sgen / Process]	
inactive	inactive, active Mode.	S.3
	Start Fault Simulation (Using the test parameters)	

Sgen . **Stop Simulation**	[Service / Test (Prot inhibit) / Sgen / Process]	
inactive	inactive, active Mode.	S.3
	Stopp Fault Simulation (Using the test parameters)	

15.1.4 Sgen: Input States

Sgen . **Ex Start Simulation-I**	[Operation / Status Display / Sgen]
	State of the module input:External Start of Fault Simulation (Using the test parameters)

Sgen . **ExBlo1-I**	[Operation / Status Display / Sgen] [Service / Test (Prot inhibit) / Sgen / State]
⇩	*Module input state: External blocking1*

Sgen . **ExBlo2-I**	[Operation / Status Display / Sgen] [Service / Test (Prot inhibit) / Sgen / State]
⇩	*Module input state: External blocking2*

Sgen . **Ex ForcePost-I**	[Operation / Status Display / Sgen] [Service / Test (Prot inhibit) / Sgen / State]
⇩	*State of the module input:Force Post state. Abort simulation.*

15.1.5 Sgen: Signals (Output States)

Sgen . **Manual Start**	[Operation / Status Display / Sgen]
⇧	*Fault Simulation has been started manually.*

Sgen . **Manual Stop**	[Operation / Status Display / Sgen]
⇧	*Fault Simulation has been stopped manually.*

Sgen . **Running**	[Operation / Status Display / Sgen] [Service / Test (Prot inhibit) / Sgen / State]
⇧	*Signal; Measuring value simulation is running*

Sgen . **Started**	[Operation / Status Display / Sgen]
⇧	*Fault Simulation has been started*

Sgen . **Stopped**	[Operation / Status Display / Sgen]
⇧	*Fault Simulation has been stopped*

Sgen . **State**	[Operation / Status Display / Sgen]
⇧	*Signal: Wave generation states: 0=Off, 1=PreFault, 2=Fault, 3=PostFault, 4=InitReset*

15.1.6 Sgen: Values

Sgen . **State**	[Service / Test (Prot inhibit) / Sgen / State]	
Off	Off, PreFault, FaultSimulation, PostFault, Init Res ⮡ State.	
	Wave generation states: 0=Off, 1=PreFault, 2=Fault, 3=PostFault, 4=InitReset	

15.1.7 Sgen . . . Sgen

Sine wave generator

15.1.7.1 Sgen: Global Parameters

Sgen . **CT W1.IL1**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
0.0In	0.00In . . . 40.00In	S.3
	Current Fundamental Magnitude in Pre State: phase L1	

Sgen . **CT W1.IL2**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
0.0In	0.00In . . . 40.00In	S.3
	Current Fundamental Magnitude in Pre State: phase L2	

Sgen . **CT W1.IL3**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
0.0In	0.00In . . . 40.00In	S.3
	Current Fundamental Magnitude in Pre State: phase L3	

Sgen . **CT W1.IG meas**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
0.0In	If: slot 3 = Current measuring inputs2 • 0.00In . . . 2.500In If: slot 3 ≠ Current measuring inputs2 • 0.00In . . . 25.00In	S.3
	Current Fundamental Magnitude in Pre State: IG	

Sgen . **CT W1.phi IL1**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
0°	-360° . . . 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Pre-Phase:phase L1	

Sgen . **CT W1.phi IL2**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
240°	-360° . . . 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Pre-Phase:phase L2	

Sgen . **CT W1.phi IL3**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
120°	-360° ... 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Pre-Phase:phase L3	

Sgen . **CT W1.phi IG meas**	[Service / Test (Prot inhibit) / Sgen / Configuration / PreFault / CT W1]	
0°	-360° ... 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Pre-Phase: IG	

Sgen . **CT W1.IL1**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
0.0In	0.00In ... 40.00In	S.3
	Current Fundamental Magnitude in Fault State: phase L1	

Sgen . **CT W1.IL2**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
0.0In	0.00In ... 40.00In	S.3
	Current Fundamental Magnitude in Fault State: phase L2	

Sgen . **CT W1.IL3**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
0.0In	0.00In ... 40.00In	S.3
	Current Fundamental Magnitude in Fault State: phase L3	

Sgen . **CT W1.IG meas**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
0.0In	If: slot 3 = Current measuring inputs2 • 0.00In ... 2.500In If: slot 3 ≠ Current measuring inputs2 • 0.00In ... 25.00In	S.3
	Current Fundamental Magnitude in Fault State: IG	

Sgen . **CT W1.phi IL1**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
0°	-360° ... 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Fault-Phase:phase L1	

Sgen . **CT W1.phi IL2**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
240°	-360° ... 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Fault-Phase:phase L2	

Sgen . **CT W1.phi IL3**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
120°	-360° ... 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Fault-Phase:phase L3	

Sgen . **CT W1.phi IG meas**	[Service / Test (Prot inhibit) / Sgen / Configuration / FaultSimulation / CT W1]	
0°	-360° ... 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Fault-Phase: IG	

Sgen . **CT W1.IL1**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
0.0In	0.00In ... 40.00In	S.3
	Current Fundamental Magnitude during Post phase: phase L1	

Sgen . **CT W1.IL2**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
0.0In	0.00In ... 40.00In	S.3
	Current Fundamental Magnitude during Post phase: phase L2	

Sgen . **CT W1.IL3**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
0.0In	0.00In ... 40.00In	S.3
	Current Fundamental Magnitude during Post phase: phase L3	

Sgen . **CT W1.IG meas**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
0.0In	If: slot 3 = Current measuring inputs2 • 0.00In ... 2.500In If: slot 3 ≠ Current measuring inputs2 • 0.00In ... 25.00In	S.3
	Current Fundamental Magnitude during Post phase: IG	

Sgen . **CT W1.phi IL1**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
0°	-360° . . . 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Post phase: phase L1	

Sgen . **CT W1.phi IL2**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
240°	-360° . . . 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Post phase: phase L2	

Sgen . **CT W1.phi IL3**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
120°	-360° . . . 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Post phase: phase L3	

Sgen . **CT W1.phi IG meas**	[Service / Test (Prot inhibit) / Sgen / Configuration / PostFault / CT W1]	
0°	-360° . . . 360°	S.3
	Start Position respectively Start Angle of the Current Phasor during Post phase: IG	

16 Selection Lists

Rec state

Recording state

Selection list referenced by the following parameters:

- ╘═▷ Disturb rec . Rec state

Rec state	Description
Ready	*Ready*
Recording	*Recording*
Writing file	*Signal: Writing file*
Trigger Blo	*Trigger signal is still active - wait for fallback. A new record can only be started if and only the trigger signal that started the previous record has fallen back once. Therewith endless records are prevented.*

Fault

Selection list referenced by the following parameters:

- ╘═▷ Disturb rec . Error code

Fault	Description
OK	*OK*
Write err	*Signal: Writing error in memory*
Clear fail	*Signal: Clear failure in memory*
Calculation err	*Calculation error*
File not found	*File not found*
Auto overwriting off	*If there is no more memory available the record is being stopped.*

State

Selection list referenced by the following parameters:

- ╘═▷ IEC 61850 . GoosePublisherState
- ╘═▷ IEC 61850 . GooseSubscriberState
- ╘═▷ IEC 61850 . MmsServerState

State	Description
Off	*Off*
On	*On*
Error	*Error*

State

Selection list referenced by the following parameters:

- ⮡ Profibus . Slave State

State	Description
Baud Search	*No connection to the PROFIBUS-DP Master*
Baud Found	*The PROFIBUS DP Slave is connected to the bus. The Slave has not yet been addressed by the Master Device (and it was not yet addressed since the last break of the connection).*
PRM OK	*The slave was addressed by the master, the parameter setting message was received and is OK, a configuration message is expected from the master.*
PRM REQ	*The slave is no longer addressed by the master (modified parameters within the master without having the connection stopped, master software is tuned off but lower PROFIBUS layer is still active)*
PRM Fault	*An Error in the parameter setting message (e.g. wrong PNO identification number)*
CFG Fault	*Configuration error the number of input/output bytes parameterised in the master does not match the number parametrised in the device (slave).*
Clear Data	*Master sends a General Control command to clear the data.*
Data exchange	*Master and slave exchange data.*

Baud rate

Selection list referenced by the following parameters:

- ⮡ Profibus . Baud rate

Baud rate	Description
12 Mb/s	*12 Mb/s*

Baud rate	Description
6 Mb/s	*6 Mb/s*
3 Mb/s	*3 Mb/s*
1.5 Mb/s	*1.5 Mb/s*
0.5 Mb/s	*0.5 Mb/s*
187500 baud	*187500 baud*
93750 baud	*93750 baud*
45450 baud	*45450 baud*
19200 baud	*19200 baud*
9600 baud	*9600 baud*
-.-	*-.-*

PNO Id

PNO Identification Number. GSD Identification Number.

Selection list referenced by the following parameters:

- ⮡ Profibus . PNO Id

PNO Id	Description
0C50h	*PnodID for the Config file.*

Config status

Status of the user-defined SCADA configuration.\nPossible values:

Selection list referenced by the following parameters:

- ⮡ Profibus . Config status

Config status	Description
Changing	*New SCADA configuration is being loaded, but not active yet.*
OK	*The SCADA configuration is active.*
Config. not avail.	*The user-defined SCADA configuration is not available (e.g. has not been loaded into the device).*
Error	*Unexpected error. Please contact our service-team.*

Server State

Server State.

Selection list referenced by the following parameters:

- ╚═▷ SNTP . Used Server

Server State	Description
Server1	*Server1 used.*
Server2	*Server2 used.*
None	*No Server used.*

State

Selection list referenced by the following parameters:

- ╚═▷ SNTP . ServerQlty
- ╚═▷ SNTP . NetConn

State	Description
GOOD	*GOOD*
SUFFICIENT	*SUFFICIENT*
BAD	*BAD*
“-”	*NO CONNECTION*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ DI Slot X1 . Inverting 1
- ╚═▷ DI Slot X6 . Inverting 1
- ╚═▷ BO Slot X2 . Latched
- ╚═▷ BO Slot X2 . Inverting

- ⮱▷ BO Slot X2 . Inverting 1
- ⮱▷ BO Slot X2 . Latched
- *[. . .]*

Mode	Description
inactive	*inactive*
active	*active*

true or not true

Selection list referenced by the following parameters:

- ⮱▷ Disturb rec . Man Trigger
- ⮱▷ SSV . Ack System LED

true or not true	Description
False	*False*
True	*True*

Type of passw. def.

Type of the password definition. This value is directly related to the security-level of the access to the device.

Selection list referenced by the following parameters:

- ⮱▷ Sys . Passw. for USB conn.
- ⮱▷ Sys . Passw.remote net.conn.

Type of passw. def.	Description
disabled	*The password disabled.*
default	*The password is the same as the factory default, i.e. it has not been altered by the user. (However, for devices with a disabled default password the password type is displayed as “disabled”, not as “default”.)*
def. by user	*The password has been defined by the user. This corresponds to the highest security-level of the access to the device.*

TLS Certificate

Type of certificate that the device uses for the encrypted communication. This value is directly related to the security-level of the communication.

Selection list referenced by the following parameters:

- ╚═▷ Sys . TLS Certificate

TLS Certificate	Description
Device-specific	*The device uses a device-specific certificate for the encrypted communication. This corresponds to the highest security-level of the communication.*
Basic	*The device uses a basic certificate for the encrypted communication. Compared with a device-specific certificate, this means a slightly reduced security level.*
Corrupt	*The certificate for the encrypted communication is corrupt and therefore unusable.*

Switching Authority

Selection list referenced by the following parameters:

- ╚═▷ Ctrl . Switching Authority
- ╚═▷ Ctrl . Switching Authority
- ╚═▷ Ctrl . Switching Authority

Switching Authority	Description
None	*None*
Local	*Local*
Remote	*Remote*
Local and Remote	*Local and Remote*

Config. Device Reset

If the »C« key is pressed while the device is performing a cold restart a general Reset Dialog appears on the screen. Select which options shall be available with this dialog.

Selection list referenced by the following parameters:

- ╚═▷ HMI . Config. Device Reset

- ⮡ HMI . Config. Device Reset
- ⮡ HMI . Config. Device Reset
- ⮡ HMI . Config. Device Reset

Config. Device Reset	Description
"Fact.def.", "PW rst"	*Two Reset Options shall be available:* *- "Reset to factory defaults",* *- "Reset passwords".*
Only “Fact.defaults”	*Only one Reset Option shall be available:* *- "Reset to factory defaults".* *CAUTION: If this option has been chosen and the password should ever get lost then the only chance to recover control is to reset the protection device to factory defaults.*
Reset deact.	*The Reset Options shall be deactivated.* *CAUTION: If this option has been chosen and the password should ever get lost, then the protection device has to be sent to the manufacturer as a service request.*

Mode

general operation mode

Selection list referenced by the following parameters:

- ⮡ Id . Mode

Mode	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ⮡ IdH . Mode

Mode	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ⮱▷ IdG[1] . Mode

Mode	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ⮱▷ IdGH[1] . Mode

Mode	Description
“-”	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ⮱▷ IH2[1] . Mode

Device planning	Description
“-”	*do not use*
use	*use*

I>

If the pickup value is exceeded, the module/element starts to time out to trip.

Selection list referenced by the following parameters:

- ╚⇒ I[1] . Mode

I>	Description
“-”	*do not use*
non directional	*non directional*

Earth overcurrent

Selection list referenced by the following parameters:

- ╚⇒ IG[1] . Mode

Earth overcurrent	Description
“-”	*do not use*
non directional	*non directional*

yes/no

Selection list referenced by the following parameters:

- ╚⇒ Sys . Reboot
- ╚⇒ IG[1] . Superv. only
- ╚⇒ RTD . W1L1
- ╚⇒ RTD . W1L2
- ╚⇒ RTD . W1L3
- ╚⇒ RTD . W2L1
- *[. . .]*

yes/no	Description
no	*no*

yes/no	Description
yes	*yes*

Device planning

Selection list referenced by the following parameters:

- ╚═▷ ThR . Mode

Device planning	Description
“-”	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╚═▷ I2>[1] . Mode

Device planning	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ SOTF . Mode

Mode	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ CLPU . Mode

Mode	Description
“-”	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╚═▷ ExP[1] . Mode

Device planning	Description
“-”	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╚═▷ Ext Sudd Press . Mode

Device planning	Description
“-”	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╚═▷ Ext Oil Temp . Mode

Device planning	Description
"-"	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╘═▷ Ext Temp Superv[1] . Mode

Device planning	Description
"-"	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╘═▷ RTD . Mode

Device planning	Description
"-"	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ╘═▷ CBF[1] . Mode

Device planning	Description
"-"	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ⮡ TCS[1] . Mode

Device planning	Description
“-”	*do not use*
use	*use*

Device planning

Selection list referenced by the following parameters:

- ⮡ CTS[1] . Mode

Device planning	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ⮡ SysA . Mode

Mode	Description
“-”	*do not use*
use	*use*

Used Protocol

Used SCADA Protocol

Selection list referenced by the following parameters:

- ⮡ Scada . Protocol

Used Protocol	Description
“-”	*do not use*
Modbus RTU	*Modbus Protocol RTU*
Modbus TCP	*Modbus Protocol TCP*
Modbus TCP/RTU	*Modbus Protocol TCP/RTU*
DNP3 RTU	*Distributed Network Protocol RTU*
DNP3 TCP	*Distributed Network Protocol TCP*
DNP3 UDP	*Distributed Network Protocol UDP*
IEC 60870-5-103	*IEC 60870-5-103 Protocol*
IEC 60870-5-104	*IEC 60870-5-104 Protocol*
IEC 61850	*IEC 61850 communication*
Profibus	*Profibus Module*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╘═▷ IRIG-B . Mode

Mode	Description
“-”	*do not use*
use	*use*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╘═▷ SNTP . Mode

Mode	Description
“-”	*do not use*
use	*use*

No of Equations:

Number of required Logic Equations:

Selection list referenced by the following parameters:

- ╚═▷ Logics . No of Equations:

No of Equations:	Description
0	*0*
5	*5*
10	*10*
20	*20*
40	*40*
80	*80*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ Sgen . Mode

Mode	Description
“-”	*do not use*
use	*use*

Scaling

Display of the measured values as primary, secondary or per unit values

Selection list referenced by the following parameters:

- ╚═▷ Sys . Scaling

Scaling	Description
Per unit values	*Per unit values*

Scaling	Description
Primary values	*Primary values*
Secondary values	*Secondary values*

Units

Units for the measurement

Selection list referenced by the following parameters:

- ╚⇒ URTD . Temperature Unit

Units	Description
Celsius	*Celsius*
Fahrenheit	*Fahrenheit*

Nom voltage

Nominal voltage of the digital inputs

Selection list referenced by the following parameters:

- ╚⇒ DI Slot X1 . Nom voltage

Nom voltage	Description
24 VDC	*24 VDC*
48 VDC	*48 VDC*
60 VDC	*60 VDC*
110 VDC	*110 VDC*
230 VDC	*230 VDC*
110 VAC	*110 VAC*
230 VAC	*230 VAC*

Debouncing time

A change of the state of a digital input will only be recognized after the debouncing time has expired (become effective). Thus, transient signals will not be misinterpreted.

Selection list referenced by the following parameters:

- ⮡ DI Slot X1 . Debouncing time 1

Debouncing time	Description
no debouncing time	*no debouncing time*
20 ms	*20 ms*
50 ms	*50 ms*
100 ms	*100 ms*

Nom voltage

Nominal voltage of the digital inputs

Selection list referenced by the following parameters:

- ⮡ DI Slot X6 . Nom voltage

Nom voltage	Description
24 VDC	*24 VDC*
48 VDC	*48 VDC*
60 VDC	*60 VDC*
110 VDC	*110 VDC*
230 VDC	*230 VDC*
110 VAC	*110 VAC*
230 VAC	*230 VAC*

Debouncing time

A change of the state of a digital input will only be recognized after the debouncing time has expired (become effective). Thus, transient signals will not be misinterpreted.

Selection list referenced by the following parameters:

- ⮡ DI Slot X6 . Debouncing time 1

Debouncing time	Description
no debouncing time	*no debouncing time*
20 ms	*20 ms*
50 ms	*50 ms*
100 ms	*100 ms*

1...n Operating Modes

Selection list referenced by the following parameters:

- ╚═▷ BO Slot X2 . Operating Mode
- ╚═▷ BO Slot X2 . Operating Mode
- ╚═▷ BO Slot X2 . Operating Mode
- ╚═▷ BO Slot X2 . Operating Mode
- ╚═▷ BO Slot X2 . Operating Mode
- ╚═▷ BO Slot X2 . Operating Mode

1...n Operating Modes	Description
Normally open (NO)	*The working principle of the relay corresponds to a normally open contact.*
Normally closed (NC)	*The working principle of the relay corresponds to a normally closed contact.*

1..n, Assignment List

Assignment List

Selection list referenced by the following parameters:

- ╚═▷ BO Slot X2 . Acknowledgement
- ╚═▷ BO Slot X2 . Assignment 1
- ╚═▷ BO Slot X2 . Assignment 2
- ╚═▷ BO Slot X2 . Acknowledgement
- ╚═▷ BO Slot X2 . Assignment 1
- ╚═▷ BO Slot X2 . Assignment 2

- *[. . .]*

1..n, Assignment List	Description
"-"	*No assignment*
Prot . **available**	*Signal: Protection is available*
Prot . **active**	*Signal: active*
Prot . **ExBlo**	*Signal: External Blocking*
Prot . **Blo TripCmd**	*Signal: Trip Command blocked*
Prot . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Prot . **Alarm L1**	*Signal: General-Alarm L1*
Prot . **Alarm L2**	*Signal: General-Alarm L2*
Prot . **Alarm L3**	*Signal: General-Alarm L3*
Prot . **Alarm G**	*Signal: General-Alarm - Earth fault*
Prot . **Alarm**	*Signal: General Alarm*
Prot . **Trip L1**	*Signal: General Trip L1*
Prot . **Trip L2**	*Signal: General Trip L2*
Prot . **Trip L3**	*Signal: General Trip L3*
Prot . **Trip G**	*Signal: General Trip Ground fault*
Prot . **Trip**	*Signal: General Trip*
Prot . **Res FaultNo a GridFaultNo**	*Signal: Resetting of fault number and grid fault number.*
Prot . **ExBlo1-I**	*Module input state: External blocking1*
Prot . **ExBlo2-I**	*Module input state: External blocking2*
Prot . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
CT W1 . **Phase seq. wrong**	*Signal that the device has detected a phase sequence (L1-L2-L3 / L1-L3-L2) that is different from the one that had been set at [Field settings / General Settings] »Phase Sequence«.*
CT W2 . **Phase seq. wrong**	*Signal that the device has detected a phase sequence (L1-L2-L3 / L1-L3-L2) that is different from the one that had been set at [Field settings / General Settings] »Phase Sequence«.*
Ctrl . **Local**	*Switching Authority: Local*
Ctrl . **Remote**	*Switching Authority: Remote*
Ctrl . **NonInterl**	*Non-Interlocking is active*
Ctrl . **SG Indeterm**	*(At least one) Switchgear is moving (Position cannot be determined).*
Ctrl . **SG Disturb**	*(At least one) Switchgear is disturbed.*
Ctrl . **NonInterl-I**	*Non-Interlocking*

1..n, Assignment List	Description
SG[1] . **SI SingleContactInd**	*Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.*
SG[1] . **Pos not ON**	*Signal: Pos not ON*
SG[1] . **Pos ON**	*Signal: Circuit Breaker is in ON-Position*
SG[1] . **Pos OFF**	*Signal: Circuit Breaker is in OFF-Position*
SG[1] . **Pos Indeterm**	*Signal: Circuit Breaker is in Indeterminate Position*
SG[1] . **Pos Disturb**	*Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.*
SG[1] . **Ready**	*Signal: Circuit breaker is ready for operation.*
SG[1] . **t-Dwell**	*Signal: Dwell time*
SG[1] . **Removed**	*Signal: The withdrawable circuit breaker is Removed*
SG[1] . **Interl ON**	*Signal: One or more IL_On inputs are active.*
SG[1] . **Interl OFF**	*Signal: One or more IL_Off inputs are active.*
SG[1] . **CES succesf**	*Signal: Command Execution Supervision: Switching command executed successfully.*
SG[1] . **CES Disturbed**	*Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.*
SG[1] . **CES Fail TripCmd**	*Signal: Command Execution Supervision: Command execution failed because trip command is pending.*
SG[1] . **CES SwitchDir**	*Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.*
SG[1] . **CES ON d OFF**	*Signal: Command Execution Supervision: On Command during a pending OFF Command.*
SG[1] . **CES SG not ready**	*Signal: Command Execution Supervision: Switchgear not ready*
SG[1] . **CES Fiel Interl**	*Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.*
SG[1] . **CES SyncTimeout**	*Signal: Command Execution Supervision: Switching Command not executed. No Synchronization signal while t-sync was running.*
SG[1] . **CES SG removed**	*Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.*
SG[1] . **Prot ON**	*Signal: ON Command issued by the Prot module*
SG[1] . **TripCmd**	*Signal: Trip Command*
SG[1] . **Ack TripCmd**	*Signal: Acknowledge Trip Command*

1..n, Assignment List	Description
SG[1] . **ON incl Prot ON**	*Signal: The ON Command includes the ON Command issued by the Protection module.*
SG[1] . **OFF incl TripCmd**	*Signal: The OFF Command includes the OFF Command issued by the Protection module.*
SG[1] . **Position Ind manipul**	*Signal: Position Indicators faked*
SG[1] . **SGwear Slow SG**	*Signal: Alarm, the circuit breaker (load-break switch) becomes slower*
SG[1] . **Res SGwear Sl SG**	*Signal: Resetting the slow Switchgear Alarm*
SG[1] . **ON Cmd**	*Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.*
SG[1] . **OFF Cmd**	*Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.*
SG[1] . **ON Cmd manual**	*Signal: ON Cmd manual*
SG[1] . **OFF Cmd manual**	*Signal: OFF Cmd manual*
SG[1] . **Sync ON request**	*Signal: Synchronous ON request*
SG[1] . **Aux ON-I**	*Module Input State: Position indicator/check-back signal of the CB (52a)*
SG[1] . **Aux OFF-I**	*Module input state: Position indicator/check-back signal of the CB (52b)*
SG[1] . **Ready-I**	*Module input state: CB ready*
SG[1] . **Sys-in-Sync-I**	*State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.*
SG[1] . **Removed-I**	*State of the module input: The withdrawable circuit breaker is Removed*
SG[1] . **Ack TripCmd-I**	*State of the module input: Acknowledgement Signal (for the Trip Command) Module input signal*
SG[1] . **Interl ON1-I**	*State of the module input: Interlocking of the ON command*
SG[1] . **Interl ON2-I**	*State of the module input: Interlocking of the ON command*
SG[1] . **Interl ON3-I**	*State of the module input: Interlocking of the ON command*
SG[1] . **Interl OFF1-I**	*State of the module input: Interlocking of the OFF command*
SG[1] . **Interl OFF2-I**	*State of the module input: Interlocking of the OFF command*
SG[1] . **Interl OFF3-I**	*State of the module input: Interlocking of the OFF command*
SG[1] . **SCmd ON-I**	*State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input*
SG[1] . **SCmd OFF-I**	*State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input*
SG[1] . **Operations Alarm**	*Signal: Too many Operations. (The operations counter »TripCmd Cr« has exceeded the limit set at »Operations Alarm«.)*

1..n, Assignment List	Description
SG[1] . **Isum Intr trip: IL1**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1*
SG[1] . **Isum Intr trip: IL2**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2*
SG[1] . **Isum Intr trip: IL3**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3*
SG[1] . **Isum Intr trip**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.*
SG[1] . **Res TripCmd Cr**	*Signal: Resetting of the Counter: Total number of trips of the switchgear*
SG[1] . **Res Sum trip**	*Signal: Reset summation of the tripping currents*
SG[1] . **WearLevel Alarm**	*Signal: Threshold for the Alarm*
SG[1] . **WearLevel Lockout**	*Signal: Threshold for the Lockout Level*
SG[1] . **Res CB OPEN capacity**	*Signal: Reset of the wear maintenance curve (i. e. of the counter for the Circuit Breaker OPEN capacity.*
SG[1] . **Isum Intr ph Alm**	*Signal: Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.*
SG[1] . **Res Isum Intr ph Alm**	*Signal: Reset of the Alarm, "the per hour Sum (Limit) of interrupting currents has been exceeded".*
SG[2] . **SI SingleContactInd**	*Signal: The Position of the Switchgear is detected by one auxiliary contact (pole) only. Thus indeterminate and disturbed Positions cannot be detected.*
SG[2] . **Pos not ON**	*Signal: Pos not ON*
SG[2] . **Pos ON**	*Signal: Circuit Breaker is in ON-Position*
SG[2] . **Pos OFF**	*Signal: Circuit Breaker is in OFF-Position*
SG[2] . **Pos Indeterm**	*Signal: Circuit Breaker is in Indeterminate Position*
SG[2] . **Pos Disturb**	*Signal: Circuit Breaker Disturbed - Undefined Breaker Position. The Position Indicators contradict themselves. After expiring of a supervision timer this signal becomes true.*
SG[2] . **Ready**	*Signal: Circuit breaker is ready for operation.*
SG[2] . **t-Dwell**	*Signal: Dwell time*
SG[2] . **Removed**	*Signal: The withdrawable circuit breaker is Removed*
SG[2] . **Interl ON**	*Signal: One or more IL_On inputs are active.*
SG[2] . **Interl OFF**	*Signal: One or more IL_Off inputs are active.*
SG[2] . **CES succesf**	*Signal: Command Execution Supervision: Switching command executed successfully.*

1..n, Assignment List	Description
SG[2] . **CES Disturbed**	*Signal: Command Execution Supervision: Switching Command unsuccessful. Switchgear in disturbed position.*
SG[2] . **CES Fail TripCmd**	*Signal: Command Execution Supervision: Command execution failed because trip command is pending.*
SG[2] . **CES SwitchDir**	*Signal: Command Execution Supervision respectively Switching Direction Control: This signal becomes true, if a switch command is issued even though the switchgear is already in the requested position. Example: A switchgear that is already OFF should be switched OFF again (doubly). The same applies to CLOSE commands.*
SG[2] . **CES ON d OFF**	*Signal: Command Execution Supervision: On Command during a pending OFF Command.*
SG[2] . **CES SG not ready**	*Signal: Command Execution Supervision: Switchgear not ready*
SG[2] . **CES Fiel Interl**	*Signal: Command Execution Supervision: Switching Command not executed because of field interlocking.*
SG[2] . **CES SyncTimeout**	*Signal: Command Execution Supervision: Switching Command not executed. No Synchronization signal while t-sync was running.*
SG[2] . **CES SG removed**	*Signal: Command Execution Supervision: Switching Command unsuccessful, Switchgear removed.*
SG[2] . **Prot ON**	*Signal: ON Command issued by the Prot module*
SG[2] . **TripCmd**	*Signal: Trip Command*
SG[2] . **Ack TripCmd**	*Signal: Acknowledge Trip Command*
SG[2] . **ON incl Prot ON**	*Signal: The ON Command includes the ON Command issued by the Protection module.*
SG[2] . **OFF incl TripCmd**	*Signal: The OFF Command includes the OFF Command issued by the Protection module.*
SG[2] . **Position Ind manipul**	*Signal: Position Indicators faked*
SG[2] . **SGwear Slow SG**	*Signal: Alarm, the circuit breaker (load-break switch) becomes slower*
SG[2] . **Res SGwear Sl SG**	*Signal: Resetting the slow Switchgear Alarm*
SG[2] . **ON Cmd**	*Signal: ON Command issued to the switchgear. Depending on the setting the signal may include the ON command of the Prot module.*
SG[2] . **OFF Cmd**	*Signal: OFF Command issued to the switchgear. Depending on the setting the signal may include the OFF command of the Prot module.*
SG[2] . **ON Cmd manual**	*Signal: ON Cmd manual*
SG[2] . **OFF Cmd manual**	*Signal: OFF Cmd manual*
SG[2] . **Sync ON request**	*Signal: Synchronous ON request*

1..n, Assignment List	Description
SG[2] . **Aux ON-I**	*Module Input State: Position indicator/check-back signal of the CB (52a)*
SG[2] . **Aux OFF-I**	*Module input state: Position indicator/check-back signal of the CB (52b)*
SG[2] . **Ready-I**	*Module input state: CB ready*
SG[2] . **Sys-in-Sync-I**	*State of the module input: This signals has to become true within the synchronization time. If not, switching is unsuccessful.*
SG[2] . **Removed-I**	*State of the module input: The withdrawable circuit breaker is Removed*
SG[2] . **Ack TripCmd-I**	*State of the module input: Acknowledgement Signal (for the Trip Command) Module input signal*
SG[2] . **Interl ON1-I**	*State of the module input: Interlocking of the ON command*
SG[2] . **Interl ON2-I**	*State of the module input: Interlocking of the ON command*
SG[2] . **Interl ON3-I**	*State of the module input: Interlocking of the ON command*
SG[2] . **Interl OFF1-I**	*State of the module input: Interlocking of the OFF command*
SG[2] . **Interl OFF2-I**	*State of the module input: Interlocking of the OFF command*
SG[2] . **Interl OFF3-I**	*State of the module input: Interlocking of the OFF command*
SG[2] . **SCmd ON-I**	*State of the module input: Switching ON Command, e.g. the state of the Logics or the state of the digital input*
SG[2] . **SCmd OFF-I**	*State of the module input: Switching OFF Command, e.g. the state of the Logics or the state of the digital input*
SG[2] . **Operations Alarm**	*Signal: Too many Operations. (The operations counter »TripCmd Cr« has exceeded the limit set at »Operations Alarm«.)*
SG[2] . **Isum Intr trip: IL1**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL1*
SG[2] . **Isum Intr trip: IL2**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL2*
SG[2] . **Isum Intr trip: IL3**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded: IL3*
SG[2] . **Isum Intr trip**	*Signal: Maximum permissible Summation of the interrupting (tripping) currents exceeded in at least one phase.*
SG[2] . **Res TripCmd Cr**	*Signal: Resetting of the Counter: Total number of trips of the switchgear*
SG[2] . **Res Sum trip**	*Signal: Reset summation of the tripping currents*
SG[2] . **WearLevel Alarm**	*Signal: Threshold for the Alarm*
SG[2] . **WearLevel Lockout**	*Signal: Threshold for the Lockout Level*
SG[2] . **Res CB OPEN capacity**	*Signal: Reset of the wear maintenance curve (i. e. of the counter for the Circuit Breaker OPEN capacity.*
SG[2] . **Isum Intr ph Alm**	*Signal: Alarm, the per hour Sum (Limit) of interrupting currents has been exceeded.*

1..n, Assignment List	Description
SG[2] . **Res Isum Intr ph Alm**	*Signal: Reset of the Alarm, "the per hour Sum (Limit) of interrupting currents has been exceeded".*
Id . **active**	*Signal: active*
Id . **ExBlo**	*Signal: External Blocking*
Id . **Blo TripCmd**	*Signal: Trip Command blocked*
Id . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Id . **Alarm L1**	*Signal: Alarm System Phase L1*
Id . **Alarm L2**	*Signal: Alarm System Phase L2*
Id . **Alarm L3**	*Signal: Alarm System L3*
Id . **Alarm**	*Signal: Alarm*
Id . **Trip L1**	*Signal: Trip System Phase L1*
Id . **Trip L2**	*Signal: Trip System Phase L2*
Id . **Trip L3**	*Signal: Trip System Phase L3*
Id . **Trip**	*Signal: Trip*
Id . **TripCmd**	*Signal: Trip Command*
Id . **Blo H2**	*Signal: Blocked by Harmonic:2*
Id . **Blo H4**	*Signal: Blocked by Harmonic:4*
Id . **Blo H5**	*Signal: Blocked by Harmonic:5*
Id . **H2,H4,H5 Blo**	*Signal: Blocked by Harmonics (Inhibit)*
Id . **CT Satur.Stab. triggered**	*Signal: Temporary restraining of the Phase Differential Protection, triggered by the detection of an external fault in case of CT saturation.*
Id . **Transient**	*Signal: Temporary stabilization of the differential protection afterwards the transformer is being engergized.*
Id . **Restraining**	*Signal: Restraining of the differential protection by means of rising the tripping curve.*
Id . **CT Satur.Stab. L1 trig.**	*Signal: Temporary restraining of the Phase Differential Protection in phase L1, triggered by the detection of an external phase L1 fault in case of CT saturation.*
Id . **CT Satur.Stab. L2 trig.**	*Signal: Temporary restraining of the Phase Differential Protection in phase L2, triggered by the detection of an external phase L2 fault in case of CT saturation.*
Id . **CT Satur.Stab. L3 trig.**	*Signal: Temporary restraining of the Phase Differential Protection in phase L3, triggered by the detection of an external phase L3 fault in case of CT saturation.*
Id . **Restraining: L1**	*Restraining: L1*
Id . **Restraining: L2**	*Restraining: L2*

1..n, Assignment List	Description
Id . **Restraining: L3**	*Restraining: L3*
Id . **IH2 Blo L1**	*Signal:Phase L1: Blocking of the Phase Differential Protection because of second Harmonic.*
Id . **IH2 Blo L2**	*Signal:Phase L2: Blocking of the Phase Differential Protection because of second Harmonic.*
Id . **IH2 Blo L3**	*Signal:Phase L3: Blocking of the Phase Differential Protection because of second Harmonic.*
Id . **IH4 Blo L1**	*Signal:Phase L1: Blocking of the Phase Differential Protection because of fourth Harmonic.*
Id . **IH4 Blo L2**	*Signal:Phase L2: Blocking of the Phase Differential Protection because of fourth Harmonic.*
Id . **IH4 Blo L3**	*Signal:Phase L3: Blocking of the Phase Differential Protection because of fourth Harmonic.*
Id . **IH5 Blo L1**	*Signal:Phase L1: Blocking of the Phase Differential Protection because of fifth Harmonic.*
Id . **IH5 Blo L2**	*Signal:Phase L2: Blocking of the Phase Differential Protection because of fifth Harmonic.*
Id . **IH5 Blo L3**	*Signal:Phase L3: Blocking of the Phase Differential Protection because of fifth Harmonic.*
Id . **ExBlo1-I**	*Module input state: External blocking1*
Id . **ExBlo2-I**	*Module input state: External blocking2*
Id . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IdH . **active**	*Signal: active*
IdH . **ExBlo**	*Signal: External Blocking*
IdH . **Blo TripCmd**	*Signal: Trip Command blocked*
IdH . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IdH . **Alarm L1**	*Signal: Alarm System Phase L1*
IdH . **Alarm L2**	*Signal: Alarm System Phase L2*
IdH . **Alarm L3**	*Signal: Alarm System L3*
IdH . **Alarm**	*Signal: Alarm*
IdH . **Trip L1**	*Signal: Trip System Phase L1*
IdH . **Trip L2**	*Signal: Trip System Phase L2*
IdH . **Trip L3**	*Signal: Trip System Phase L3*
IdH . **Trip**	*Signal: Trip*
IdH . **TripCmd**	*Signal: Trip Command*
IdH . **ExBlo1-I**	*Module input state: External blocking1*

1..n, Assignment List	Description
IdH . **ExBlo2-I**	*Module input state: External blocking2*
IdH . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IdG[1] . **active**	*Signal: active*
IdG[1] . **ExBlo**	*Signal: External Blocking*
IdG[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
IdG[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IdG[1] . **Alarm**	*Signal: Alarm*
IdG[1] . **Trip**	*Signal: Trip*
IdG[1] . **TripCmd**	*Signal: Trip Command*
IdG[1] . **ExBlo1-I**	*Module input state: External blocking1*
IdG[1] . **ExBlo2-I**	*Module input state: External blocking2*
IdG[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IdGH[1] . **active**	*Signal: active*
IdGH[1] . **ExBlo**	*Signal: External Blocking*
IdGH[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
IdGH[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IdGH[1] . **Alarm**	*Signal: Alarm*
IdGH[1] . **Trip**	*Signal: Trip*
IdGH[1] . **TripCmd**	*Signal: Trip Command*
IdGH[1] . **ExBlo1-I**	*Module input state: External blocking1*
IdGH[1] . **ExBlo2-I**	*Module input state: External blocking2*
IdGH[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IdG[2] . **active**	*Signal: active*
IdG[2] . **ExBlo**	*Signal: External Blocking*
IdG[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
IdG[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IdG[2] . **Alarm**	*Signal: Alarm*
IdG[2] . **Trip**	*Signal: Trip*
IdG[2] . **TripCmd**	*Signal: Trip Command*
IdG[2] . **ExBlo1-I**	*Module input state: External blocking1*
IdG[2] . **ExBlo2-I**	*Module input state: External blocking2*

1..n, Assignment List	Description
IdG[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IdGH[2] . **active**	*Signal: active*
IdGH[2] . **ExBlo**	*Signal: External Blocking*
IdGH[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
IdGH[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IdGH[2] . **Alarm**	*Signal: Alarm*
IdGH[2] . **Trip**	*Signal: Trip*
IdGH[2] . **TripCmd**	*Signal: Trip Command*
IdGH[2] . **ExBlo1-I**	*Module input state: External blocking1*
IdGH[2] . **ExBlo2-I**	*Module input state: External blocking2*
IdGH[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IH2[1] . **active**	*Signal: active*
IH2[1] . **ExBlo**	*Signal: External Blocking*
IH2[1] . **Blo L1**	*Signal: Blocked L1*
IH2[1] . **Blo L2**	*Signal: Blocked L2*
IH2[1] . **Blo L3**	*Signal: Blocked L3*
IH2[1] . **Blo IG meas**	*Signal: Blocking of the ground (earth) protection module (measured ground current)*
IH2[1] . **Blo IG calc**	*Signal: Blocking of the ground (earth) protection module (calculated ground current)*
IH2[1] . **3-ph Blo**	*Signal: Inrush was detected in at least one phase - trip command blocked.*
IH2[1] . **ExBlo1-I**	*Module input state: External blocking1*
IH2[1] . **ExBlo2-I**	*Module input state: External blocking2*
IH2[2] . **active**	*Signal: active*
IH2[2] . **ExBlo**	*Signal: External Blocking*
IH2[2] . **Blo L1**	*Signal: Blocked L1*
IH2[2] . **Blo L2**	*Signal: Blocked L2*
IH2[2] . **Blo L3**	*Signal: Blocked L3*
IH2[2] . **Blo IG meas**	*Signal: Blocking of the ground (earth) protection module (measured ground current)*
IH2[2] . **Blo IG calc**	*Signal: Blocking of the ground (earth) protection module (calculated ground current)*

1..n, Assignment List	Description
IH2[2] . **3-ph Blo**	*Signal: Inrush was detected in at least one phase - trip command blocked.*
IH2[2] . **ExBlo1-I**	*Module input state: External blocking1*
IH2[2] . **ExBlo2-I**	*Module input state: External blocking2*
I[1] . **active**	*Signal: active*
I[1] . **ExBlo**	*Signal: External Blocking*
I[1] . **Ex rev Interl**	*Signal: External reverse Interlocking*
I[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
I[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I[1] . **IH2 Blo**	*Signal: Blocking the trip command by an inrush*
I[1] . **Alarm L1**	*Signal: Alarm L1*
I[1] . **Alarm L2**	*Signal: Alarm L2*
I[1] . **Alarm L3**	*Signal: Alarm L3*
I[1] . **Alarm**	*Signal: Alarm*
I[1] . **Trip L1**	*Signal: General Trip Phase L1*
I[1] . **Trip L2**	*Signal: General Trip Phase L2*
I[1] . **Trip L3**	*Signal: General Trip Phase L3*
I[1] . **Trip**	*Signal: Trip*
I[1] . **TripCmd**	*Signal: Trip Command*
I[1] . **DefaultSet**	*Signal: Default Parameter Set*
I[1] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
I[1] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
I[1] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
I[1] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
I[1] . **ExBlo1-I**	*Module input state: External blocking1*
I[1] . **ExBlo2-I**	*Module input state: External blocking2*
I[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I[1] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
I[1] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
I[1] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
I[1] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
I[1] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*

1..n, Assignment List	Description
I[2] . **active**	*Signal: active*
I[2] . **ExBlo**	*Signal: External Blocking*
I[2] . **Ex rev Interl**	*Signal: External reverse Interlocking*
I[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
I[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I[2] . **IH2 Blo**	*Signal: Blocking the trip command by an inrush*
I[2] . **Alarm L1**	*Signal: Alarm L1*
I[2] . **Alarm L2**	*Signal: Alarm L2*
I[2] . **Alarm L3**	*Signal: Alarm L3*
I[2] . **Alarm**	*Signal: Alarm*
I[2] . **Trip L1**	*Signal: General Trip Phase L1*
I[2] . **Trip L2**	*Signal: General Trip Phase L2*
I[2] . **Trip L3**	*Signal: General Trip Phase L3*
I[2] . **Trip**	*Signal: Trip*
I[2] . **TripCmd**	*Signal: Trip Command*
I[2] . **DefaultSet**	*Signal: Default Parameter Set*
I[2] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
I[2] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
I[2] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
I[2] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
I[2] . **ExBlo1-I**	*Module input state: External blocking1*
I[2] . **ExBlo2-I**	*Module input state: External blocking2*
I[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I[2] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
I[2] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
I[2] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
I[2] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
I[2] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
I[3] . **active**	*Signal: active*
I[3] . **ExBlo**	*Signal: External Blocking*
I[3] . **Ex rev Interl**	*Signal: External reverse Interlocking*
I[3] . **Blo TripCmd**	*Signal: Trip Command blocked*

1..n, Assignment List	Description
I[3] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I[3] . **IH2 Blo**	*Signal: Blocking the trip command by an inrush*
I[3] . **Alarm L1**	*Signal: Alarm L1*
I[3] . **Alarm L2**	*Signal: Alarm L2*
I[3] . **Alarm L3**	*Signal: Alarm L3*
I[3] . **Alarm**	*Signal: Alarm*
I[3] . **Trip L1**	*Signal: General Trip Phase L1*
I[3] . **Trip L2**	*Signal: General Trip Phase L2*
I[3] . **Trip L3**	*Signal: General Trip Phase L3*
I[3] . **Trip**	*Signal: Trip*
I[3] . **TripCmd**	*Signal: Trip Command*
I[3] . **DefaultSet**	*Signal: Default Parameter Set*
I[3] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
I[3] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
I[3] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
I[3] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
I[3] . **ExBlo1-I**	*Module input state: External blocking1*
I[3] . **ExBlo2-I**	*Module input state: External blocking2*
I[3] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I[3] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
I[3] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
I[3] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
I[3] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
I[3] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
I[4] . **active**	*Signal: active*
I[4] . **ExBlo**	*Signal: External Blocking*
I[4] . **Ex rev Interl**	*Signal: External reverse Interlocking*
I[4] . **Blo TripCmd**	*Signal: Trip Command blocked*
I[4] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I[4] . **IH2 Blo**	*Signal: Blocking the trip command by an inrush*
I[4] . **Alarm L1**	*Signal: Alarm L1*
I[4] . **Alarm L2**	*Signal: Alarm L2*

1..n, Assignment List	Description
I[4] . **Alarm L3**	*Signal: Alarm L3*
I[4] . **Alarm**	*Signal: Alarm*
I[4] . **Trip L1**	*Signal: General Trip Phase L1*
I[4] . **Trip L2**	*Signal: General Trip Phase L2*
I[4] . **Trip L3**	*Signal: General Trip Phase L3*
I[4] . **Trip**	*Signal: Trip*
I[4] . **TripCmd**	*Signal: Trip Command*
I[4] . **DefaultSet**	*Signal: Default Parameter Set*
I[4] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
I[4] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
I[4] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
I[4] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
I[4] . **ExBlo1-I**	*Module input state: External blocking1*
I[4] . **ExBlo2-I**	*Module input state: External blocking2*
I[4] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I[4] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
I[4] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
I[4] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
I[4] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
I[4] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
I[5] . **active**	*Signal: active*
I[5] . **ExBlo**	*Signal: External Blocking*
I[5] . **Ex rev Interl**	*Signal: External reverse Interlocking*
I[5] . **Blo TripCmd**	*Signal: Trip Command blocked*
I[5] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I[5] . **IH2 Blo**	*Signal: Blocking the trip command by an inrush*
I[5] . **Alarm L1**	*Signal: Alarm L1*
I[5] . **Alarm L2**	*Signal: Alarm L2*
I[5] . **Alarm L3**	*Signal: Alarm L3*
I[5] . **Alarm**	*Signal: Alarm*
I[5] . **Trip L1**	*Signal: General Trip Phase L1*
I[5] . **Trip L2**	*Signal: General Trip Phase L2*

1..n, Assignment List	Description
I[5] . **Trip L3**	*Signal: General Trip Phase L3*
I[5] . **Trip**	*Signal: Trip*
I[5] . **TripCmd**	*Signal: Trip Command*
I[5] . **DefaultSet**	*Signal: Default Parameter Set*
I[5] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
I[5] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
I[5] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
I[5] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
I[5] . **ExBlo1-I**	*Module input state: External blocking1*
I[5] . **ExBlo2-I**	*Module input state: External blocking2*
I[5] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I[5] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
I[5] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
I[5] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
I[5] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
I[5] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
I[6] . **active**	*Signal: active*
I[6] . **ExBlo**	*Signal: External Blocking*
I[6] . **Ex rev Interl**	*Signal: External reverse Interlocking*
I[6] . **Blo TripCmd**	*Signal: Trip Command blocked*
I[6] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I[6] . **IH2 Blo**	*Signal: Blocking the trip command by an inrush*
I[6] . **Alarm L1**	*Signal: Alarm L1*
I[6] . **Alarm L2**	*Signal: Alarm L2*
I[6] . **Alarm L3**	*Signal: Alarm L3*
I[6] . **Alarm**	*Signal: Alarm*
I[6] . **Trip L1**	*Signal: General Trip Phase L1*
I[6] . **Trip L2**	*Signal: General Trip Phase L2*
I[6] . **Trip L3**	*Signal: General Trip Phase L3*
I[6] . **Trip**	*Signal: Trip*
I[6] . **TripCmd**	*Signal: Trip Command*
I[6] . **DefaultSet**	*Signal: Default Parameter Set*

1..n, Assignment List	Description
I[6] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
I[6] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
I[6] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
I[6] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
I[6] . **ExBlo1-I**	*Module input state: External blocking1*
I[6] . **ExBlo2-I**	*Module input state: External blocking2*
I[6] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I[6] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
I[6] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
I[6] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
I[6] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
I[6] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
IG[1] . **active**	*Signal: active*
IG[1] . **ExBlo**	*Signal: External Blocking*
IG[1] . **Ex rev Interl**	*Signal: External reverse Interlocking*
IG[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
IG[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IG[1] . **Alarm**	*Signal: Alarm IG*
IG[1] . **Trip**	*Signal: Trip*
IG[1] . **TripCmd**	*Signal: Trip Command*
IG[1] . **IGH2 Blo**	*Signal: blocked by an inrush*
IG[1] . **DefaultSet**	*Signal: Default Parameter Set*
IG[1] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
IG[1] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
IG[1] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
IG[1] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
IG[1] . **ExBlo1-I**	*Module input state: External blocking1*
IG[1] . **ExBlo2-I**	*Module input state: External blocking2*
IG[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IG[1] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
IG[1] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
IG[1] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*

1..n, Assignment List	Description
IG[1] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
IG[1] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
IG[2] . **active**	*Signal: active*
IG[2] . **ExBlo**	*Signal: External Blocking*
IG[2] . **Ex rev Interl**	*Signal: External reverse Interlocking*
IG[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
IG[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IG[2] . **Alarm**	*Signal: Alarm IG*
IG[2] . **Trip**	*Signal: Trip*
IG[2] . **TripCmd**	*Signal: Trip Command*
IG[2] . **IGH2 Blo**	*Signal: blocked by an inrush*
IG[2] . **DefaultSet**	*Signal: Default Parameter Set*
IG[2] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
IG[2] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
IG[2] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
IG[2] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
IG[2] . **ExBlo1-I**	*Module input state: External blocking1*
IG[2] . **ExBlo2-I**	*Module input state: External blocking2*
IG[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IG[2] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
IG[2] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
IG[2] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
IG[2] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
IG[2] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
IG[3] . **active**	*Signal: active*
IG[3] . **ExBlo**	*Signal: External Blocking*
IG[3] . **Ex rev Interl**	*Signal: External reverse Interlocking*
IG[3] . **Blo TripCmd**	*Signal: Trip Command blocked*
IG[3] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IG[3] . **Alarm**	*Signal: Alarm IG*
IG[3] . **Trip**	*Signal: Trip*
IG[3] . **TripCmd**	*Signal: Trip Command*

1..n, Assignment List	Description
IG[3] . **IGH2 Blo**	*Signal: blocked by an inrush*
IG[3] . **DefaultSet**	*Signal: Default Parameter Set*
IG[3] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
IG[3] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
IG[3] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
IG[3] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
IG[3] . **ExBlo1-I**	*Module input state: External blocking1*
IG[3] . **ExBlo2-I**	*Module input state: External blocking2*
IG[3] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IG[3] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
IG[3] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
IG[3] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
IG[3] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
IG[3] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
IG[4] . **active**	*Signal: active*
IG[4] . **ExBlo**	*Signal: External Blocking*
IG[4] . **Ex rev Interl**	*Signal: External reverse Interlocking*
IG[4] . **Blo TripCmd**	*Signal: Trip Command blocked*
IG[4] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
IG[4] . **Alarm**	*Signal: Alarm IG*
IG[4] . **Trip**	*Signal: Trip*
IG[4] . **TripCmd**	*Signal: Trip Command*
IG[4] . **IGH2 Blo**	*Signal: blocked by an inrush*
IG[4] . **DefaultSet**	*Signal: Default Parameter Set*
IG[4] . **AdaptSet 1**	*Signal: Adaptive Parameter 1*
IG[4] . **AdaptSet 2**	*Signal: Adaptive Parameter 2*
IG[4] . **AdaptSet 3**	*Signal: Adaptive Parameter 3*
IG[4] . **AdaptSet 4**	*Signal: Adaptive Parameter 4*
IG[4] . **ExBlo1-I**	*Module input state: External blocking1*
IG[4] . **ExBlo2-I**	*Module input state: External blocking2*
IG[4] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
IG[4] . **Ex rev Interl-I**	*Module input state: External reverse interlocking*

1..n, Assignment List	Description
IG[4] . **AdaptSet1-I**	*Module input state: Adaptive Parameter1*
IG[4] . **AdaptSet2-I**	*Module input state: Adaptive Parameter2*
IG[4] . **AdaptSet3-I**	*Module input state: Adaptive Parameter3*
IG[4] . **AdaptSet4-I**	*Module input state: Adaptive Parameter4*
ThR . **active**	*Signal: active*
ThR . **ExBlo**	*Signal: External Blocking*
ThR . **Blo TripCmd**	*Signal: Trip Command blocked*
ThR . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
ThR . **Alarm**	*Signal: Alarm Thermal Overload*
ThR . **Trip**	*Signal: Trip*
ThR . **TripCmd**	*Signal: Trip Command*
ThR . **Res Thermal Cap**	*Signal: Resetting Thermal Replica*
ThR . **ExBlo1-I**	*Module input state: External blocking1*
ThR . **ExBlo2-I**	*Module input state: External blocking2*
ThR . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I2>[1] . **active**	*Signal: active*
I2>[1] . **ExBlo**	*Signal: External Blocking*
I2>[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
I2>[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I2>[1] . **Alarm**	*Signal: Alarm Negative Sequence*
I2>[1] . **Trip**	*Signal: Trip*
I2>[1] . **TripCmd**	*Signal: Trip Command*
I2>[1] . **ExBlo1-I**	*Module input state: External blocking1*
I2>[1] . **ExBlo2-I**	*Module input state: External blocking2*
I2>[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
I2>[2] . **active**	*Signal: active*
I2>[2] . **ExBlo**	*Signal: External Blocking*
I2>[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
I2>[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
I2>[2] . **Alarm**	*Signal: Alarm Negative Sequence*
I2>[2] . **Trip**	*Signal: Trip*
I2>[2] . **TripCmd**	*Signal: Trip Command*

1..n, Assignment List	Description
I2>[2] . **ExBlo1-I**	*Module input state: External blocking1*
I2>[2] . **ExBlo2-I**	*Module input state: External blocking2*
I2>[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
SOTF . **active**	*Signal: active*
SOTF . **ExBlo**	*Signal: External Blocking*
SOTF . **Ex rev Interl**	*Signal: External reverse Interlocking*
SOTF . **enabled**	*Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.*
SOTF . **I<**	*Signal: No Load Current.*
SOTF . **ExBlo1-I**	*Module input state: External blocking*
SOTF . **ExBlo2-I**	*Module input state: External blocking*
SOTF . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
SOTF . **Ext SOTF-I**	*Module input state: External Switch Onto Fault Alarm*
CLPU . **active**	*Signal: active*
CLPU . **ExBlo**	*Signal: External Blocking*
CLPU . **Ex rev Interl**	*Signal: External reverse Interlocking*
CLPU . **enabled**	*Signal: Cold Load enabled*
CLPU . **detected**	*Signal: Cold Load detected*
CLPU . **I<**	*Signal: No Load Current.*
CLPU . **Load Inrush**	*Signal: Load Inrush*
CLPU . **Settle Time**	*Signal: Settle Time*
CLPU . **ExBlo1-I**	*Module input state: External blocking*
CLPU . **ExBlo2-I**	*Module input state: External blocking*
CLPU . **Ex rev Interl-I**	*Module input state: External reverse interlocking*
ExP[1] . **active**	*Signal: active*
ExP[1] . **ExBlo**	*Signal: External Blocking*
ExP[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
ExP[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
ExP[1] . **Alarm**	*Signal: Alarm*
ExP[1] . **Trip**	*Signal: Trip*
ExP[1] . **TripCmd**	*Signal: Trip Command*
ExP[1] . **ExBlo1-I**	*Module input state: External blocking1*

1..n, Assignment List	Description
ExP[1] . **ExBlo2-I**	*Module input state: External blocking2*
ExP[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
ExP[1] . **Alarm-I**	*Module input state: Alarm*
ExP[1] . **Trip-I**	*Module input state: Trip*
ExP[2] . **active**	*Signal: active*
ExP[2] . **ExBlo**	*Signal: External Blocking*
ExP[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
ExP[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
ExP[2] . **Alarm**	*Signal: Alarm*
ExP[2] . **Trip**	*Signal: Trip*
ExP[2] . **TripCmd**	*Signal: Trip Command*
ExP[2] . **ExBlo1-I**	*Module input state: External blocking1*
ExP[2] . **ExBlo2-I**	*Module input state: External blocking2*
ExP[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
ExP[2] . **Alarm-I**	*Module input state: Alarm*
ExP[2] . **Trip-I**	*Module input state: Trip*
ExP[3] . **active**	*Signal: active*
ExP[3] . **ExBlo**	*Signal: External Blocking*
ExP[3] . **Blo TripCmd**	*Signal: Trip Command blocked*
ExP[3] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
ExP[3] . **Alarm**	*Signal: Alarm*
ExP[3] . **Trip**	*Signal: Trip*
ExP[3] . **TripCmd**	*Signal: Trip Command*
ExP[3] . **ExBlo1-I**	*Module input state: External blocking1*
ExP[3] . **ExBlo2-I**	*Module input state: External blocking2*
ExP[3] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
ExP[3] . **Alarm-I**	*Module input state: Alarm*
ExP[3] . **Trip-I**	*Module input state: Trip*
ExP[4] . **active**	*Signal: active*
ExP[4] . **ExBlo**	*Signal: External Blocking*
ExP[4] . **Blo TripCmd**	*Signal: Trip Command blocked*
ExP[4] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*

1..n, Assignment List	Description
ExP[4] . **Alarm**	*Signal: Alarm*
ExP[4] . **Trip**	*Signal: Trip*
ExP[4] . **TripCmd**	*Signal: Trip Command*
ExP[4] . **ExBlo1-I**	*Module input state: External blocking1*
ExP[4] . **ExBlo2-I**	*Module input state: External blocking2*
ExP[4] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
ExP[4] . **Alarm-I**	*Module input state: Alarm*
ExP[4] . **Trip-I**	*Module input state: Trip*
Ext Sudd Press . **active**	*Signal: active*
Ext Sudd Press . **ExBlo**	*Signal: External Blocking*
Ext Sudd Press . **Blo TripCmd**	*Signal: Trip Command blocked*
Ext Sudd Press . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Ext Sudd Press . **Alarm**	*Signal: Alarm*
Ext Sudd Press . **Trip**	*Signal: Trip*
Ext Sudd Press . **TripCmd**	*Signal: Trip Command*
Ext Sudd Press . **ExBlo1-I**	*Module input state: External blocking1*
Ext Sudd Press . **ExBlo2-I**	*Module input state: External blocking2*
Ext Sudd Press . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
Ext Sudd Press . **Alarm-I**	*Module input state: Alarm*
Ext Sudd Press . **Trip-I**	*Module input state: Trip*
Ext Oil Temp . **active**	*Signal: active*
Ext Oil Temp . **ExBlo**	*Signal: External Blocking*
Ext Oil Temp . **Blo TripCmd**	*Signal: Trip Command blocked*
Ext Oil Temp . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Ext Oil Temp . **Alarm**	*Signal: Alarm*
Ext Oil Temp . **Trip**	*Signal: Trip*
Ext Oil Temp . **TripCmd**	*Signal: Trip Command*
Ext Oil Temp . **ExBlo1-I**	*Module input state: External blocking1*
Ext Oil Temp . **ExBlo2-I**	*Module input state: External blocking2*

1..n, Assignment List	Description
Ext Oil Temp . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
Ext Oil Temp . **Alarm-I**	*Module input state: Alarm*
Ext Oil Temp . **Trip-I**	*Module input state: Trip*
Ext Temp Superv[1] . **active**	*Signal: active*
Ext Temp Superv[1] . **ExBlo**	*Signal: External Blocking*
Ext Temp Superv[1] . **Blo TripCmd**	*Signal: Trip Command blocked*
Ext Temp Superv[1] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Ext Temp Superv[1] . **Alarm**	*Signal: Alarm*
Ext Temp Superv[1] . **Trip**	*Signal: Trip*
Ext Temp Superv[1] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[1] . **ExBlo1-I**	*Module input state: External blocking1*
Ext Temp Superv[1] . **ExBlo2-I**	*Module input state: External blocking2*
Ext Temp Superv[1] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
Ext Temp Superv[1] . **Alarm-I**	*Module input state: Alarm*
Ext Temp Superv[1] . **Trip-I**	*Module input state: Trip*
Ext Temp Superv[2] . **active**	*Signal: active*
Ext Temp Superv[2] . **ExBlo**	*Signal: External Blocking*
Ext Temp Superv[2] . **Blo TripCmd**	*Signal: Trip Command blocked*
Ext Temp Superv[2] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Ext Temp Superv[2] . **Alarm**	*Signal: Alarm*
Ext Temp Superv[2] . **Trip**	*Signal: Trip*
Ext Temp Superv[2] . **TripCmd**	*Signal: Trip Command*

1..n, Assignment List	Description
Ext Temp Superv[2] . **ExBlo1-I**	*Module input state: External blocking1*
Ext Temp Superv[2] . **ExBlo2-I**	*Module input state: External blocking2*
Ext Temp Superv[2] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
Ext Temp Superv[2] . **Alarm-I**	*Module input state: Alarm*
Ext Temp Superv[2] . **Trip-I**	*Module input state: Trip*
Ext Temp Superv[3] . **active**	*Signal: active*
Ext Temp Superv[3] . **ExBlo**	*Signal: External Blocking*
Ext Temp Superv[3] . **Blo TripCmd**	*Signal: Trip Command blocked*
Ext Temp Superv[3] . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
Ext Temp Superv[3] . **Alarm**	*Signal: Alarm*
Ext Temp Superv[3] . **Trip**	*Signal: Trip*
Ext Temp Superv[3] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[3] . **ExBlo1-I**	*Module input state: External blocking1*
Ext Temp Superv[3] . **ExBlo2-I**	*Module input state: External blocking2*
Ext Temp Superv[3] . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
Ext Temp Superv[3] . **Alarm-I**	*Module input state: Alarm*
Ext Temp Superv[3] . **Trip-I**	*Module input state: Trip*
URTD . **W1L1 Superv**	*Signal: Winding1 Phase L1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **W1L2 Superv**	*Signal: Winding1 Phase L2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **W1L3 Superv**	*Signal: Winding1 Phase L3, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*

1..n, Assignment List	Description
URTD . **W2L1 Superv**	*Signal: Winding2 Phase L1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **W2L2 Superv**	*Signal: Winding2 Phase L2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **W2L3 Superv**	*Signal: Winding2 Phase L3, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Amb1 Superv**	*Signal: Ambient1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Amb2 Superv**	*Signal: Ambient2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Aux1 Superv**	*Signal: Auxiliary1, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Aux2 Superv**	*Signal: Auxiliary2, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Aux3 Superv**	*Signal: Auxiliary3, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Aux4 Superv**	*Signal: Auxiliary4, Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that this RTD channel is healthy.)*
URTD . **Superv**	*Signal: URTD Channel Supervision. The value “1” reports a detected channel failure. (The value “0” means that all RTD channels are healthy.)*
URTD . **Connection active**	*Signal: There is an active connection between the Temperature Detector (URTD) and the protective relay.*
URTD . **Outs forced**	*Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.*
RTD . **active**	*Signal: active*
RTD . **ExBlo**	*Signal: External Blocking*
RTD . **Blo TripCmd**	*Signal: Trip Command blocked*
RTD . **ExBlo TripCmd**	*Signal: External Blocking of the Trip Command*
RTD . **Alarm**	*Alarm RTD Temperature Protection*
RTD . **Trip**	*Signal: Trip*
RTD . **TripCmd**	*Signal: Trip Command*

1..n, Assignment List	Description
RTD . **W1L1 Trip**	*Winding1 Phase L1 Signal: Trip*
RTD . **W1L1 Alarm**	*Winding1 Phase L1 Alarm RTD Temperature Protection*
RTD . **W1L1 Timeout Alarm**	*Winding1 Phase L1 Timeout Alarm*
RTD . **W1L1 Invalid**	*Winding1 Phase L1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **W1L2 Trip**	*Winding1 Phase L2 Signal: Trip*
RTD . **W1L2 Alarm**	*Winding1 Phase L2 Alarm RTD Temperature Protection*
RTD . **W1L2 Timeout Alarm**	*Winding1 Phase L2 Timeout Alarm*
RTD . **W1L2 Invalid**	*Winding1 Phase L2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **W1L3 Trip**	*Winding1 Phase L3 Signal: Trip*
RTD . **W1L3 Alarm**	*Winding1 Phase L3 Alarm RTD Temperature Protection*
RTD . **W1L3 Timeout Alarm**	*Winding1 Phase L3 Timeout Alarm*
RTD . **W1L3 Invalid**	*Winding1 Phase L3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **W2L1 Trip**	*Winding2 Phase L1 Signal: Trip*
RTD . **W2L1 Alarm**	*Winding2 Phase L1 Alarm RTD Temperature Protection*
RTD . **W2L1 Timeout Alarm**	*Winding2 Phase L1 Timeout Alarm*
RTD . **W2L1 Invalid**	*Winding2 Phase L1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **W2L2 Trip**	*Winding2 Phase L2 Signal: Trip*
RTD . **W2L2 Alarm**	*Winding2 Phase L2 Alarm RTD Temperature Protection*
RTD . **W2L2 Timeout Alarm**	*Winding2 Phase L2 Timeout Alarm*
RTD . **W2L2 Invalid**	*Winding2 Phase L2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **W2L3 Trip**	*Winding2 Phase L3 Signal: Trip*
RTD . **W2L3 Alarm**	*Winding2 Phase L3 Alarm RTD Temperature Protection*
RTD . **W2L3 Timeout Alarm**	*Winding2 Phase L3 Timeout Alarm*
RTD . **W2L3 Invalid**	*Winding2 Phase L3 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Amb 1 Trip**	*Ambient 1 Signal: Trip*

1..n, Assignment List	Description
RTD . **Amb 1 Alarm**	*Ambient 1 Alarm RTD Temperature Protection*
RTD . **Amb 1 Timeout Alarm**	*Ambient 1 Timeout Alarm*
RTD . **Amb 1 Invalid**	*Ambient 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Amb 2 Trip**	*Ambient 2 Signal: Trip*
RTD . **Amb 2 Alarm**	*Ambient 2 Alarm RTD Temperature Protection*
RTD . **Amb 2 Timeout Alarm**	*Ambient 2 Timeout Alarm*
RTD . **Amb 2 Invalid**	*Ambient 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Aux 1 Trip**	*Auxiliary 1 Signal: Trip*
RTD . **Aux 1 Alarm**	*Auxiliary 1 Alarm RTD Temperature Protection*
RTD . **Aux 1 Timeout Alarm**	*Auxiliary 1 Timeout Alarm*
RTD . **Aux 1 Invalid**	*Auxiliary 1 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Aux 2 Trip**	*Auxiliary 2 Signal: Trip*
RTD . **Aux 2 Alarm**	*Auxiliary 2 Alarm RTD Temperature Protection*
RTD . **Aux 2 Timeout Alarm**	*Auxiliary 2 Timeout Alarm*
RTD . **Aux 2 Invalid**	*Auxiliary 2 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Aux 3 Trip**	*Auxiliary 3 Signal: Trip*
RTD . **Aux 3 Alarm**	*Auxiliary 3 Alarm RTD Temperature Protection*
RTD . **Aux 3 Timeout Alarm**	*Auxiliary 3 Timeout Alarm*
RTD . **Aux 3 Invalid**	*Auxiliary 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Aux4 Trip**	*Auxiliary 4 Signal: Trip*
RTD . **Aux4 Alarm**	*Auxiliary 4 Alarm RTD Temperature Protection*
RTD . **Aux4 Timeout Alarm**	*Auxiliary 4 Timeout Alarm*
RTD . **Aux4 Invalid**	*Auxiliary 4 Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Trip WD W1 Group**	*Trip all Windings of group W1*

1..n, Assignment List	Description
RTD . **Alarm WD W1 Group**	*Alarm all Windings of group W1*
RTD . **TimeoutAlmWDW1Grp**	*Timeout Alarm of group W1*
RTD . **Windg W1 Group Invalid**	*Winding W1 Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Trip WD W2 Group**	*Trip all Windings of group W2*
RTD . **Alarm WD W2 Group**	*Alarm all Windings of group W2*
RTD . **TimeoutAlmWDW2Grp**	*Timeout Alarm of group W2*
RTD . **Windg W2 Group Invalid**	*Winding W2 Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Trip Amb Group**	*Trip all Windings of group Ambient*
RTD . **Alarm Amb Group**	*Alarm all Windings of group Ambient*
RTD . **TimeoutAlmAmbGrp**	*Timeout Alarm of group Ambient*
RTD . **Amb Group Invalid**	*Ambient Group Signal: Invalid Temperature Measurement Value (e.g caused by an defective or interrupted RTD Measurement)*
RTD . **Trip Any Group**	*Trip Any Group*
RTD . **Alarm Any Group**	*Alarm Any Group*
RTD . **TimeoutAlmAnyGrp**	*Timeout Alarm Any Group*
RTD . **Trip Group 1**	*Trip Group 1*
RTD . **Trip Group 2**	*Trip Group 2*
RTD . **Timeout Alarm**	*Alarm timeout expired*
RTD . **Trip Aux Group**	*Trip Auxiliary Group*
RTD . **Alarm Aux Group**	*Alarm Auxiliary Group*
RTD . **TimeoutAlmAuxGrp**	*Timeout Alarm Auxiliary Group*
RTD . **AuxGrpInvalid**	*Invalid Auxiliary Group*
RTD . **ExBlo1-I**	*Module input state: External blocking1*
RTD . **ExBlo2-I**	*Module input state: External blocking2*
RTD . **ExBlo TripCmd-I**	*Module input state: External Blocking of the Trip Command*
CBF[1] . **active**	*Signal: active*
CBF[1] . **ExBlo**	*Signal: External Blocking*

1..n, Assignment List	Description
CBF[1] . **Waiting for Trigger**	*Waiting for Trigger*
CBF[1] . **running**	*Signal: CBF-Module started*
CBF[1] . **Alarm**	*Signal: Circuit Breaker Failure*
CBF[1] . **Lockout**	*Signal: Lockout*
CBF[1] . **Res Lockout**	*Signal: Reset Lockout*
CBF[1] . **ExBlo1-I**	*Module input state: External blocking1*
CBF[1] . **ExBlo2-I**	*Module input state: External blocking2*
CBF[1] . **Trigger1-I**	*Module Input: Trigger that will start the CBF*
CBF[1] . **Trigger2-I**	*Module Input: Trigger that will start the CBF*
CBF[1] . **Trigger3-I**	*Module Input: Trigger that will start the CBF*
CBF[2] . **active**	*Signal: active*
CBF[2] . **ExBlo**	*Signal: External Blocking*
CBF[2] . **Waiting for Trigger**	*Waiting for Trigger*
CBF[2] . **running**	*Signal: CBF-Module started*
CBF[2] . **Alarm**	*Signal: Circuit Breaker Failure*
CBF[2] . **Lockout**	*Signal: Lockout*
CBF[2] . **Res Lockout**	*Signal: Reset Lockout*
CBF[2] . **ExBlo1-I**	*Module input state: External blocking1*
CBF[2] . **ExBlo2-I**	*Module input state: External blocking2*
CBF[2] . **Trigger1-I**	*Module Input: Trigger that will start the CBF*
CBF[2] . **Trigger2-I**	*Module Input: Trigger that will start the CBF*
CBF[2] . **Trigger3-I**	*Module Input: Trigger that will start the CBF*
TCS[1] . **active**	*Signal: active*
TCS[1] . **ExBlo**	*Signal: External Blocking*
TCS[1] . **Alarm**	*Signal: Alarm Trip Circuit Supervision*
TCS[1] . **Not Possible**	*Not possible because no state indicator assigned to the breaker.*
TCS[1] . **Aux ON-I**	*Module Input State: Position indicator/check-back signal of the CB (52a)*
TCS[1] . **Aux OFF-I**	*Module input state: Position indicator/check-back signal of the CB (52b)*
TCS[1] . **ExBlo1-I**	*Module input state: External blocking1*
TCS[1] . **ExBlo2-I**	*Module input state: External blocking2*
TCS[2] . **active**	*Signal: active*

1..n, Assignment List	Description
TCS[2] . **ExBlo**	*Signal: External Blocking*
TCS[2] . **Alarm**	*Signal: Alarm Trip Circuit Supervision*
TCS[2] . **Not Possible**	*Not possible because no state indicator assigned to the breaker.*
TCS[2] . **Aux ON-I**	*Module Input State: Position indicator/check-back signal of the CB (52a)*
TCS[2] . **Aux OFF-I**	*Module input state: Position indicator/check-back signal of the CB (52b)*
TCS[2] . **ExBlo1-I**	*Module input state: External blocking1*
TCS[2] . **ExBlo2-I**	*Module input state: External blocking2*
CTS[1] . **active**	*Signal: active*
CTS[1] . **ExBlo**	*Signal: External Blocking*
CTS[1] . **Alarm**	*Signal: Alarm Current Transformer Measuring Circuit Supervision*
CTS[1] . **ExBlo1-I**	*Module input state: External blocking1*
CTS[1] . **ExBlo2-I**	*Module input state: External blocking2*
CTS[2] . **active**	*Signal: active*
CTS[2] . **ExBlo**	*Signal: External Blocking*
CTS[2] . **Alarm**	*Signal: Alarm Current Transformer Measuring Circuit Supervision*
CTS[2] . **ExBlo1-I**	*Module input state: External blocking1*
CTS[2] . **ExBlo2-I**	*Module input state: External blocking2*
SysA . **active**	*Signal: active*
SysA . **ExBlo**	*Signal: External Blocking*
SysA . **Alm Current Demd**	*Signal: Alarm averaged demand current*
SysA . **Alarm I THD**	*Signal: Alarm Total Harmonic Distortion Current*
SysA . **Trip Current Demand**	*Signal: Trip averaged demand current*
SysA . **Trip I THD**	*Signal: Trip Total Harmonic Distortion Current*
SysA . **ExBlo-I**	*Module input state: External blocking*
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*

1..n, Assignment List	Description
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*
BO Slot X2 . **BO 1**	*Signal: Binary Output Relay*
BO Slot X2 . **BO 2**	*Signal: Binary Output Relay*
BO Slot X2 . **BO 3**	*Signal: Binary Output Relay*
BO Slot X2 . **BO 4**	*Signal: Binary Output Relay*
BO Slot X2 . **BO 5**	*Signal: Binary Output Relay*
BO Slot X2 . **BO 6**	*Signal: Binary Output Relay*
BO Slot X2 . **DISARMED!**	*Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance*
BO Slot X2 . **Outs forced**	*Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.*
BO Slot X5 . **BO 1**	*Signal: Binary Output Relay*
BO Slot X5 . **BO 2**	*Signal: Binary Output Relay*
BO Slot X5 . **BO 3**	*Signal: Binary Output Relay*
BO Slot X5 . **BO 4**	*Signal: Binary Output Relay*
BO Slot X5 . **BO 5**	*Signal: Binary Output Relay*
BO Slot X5 . **BO 6**	*Signal: Binary Output Relay*
BO Slot X5 . **DISARMED!**	*Signal: CAUTION! RELAYS DISARMED in order to safely perform maintenance while eliminating the risk of taking an entire process off-line. (Note: The Self Supervision Contact cannot be disarmed). YOU MUST ENSURE that the relays are ARMED AGAIN after maintenance*
BO Slot X5 . **Outs forced**	*Signal: The State of at least one Relay Output has been set by force. That means that the state of at least one Relay is forced and hence does not show the state of the assigned signals.*
Event rec . **Res all records**	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*

1..n, Assignment List	Description
Disturb rec . **recording**	*Signal: Recording*
Disturb rec . **memory full**	*Signal: Memory full*
Disturb rec . **Clear fail**	*Signal: Clear failure in memory*
Disturb rec . **Res all records**	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*
Disturb rec . **Res all records**	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*
Disturb rec . **Man Trigger**	*Signal: Manual Trigger*
Disturb rec . **Start1-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start2-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start3-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start4-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start5-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start6-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start7-I**	*State of the module input:: Trigger event / start recording*
Disturb rec . **Start8-I**	*State of the module input:: Trigger event / start recording*
Fault rec . **Res all records**	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*
Trend rec . **Res all records**	*Signal: All records are being deleted. (Remark: Immediately afterwards, this signal becomes inactive again.)*
SSV . **System Error**	*Signal: Device Failure*
SSV . **SelfSuperVision Contact**	*Signal: SelfSuperVision Contact*
SSV . **New error**	*Signal: A new error message has been issued.*
SSV . **New warning**	*Signal: A new warning message has been issued.*
Syslog . **active**	*Signal: active*
Sys . **Smart view via USB**	*Information whether or not the Smart view access via the USB interface is activated (allowed).*
Sys . **Smart view via Eth**	*Information whether or not the Smart view access via the Ethernet interface is activated (allowed).*
Scada . **SCADA connected**	*At least one SCADA System is connected to the device.*
Scada . **SCADA not connected**	*No SCADA System is connected to the device*

1..n, Assignment List	Description
DNP3 . **busy**	*This message is set if the protocol is started. It will be reset if the protocol is shut down.*
DNP3 . **ready**	*The message will be set if the protocol is successfully started and ready for data exchange.*
DNP3 . **active**	*The communication with the Master (SCADA) is active.* *Note that for TCP/UDP, this state is permanently “Low” unless »DataLink confirm« is set to “Always”.*
DNP3 . **BinaryOutput0**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput1**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput2**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput3**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput4**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput5**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput6**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput7**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput8**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput9**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput10**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput11**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput12**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput13**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput14**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput15**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*

1..n, Assignment List	Description
DNP3 . **BinaryOutput16**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput17**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput18**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput19**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput20**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput21**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput22**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput23**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput24**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput25**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput26**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput27**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput28**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput29**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput30**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput31**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryInput0-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput1-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput2-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput3-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*

1..n, Assignment List	Description
DNP3 . **BinaryInput4-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput5-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput6-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput7-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput8-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput9-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput10-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput11-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput12-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput13-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput14-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput15-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput16-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput17-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput18-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput19-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput20-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput21-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput22-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput23-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*

1..n, Assignment List	Description
DNP3 . **BinaryInput24-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput25-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput26-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput27-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput28-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput29-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput30-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput31-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput32-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput33-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput34-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput35-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput36-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput37-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput38-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput39-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput40-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput41-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput42-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput43-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*

1..n, Assignment List	Description
DNP3 . **BinaryInput44-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput45-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput46-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput47-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput48-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput49-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput50-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput51-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput52-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput53-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput54-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput55-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput56-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput57-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput58-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput59-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput60-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput61-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput62-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*
DNP3 . **BinaryInput63-I**	*Virtual Digital Input (DNP). This corresponds to a virtual binary output of the protective device.*

1..n, Assignment List	Description
Modbus . **Transmission RTU**	*Signal: SCADA active*
Modbus . **Transmission TCP**	*Signal: SCADA active*
Modbus . **Scada Cmd 1**	*Scada Command*
Modbus . **Scada Cmd 2**	*Scada Command*
Modbus . **Scada Cmd 3**	*Scada Command*
Modbus . **Scada Cmd 4**	*Scada Command*
Modbus . **Scada Cmd 5**	*Scada Command*
Modbus . **Scada Cmd 6**	*Scada Command*
Modbus . **Scada Cmd 7**	*Scada Command*
Modbus . **Scada Cmd 8**	*Scada Command*
Modbus . **Scada Cmd 9**	*Scada Command*
Modbus . **Scada Cmd 10**	*Scada Command*
Modbus . **Scada Cmd 11**	*Scada Command*
Modbus . **Scada Cmd 12**	*Scada Command*
Modbus . **Scada Cmd 13**	*Scada Command*
Modbus . **Scada Cmd 14**	*Scada Command*
Modbus . **Scada Cmd 15**	*Scada Command*
Modbus . **Scada Cmd 16**	*Scada Command*
Modbus . **Config Bin Inp1-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp2-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp3-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp4-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp5-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp6-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp7-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp8-I**	*State of the module input: Config Bin Inp*

1..n, Assignment List	Description
Modbus . **Config Bin Inp9-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp10-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp11-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp12-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp13-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp14-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp15-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp16-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp17-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp18-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp19-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp20-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp21-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp22-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp23-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp24-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp25-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp26-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp27-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp28-I**	*State of the module input: Config Bin Inp*

1..n, Assignment List	Description
Modbus . **Config Bin Inp29-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp30-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp31-I**	*State of the module input: Config Bin Inp*
Modbus . **Config Bin Inp32-I**	*State of the module input: Config Bin Inp*
IEC 61850 . **MMS Client connected**	*At least one MMS client is connected to the device*
IEC 61850 . **All Goose Subscriber active**	*All Goose subscriber in the device are working*
IEC 61850 . **GOSINGGIO1.Ind1.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind2.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind3.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind4.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind5.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind6.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind7.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind8.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind9.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind10.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind11.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind12.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind13.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind14.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*

1..n, Assignment List	Description
IEC 61850 . **GOSINGGIO1.Ind15.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind16.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind17.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind18.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind19.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind20.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind21.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind22.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind23.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind24.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind25.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind26.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind27.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind28.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind29.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind30.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind31.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind32.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind1.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind2.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*

1..n, Assignment List	Description
IEC 61850 . **GOSINGGIO2.Ind3.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind4.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind5.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind6.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind7.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind8.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind9.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind10.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind11.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind12.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind13.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind14.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind15.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind16.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind17.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind18.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind19.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind20.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind21.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind22.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*

1..n, Assignment List	Description
IEC 61850 . **GOSINGGIO2.Ind23.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind24.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind25.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind26.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind27.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind28.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind29.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind30.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind31.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO2.Ind32.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind1.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind2.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind3.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind4.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind5.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind6.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind7.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind8.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind9.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind10.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*

1..n, Assignment List	Description
IEC 61850 . **GOSINGGIO1.Ind11.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind12.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind13.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind14.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind15.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind16.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind17.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind18.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind19.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind20.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind21.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind22.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind23.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind24.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind25.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind26.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind27.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind28.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind29.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind30.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*

1..n, Assignment List	Description
IEC 61850 . **GOSINGGIO1.Ind31.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO1.Ind32.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind1.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind2.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind3.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind4.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind5.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind6.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind7.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind8.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind9.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind10.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind11.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind12.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind13.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind14.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind15.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind16.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind17.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind18.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*

1..n, Assignment List	Description
IEC 61850 . **GOSINGGIO2.Ind19.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind20.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind21.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind22.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind23.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind24.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind25.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind26.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind27.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind28.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind29.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind30.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind31.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **GOSINGGIO2.Ind32.q**	*Signal: Virtual Input (IEC61850 GGIO Ind): Self-Supervision of the GGIO Input*
IEC 61850 . **SPCSO1**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO2**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO3**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO4**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO5**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO6**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*

1..n, Assignment List	Description
IEC 61850 . **SPCSO7**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO8**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO9**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO10**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO11**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO12**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO13**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO14**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO15**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO16**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO17**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO18**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO19**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO20**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO21**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO22**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO23**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO24**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO25**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO26**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*

1..n, Assignment List	Description
IEC 61850 . **SPCSO27**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO28**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO29**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO30**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO31**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO32**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC103 . **Scada Cmd 1**	*Scada Command*
IEC103 . **Scada Cmd 2**	*Scada Command*
IEC103 . **Scada Cmd 3**	*Scada Command*
IEC103 . **Scada Cmd 4**	*Scada Command*
IEC103 . **Scada Cmd 5**	*Scada Command*
IEC103 . **Scada Cmd 6**	*Scada Command*
IEC103 . **Scada Cmd 7**	*Scada Command*
IEC103 . **Scada Cmd 8**	*Scada Command*
IEC103 . **Scada Cmd 9**	*Scada Command*
IEC103 . **Scada Cmd 10**	*Scada Command*
IEC103 . **Transmission**	*Signal: SCADA active*
IEC103 . **Failure Event lost**	*Failure event lost*
IEC103 . **Test mode active**	*Signal: IEC103 communication has been switched over into Test Mode.*
IEC103 . **Block MD active**	*Signal: The blocking of IEC103 transmission in monitor direction has been activated.*
IEC103 . **Ex activate test mode-I**	*Module input state: Test Mode of the IEC103 communication.*
IEC103 . **Ex activate Block MD-I**	*Module input state: Activation of the blocking of IEC103 transmission in monitor direction.*
IEC104 . **busy**	*This message is set if the protocol is started. It will be reset if the protocol is shut down.*
IEC104 . **ready**	*The message will be set if the protocol is successfully started and ready for data exchange.*
IEC104 . **Transmission**	*Signal: SCADA active*

1..n, Assignment List	Description
IEC104 . **Failure Event lost**	*Failure event lost*
IEC104 . **Scada Cmd 1**	*Scada Command*
IEC104 . **Scada Cmd 2**	*Scada Command*
IEC104 . **Scada Cmd 3**	*Scada Command*
IEC104 . **Scada Cmd 4**	*Scada Command*
IEC104 . **Scada Cmd 5**	*Scada Command*
IEC104 . **Scada Cmd 6**	*Scada Command*
IEC104 . **Scada Cmd 7**	*Scada Command*
IEC104 . **Scada Cmd 8**	*Scada Command*
IEC104 . **Scada Cmd 9**	*Scada Command*
IEC104 . **Scada Cmd 10**	*Scada Command*
IEC104 . **Scada Cmd 11**	*Scada Command*
IEC104 . **Scada Cmd 12**	*Scada Command*
IEC104 . **Scada Cmd 13**	*Scada Command*
IEC104 . **Scada Cmd 14**	*Scada Command*
IEC104 . **Scada Cmd 15**	*Scada Command*
IEC104 . **Scada Cmd 16**	*Scada Command*
Profibus . **Data OK**	*Data within the Input field are OK (Yes=1)*
Profibus . **SubModul Err**	*Assignable Signal, Failure in Sub-Module, Communication Failure.*
Profibus . **Connection active**	*Connection active*
Profibus . **Scada Cmd 1**	*Scada Command*
Profibus . **Scada Cmd 2**	*Scada Command*
Profibus . **Scada Cmd 3**	*Scada Command*
Profibus . **Scada Cmd 4**	*Scada Command*
Profibus . **Scada Cmd 5**	*Scada Command*
Profibus . **Scada Cmd 6**	*Scada Command*
Profibus . **Scada Cmd 7**	*Scada Command*
Profibus . **Scada Cmd 8**	*Scada Command*
Profibus . **Scada Cmd 9**	*Scada Command*
Profibus . **Scada Cmd 10**	*Scada Command*
Profibus . **Scada Cmd 11**	*Scada Command*

1..n, Assignment List	Description
Profibus . **Scada Cmd 12**	*Scada Command*
Profibus . **Scada Cmd 13**	*Scada Command*
Profibus . **Scada Cmd 14**	*Scada Command*
Profibus . **Scada Cmd 15**	*Scada Command*
Profibus . **Scada Cmd 16**	*Scada Command*
IRIG-B . **IRIG-B active**	*Signal: If there is no valid IRIG-B signal for 60 sec, IRIG-B is regarded as inactive.*
IRIG-B . **High-Low Invert**	*Signal: The High and Low signals of the IRIG-B are inverted. This does NOT mean that the wiring is faulty. If the wiring is faulty no IRIG-B signal will be detected.*
IRIG-B . **Control Signal1**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal2**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal3**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal4**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal5**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal6**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal7**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal8**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal9**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal10**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal11**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*

1..n, Assignment List	Description
IRIG-B . **Control Signal12**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal13**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal14**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal15**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal16**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal17**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
IRIG-B . **Control Signal18**	*Signal: IRIG-B Control Signal. The external IRIG-B generator can set these signals. They can be used for further control procedures inside the device (e.g. logic funtions).*
SNTP . **SNTP active**	*Signal: If there is no valid SNTP signal for 120 sec, SNTP is regarded as inactive.*
TimeSync . **synchronized**	*Clock is synchronized.*
Statistics . **ResFc all**	*Signal: Resetting of all Statistic values (Current Demand, Power Demand, Min, Max)*
Statistics . **ResFc I Demand**	*Signal: Resetting of Statistics - Current Demand (avg, peak avg)*
Statistics . **ResFc Max**	*Signal: Resetting of all Maximum values*
Statistics . **ResFc Min**	*Signal: Resetting of all Minimum values*
Statistics . **StartFc I Demand-I**	*State of the module input: Start of the Statistics of the Current Demand*
Logics . **LE1.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE1.Timer Out**	*Signal: Timer Output*
Logics . **LE1.Out**	*Signal: Latched Output (Q)*
Logics . **LE1.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE1.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE1.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE1.Gate In3-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE1.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE1.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE2.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE2.Timer Out**	*Signal: Timer Output*
Logics . **LE2.Out**	*Signal: Latched Output (Q)*
Logics . **LE2.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE2.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE2.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE2.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE2.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE2.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE3.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE3.Timer Out**	*Signal: Timer Output*
Logics . **LE3.Out**	*Signal: Latched Output (Q)*
Logics . **LE3.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE3.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE3.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE3.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE3.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE3.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE4.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE4.Timer Out**	*Signal: Timer Output*
Logics . **LE4.Out**	*Signal: Latched Output (Q)*
Logics . **LE4.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE4.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE4.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE4.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE4.Gate In4-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE4.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE5.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE5.Timer Out**	*Signal: Timer Output*
Logics . **LE5.Out**	*Signal: Latched Output (Q)*
Logics . **LE5.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE5.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE5.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE5.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE5.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE5.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE6.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE6.Timer Out**	*Signal: Timer Output*
Logics . **LE6.Out**	*Signal: Latched Output (Q)*
Logics . **LE6.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE6.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE6.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE6.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE6.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE6.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE7.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE7.Timer Out**	*Signal: Timer Output*
Logics . **LE7.Out**	*Signal: Latched Output (Q)*
Logics . **LE7.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE7.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE7.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE7.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE7.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE7.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*

1..n, Assignment List	Description
Logics . **LE8.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE8.Timer Out**	*Signal: Timer Output*
Logics . **LE8.Out**	*Signal: Latched Output (Q)*
Logics . **LE8.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE8.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE8.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE8.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE8.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE8.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE9.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE9.Timer Out**	*Signal: Timer Output*
Logics . **LE9.Out**	*Signal: Latched Output (Q)*
Logics . **LE9.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE9.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE9.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE9.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE9.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE9.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE10.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE10.Timer Out**	*Signal: Timer Output*
Logics . **LE10.Out**	*Signal: Latched Output (Q)*
Logics . **LE10.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE10.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE10.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE10.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE10.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE10.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE11.Gate Out**	*Signal: Output of the logic gate*

1..n, Assignment List	Description
Logics . **LE11.Timer Out**	*Signal: Timer Output*
Logics . **LE11.Out**	*Signal: Latched Output (Q)*
Logics . **LE11.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE11.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE11.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE11.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE11.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE11.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE12.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE12.Timer Out**	*Signal: Timer Output*
Logics . **LE12.Out**	*Signal: Latched Output (Q)*
Logics . **LE12.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE12.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE12.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE12.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE12.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE12.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE13.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE13.Timer Out**	*Signal: Timer Output*
Logics . **LE13.Out**	*Signal: Latched Output (Q)*
Logics . **LE13.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE13.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE13.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE13.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE13.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE13.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE14.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE14.Timer Out**	*Signal: Timer Output*

1..n, Assignment List	Description
Logics . **LE14.Out**	*Signal: Latched Output (Q)*
Logics . **LE14.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE14.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE14.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE14.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE14.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE14.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE15.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE15.Timer Out**	*Signal: Timer Output*
Logics . **LE15.Out**	*Signal: Latched Output (Q)*
Logics . **LE15.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE15.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE15.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE15.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE15.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE15.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE16.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE16.Timer Out**	*Signal: Timer Output*
Logics . **LE16.Out**	*Signal: Latched Output (Q)*
Logics . **LE16.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE16.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE16.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE16.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE16.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE16.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE17.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE17.Timer Out**	*Signal: Timer Output*
Logics . **LE17.Out**	*Signal: Latched Output (Q)*

1..n, Assignment List	Description
Logics . **LE17.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE17.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE17.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE17.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE17.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE17.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE18.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE18.Timer Out**	*Signal: Timer Output*
Logics . **LE18.Out**	*Signal: Latched Output (Q)*
Logics . **LE18.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE18.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE18.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE18.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE18.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE18.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE19.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE19.Timer Out**	*Signal: Timer Output*
Logics . **LE19.Out**	*Signal: Latched Output (Q)*
Logics . **LE19.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE19.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE19.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE19.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE19.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE19.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE20.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE20.Timer Out**	*Signal: Timer Output*
Logics . **LE20.Out**	*Signal: Latched Output (Q)*
Logics . **LE20.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, Assignment List	Description
Logics . **LE20.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE20.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE20.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE20.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE20.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE21.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE21.Timer Out**	*Signal: Timer Output*
Logics . **LE21.Out**	*Signal: Latched Output (Q)*
Logics . **LE21.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE21.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE21.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE21.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE21.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE21.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE22.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE22.Timer Out**	*Signal: Timer Output*
Logics . **LE22.Out**	*Signal: Latched Output (Q)*
Logics . **LE22.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE22.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE22.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE22.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE22.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE22.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE23.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE23.Timer Out**	*Signal: Timer Output*
Logics . **LE23.Out**	*Signal: Latched Output (Q)*
Logics . **LE23.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE23.Gate In1-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE23.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE23.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE23.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE23.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE24.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE24.Timer Out**	*Signal: Timer Output*
Logics . **LE24.Out**	*Signal: Latched Output (Q)*
Logics . **LE24.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE24.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE24.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE24.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE24.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE24.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE25.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE25.Timer Out**	*Signal: Timer Output*
Logics . **LE25.Out**	*Signal: Latched Output (Q)*
Logics . **LE25.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE25.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE25.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE25.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE25.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE25.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE26.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE26.Timer Out**	*Signal: Timer Output*
Logics . **LE26.Out**	*Signal: Latched Output (Q)*
Logics . **LE26.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE26.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE26.Gate In2-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE26.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE26.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE26.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE27.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE27.Timer Out**	*Signal: Timer Output*
Logics . **LE27.Out**	*Signal: Latched Output (Q)*
Logics . **LE27.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE27.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE27.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE27.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE27.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE27.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE28.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE28.Timer Out**	*Signal: Timer Output*
Logics . **LE28.Out**	*Signal: Latched Output (Q)*
Logics . **LE28.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE28.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE28.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE28.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE28.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE28.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE29.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE29.Timer Out**	*Signal: Timer Output*
Logics . **LE29.Out**	*Signal: Latched Output (Q)*
Logics . **LE29.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE29.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE29.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE29.Gate In3-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE29.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE29.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE30.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE30.Timer Out**	*Signal: Timer Output*
Logics . **LE30.Out**	*Signal: Latched Output (Q)*
Logics . **LE30.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE30.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE30.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE30.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE30.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE30.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE31.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE31.Timer Out**	*Signal: Timer Output*
Logics . **LE31.Out**	*Signal: Latched Output (Q)*
Logics . **LE31.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE31.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE31.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE31.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE31.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE31.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE32.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE32.Timer Out**	*Signal: Timer Output*
Logics . **LE32.Out**	*Signal: Latched Output (Q)*
Logics . **LE32.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE32.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE32.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE32.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE32.Gate In4-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE32.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE33.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE33.Timer Out**	*Signal: Timer Output*
Logics . **LE33.Out**	*Signal: Latched Output (Q)*
Logics . **LE33.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE33.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE33.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE33.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE33.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE33.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE34.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE34.Timer Out**	*Signal: Timer Output*
Logics . **LE34.Out**	*Signal: Latched Output (Q)*
Logics . **LE34.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE34.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE34.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE34.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE34.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE34.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE35.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE35.Timer Out**	*Signal: Timer Output*
Logics . **LE35.Out**	*Signal: Latched Output (Q)*
Logics . **LE35.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE35.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE35.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE35.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE35.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE35.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*

1..n, Assignment List	Description
Logics . **LE36.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE36.Timer Out**	*Signal: Timer Output*
Logics . **LE36.Out**	*Signal: Latched Output (Q)*
Logics . **LE36.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE36.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE36.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE36.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE36.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE36.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE37.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE37.Timer Out**	*Signal: Timer Output*
Logics . **LE37.Out**	*Signal: Latched Output (Q)*
Logics . **LE37.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE37.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE37.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE37.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE37.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE37.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE38.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE38.Timer Out**	*Signal: Timer Output*
Logics . **LE38.Out**	*Signal: Latched Output (Q)*
Logics . **LE38.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE38.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE38.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE38.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE38.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE38.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE39.Gate Out**	*Signal: Output of the logic gate*

1..n, Assignment List	Description
Logics . **LE39.Timer Out**	*Signal: Timer Output*
Logics . **LE39.Out**	*Signal: Latched Output (Q)*
Logics . **LE39.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE39.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE39.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE39.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE39.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE39.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE40.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE40.Timer Out**	*Signal: Timer Output*
Logics . **LE40.Out**	*Signal: Latched Output (Q)*
Logics . **LE40.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE40.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE40.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE40.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE40.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE40.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE41.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE41.Timer Out**	*Signal: Timer Output*
Logics . **LE41.Out**	*Signal: Latched Output (Q)*
Logics . **LE41.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE41.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE41.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE41.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE41.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE41.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE42.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE42.Timer Out**	*Signal: Timer Output*

1..n, Assignment List	Description
Logics . **LE42.Out**	*Signal: Latched Output (Q)*
Logics . **LE42.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE42.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE42.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE42.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE42.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE42.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE43.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE43.Timer Out**	*Signal: Timer Output*
Logics . **LE43.Out**	*Signal: Latched Output (Q)*
Logics . **LE43.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE43.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE43.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE43.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE43.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE43.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE44.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE44.Timer Out**	*Signal: Timer Output*
Logics . **LE44.Out**	*Signal: Latched Output (Q)*
Logics . **LE44.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE44.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE44.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE44.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE44.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE44.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE45.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE45.Timer Out**	*Signal: Timer Output*
Logics . **LE45.Out**	*Signal: Latched Output (Q)*

1..n, Assignment List	Description
Logics . **LE45.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE45.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE45.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE45.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE45.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE45.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE46.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE46.Timer Out**	*Signal: Timer Output*
Logics . **LE46.Out**	*Signal: Latched Output (Q)*
Logics . **LE46.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE46.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE46.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE46.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE46.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE46.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE47.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE47.Timer Out**	*Signal: Timer Output*
Logics . **LE47.Out**	*Signal: Latched Output (Q)*
Logics . **LE47.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE47.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE47.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE47.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE47.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE47.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE48.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE48.Timer Out**	*Signal: Timer Output*
Logics . **LE48.Out**	*Signal: Latched Output (Q)*
Logics . **LE48.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, Assignment List	Description
Logics . **LE48.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE48.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE48.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE48.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE48.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE49.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE49.Timer Out**	*Signal: Timer Output*
Logics . **LE49.Out**	*Signal: Latched Output (Q)*
Logics . **LE49.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE49.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE49.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE49.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE49.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE49.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE50.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE50.Timer Out**	*Signal: Timer Output*
Logics . **LE50.Out**	*Signal: Latched Output (Q)*
Logics . **LE50.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE50.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE50.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE50.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE50.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE50.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE51.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE51.Timer Out**	*Signal: Timer Output*
Logics . **LE51.Out**	*Signal: Latched Output (Q)*
Logics . **LE51.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE51.Gate In1-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE51.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE51.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE51.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE51.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE52.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE52.Timer Out**	*Signal: Timer Output*
Logics . **LE52.Out**	*Signal: Latched Output (Q)*
Logics . **LE52.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE52.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE52.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE52.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE52.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE52.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE53.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE53.Timer Out**	*Signal: Timer Output*
Logics . **LE53.Out**	*Signal: Latched Output (Q)*
Logics . **LE53.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE53.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE53.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE53.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE53.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE53.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE54.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE54.Timer Out**	*Signal: Timer Output*
Logics . **LE54.Out**	*Signal: Latched Output (Q)*
Logics . **LE54.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE54.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE54.Gate In2-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE54.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE54.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE54.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE55.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE55.Timer Out**	*Signal: Timer Output*
Logics . **LE55.Out**	*Signal: Latched Output (Q)*
Logics . **LE55.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE55.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE55.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE55.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE55.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE55.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE56.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE56.Timer Out**	*Signal: Timer Output*
Logics . **LE56.Out**	*Signal: Latched Output (Q)*
Logics . **LE56.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE56.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE56.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE56.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE56.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE56.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE57.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE57.Timer Out**	*Signal: Timer Output*
Logics . **LE57.Out**	*Signal: Latched Output (Q)*
Logics . **LE57.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE57.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE57.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE57.Gate In3-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE57.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE57.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE58.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE58.Timer Out**	*Signal: Timer Output*
Logics . **LE58.Out**	*Signal: Latched Output (Q)*
Logics . **LE58.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE58.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE58.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE58.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE58.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE58.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE59.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE59.Timer Out**	*Signal: Timer Output*
Logics . **LE59.Out**	*Signal: Latched Output (Q)*
Logics . **LE59.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE59.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE59.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE59.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE59.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE59.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE60.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE60.Timer Out**	*Signal: Timer Output*
Logics . **LE60.Out**	*Signal: Latched Output (Q)*
Logics . **LE60.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE60.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE60.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE60.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE60.Gate In4-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE60.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE61.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE61.Timer Out**	*Signal: Timer Output*
Logics . **LE61.Out**	*Signal: Latched Output (Q)*
Logics . **LE61.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE61.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE61.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE61.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE61.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE61.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE62.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE62.Timer Out**	*Signal: Timer Output*
Logics . **LE62.Out**	*Signal: Latched Output (Q)*
Logics . **LE62.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE62.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE62.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE62.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE62.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE62.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE63.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE63.Timer Out**	*Signal: Timer Output*
Logics . **LE63.Out**	*Signal: Latched Output (Q)*
Logics . **LE63.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE63.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE63.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE63.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE63.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE63.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*

1..n, Assignment List	Description
Logics . **LE64.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE64.Timer Out**	*Signal: Timer Output*
Logics . **LE64.Out**	*Signal: Latched Output (Q)*
Logics . **LE64.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE64.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE64.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE64.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE64.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE64.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE65.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE65.Timer Out**	*Signal: Timer Output*
Logics . **LE65.Out**	*Signal: Latched Output (Q)*
Logics . **LE65.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE65.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE65.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE65.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE65.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE65.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE66.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE66.Timer Out**	*Signal: Timer Output*
Logics . **LE66.Out**	*Signal: Latched Output (Q)*
Logics . **LE66.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE66.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE66.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE66.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE66.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE66.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE67.Gate Out**	*Signal: Output of the logic gate*

1..n, Assignment List	Description
Logics . **LE67.Timer Out**	*Signal: Timer Output*
Logics . **LE67.Out**	*Signal: Latched Output (Q)*
Logics . **LE67.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE67.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE67.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE67.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE67.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE67.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE68.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE68.Timer Out**	*Signal: Timer Output*
Logics . **LE68.Out**	*Signal: Latched Output (Q)*
Logics . **LE68.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE68.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE68.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE68.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE68.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE68.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE69.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE69.Timer Out**	*Signal: Timer Output*
Logics . **LE69.Out**	*Signal: Latched Output (Q)*
Logics . **LE69.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE69.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE69.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE69.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE69.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE69.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE70.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE70.Timer Out**	*Signal: Timer Output*

1..n, Assignment List	Description
Logics . **LE70.Out**	*Signal: Latched Output (Q)*
Logics . **LE70.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE70.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE70.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE70.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE70.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE70.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE71.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE71.Timer Out**	*Signal: Timer Output*
Logics . **LE71.Out**	*Signal: Latched Output (Q)*
Logics . **LE71.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE71.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE71.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE71.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE71.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE71.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE72.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE72.Timer Out**	*Signal: Timer Output*
Logics . **LE72.Out**	*Signal: Latched Output (Q)*
Logics . **LE72.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE72.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE72.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE72.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE72.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE72.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE73.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE73.Timer Out**	*Signal: Timer Output*
Logics . **LE73.Out**	*Signal: Latched Output (Q)*

1..n, Assignment List	Description
Logics . **LE73.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE73.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE73.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE73.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE73.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE73.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE74.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE74.Timer Out**	*Signal: Timer Output*
Logics . **LE74.Out**	*Signal: Latched Output (Q)*
Logics . **LE74.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE74.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE74.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE74.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE74.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE74.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE75.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE75.Timer Out**	*Signal: Timer Output*
Logics . **LE75.Out**	*Signal: Latched Output (Q)*
Logics . **LE75.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE75.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE75.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE75.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE75.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE75.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE76.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE76.Timer Out**	*Signal: Timer Output*
Logics . **LE76.Out**	*Signal: Latched Output (Q)*
Logics . **LE76.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, Assignment List	Description
Logics . **LE76.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE76.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE76.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE76.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE76.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE77.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE77.Timer Out**	*Signal: Timer Output*
Logics . **LE77.Out**	*Signal: Latched Output (Q)*
Logics . **LE77.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE77.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE77.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE77.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE77.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE77.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE78.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE78.Timer Out**	*Signal: Timer Output*
Logics . **LE78.Out**	*Signal: Latched Output (Q)*
Logics . **LE78.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE78.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE78.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE78.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE78.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE78.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE79.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE79.Timer Out**	*Signal: Timer Output*
Logics . **LE79.Out**	*Signal: Latched Output (Q)*
Logics . **LE79.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE79.Gate In1-I**	*State of the module input: Assignment of the Input Signal*

1..n, Assignment List	Description
Logics . **LE79.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE79.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE79.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE79.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Logics . **LE80.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE80.Timer Out**	*Signal: Timer Output*
Logics . **LE80.Out**	*Signal: Latched Output (Q)*
Logics . **LE80.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE80.Gate In1-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE80.Gate In2-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE80.Gate In3-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE80.Gate In4-I**	*State of the module input: Assignment of the Input Signal*
Logics . **LE80.Reset Latch-I**	*State of the module input: Reset Signal for the Latching*
Sgen . **Manual Start**	*Fault Simulation has been started manually.*
Sgen . **Manual Stop**	*Fault Simulation has been stopped manually.*
Sgen . **Running**	*Signal; Measuring value simulation is running*
Sgen . **Started**	*Fault Simulation has been started*
Sgen . **Stopped**	*Fault Simulation has been stopped*
Sgen . **Ex Start Simulation-I**	*State of the module input:External Start of Fault Simulation (Using the test parameters)*
Sgen . **ExBlo1-I**	*Module input state: External blocking1*
Sgen . **ExBlo2-I**	*Module input state: External blocking2*
Sgen . **Ex ForcePost-I**	*State of the module input:Force Post state. Abort simulation.*
Sys . **PS 1**	*Signal: The currently active Parameter Set is PS 1*
Sys . **PS 2**	*Signal: The currently active Parameter Set is PS 2*
Sys . **PS 3**	*Signal: The currently active Parameter Set is PS 3*
Sys . **PS 4**	*Signal: The currently active Parameter Set is PS 4*
Sys . **PSS manual**	*Signal: Manual Switch over of a Parameter Set*
Sys . **PSS via Scada**	*Signal: Parameter Set Switch via Scada. Write into this output byte the integer of the parameter set that should become active (e.g. 4 => Switch onto parameter set 4).*

1..n, Assignment List	Description
Sys . **PSS via Inp fct**	*Signal: Parameter Set Switch via input function*
Sys . **min 1 param changed**	*Signal: At least one parameter has been changed*
Sys . **Setting Lock Bypass**	*Signal: Short-period unlock of the Setting Lock*
Sys . **Ack LED**	*Signal: LEDs acknowledgement*
Sys . **Ack BO**	*Signal: Acknowledgement of the Binary Outputs*
Sys . **Ack Scada**	*Signal: Acknowledge latched SCADA signals*
Sys . **Ack TripCmd**	*Signal: Reset Trip Command*
Sys . **Ack LED-HMI**	*Signal: LEDs acknowledgement :HMI*
Sys . **Ack BO-HMI**	*Signal: Acknowledgement of the Binary Outputs :HMI*
Sys . **Ack Scada-HMI**	*Signal: Acknowledge latched SCADA signals :HMI*
Sys . **Ack TripCmd-HMI**	*Signal: Reset Trip Command :HMI*
Sys . **Ack LED-Sca**	*Signal: LEDs acknowledgement :SCADA*
Sys . **Ack BO-Sca**	*Signal: Acknowledgement of the Binary Outputs :SCADA*
Sys . **Ack Counter-Sca**	*Signal: Reset of all Counters :SCADA*
Sys . **Ack Scada-Sca**	*Signal: Acknowledge latched SCADA signals :SCADA*
Sys . **Ack TripCmd-Sca**	*Signal: Reset Trip Command :SCADA*
Sys . **Res OperationsCr**	*Signal:: Res OperationsCr*
Sys . **Res AlarmCr**	*Signal:: Res AlarmCr*
Sys . **Res TripCmdCr**	*Signal:: Res TripCmdCr*
Sys . **Res TotalCr**	*Signal:: Res TotalCr*
Sys . **Ack LED-I**	*Module input state: LEDs acknowledgement by digital input*
Sys . **Ack BO-I**	*Module input state: Acknowledgement of the binary Output Relays*
Sys . **Ack Scada-I**	*Module input state: Acknowledge latched SCADA signals.*
Sys . **PS1-I**	*State of the module input respectively of the signal, that should activate this Parameter Setting Group.*
Sys . **PS2-I**	*State of the module input respectively of the signal, that should activate this Parameter Setting Group.*
Sys . **PS3-I**	*State of the module input respectively of the signal, that should activate this Parameter Setting Group.*
Sys . **PS4-I**	*State of the module input respectively of the signal, that should activate this Parameter Setting Group.*
Sys . **Setting Lock-I**	*State of the module input: No parameters can be changed as long as this input is true. The parameter settings are locked.*

1..n, Assignment List	Description
Sys . **Internal test state**	*Auxiliary state for testing purposes.*

1...n Operating Modes

Selection list referenced by the following parameters:

- ↳ BO Slot X5 . Operating Mode
- ↳ BO Slot X5 . Operating Mode
- ↳ BO Slot X5 . Operating Mode
- ↳ BO Slot X5 . Operating Mode
- ↳ BO Slot X5 . Operating Mode
- ↳ BO Slot X5 . Operating Mode

1...n Operating Modes	Description
Normally open (NO)	*The working principle of the relay corresponds to a normally open contact.*
Normally closed (NC)	*The working principle of the relay corresponds to a normally closed contact.*

Mode

general operation mode

Selection list referenced by the following parameters:

- ↳ LEDs group A . Latched
- ↳ LEDs group A . Latched
- ↳ LEDs group A . Latched
- ↳ LEDs group A . Latched
- ↳ LEDs group A . Latched
- ↳ LEDs group A . Latched
- *[. . .]*

Mode	Description
inactive	*inactive*

Mode	Description
active	*active*
active, ack. by alarm	*Latching of LEDs is active, but will be acknowledged (reset) automatically (by a protection function) in case of a new alarm.*

LED active color

Selection list referenced by the following parameters:

- ╚═▷ LEDs group A . LED active color
- ╚═▷ LEDs group A . LED inactive color
- ╚═▷ LEDs group A . LED active color
- ╚═▷ LEDs group A . LED inactive color
- ╚═▷ LEDs group A . LED active color
- ╚═▷ LEDs group A . LED inactive color
- *[. . .]*

LED active color	Description
green	*green*
red	*red*
red flash	*red flashing*
green flash	*green blinking*
“-”	*No assignment*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ LEDs group B . Latched
- ╚═▷ LEDs group B . Latched
- ╚═▷ LEDs group B . Latched
- ╚═▷ LEDs group B . Latched
- ╚═▷ LEDs group B . Latched

- ╚═▷ LEDs group B . Latched
- *[. . .]*

Mode	Description
inactive	*inactive*
active	*active*
active, ack. by alarm	*Latching of LEDs is active, but will be acknowledged (reset) automatically (by a protection function) in case of a new alarm.*

LED active color

Selection list referenced by the following parameters:

- ╚═▷ LEDs group B . LED active color
- ╚═▷ LEDs group B . LED inactive color
- ╚═▷ LEDs group B . LED active color
- ╚═▷ LEDs group B . LED inactive color
- ╚═▷ LEDs group B . LED active color
- ╚═▷ LEDs group B . LED inactive color
- *[. . .]*

LED active color	Description
green	*green*
red	*red*
red flash	*red flashing*
green flash	*green blinking*
“-”	*No assignment*

Ack via »C« key

Select which acknowledgeable elements can be reset via pressing the »C« key.

Selection list referenced by the following parameters:

- ╚═▷ Sys . Ack via »C« key

Ack via »C« key	Description
Nothing	*No elements can be simply reset via pressing the »C« key for a long time (ca. 1 second). This has the consequence that pressing the »C« key is only a shortcut to the Acknowledge menu, from which the user has to select the elements to be reset.*
Ack LEDs w/o passw.	*All LEDs are acknowledged (reset) via pressing the »C« key for ca. 1 second. No password has to be entered for this. The reset activity can be recognized from the fact that it always includes an LED test, i.e. all LEDs flash in red color for a second, then flash in green color for a second.*
Ack LEDs	*All LEDs are reset via pressing the »C« key (for ca. 1 second). The reset activity can be recognized from the fact that it always includes an LED test, i.e. all LEDs flash in red color for a second, then flash in green color for a second.*
Ack LEDs and relays	*All LEDs and all acknowledgeable binary output relays are reset via pressing the »C« key (for ca. 1 second). The reset activity can be recognized from the fact that it always includes an LED test, i.e. all LEDs flash in red color for a second, then flash in green color for a second.*
Ack Everything	*All acknowledgeable elements are reset via pressing the »C« key (for ca. 1 second):* *- All LEDs, and* *- all binary output relays, and* *- all latched SCADA signals, and* *- the Trip command.* *The reset activity can be recognized from the fact that it always includes an LED test, i.e. all LEDs flash in red color for a second, then flash in green color for a second.*

Duration

Recording time

Selection list referenced by the following parameters:

- ╚═▷ Statistics . Start I Demand via:

Duration	Description
Duration	*Recording time*
StartFct	*Start function*

Duration

Recording time

Selection list referenced by the following parameters:

- └═▷ Statistics . Duration I Demand

Duration	Description
2 s	*s*
5 s	*s*
10 s	*s*
15 s	*seconds*
30 s	*seconds*
1 min	*minute*
5 min	*minute*
10 min	*minute*
15 min	*minute*
30 min	*minute*
1 h	*Hours*
2 h	*Hours*
6 h	*Hours*
12 h	*Hours*
1 d	*days*
2 d	*days*
5 d	*days*
7 d	*days*
10 d	*days*
30 d	*days*

Window configuration

Selection list referenced by the following parameters:

- └═▷ Statistics . Window I Demand

Window configuration	Description
sliding	*Moving mean: Continuously the newest measuring value is added and the oldest measuring value is removed from the moving mean (average value).*
fixed	*The average value is calculated for a fixed window.*

Selection

Selection list referenced by the following parameters:

- ⮑ HMI . Menu language

Selection	Description
English	*English*
German	*German*
Russian	*Russian*
Polish	*Polish*
French	*French*
Portuguese	*Portuguese*
Spanish	*Spanish*
Romanian	*Romanian*

Record-Mode

Recorder Mode (Set the behaviour of the recorder)

Selection list referenced by the following parameters:

- ⮑ Fault rec . Record-Mode

Record-Mode	Description
Alarms and Trips	*A recording is started in case of an alarm or a trip.*
Trips only	*A recording is started only in case of a trip.*

Resolution

Resolution (recording frequency)

Selection list referenced by the following parameters:

- ╚═▷ Trend rec . Resolution

Resolution	**Description**
60 min	*Add next entry: 60 min*
30 min	*Add next entry: 30 min*
15 min	*Add next entry: 15 min*
10 min	*Add next entry: 10 min*
5 min	*Add next entry: 5 min*

1..n, TrendRecList

Selection list referenced by the following parameters:

- ╚═▷ DNP3 . Analog value 0
- ╚═▷ Modbus . Mapped Meas 1
- ╚═▷ Trend rec . Trend1
- ╚═▷ Trend rec . Trend2
- ╚═▷ Trend rec . Trend3
- ╚═▷ Trend rec . Trend4
- *[. . .]*

1..n, TrendRecList	**Description**
“-”	*No assignment*
CT W1 . **IL1**	*Measured value: Phase current (fundamental)*
CT W1 . **IL2**	*Measured value: Phase current (fundamental)*
CT W1 . **IL3**	*Measured value: Phase current (fundamental)*
CT W1 . **IG meas**	*Measured value (measured): IG (fundamental)*
CT W1 . **IG calc**	*Measured value (calculated): IG (fundamental)*
CT W1 . **IL1 RMS**	*Measured value: Phase current (RMS)*
CT W1 . **IL2 RMS**	*Measured value: Phase current (RMS)*
CT W1 . **IL3 RMS**	*Measured value: Phase current (RMS)*

1..n, TrendRecList	Description
CT W1 . **IG meas RMS**	*Measured value (measured): IG (RMS)*
CT W1 . **IG calc RMS**	*Measured value (calculated): IG (RMS)*
CT W1 . **I0**	*Measured value (calculated): Zero current (fundamental)*
CT W1 . **I1**	*Measured value (calculated): Positive phase sequence current (fundamental)*
CT W1 . **I2**	*Measured value (calculated): Unbalanced load current (fundamental)*
CT W1 . **%(I2/I1)**	*Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.*
CT W1 . **IL1 avg RMS**	*IL1 average value (RMS)*
CT W1 . **IL2 avg RMS**	*IL2 average value (RMS)*
CT W1 . **IL3 avg RMS**	*IL3 average value (RMS)*
CT W1 . **IL1 THD**	*Measured value (calculated): IL1 Total Harmonic Current*
CT W1 . **IL2 THD**	*Measured value (calculated): IL2 Total Harmonic Current*
CT W1 . **IL3 THD**	*Measured value (calculated): IL3 Total Harmonic Current*
CT W2 . **IL1**	*Measured value: Phase current (fundamental)*
CT W2 . **IL2**	*Measured value: Phase current (fundamental)*
CT W2 . **IL3**	*Measured value: Phase current (fundamental)*
CT W2 . **IG meas**	*Measured value (measured): IG (fundamental)*
CT W2 . **IG calc**	*Measured value (calculated): IG (fundamental)*
CT W2 . **IL1 RMS**	*Measured value: Phase current (RMS)*
CT W2 . **IL2 RMS**	*Measured value: Phase current (RMS)*
CT W2 . **IL3 RMS**	*Measured value: Phase current (RMS)*
CT W2 . **IG meas RMS**	*Measured value (measured): IG (RMS)*
CT W2 . **IG calc RMS**	*Measured value (calculated): IG (RMS)*
CT W2 . **I0**	*Measured value (calculated): Zero current (fundamental)*
CT W2 . **I1**	*Measured value (calculated): Positive phase sequence current (fundamental)*
CT W2 . **I2**	*Measured value (calculated): Unbalanced load current (fundamental)*
CT W2 . **%(I2/I1)**	*Measured value (calculated): I2/I1, phase sequence will be taken into account automatically.*
CT W2 . **IL1 avg RMS**	*IL1 average value (RMS)*
CT W2 . **IL2 avg RMS**	*IL2 average value (RMS)*
CT W2 . **IL3 avg RMS**	*IL3 average value (RMS)*

1..n, TrendRecList	Description
CT W2 . **IL1 THD**	*Measured value (calculated): IL1 Total Harmonic Current*
CT W2 . **IL2 THD**	*Measured value (calculated): IL2 Total Harmonic Current*
CT W2 . **IL3 THD**	*Measured value (calculated): IL3 Total Harmonic Current*
ThR . **Thermal Cap Used**	*Measured value: Thermal Capacity Used*
URTD . **W1 L1**	*Measured Value: Winding Temperature*
URTD . **W1 L1 max**	*Measured Value: Winding Temperature Maximum Value*
URTD . **W1 L2**	*Measured Value: Winding Temperature*
URTD . **W1 L2 max**	*Measured Value: Winding Temperature Maximum Value*
URTD . **W1 L3**	*Measured Value: Winding Temperature*
URTD . **W1 L3 max**	*Measured Value: Winding Temperature Maximum Value*
URTD . **W2 L1**	*Measured Value: Winding Temperature*
URTD . **W2 L1 max**	*Measured Value: Winding Temperature Maximum Value*
URTD . **W2 L2**	*Measured Value: Winding Temperature*
URTD . **W2 L2 max**	*Measured Value: Winding Temperature Maximum Value*
URTD . **W2 L3**	*Measured Value: Winding Temperature*
URTD . **W2 L3 max**	*Measured Value: Winding Temperature Maximum Value*
URTD . **Amb1**	*Measured Value: Ambient Temperature*
URTD . **Amb1 max**	*Measured Value: Ambient Temperature Maximum Value*
URTD . **Amb2**	*Measured Value: Ambient Temperature*
URTD . **Amb2 max**	*Measured Value: Ambient Temperature Maximum Value*
URTD . **Aux1**	*Measured Value: Auxiliary Temperature*
URTD . **Aux1 max**	*Measured Value: Auxiliary Temperature Maximum Value*
URTD . **Aux2**	*Measured Value: Auxiliary Temperature*
URTD . **Aux2 max**	*Measured Value: Auxiliary Temperature Maximum Value*
URTD . **Aux3**	*Measured Value: Auxiliary Temperature*
URTD . **Aux3 max**	*Measured Value: Auxiliary Temperature Maximum Value*
URTD . **Aux4**	*Measured Value: Auxiliary Temperature*
URTD . **Aux4 max**	*Measured Value: Auxiliary Temperature Maximum Value*
URTD . **RTD Max**	*Maximum temperature of all channels.*
RTD . **Hottest WD W1**	*Hottest winding on side W1*
RTD . **Hottest WD W2**	*Hottest winding on side W2*
RTD . **Hottest Amb**	*Hottest Ambient Temperature*

1..n, TrendRecList	Description
RTD . **Hottest Aux Temp**	*Hottest Auxiliary temperature in degrees C.*

1..n, OnOffList

Selection list referenced by the following parameters:

- ╚═▷ IEC 61850 . Function

1..n, OnOffList	Description
inactive	*inactive*
active	*active*

Baud rate

Selection list referenced by the following parameters:

- ╚═▷ DNP3 . Baud rate

Baud rate	Description
1200	*1200*
2400	*2400*
4800	*4800*
9600	*9600*
19200	*19200*
38400	*38400*
57600	*57600*
115200	*115200*

Byte Frame

Selection list referenced by the following parameters:

- ╚═▷ DNP3 . Frame Layout

Byte Frame	Description
8E1	*8 data bits, even parity, 1 stopbit.*
8O1	*8 data bits, odd, 1 stopbit.*
8N1	*8 data bits, no parity, 1 stopbit.*
8N2	*8 data bits, no parity, 2 stopbits.*

Optical rest position

Selection list referenced by the following parameters:

- ⮱▷ DNP3 . Optical rest position

Optical rest position	Description
Light off	*Light off*
Light on	*Light on*

Communication Start Variants

Selection list referenced by the following parameters:

- ⮱▷ DNP3 . DataLink confirm

Communication Start Variants	Description
Never	*Option Never is recommended*
Always	*If this variable is set to Always then LinkLayer needs to establish a connection before sending any Frame.*
On_Large	*If set to On_Large then a connection needs to be established before sending the first Frame of a multi Term Message*

_AL_ResponseType_k

_AL_ResponseType_h

Selection list referenced by the following parameters:

- ⮱▷ DNP3 . AppLink confirm

_AL_ResponseType_k	Description
Never	*Never*
Always	*Always*
Event	*Event*

1..n, Assignment List

Assignment List

Selection list referenced by the following parameters:

- ╚═▷ DNP3 . DoubleBitInput 0

1..n, Assignment List	Description
“-”	*No assignment*
SG[1] . **Pos**	*Signal: Circuit Breaker Position (0 = Indeterminate, 1 = OFF, 2 = ON, 3 = Disturbed)*
SG[2] . **Pos**	*Signal: Circuit Breaker Position (0 = Indeterminate, 1 = OFF, 2 = ON, 3 = Disturbed)*

1..n, Assignment List

Assignment List

Selection list referenced by the following parameters:

- ╚═▷ DNP3 . BinaryCounter 0

1..n, Assignment List	Description
“-”	*No assignment*
Prot . **FaultNo**	*Fault number*
Prot . **No. of Grid Fault**	*Number of grid fault: A grid fault, e.g. a short circuit, might cause several faults with trip and autoreclosing; in this case, the fault number counts each fault, but the grid fault number remains the same.*
SG[1] . **TripCmd Cr**	*Counter: Total number of trips of the switchgear.*
SG[2] . **TripCmd Cr**	*Counter: Total number of trips of the switchgear.*
Sys . **Operating hours Cr**	*Operating hours counter of the protective device*

Scale Factor

Multiplier in order to convert float values into integer.

Selection list referenced by the following parameters:

- ⮡ DNP3 . Scale Factor 0

Scale Factor	Description
0.001	*0.001*
0.01	*0.01*
0.1	*0.1*
1	*1*
10	*10*
100	*100*
1000	*1000*
10000	*10000*
100000	*100000*
1000000	*1000000*

Optical rest position

Selection list referenced by the following parameters:

- ⮡ Modbus . Optical rest position

Optical rest position	Description
Light off	*Light off*
Light on	*Light on*

Port selection

Selection list referenced by the following parameters:

- ⮡ Modbus . TCP Port Config

Port selection	Description
Default	*Default Port*
Private	*Private Port*

Baud rate

Selection list referenced by the following parameters:

- ⮡ Modbus . Baud rate

Baud rate	Description
1200	*1200*
2400	*2400*
4800	*4800*
9600	*9600*
19200	*19200*
38400	*38400*

Byte Frame

Selection list referenced by the following parameters:

- ⮡ Modbus . Physical Settings

Byte Frame	Description
8E1	*8 data bits, even parity, 1 stopbit.*
8O1	*8 data bits, odd, 1 stopbit.*
8N1	*8 data bits, no parity, 1 stopbit.*
8N2	*8 data bits, no parity, 2 stopbits.*

Type of SCADA mapping

This setting decides whether the communication protocol shall use the default mapping of data objects, or some user-defined mapping that has been loaded from a *.HptSMap file.

Selection list referenced by the following parameters:

- ⮡ Modbus . Type of SCADA mapping

Type of SCADA mapping	Description
Standard	*Default mapping of data objects*
User-defined	*User-defined mapping of data objects*

Config status

Status of the user-defined SCADA configuration.\nPossible values:

Selection list referenced by the following parameters:

- ⮡ Modbus . Config status

Config status	Description
Changing	*New SCADA configuration is being loaded, but not active yet.*
OK	*The SCADA configuration is active.*
Config. not avail.	*The user-defined SCADA configuration is not available (e.g. has not been loaded into the device).*
Error	*Unexpected error. Please contact our service-team.*

Baud rate

Selection list referenced by the following parameters:

- ⮡ IEC103 . Baud rate

Baud rate	Description
1200	*1200*
2400	*2400*
4800	*4800*
9600	*9600*
19200	*19200*
38400	*38400*
57600	*57600*

Byte Frame

Selection list referenced by the following parameters:

- ╚═▷ IEC103 . Physical Settings

Byte Frame	Description
8E1	*8 data bits, even parity, 1 stopbit.*
8O1	*8 data bits, odd, 1 stopbit.*
8N1	*8 data bits, no parity, 1 stopbit.*
8N2	*8 data bits, no parity, 2 stopbits.*

Timezone

Selection whether the timestamps in IEC103 messages shall be given as UTC or local time. (“Local time” always includes the actual daylight saving settings.)

Selection list referenced by the following parameters:

- ╚═▷ IEC103 . Timezone

Timezone	Description
UTC	*UTC*
Local Time	*Local time according to the »Time Zones« setting (in Device Parameters) (incl. daylight saving settings).*

Optical rest position

Selection list referenced by the following parameters:

- ╚═▷ IEC103 . Optical rest position

Optical rest position	Description
Light off	*Light off*
Light on	*Light on*

Port selection

Selection list referenced by the following parameters:

- ╚═▷ IEC104 . TCP Port Config

Port selection	Description
Default	*Default Port*
Private	*Private Port*

Timezone

Selection whether the timestamps in the transmitted communication telegrams shall be given as UTC or local time. (“Local time” always includes the actual daylight saving settings.)

Selection list referenced by the following parameters:

- ╚═▷ IEC104 . Timezone

Timezone	Description
UTC	*UTC*
Local Time	*Local time according to the »Time Zones« setting (in Device Parameters) (incl. daylight saving settings).*

Type of SCADA mapping

This setting decides whether the communication protocol shall use the default mapping of data objects, or some user-defined mapping that has been loaded from a *.HptSMap file.

Selection list referenced by the following parameters:

- ╚═▷ IEC104 . Type of SCADA mapping

Type of SCADA mapping	Description
Standard	*Default mapping of data objects*
User-defined	*User-defined mapping of data objects*

Config status

Status of the user-defined SCADA configuration.\nPossible values:

Selection list referenced by the following parameters:

- ⮡ IEC104 . Config status

Config status	Description
Changing	*New SCADA configuration is being loaded, but not active yet.*
OK	*The SCADA configuration is active.*
Config. not avail.	*The user-defined SCADA configuration is not available (e.g. has not been loaded into the device).*
Error	*Unexpected error. Please contact our service-team.*

Type of SCADA mapping

This setting decides whether the communication protocol shall use the default mapping of data objects, or some user-defined mapping that has been loaded from a *.HptSMap file.

Selection list referenced by the following parameters:

- ⮡ Profibus . Type of SCADA mapping

Type of SCADA mapping	Description
Standard	*Default mapping of data objects*
User-defined	*User-defined mapping of data objects*

Time Zones

Selection list referenced by the following parameters:

- ⮡ TimeSync . Time Zones

Time Zones	Description
UTC+14 Kiritimati	*UTC+14 Kiritimati*
UTC+13 Rawaki	*UTC+13 Rawaki*
UTC+12.75 Chatham Island	*UTC+12.75 Chatham Island*

Time Zones	Description
UTC+12 Wellington	*UTC+12 Wellington*
UTC+11.5 Kingston	*UTC+11.5 Kingston*
UTC+11 Port Vila	*UTC+11 Port Vila*
UTC+10.5 Lord Howe Island	*UTC+10.5 Lord Howe Island*
UTC+10 Sydney	*UTC+10 Sydney*
UTC+9.5 Adelaide	*UTC+9.5 Adelaide*
UTC+9 Tokyo	*UTC+9 Tokyo*
UTC+8 Hong Kong	*UTC+8 Hong Kong*
UTC+7 Bangkok	*UTC+7 Bangkok*
UTC+6.5 Rangoon	*UTC+6.5 Rangoon*
UTC+6 Colombo	*UTC+6 Colombo*
UTC+5.75 Kathmandu	*UTC+5.75 Kathmandu*
UTC+5.5 New Delhi	*UTC+5.5 New Delhi*
UTC+5 Islamabad	*UTC+5 Islamabad*
UTC+4.5 Kabul	*UTC+4.5 Kabul*
UTC+4 Abu Dhabi	*UTC+4 Abu Dhabi*
UTC+3.5 Tehran	*UTC+3.5 Tehran*
UTC+3 Moscow	*UTC+3 Moscow*
UTC+2 Athens	*UTC+2 Athens*
UTC+1 Berlin	*UTC+1 Berlin*
UTC+0 London	*UTC+0 London*
UTC-1 Azores	*UTC-1 Azores*
UTC-2 Fern. d. Noronha	*UTC-2 Fern. d. Noronha*
UTC-3 Buenos Aires	*UTC-3 Buenos Aires*
UTC-3.5 St. John’s	*UTC-3.5 St. John’s*
UTC-4 Santiago	*UTC-4 Santiago*
UTC-5 New York	*UTC-5 New York*
UTC-6 Chicago	*UTC-6 Chicago*
UTC-7 Salt Lake City	*UTC-7 Salt Lake City*
UTC-8 Los Angeles	*UTC-8 Los Angeles*
UTC-9 Anchorage	*UTC-9 Anchorage*

Time Zones	Description
UTC-9.5 Taiohae	*UTC-9.5 Taiohae*
UTC-10 Honolulu	*UTC-10 Honolulu*
UTC-11 Midway Islands	*UTC-11 Midway Islands*

Month of clock change

Selection list referenced by the following parameters:

- ⮡ TimeSync . Summertime m
- ⮡ TimeSync . Wintertime m

Month of clock change	Description
January	*January*
February	*February*
March	*March*
April	*April*
May	*May*
June	*June*
July	*July*
August	*August*
September	*September*
October	*October*
November	*November*
December	*December*

Date

Selection list referenced by the following parameters:

- ⮡ TimeSync . Summertime d
- ⮡ TimeSync . Wintertime d

Date	Description
Sunday	*Sunday*
Monday	*Monday*
Tuesday	*Tuesday*
Wednesday	*Wednesday*
Thursday	*Thursday*
Friday	*Friday*
Saturday	*Saturday*
General day	*General day: Examples: first day of month, last day of month*

Day of clock change

Day of Time Saving change

Selection list referenced by the following parameters:

- ╚═▷ TimeSync . Summertime w
- ╚═▷ TimeSync . Wintertime w

Day of clock change	Description
First	*First week of the month*
Second	*Second week of the month*
Third	*Third week of the month*
Fourth	*Fourth week of the month*
Last	*Last week of the month*

Used Protocol

Selection list referenced by the following parameters:

- ╚═▷ TimeSync . TimeSync

Used Protocol	Description
“-”	-
IRIG-B . **IRIG-B**	*IRIG-B-Module*

Used Protocol	Description
SNTP . **SNTP**	*SNTP-Module*
Modbus . **Modbus**	*Modbus Protocol*
IEC103 . **IEC 60870-5-103**	*IEC 60870-5-103 Protocol*
IEC104 . **IEC104**	*IEC 60870-5-104 communication*
DNP3 . **DNP3**	*Distributed Network Protocol*

IRIG-B00X

Determination of the Type: IRIG-B00X. IRIG-B types differ in types of included “Coded Expressions” (year, control-functions, straight-binary-seconds).

Selection list referenced by the following parameters:

- ╚═▷ IRIG-B . IRIG-B00X

IRIG-B00X	Description
IRIGB-000	*Please refer to: IRIG STANDARD 200-04*
IRIGB-001	*Please refer to: IRIG STANDARD 200-04*
IRIGB-002	*Please refer to: IRIG STANDARD 200-04*
IRIGB-003	*Please refer to: IRIG STANDARD 200-04*
IRIGB-004	*Please refer to: IRIG STANDARD 200-04*
IRIGB-005	*Please refer to: IRIG STANDARD 200-04*
IRIGB-006	*Please refer to: IRIG STANDARD 200-04*
IRIGB-007	*Please refer to: IRIG STANDARD 200-04*

Selection list referenced by the following parameters:

- ╚═▷ Sys . DM version

	Description
3.6.b	*Version*

Phase Sequence

Phase Sequence direction

Selection list referenced by the following parameters:

- ⮡ Field Para . Phase Sequence

Phase Sequence	Description
ABC	*rotating clockwise*
ACB	*Counter-clockwise phase sequence. Positive and negative phase sequence are exchanged and MTA is turned for 180°.*

fN

Selection list referenced by the following parameters:

- ⮡ Field Para . f

fN	Description
50	*Rated frequency*
60	*Rated frequency*

W1 Connection/Grounding

Note: The zero current will be removed in order to prevent faulty tripping of the differential protection. If a star point is connected to ground according to the winding connection, the zero current (symmetrical components) will be removed.

Selection list referenced by the following parameters:

- ⮡ Transformer . W1 Connection/Grounding

W1 Connection/ Grounding	Description
Y	*Star (connection group winding on primary side)*
D	*Delta (connection group winding on primary side)*
Z	*Zig-Zag (connection group winding on primary side)*
YN	*Star grounded (connection group winding on primary side)*
ZN	*Zig-Zag with ground connection (connection group winding on primary side)*

W2 Connection/Grounding

Note: The zero current will be removed in order to prevent faulty tripping of the differential protection. If a star point is connected to ground according to the winding connection, the zero current (symmetrical components) will be removed.

Selection list referenced by the following parameters:

- ╚═▷ Transformer . W2 Connection/Grounding

W2 Connection/ Grounding	Description
y	*Star (connection group winding on secondary side)*
d	*Delta (connection group winding on secondary side)*
z	*Zig-Zag (connection group winding on secondary side)*
yn	*Star grounded (connection group winding on secondary side)*
zn	*Zig-Zag with ground connection (connection group winding on secondary side)*

Ratio prim/sec

w_prim/w_sec

Selection list referenced by the following parameters:

- ╚═▷ CT W1 . CT sec
- ╚═▷ CT W1 . ECT sec

Ratio prim/sec	Description
1	*Rated value of the secondary side of the current transformers.*
5	*Rated value of the secondary side of the current transformers.*

Polarity

Selection list referenced by the following parameters:

- ╚═▷ CT W1 . CT dir
- ╚═▷ CT W1 . ECT dir

Polarity	Description
0	*0*
180	*180 degree polarity correction (wiring faults)*

Ratio prim/sec

w_prim/w_sec

Selection list referenced by the following parameters:

- ╚═▷ CT W2 . CT sec
- ╚═▷ CT W2 . ECT sec

Ratio prim/sec	Description
1	*Rated value of the secondary side of the current transformers.*
5	*Rated value of the secondary side of the current transformers.*

Polarity

Selection list referenced by the following parameters:

- ╚═▷ CT W2 . CT dir
- ╚═▷ CT W2 . ECT dir

Polarity	Description
0	*0*
180	*180 degree polarity correction (wiring faults)*

active/inactive

Selection list referenced by the following parameters:

- ╚═▷ BO Slot X2 . DISARMED Ctrl
- ╚═▷ BO Slot X5 . DISARMED Ctrl
- ╚═▷ Prot . ExBlo Fc

- ╚═▷ Prot . ExBlo TripCmd Fc
- ╚═▷ Id . ExBlo Fc
- ╚═▷ Id . ExBlo TripCmd Fc
- *[. . .]*

active/inactive	Description
inactive	*inactive*
active	*active*

CT Winding Side

Measuring values will be used from this winding side

Selection list referenced by the following parameters:

- ╚═▷ IdG[1] . CT Winding Side
- ╚═▷ IdGH[1] . CT Winding Side
- ╚═▷ IH2[1] . CT Winding Side
- ╚═▷ I[1] . CT Winding Side
- ╚═▷ IG[1] . CT Winding Side
- ╚═▷ ThR . CT Winding Side
- *[. . .]*

CT Winding Side	Description
W1	*W1*
W2	*W2*

AdaptSet

Adaptive Parameters

Selection list referenced by the following parameters:

- ╚═▷ I[1] . AdaptSet 1
- ╚═▷ I[1] . AdaptSet 2
- ╚═▷ I[1] . AdaptSet 3

- ╚⇒ I[1] . AdaptSet 4
- ╚⇒ IG[1] . AdaptSet 1
- ╚⇒ IG[1] . AdaptSet 2
- *[. . .]*

AdaptSet	Description
"-"	*No assignment*
IH2[1] . **Blo L1**	*Signal: Blocked L1*
IH2[1] . **Blo L2**	*Signal: Blocked L2*
IH2[1] . **Blo L3**	*Signal: Blocked L3*
IH2[1] . **Blo IG meas**	*Signal: Blocking of the ground (earth) protection module (measured ground current)*
IH2[1] . **Blo IG calc**	*Signal: Blocking of the ground (earth) protection module (calculated ground current)*
IH2[1] . **3-ph Blo**	*Signal: Inrush was detected in at least one phase - trip command blocked.*
IH2[2] . **Blo L1**	*Signal: Blocked L1*
IH2[2] . **Blo L2**	*Signal: Blocked L2*
IH2[2] . **Blo L3**	*Signal: Blocked L3*
IH2[2] . **Blo IG meas**	*Signal: Blocking of the ground (earth) protection module (measured ground current)*
IH2[2] . **Blo IG calc**	*Signal: Blocking of the ground (earth) protection module (calculated ground current)*
IH2[2] . **3-ph Blo**	*Signal: Inrush was detected in at least one phase - trip command blocked.*
SOTF . **enabled**	*Signal: Switch Onto Fault enabled. This Signal can be used to modify Overcurrent Protection Settings.*
CLPU . **enabled**	*Signal: Cold Load enabled*
ExP[1] . **Alarm**	*Signal: Alarm*
ExP[2] . **Alarm**	*Signal: Alarm*
ExP[3] . **Alarm**	*Signal: Alarm*
ExP[4] . **Alarm**	*Signal: Alarm*
Ext Sudd Press . **Alarm**	*Signal: Alarm*
Ext Oil Temp . **Alarm**	*Signal: Alarm*
Ext Temp Superv[1] . **Alarm**	*Signal: Alarm*

AdaptSet	Description
Ext Temp Superv[2] . **Alarm**	*Signal: Alarm*
Ext Temp Superv[3] . **Alarm**	*Signal: Alarm*
CTS[1] . **Alarm**	*Signal: Alarm Current Transformer Measuring Circuit Supervision*
CTS[2] . **Alarm**	*Signal: Alarm Current Transformer Measuring Circuit Supervision*
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*
Modbus . **Scada Cmd 1**	*Scada Command*
Modbus . **Scada Cmd 2**	*Scada Command*
Modbus . **Scada Cmd 3**	*Scada Command*
Modbus . **Scada Cmd 4**	*Scada Command*
Modbus . **Scada Cmd 5**	*Scada Command*
Modbus . **Scada Cmd 6**	*Scada Command*
Modbus . **Scada Cmd 7**	*Scada Command*
Modbus . **Scada Cmd 8**	*Scada Command*
Modbus . **Scada Cmd 9**	*Scada Command*
Modbus . **Scada Cmd 10**	*Scada Command*
Modbus . **Scada Cmd 11**	*Scada Command*

AdaptSet	Description
Modbus . **Scada Cmd 12**	*Scada Command*
Modbus . **Scada Cmd 13**	*Scada Command*
Modbus . **Scada Cmd 14**	*Scada Command*
Modbus . **Scada Cmd 15**	*Scada Command*
Modbus . **Scada Cmd 16**	*Scada Command*
IEC 61850 . **GOSINGGIO1.Ind1.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind2.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind3.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind4.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind5.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind6.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind7.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind8.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind9.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind10.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind11.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind12.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind13.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind14.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind15.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind16.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind17.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*

AdaptSet	Description
IEC 61850 . **GOSINGGIO1.Ind18.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind19.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind20.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind21.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind22.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind23.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind24.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind25.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind26.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind27.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind28.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind29.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind30.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind31.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **GOSINGGIO1.Ind32.stVal**	*Signal: Virtual Input (IEC61850 GGIO Ind): State*
IEC 61850 . **SPCSO1**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO2**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO3**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO4**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO5**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*

AdaptSet	Description
IEC 61850 . **SPCSO6**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO7**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO8**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO9**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO10**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO11**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO12**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO13**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO14**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO15**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC 61850 . **SPCSO16**	*Status bit that can be set by clients like e.g. SCADA (Single Point Controllable Status Output).*
IEC103 . **Scada Cmd 1**	*Scada Command*
IEC103 . **Scada Cmd 2**	*Scada Command*
IEC103 . **Scada Cmd 3**	*Scada Command*
IEC103 . **Scada Cmd 4**	*Scada Command*
IEC103 . **Scada Cmd 5**	*Scada Command*
IEC103 . **Scada Cmd 6**	*Scada Command*
IEC103 . **Scada Cmd 7**	*Scada Command*
IEC103 . **Scada Cmd 8**	*Scada Command*
IEC103 . **Scada Cmd 9**	*Scada Command*
IEC103 . **Scada Cmd 10**	*Scada Command*
IEC104 . **Scada Cmd 1**	*Scada Command*
IEC104 . **Scada Cmd 2**	*Scada Command*
IEC104 . **Scada Cmd 3**	*Scada Command*
IEC104 . **Scada Cmd 4**	*Scada Command*
IEC104 . **Scada Cmd 5**	*Scada Command*

AdaptSet	Description
IEC104 . **Scada Cmd 6**	*Scada Command*
IEC104 . **Scada Cmd 7**	*Scada Command*
IEC104 . **Scada Cmd 8**	*Scada Command*
IEC104 . **Scada Cmd 9**	*Scada Command*
IEC104 . **Scada Cmd 10**	*Scada Command*
IEC104 . **Scada Cmd 11**	*Scada Command*
IEC104 . **Scada Cmd 12**	*Scada Command*
IEC104 . **Scada Cmd 13**	*Scada Command*
IEC104 . **Scada Cmd 14**	*Scada Command*
IEC104 . **Scada Cmd 15**	*Scada Command*
IEC104 . **Scada Cmd 16**	*Scada Command*
Profibus . **Scada Cmd 1**	*Scada Command*
Profibus . **Scada Cmd 2**	*Scada Command*
Profibus . **Scada Cmd 3**	*Scada Command*
Profibus . **Scada Cmd 4**	*Scada Command*
Profibus . **Scada Cmd 5**	*Scada Command*
Profibus . **Scada Cmd 6**	*Scada Command*
Profibus . **Scada Cmd 7**	*Scada Command*
Profibus . **Scada Cmd 8**	*Scada Command*
Profibus . **Scada Cmd 9**	*Scada Command*
Profibus . **Scada Cmd 10**	*Scada Command*
Profibus . **Scada Cmd 11**	*Scada Command*
Profibus . **Scada Cmd 12**	*Scada Command*
Profibus . **Scada Cmd 13**	*Scada Command*
Profibus . **Scada Cmd 14**	*Scada Command*
Profibus . **Scada Cmd 15**	*Scada Command*
Profibus . **Scada Cmd 16**	*Scada Command*
Logics . **LE1.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE1.Timer Out**	*Signal: Timer Output*
Logics . **LE1.Out**	*Signal: Latched Output (Q)*
Logics . **LE1.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE2.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE2.Timer Out**	*Signal: Timer Output*
Logics . **LE2.Out**	*Signal: Latched Output (Q)*
Logics . **LE2.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE3.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE3.Timer Out**	*Signal: Timer Output*
Logics . **LE3.Out**	*Signal: Latched Output (Q)*
Logics . **LE3.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE4.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE4.Timer Out**	*Signal: Timer Output*
Logics . **LE4.Out**	*Signal: Latched Output (Q)*
Logics . **LE4.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE5.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE5.Timer Out**	*Signal: Timer Output*
Logics . **LE5.Out**	*Signal: Latched Output (Q)*
Logics . **LE5.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE6.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE6.Timer Out**	*Signal: Timer Output*
Logics . **LE6.Out**	*Signal: Latched Output (Q)*
Logics . **LE6.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE7.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE7.Timer Out**	*Signal: Timer Output*
Logics . **LE7.Out**	*Signal: Latched Output (Q)*
Logics . **LE7.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE8.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE8.Timer Out**	*Signal: Timer Output*
Logics . **LE8.Out**	*Signal: Latched Output (Q)*
Logics . **LE8.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE9.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE9.Timer Out**	*Signal: Timer Output*
Logics . **LE9.Out**	*Signal: Latched Output (Q)*
Logics . **LE9.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE10.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE10.Timer Out**	*Signal: Timer Output*
Logics . **LE10.Out**	*Signal: Latched Output (Q)*
Logics . **LE10.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE11.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE11.Timer Out**	*Signal: Timer Output*
Logics . **LE11.Out**	*Signal: Latched Output (Q)*
Logics . **LE11.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE12.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE12.Timer Out**	*Signal: Timer Output*
Logics . **LE12.Out**	*Signal: Latched Output (Q)*
Logics . **LE12.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE13.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE13.Timer Out**	*Signal: Timer Output*
Logics . **LE13.Out**	*Signal: Latched Output (Q)*
Logics . **LE13.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE14.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE14.Timer Out**	*Signal: Timer Output*
Logics . **LE14.Out**	*Signal: Latched Output (Q)*
Logics . **LE14.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE15.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE15.Timer Out**	*Signal: Timer Output*
Logics . **LE15.Out**	*Signal: Latched Output (Q)*
Logics . **LE15.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE16.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE16.Timer Out**	*Signal: Timer Output*
Logics . **LE16.Out**	*Signal: Latched Output (Q)*
Logics . **LE16.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE17.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE17.Timer Out**	*Signal: Timer Output*
Logics . **LE17.Out**	*Signal: Latched Output (Q)*
Logics . **LE17.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE18.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE18.Timer Out**	*Signal: Timer Output*
Logics . **LE18.Out**	*Signal: Latched Output (Q)*
Logics . **LE18.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE19.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE19.Timer Out**	*Signal: Timer Output*
Logics . **LE19.Out**	*Signal: Latched Output (Q)*
Logics . **LE19.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE20.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE20.Timer Out**	*Signal: Timer Output*
Logics . **LE20.Out**	*Signal: Latched Output (Q)*
Logics . **LE20.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE21.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE21.Timer Out**	*Signal: Timer Output*
Logics . **LE21.Out**	*Signal: Latched Output (Q)*
Logics . **LE21.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE22.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE22.Timer Out**	*Signal: Timer Output*
Logics . **LE22.Out**	*Signal: Latched Output (Q)*
Logics . **LE22.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE23.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE23.Timer Out**	*Signal: Timer Output*
Logics . **LE23.Out**	*Signal: Latched Output (Q)*
Logics . **LE23.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE24.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE24.Timer Out**	*Signal: Timer Output*
Logics . **LE24.Out**	*Signal: Latched Output (Q)*
Logics . **LE24.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE25.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE25.Timer Out**	*Signal: Timer Output*
Logics . **LE25.Out**	*Signal: Latched Output (Q)*
Logics . **LE25.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE26.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE26.Timer Out**	*Signal: Timer Output*
Logics . **LE26.Out**	*Signal: Latched Output (Q)*
Logics . **LE26.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE27.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE27.Timer Out**	*Signal: Timer Output*
Logics . **LE27.Out**	*Signal: Latched Output (Q)*
Logics . **LE27.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE28.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE28.Timer Out**	*Signal: Timer Output*
Logics . **LE28.Out**	*Signal: Latched Output (Q)*
Logics . **LE28.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE29.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE29.Timer Out**	*Signal: Timer Output*
Logics . **LE29.Out**	*Signal: Latched Output (Q)*
Logics . **LE29.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE30.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE30.Timer Out**	*Signal: Timer Output*
Logics . **LE30.Out**	*Signal: Latched Output (Q)*
Logics . **LE30.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE31.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE31.Timer Out**	*Signal: Timer Output*
Logics . **LE31.Out**	*Signal: Latched Output (Q)*
Logics . **LE31.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE32.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE32.Timer Out**	*Signal: Timer Output*
Logics . **LE32.Out**	*Signal: Latched Output (Q)*
Logics . **LE32.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE33.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE33.Timer Out**	*Signal: Timer Output*
Logics . **LE33.Out**	*Signal: Latched Output (Q)*
Logics . **LE33.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE34.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE34.Timer Out**	*Signal: Timer Output*
Logics . **LE34.Out**	*Signal: Latched Output (Q)*
Logics . **LE34.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE35.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE35.Timer Out**	*Signal: Timer Output*
Logics . **LE35.Out**	*Signal: Latched Output (Q)*
Logics . **LE35.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE36.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE36.Timer Out**	*Signal: Timer Output*
Logics . **LE36.Out**	*Signal: Latched Output (Q)*
Logics . **LE36.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE37.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE37.Timer Out**	*Signal: Timer Output*
Logics . **LE37.Out**	*Signal: Latched Output (Q)*
Logics . **LE37.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE38.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE38.Timer Out**	*Signal: Timer Output*
Logics . **LE38.Out**	*Signal: Latched Output (Q)*
Logics . **LE38.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE39.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE39.Timer Out**	*Signal: Timer Output*
Logics . **LE39.Out**	*Signal: Latched Output (Q)*
Logics . **LE39.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE40.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE40.Timer Out**	*Signal: Timer Output*
Logics . **LE40.Out**	*Signal: Latched Output (Q)*
Logics . **LE40.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE41.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE41.Timer Out**	*Signal: Timer Output*
Logics . **LE41.Out**	*Signal: Latched Output (Q)*
Logics . **LE41.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE42.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE42.Timer Out**	*Signal: Timer Output*
Logics . **LE42.Out**	*Signal: Latched Output (Q)*
Logics . **LE42.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE43.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE43.Timer Out**	*Signal: Timer Output*
Logics . **LE43.Out**	*Signal: Latched Output (Q)*
Logics . **LE43.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE44.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE44.Timer Out**	*Signal: Timer Output*
Logics . **LE44.Out**	*Signal: Latched Output (Q)*
Logics . **LE44.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE45.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE45.Timer Out**	*Signal: Timer Output*
Logics . **LE45.Out**	*Signal: Latched Output (Q)*
Logics . **LE45.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE46.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE46.Timer Out**	*Signal: Timer Output*
Logics . **LE46.Out**	*Signal: Latched Output (Q)*
Logics . **LE46.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE47.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE47.Timer Out**	*Signal: Timer Output*
Logics . **LE47.Out**	*Signal: Latched Output (Q)*
Logics . **LE47.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE48.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE48.Timer Out**	*Signal: Timer Output*
Logics . **LE48.Out**	*Signal: Latched Output (Q)*
Logics . **LE48.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE49.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE49.Timer Out**	*Signal: Timer Output*
Logics . **LE49.Out**	*Signal: Latched Output (Q)*
Logics . **LE49.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE50.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE50.Timer Out**	*Signal: Timer Output*
Logics . **LE50.Out**	*Signal: Latched Output (Q)*
Logics . **LE50.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE51.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE51.Timer Out**	*Signal: Timer Output*
Logics . **LE51.Out**	*Signal: Latched Output (Q)*
Logics . **LE51.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE52.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE52.Timer Out**	*Signal: Timer Output*
Logics . **LE52.Out**	*Signal: Latched Output (Q)*
Logics . **LE52.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE53.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE53.Timer Out**	*Signal: Timer Output*
Logics . **LE53.Out**	*Signal: Latched Output (Q)*
Logics . **LE53.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE54.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE54.Timer Out**	*Signal: Timer Output*
Logics . **LE54.Out**	*Signal: Latched Output (Q)*
Logics . **LE54.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE55.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE55.Timer Out**	*Signal: Timer Output*
Logics . **LE55.Out**	*Signal: Latched Output (Q)*
Logics . **LE55.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE56.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE56.Timer Out**	*Signal: Timer Output*
Logics . **LE56.Out**	*Signal: Latched Output (Q)*
Logics . **LE56.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE57.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE57.Timer Out**	*Signal: Timer Output*
Logics . **LE57.Out**	*Signal: Latched Output (Q)*
Logics . **LE57.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE58.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE58.Timer Out**	*Signal: Timer Output*
Logics . **LE58.Out**	*Signal: Latched Output (Q)*
Logics . **LE58.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE59.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE59.Timer Out**	*Signal: Timer Output*
Logics . **LE59.Out**	*Signal: Latched Output (Q)*
Logics . **LE59.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE60.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE60.Timer Out**	*Signal: Timer Output*
Logics . **LE60.Out**	*Signal: Latched Output (Q)*
Logics . **LE60.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE61.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE61.Timer Out**	*Signal: Timer Output*
Logics . **LE61.Out**	*Signal: Latched Output (Q)*
Logics . **LE61.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE62.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE62.Timer Out**	*Signal: Timer Output*
Logics . **LE62.Out**	*Signal: Latched Output (Q)*
Logics . **LE62.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE63.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE63.Timer Out**	*Signal: Timer Output*
Logics . **LE63.Out**	*Signal: Latched Output (Q)*
Logics . **LE63.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE64.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE64.Timer Out**	*Signal: Timer Output*
Logics . **LE64.Out**	*Signal: Latched Output (Q)*
Logics . **LE64.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE65.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE65.Timer Out**	*Signal: Timer Output*
Logics . **LE65.Out**	*Signal: Latched Output (Q)*
Logics . **LE65.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE66.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE66.Timer Out**	*Signal: Timer Output*
Logics . **LE66.Out**	*Signal: Latched Output (Q)*
Logics . **LE66.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE67.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE67.Timer Out**	*Signal: Timer Output*
Logics . **LE67.Out**	*Signal: Latched Output (Q)*
Logics . **LE67.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE68.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE68.Timer Out**	*Signal: Timer Output*
Logics . **LE68.Out**	*Signal: Latched Output (Q)*
Logics . **LE68.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE69.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE69.Timer Out**	*Signal: Timer Output*
Logics . **LE69.Out**	*Signal: Latched Output (Q)*
Logics . **LE69.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE70.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE70.Timer Out**	*Signal: Timer Output*
Logics . **LE70.Out**	*Signal: Latched Output (Q)*
Logics . **LE70.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE71.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE71.Timer Out**	*Signal: Timer Output*
Logics . **LE71.Out**	*Signal: Latched Output (Q)*
Logics . **LE71.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE72.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE72.Timer Out**	*Signal: Timer Output*
Logics . **LE72.Out**	*Signal: Latched Output (Q)*
Logics . **LE72.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE73.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE73.Timer Out**	*Signal: Timer Output*
Logics . **LE73.Out**	*Signal: Latched Output (Q)*
Logics . **LE73.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE74.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE74.Timer Out**	*Signal: Timer Output*
Logics . **LE74.Out**	*Signal: Latched Output (Q)*
Logics . **LE74.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE75.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE75.Timer Out**	*Signal: Timer Output*
Logics . **LE75.Out**	*Signal: Latched Output (Q)*
Logics . **LE75.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE76.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE76.Timer Out**	*Signal: Timer Output*
Logics . **LE76.Out**	*Signal: Latched Output (Q)*
Logics . **LE76.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE77.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE77.Timer Out**	*Signal: Timer Output*
Logics . **LE77.Out**	*Signal: Latched Output (Q)*
Logics . **LE77.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE78.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE78.Timer Out**	*Signal: Timer Output*
Logics . **LE78.Out**	*Signal: Latched Output (Q)*
Logics . **LE78.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

AdaptSet	Description
Logics . **LE79.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE79.Timer Out**	*Signal: Timer Output*
Logics . **LE79.Out**	*Signal: Latched Output (Q)*
Logics . **LE79.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE80.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE80.Timer Out**	*Signal: Timer Output*
Logics . **LE80.Out**	*Signal: Latched Output (Q)*
Logics . **LE80.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

CurrentBase

Base Current Selection (based on Device Rating (1A/5A)/Protected Object Rating).

Selection list referenced by the following parameters:

- ↳ I2>[1] . CurrentBase

CurrentBase	Description
Device Rating	*Device Rating*
Protected Object Rating	*Protected Object Rating*

Mode

general operation mode

Selection list referenced by the following parameters:

- ↳ SOTF . Mode

Mode	Description
CB Pos	*The CB Pos Indicator starts the Timer.*
I<	*The CB is in the OFF Position, if the measured current is less than this parameter.*
CB Pos And I<	*(The CB Pos Indicator starts the Timer.) And (The CB is in the OFF Position, if the measured current is less than this parameter.)*

Mode	Description
CB manual ON	*Circuit breaker was switched on manually*
Ext SOTF	*External Switch Onto Fault*

CB List

Selection list referenced by the following parameters:

- ⮱▷ SOTF . Assigned SG

CB List	Description
“-”	*No assignment*
. **SG[1]**	*Switchgear*
. **SG[2]**	*Switchgear*

1..n, DI-LogicList

Selection list referenced by the following parameters:

- ⮱▷ SOTF . Ext SOTF
- ⮱▷ SG[1] . Aux ON
- ⮱▷ SG[1] . Aux OFF
- ⮱▷ SG[1] . Ready
- ⮱▷ SG[1] . Removed
- ⮱▷ SG[1] . SCmd ON
- *[. . .]*

1..n, DI-LogicList	Description
“-”	*No assignment*
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*

1..n, DI-LogicList	Description
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*
DNP3 . **BinaryOutput0**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput1**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput2**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput3**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput4**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput5**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput6**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput7**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput8**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput9**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput10**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput11**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput12**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*

1..n, DI-LogicList	Description
DNP3 . **BinaryOutput13**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput14**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput15**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput16**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput17**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput18**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput19**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput20**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput21**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput22**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput23**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput24**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput25**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput26**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput27**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput28**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput29**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput30**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
DNP3 . **BinaryOutput31**	*Virtual Digital Output (DNP). This corresponds to a virtual binary input of the protective device.*
IEC104 . **Scada Cmd 1**	*Scada Command*
IEC104 . **Scada Cmd 2**	*Scada Command*

1..n, DI-LogicList	Description
IEC104 . **Scada Cmd 3**	*Scada Command*
IEC104 . **Scada Cmd 4**	*Scada Command*
IEC104 . **Scada Cmd 5**	*Scada Command*
IEC104 . **Scada Cmd 6**	*Scada Command*
IEC104 . **Scada Cmd 7**	*Scada Command*
IEC104 . **Scada Cmd 8**	*Scada Command*
IEC104 . **Scada Cmd 9**	*Scada Command*
IEC104 . **Scada Cmd 10**	*Scada Command*
IEC104 . **Scada Cmd 11**	*Scada Command*
IEC104 . **Scada Cmd 12**	*Scada Command*
IEC104 . **Scada Cmd 13**	*Scada Command*
IEC104 . **Scada Cmd 14**	*Scada Command*
IEC104 . **Scada Cmd 15**	*Scada Command*
IEC104 . **Scada Cmd 16**	*Scada Command*
Logics . **LE1.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE1.Timer Out**	*Signal: Timer Output*
Logics . **LE1.Out**	*Signal: Latched Output (Q)*
Logics . **LE1.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE2.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE2.Timer Out**	*Signal: Timer Output*
Logics . **LE2.Out**	*Signal: Latched Output (Q)*
Logics . **LE2.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE3.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE3.Timer Out**	*Signal: Timer Output*
Logics . **LE3.Out**	*Signal: Latched Output (Q)*
Logics . **LE3.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE4.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE4.Timer Out**	*Signal: Timer Output*
Logics . **LE4.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE4.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE5.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE5.Timer Out**	*Signal: Timer Output*
Logics . **LE5.Out**	*Signal: Latched Output (Q)*
Logics . **LE5.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE6.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE6.Timer Out**	*Signal: Timer Output*
Logics . **LE6.Out**	*Signal: Latched Output (Q)*
Logics . **LE6.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE7.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE7.Timer Out**	*Signal: Timer Output*
Logics . **LE7.Out**	*Signal: Latched Output (Q)*
Logics . **LE7.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE8.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE8.Timer Out**	*Signal: Timer Output*
Logics . **LE8.Out**	*Signal: Latched Output (Q)*
Logics . **LE8.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE9.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE9.Timer Out**	*Signal: Timer Output*
Logics . **LE9.Out**	*Signal: Latched Output (Q)*
Logics . **LE9.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE10.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE10.Timer Out**	*Signal: Timer Output*
Logics . **LE10.Out**	*Signal: Latched Output (Q)*
Logics . **LE10.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE11.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE11.Timer Out**	*Signal: Timer Output*
Logics . **LE11.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE11.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE12.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE12.Timer Out**	*Signal: Timer Output*
Logics . **LE12.Out**	*Signal: Latched Output (Q)*
Logics . **LE12.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE13.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE13.Timer Out**	*Signal: Timer Output*
Logics . **LE13.Out**	*Signal: Latched Output (Q)*
Logics . **LE13.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE14.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE14.Timer Out**	*Signal: Timer Output*
Logics . **LE14.Out**	*Signal: Latched Output (Q)*
Logics . **LE14.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE15.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE15.Timer Out**	*Signal: Timer Output*
Logics . **LE15.Out**	*Signal: Latched Output (Q)*
Logics . **LE15.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE16.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE16.Timer Out**	*Signal: Timer Output*
Logics . **LE16.Out**	*Signal: Latched Output (Q)*
Logics . **LE16.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE17.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE17.Timer Out**	*Signal: Timer Output*
Logics . **LE17.Out**	*Signal: Latched Output (Q)*
Logics . **LE17.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE18.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE18.Timer Out**	*Signal: Timer Output*
Logics . **LE18.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE18.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE19.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE19.Timer Out**	*Signal: Timer Output*
Logics . **LE19.Out**	*Signal: Latched Output (Q)*
Logics . **LE19.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE20.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE20.Timer Out**	*Signal: Timer Output*
Logics . **LE20.Out**	*Signal: Latched Output (Q)*
Logics . **LE20.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE21.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE21.Timer Out**	*Signal: Timer Output*
Logics . **LE21.Out**	*Signal: Latched Output (Q)*
Logics . **LE21.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE22.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE22.Timer Out**	*Signal: Timer Output*
Logics . **LE22.Out**	*Signal: Latched Output (Q)*
Logics . **LE22.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE23.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE23.Timer Out**	*Signal: Timer Output*
Logics . **LE23.Out**	*Signal: Latched Output (Q)*
Logics . **LE23.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE24.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE24.Timer Out**	*Signal: Timer Output*
Logics . **LE24.Out**	*Signal: Latched Output (Q)*
Logics . **LE24.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE25.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE25.Timer Out**	*Signal: Timer Output*
Logics . **LE25.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE25.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE26.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE26.Timer Out**	*Signal: Timer Output*
Logics . **LE26.Out**	*Signal: Latched Output (Q)*
Logics . **LE26.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE27.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE27.Timer Out**	*Signal: Timer Output*
Logics . **LE27.Out**	*Signal: Latched Output (Q)*
Logics . **LE27.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE28.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE28.Timer Out**	*Signal: Timer Output*
Logics . **LE28.Out**	*Signal: Latched Output (Q)*
Logics . **LE28.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE29.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE29.Timer Out**	*Signal: Timer Output*
Logics . **LE29.Out**	*Signal: Latched Output (Q)*
Logics . **LE29.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE30.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE30.Timer Out**	*Signal: Timer Output*
Logics . **LE30.Out**	*Signal: Latched Output (Q)*
Logics . **LE30.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE31.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE31.Timer Out**	*Signal: Timer Output*
Logics . **LE31.Out**	*Signal: Latched Output (Q)*
Logics . **LE31.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE32.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE32.Timer Out**	*Signal: Timer Output*
Logics . **LE32.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE32.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE33.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE33.Timer Out**	*Signal: Timer Output*
Logics . **LE33.Out**	*Signal: Latched Output (Q)*
Logics . **LE33.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE34.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE34.Timer Out**	*Signal: Timer Output*
Logics . **LE34.Out**	*Signal: Latched Output (Q)*
Logics . **LE34.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE35.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE35.Timer Out**	*Signal: Timer Output*
Logics . **LE35.Out**	*Signal: Latched Output (Q)*
Logics . **LE35.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE36.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE36.Timer Out**	*Signal: Timer Output*
Logics . **LE36.Out**	*Signal: Latched Output (Q)*
Logics . **LE36.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE37.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE37.Timer Out**	*Signal: Timer Output*
Logics . **LE37.Out**	*Signal: Latched Output (Q)*
Logics . **LE37.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE38.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE38.Timer Out**	*Signal: Timer Output*
Logics . **LE38.Out**	*Signal: Latched Output (Q)*
Logics . **LE38.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE39.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE39.Timer Out**	*Signal: Timer Output*
Logics . **LE39.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE39.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE40.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE40.Timer Out**	*Signal: Timer Output*
Logics . **LE40.Out**	*Signal: Latched Output (Q)*
Logics . **LE40.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE41.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE41.Timer Out**	*Signal: Timer Output*
Logics . **LE41.Out**	*Signal: Latched Output (Q)*
Logics . **LE41.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE42.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE42.Timer Out**	*Signal: Timer Output*
Logics . **LE42.Out**	*Signal: Latched Output (Q)*
Logics . **LE42.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE43.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE43.Timer Out**	*Signal: Timer Output*
Logics . **LE43.Out**	*Signal: Latched Output (Q)*
Logics . **LE43.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE44.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE44.Timer Out**	*Signal: Timer Output*
Logics . **LE44.Out**	*Signal: Latched Output (Q)*
Logics . **LE44.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE45.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE45.Timer Out**	*Signal: Timer Output*
Logics . **LE45.Out**	*Signal: Latched Output (Q)*
Logics . **LE45.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE46.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE46.Timer Out**	*Signal: Timer Output*
Logics . **LE46.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE46.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE47.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE47.Timer Out**	*Signal: Timer Output*
Logics . **LE47.Out**	*Signal: Latched Output (Q)*
Logics . **LE47.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE48.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE48.Timer Out**	*Signal: Timer Output*
Logics . **LE48.Out**	*Signal: Latched Output (Q)*
Logics . **LE48.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE49.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE49.Timer Out**	*Signal: Timer Output*
Logics . **LE49.Out**	*Signal: Latched Output (Q)*
Logics . **LE49.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE50.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE50.Timer Out**	*Signal: Timer Output*
Logics . **LE50.Out**	*Signal: Latched Output (Q)*
Logics . **LE50.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE51.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE51.Timer Out**	*Signal: Timer Output*
Logics . **LE51.Out**	*Signal: Latched Output (Q)*
Logics . **LE51.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE52.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE52.Timer Out**	*Signal: Timer Output*
Logics . **LE52.Out**	*Signal: Latched Output (Q)*
Logics . **LE52.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE53.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE53.Timer Out**	*Signal: Timer Output*
Logics . **LE53.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE53.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE54.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE54.Timer Out**	*Signal: Timer Output*
Logics . **LE54.Out**	*Signal: Latched Output (Q)*
Logics . **LE54.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE55.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE55.Timer Out**	*Signal: Timer Output*
Logics . **LE55.Out**	*Signal: Latched Output (Q)*
Logics . **LE55.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE56.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE56.Timer Out**	*Signal: Timer Output*
Logics . **LE56.Out**	*Signal: Latched Output (Q)*
Logics . **LE56.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE57.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE57.Timer Out**	*Signal: Timer Output*
Logics . **LE57.Out**	*Signal: Latched Output (Q)*
Logics . **LE57.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE58.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE58.Timer Out**	*Signal: Timer Output*
Logics . **LE58.Out**	*Signal: Latched Output (Q)*
Logics . **LE58.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE59.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE59.Timer Out**	*Signal: Timer Output*
Logics . **LE59.Out**	*Signal: Latched Output (Q)*
Logics . **LE59.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE60.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE60.Timer Out**	*Signal: Timer Output*
Logics . **LE60.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE60.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE61.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE61.Timer Out**	*Signal: Timer Output*
Logics . **LE61.Out**	*Signal: Latched Output (Q)*
Logics . **LE61.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE62.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE62.Timer Out**	*Signal: Timer Output*
Logics . **LE62.Out**	*Signal: Latched Output (Q)*
Logics . **LE62.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE63.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE63.Timer Out**	*Signal: Timer Output*
Logics . **LE63.Out**	*Signal: Latched Output (Q)*
Logics . **LE63.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE64.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE64.Timer Out**	*Signal: Timer Output*
Logics . **LE64.Out**	*Signal: Latched Output (Q)*
Logics . **LE64.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE65.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE65.Timer Out**	*Signal: Timer Output*
Logics . **LE65.Out**	*Signal: Latched Output (Q)*
Logics . **LE65.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE66.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE66.Timer Out**	*Signal: Timer Output*
Logics . **LE66.Out**	*Signal: Latched Output (Q)*
Logics . **LE66.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE67.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE67.Timer Out**	*Signal: Timer Output*
Logics . **LE67.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE67.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE68.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE68.Timer Out**	*Signal: Timer Output*
Logics . **LE68.Out**	*Signal: Latched Output (Q)*
Logics . **LE68.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE69.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE69.Timer Out**	*Signal: Timer Output*
Logics . **LE69.Out**	*Signal: Latched Output (Q)*
Logics . **LE69.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE70.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE70.Timer Out**	*Signal: Timer Output*
Logics . **LE70.Out**	*Signal: Latched Output (Q)*
Logics . **LE70.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE71.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE71.Timer Out**	*Signal: Timer Output*
Logics . **LE71.Out**	*Signal: Latched Output (Q)*
Logics . **LE71.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE72.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE72.Timer Out**	*Signal: Timer Output*
Logics . **LE72.Out**	*Signal: Latched Output (Q)*
Logics . **LE72.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE73.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE73.Timer Out**	*Signal: Timer Output*
Logics . **LE73.Out**	*Signal: Latched Output (Q)*
Logics . **LE73.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE74.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE74.Timer Out**	*Signal: Timer Output*
Logics . **LE74.Out**	*Signal: Latched Output (Q)*

1..n, DI-LogicList	Description
Logics . **LE74.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE75.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE75.Timer Out**	*Signal: Timer Output*
Logics . **LE75.Out**	*Signal: Latched Output (Q)*
Logics . **LE75.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE76.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE76.Timer Out**	*Signal: Timer Output*
Logics . **LE76.Out**	*Signal: Latched Output (Q)*
Logics . **LE76.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE77.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE77.Timer Out**	*Signal: Timer Output*
Logics . **LE77.Out**	*Signal: Latched Output (Q)*
Logics . **LE77.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE78.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE78.Timer Out**	*Signal: Timer Output*
Logics . **LE78.Out**	*Signal: Latched Output (Q)*
Logics . **LE78.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE79.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE79.Timer Out**	*Signal: Timer Output*
Logics . **LE79.Out**	*Signal: Latched Output (Q)*
Logics . **LE79.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE80.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE80.Timer Out**	*Signal: Timer Output*
Logics . **LE80.Out**	*Signal: Latched Output (Q)*
Logics . **LE80.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ CLPU . Mode

Mode	Description
CB Pos	*The CB Pos Indicator starts the Timer.*
I<	*The Pickup Timer will be started, if the measured current is less than parameter "I<".*
CB Pos Or I<	*(The CB Pos Indicator starts the Timer.) Or (The Pickup Timer will be started, if the measured current is less than parameter "I<".)*
CB Pos And I<	*(The CB Pos Indicator starts the Timer.) And (The Pickup Timer will be started, if the measured current is less than parameter "I<".)*

CB Manager

Circuit Breaker States

Selection list referenced by the following parameters:

- ╚═▷ CLPU . CB Pos Detect
- ╚═▷ TCS[1] . CB Pos Detect

CB Manager	Description
“-”	*No assignment*
SG[1] . **Pos**	*Signal: Circuit Breaker Position (0 = Indeterminate, 1 = OFF, 2 = ON, 3 = Disturbed)*
SG[2] . **Pos**	*Signal: Circuit Breaker Position (0 = Indeterminate, 1 = OFF, 2 = ON, 3 = Disturbed)*

TripCmd Selection

This parameter determines if the final trip of the RTD module is issued by the default way or by the voting groups.

Selection list referenced by the following parameters:

- ╚═▷ RTD . TripCmd Selection

TripCmd Selection	Description
Trip	*Default RTD Trip*
Voting Trip	*Voting Trip. Trip if one of the voting groups has a pending/active trip.*

Scheme

Via this selection menu, the BF supervision scheme is to be selected.

Selection list referenced by the following parameters:

- ╚═▷ CBF[1] . Scheme

Scheme	Description
50BF	*A Breaker Failure is detected, if the measured currents do not fall below a settable threshold within a settable time interval.*
CB Pos	*A Circuit Breaker Failure is detected after a CB open command, if the Position Contacts of the Circuit Breaker do not allow the conclusion that the Breaker is now in the Open Position within a settable time interval.*
50BF and CB Pos	*A Circuit Breaker Failure is detected if the evaluation of the Position Indicators or the evaluation of the current measurement indicate that the CB Off-Command was not excecuted. This scheme is called "Minimal Current Scheme" according to IEEEC37.119.*

CB List

Selection list referenced by the following parameters:

- ╚═▷ CBF[1] . CB

CB List	Description
“-”	*No assignment*
SG[1] .	
SG[2] .	

Trigger

Determining the trigger mode for the Breaker Failure. The selection will pickup the Breaker Failure as well as the assignments (Trigger 1, Trigger 2, Trigger 3). They are OR connected.

Selection list referenced by the following parameters:

- ╘═▷ CBF[1] . Trigger

Trigger	Description
- . -	*no assignment*
All Trips	*All trip signals that are assigned to this breaker (within the trip manager) will start the BF module.*
External Trips	*All external trips that are assigned to this breaker (within the trip manager) will start the BF module.*
Current Trips	*All current trips that are assigned to this breaker (within the trip manager) will start the BF module.*

External Trips

All external trips that are assigned to this breaker (within the trip manager) will start the BF module.

External Trips	Description
"-"	*No assignment*
ExP[1] . **TripCmd**	*Signal: Trip Command*
ExP[2] . **TripCmd**	*Signal: Trip Command*
ExP[3] . **TripCmd**	*Signal: Trip Command*
ExP[4] . **TripCmd**	*Signal: Trip Command*
Ext Sudd Press . **TripCmd**	*Signal: Trip Command*
Ext Oil Temp . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[1] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[2] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[3] . **TripCmd**	*Signal: Trip Command*

Current Trips

All current trips that are assigned to this breaker (within the trip manager) will start the BF module.

Current Trips	Description
“-”	*No assignment*
Id . **TripCmd**	*Signal: Trip Command*
IdH . **TripCmd**	*Signal: Trip Command*
IdG[1] . **TripCmd**	*Signal: Trip Command*
IdGH[1] . **TripCmd**	*Signal: Trip Command*
IdG[2] . **TripCmd**	*Signal: Trip Command*
IdGH[2] . **TripCmd**	*Signal: Trip Command*
I[1] . **TripCmd**	*Signal: Trip Command*
I[2] . **TripCmd**	*Signal: Trip Command*
I[3] . **TripCmd**	*Signal: Trip Command*
I[4] . **TripCmd**	*Signal: Trip Command*
I[5] . **TripCmd**	*Signal: Trip Command*
I[6] . **TripCmd**	*Signal: Trip Command*
IG[1] . **TripCmd**	*Signal: Trip Command*
IG[2] . **TripCmd**	*Signal: Trip Command*
IG[3] . **TripCmd**	*Signal: Trip Command*
IG[4] . **TripCmd**	*Signal: Trip Command*
ThR . **TripCmd**	*Signal: Trip Command*
I2>[1] . **TripCmd**	*Signal: Trip Command*
I2>[2] . **TripCmd**	*Signal: Trip Command*

Trigger

Determining the trigger mode for the Breaker Failure. The selection will pickup the Breaker Failure as well as the assignments (Trigger 1, Trigger 2, Trigger 3). They are OR connected.

Selection list referenced by the following parameters:

- ╚═▷ CBF[1] . Trigger1

Trigger	Description
“-”	*No assignment*
Id . **TripCmd**	*Signal: Trip Command*
IdH . **TripCmd**	*Signal: Trip Command*
IdG[1] . **TripCmd**	*Signal: Trip Command*
IdGH[1] . **TripCmd**	*Signal: Trip Command*
IdG[2] . **TripCmd**	*Signal: Trip Command*
IdGH[2] . **TripCmd**	*Signal: Trip Command*
I[1] . **TripCmd**	*Signal: Trip Command*
I[2] . **TripCmd**	*Signal: Trip Command*
I[3] . **TripCmd**	*Signal: Trip Command*
I[4] . **TripCmd**	*Signal: Trip Command*
I[5] . **TripCmd**	*Signal: Trip Command*
I[6] . **TripCmd**	*Signal: Trip Command*
IG[1] . **TripCmd**	*Signal: Trip Command*
IG[2] . **TripCmd**	*Signal: Trip Command*
IG[3] . **TripCmd**	*Signal: Trip Command*
IG[4] . **TripCmd**	*Signal: Trip Command*
ThR . **TripCmd**	*Signal: Trip Command*
I2>[1] . **TripCmd**	*Signal: Trip Command*
I2>[2] . **TripCmd**	*Signal: Trip Command*
ExP[1] . **TripCmd**	*Signal: Trip Command*
ExP[2] . **TripCmd**	*Signal: Trip Command*
ExP[3] . **TripCmd**	*Signal: Trip Command*
ExP[4] . **TripCmd**	*Signal: Trip Command*
Ext Sudd Press . **TripCmd**	*Signal: Trip Command*
Ext Oil Temp . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[1] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[2] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[3] . **TripCmd**	*Signal: Trip Command*
RTD . **TripCmd**	*Signal: Trip Command*

Trigger	Description
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*
Logics . **LE1.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE1.Timer Out**	*Signal: Timer Output*
Logics . **LE1.Out**	*Signal: Latched Output (Q)*
Logics . **LE1.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE2.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE2.Timer Out**	*Signal: Timer Output*
Logics . **LE2.Out**	*Signal: Latched Output (Q)*
Logics . **LE2.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE3.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE3.Timer Out**	*Signal: Timer Output*
Logics . **LE3.Out**	*Signal: Latched Output (Q)*
Logics . **LE3.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE4.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE4.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE4.Out**	*Signal: Latched Output (Q)*
Logics . **LE4.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE5.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE5.Timer Out**	*Signal: Timer Output*
Logics . **LE5.Out**	*Signal: Latched Output (Q)*
Logics . **LE5.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE6.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE6.Timer Out**	*Signal: Timer Output*
Logics . **LE6.Out**	*Signal: Latched Output (Q)*
Logics . **LE6.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE7.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE7.Timer Out**	*Signal: Timer Output*
Logics . **LE7.Out**	*Signal: Latched Output (Q)*
Logics . **LE7.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE8.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE8.Timer Out**	*Signal: Timer Output*
Logics . **LE8.Out**	*Signal: Latched Output (Q)*
Logics . **LE8.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE9.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE9.Timer Out**	*Signal: Timer Output*
Logics . **LE9.Out**	*Signal: Latched Output (Q)*
Logics . **LE9.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE10.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE10.Timer Out**	*Signal: Timer Output*
Logics . **LE10.Out**	*Signal: Latched Output (Q)*
Logics . **LE10.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE11.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE11.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE11.Out**	*Signal: Latched Output (Q)*
Logics . **LE11.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE12.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE12.Timer Out**	*Signal: Timer Output*
Logics . **LE12.Out**	*Signal: Latched Output (Q)*
Logics . **LE12.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE13.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE13.Timer Out**	*Signal: Timer Output*
Logics . **LE13.Out**	*Signal: Latched Output (Q)*
Logics . **LE13.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE14.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE14.Timer Out**	*Signal: Timer Output*
Logics . **LE14.Out**	*Signal: Latched Output (Q)*
Logics . **LE14.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE15.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE15.Timer Out**	*Signal: Timer Output*
Logics . **LE15.Out**	*Signal: Latched Output (Q)*
Logics . **LE15.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE16.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE16.Timer Out**	*Signal: Timer Output*
Logics . **LE16.Out**	*Signal: Latched Output (Q)*
Logics . **LE16.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE17.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE17.Timer Out**	*Signal: Timer Output*
Logics . **LE17.Out**	*Signal: Latched Output (Q)*
Logics . **LE17.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE18.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE18.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE18.Out**	*Signal: Latched Output (Q)*
Logics . **LE18.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE19.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE19.Timer Out**	*Signal: Timer Output*
Logics . **LE19.Out**	*Signal: Latched Output (Q)*
Logics . **LE19.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE20.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE20.Timer Out**	*Signal: Timer Output*
Logics . **LE20.Out**	*Signal: Latched Output (Q)*
Logics . **LE20.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE21.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE21.Timer Out**	*Signal: Timer Output*
Logics . **LE21.Out**	*Signal: Latched Output (Q)*
Logics . **LE21.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE22.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE22.Timer Out**	*Signal: Timer Output*
Logics . **LE22.Out**	*Signal: Latched Output (Q)*
Logics . **LE22.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE23.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE23.Timer Out**	*Signal: Timer Output*
Logics . **LE23.Out**	*Signal: Latched Output (Q)*
Logics . **LE23.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE24.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE24.Timer Out**	*Signal: Timer Output*
Logics . **LE24.Out**	*Signal: Latched Output (Q)*
Logics . **LE24.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE25.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE25.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE25.Out**	*Signal: Latched Output (Q)*
Logics . **LE25.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE26.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE26.Timer Out**	*Signal: Timer Output*
Logics . **LE26.Out**	*Signal: Latched Output (Q)*
Logics . **LE26.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE27.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE27.Timer Out**	*Signal: Timer Output*
Logics . **LE27.Out**	*Signal: Latched Output (Q)*
Logics . **LE27.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE28.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE28.Timer Out**	*Signal: Timer Output*
Logics . **LE28.Out**	*Signal: Latched Output (Q)*
Logics . **LE28.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE29.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE29.Timer Out**	*Signal: Timer Output*
Logics . **LE29.Out**	*Signal: Latched Output (Q)*
Logics . **LE29.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE30.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE30.Timer Out**	*Signal: Timer Output*
Logics . **LE30.Out**	*Signal: Latched Output (Q)*
Logics . **LE30.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE31.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE31.Timer Out**	*Signal: Timer Output*
Logics . **LE31.Out**	*Signal: Latched Output (Q)*
Logics . **LE31.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE32.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE32.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE32.Out**	*Signal: Latched Output (Q)*
Logics . **LE32.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE33.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE33.Timer Out**	*Signal: Timer Output*
Logics . **LE33.Out**	*Signal: Latched Output (Q)*
Logics . **LE33.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE34.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE34.Timer Out**	*Signal: Timer Output*
Logics . **LE34.Out**	*Signal: Latched Output (Q)*
Logics . **LE34.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE35.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE35.Timer Out**	*Signal: Timer Output*
Logics . **LE35.Out**	*Signal: Latched Output (Q)*
Logics . **LE35.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE36.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE36.Timer Out**	*Signal: Timer Output*
Logics . **LE36.Out**	*Signal: Latched Output (Q)*
Logics . **LE36.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE37.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE37.Timer Out**	*Signal: Timer Output*
Logics . **LE37.Out**	*Signal: Latched Output (Q)*
Logics . **LE37.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE38.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE38.Timer Out**	*Signal: Timer Output*
Logics . **LE38.Out**	*Signal: Latched Output (Q)*
Logics . **LE38.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE39.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE39.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE39.Out**	*Signal: Latched Output (Q)*
Logics . **LE39.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE40.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE40.Timer Out**	*Signal: Timer Output*
Logics . **LE40.Out**	*Signal: Latched Output (Q)*
Logics . **LE40.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE41.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE41.Timer Out**	*Signal: Timer Output*
Logics . **LE41.Out**	*Signal: Latched Output (Q)*
Logics . **LE41.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE42.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE42.Timer Out**	*Signal: Timer Output*
Logics . **LE42.Out**	*Signal: Latched Output (Q)*
Logics . **LE42.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE43.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE43.Timer Out**	*Signal: Timer Output*
Logics . **LE43.Out**	*Signal: Latched Output (Q)*
Logics . **LE43.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE44.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE44.Timer Out**	*Signal: Timer Output*
Logics . **LE44.Out**	*Signal: Latched Output (Q)*
Logics . **LE44.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE45.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE45.Timer Out**	*Signal: Timer Output*
Logics . **LE45.Out**	*Signal: Latched Output (Q)*
Logics . **LE45.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE46.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE46.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE46.Out**	*Signal: Latched Output (Q)*
Logics . **LE46.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE47.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE47.Timer Out**	*Signal: Timer Output*
Logics . **LE47.Out**	*Signal: Latched Output (Q)*
Logics . **LE47.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE48.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE48.Timer Out**	*Signal: Timer Output*
Logics . **LE48.Out**	*Signal: Latched Output (Q)*
Logics . **LE48.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE49.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE49.Timer Out**	*Signal: Timer Output*
Logics . **LE49.Out**	*Signal: Latched Output (Q)*
Logics . **LE49.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE50.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE50.Timer Out**	*Signal: Timer Output*
Logics . **LE50.Out**	*Signal: Latched Output (Q)*
Logics . **LE50.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE51.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE51.Timer Out**	*Signal: Timer Output*
Logics . **LE51.Out**	*Signal: Latched Output (Q)*
Logics . **LE51.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE52.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE52.Timer Out**	*Signal: Timer Output*
Logics . **LE52.Out**	*Signal: Latched Output (Q)*
Logics . **LE52.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE53.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE53.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE53.Out**	*Signal: Latched Output (Q)*
Logics . **LE53.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE54.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE54.Timer Out**	*Signal: Timer Output*
Logics . **LE54.Out**	*Signal: Latched Output (Q)*
Logics . **LE54.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE55.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE55.Timer Out**	*Signal: Timer Output*
Logics . **LE55.Out**	*Signal: Latched Output (Q)*
Logics . **LE55.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE56.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE56.Timer Out**	*Signal: Timer Output*
Logics . **LE56.Out**	*Signal: Latched Output (Q)*
Logics . **LE56.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE57.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE57.Timer Out**	*Signal: Timer Output*
Logics . **LE57.Out**	*Signal: Latched Output (Q)*
Logics . **LE57.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE58.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE58.Timer Out**	*Signal: Timer Output*
Logics . **LE58.Out**	*Signal: Latched Output (Q)*
Logics . **LE58.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE59.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE59.Timer Out**	*Signal: Timer Output*
Logics . **LE59.Out**	*Signal: Latched Output (Q)*
Logics . **LE59.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE60.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE60.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE60.Out**	*Signal: Latched Output (Q)*
Logics . **LE60.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE61.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE61.Timer Out**	*Signal: Timer Output*
Logics . **LE61.Out**	*Signal: Latched Output (Q)*
Logics . **LE61.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE62.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE62.Timer Out**	*Signal: Timer Output*
Logics . **LE62.Out**	*Signal: Latched Output (Q)*
Logics . **LE62.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE63.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE63.Timer Out**	*Signal: Timer Output*
Logics . **LE63.Out**	*Signal: Latched Output (Q)*
Logics . **LE63.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE64.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE64.Timer Out**	*Signal: Timer Output*
Logics . **LE64.Out**	*Signal: Latched Output (Q)*
Logics . **LE64.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE65.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE65.Timer Out**	*Signal: Timer Output*
Logics . **LE65.Out**	*Signal: Latched Output (Q)*
Logics . **LE65.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE66.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE66.Timer Out**	*Signal: Timer Output*
Logics . **LE66.Out**	*Signal: Latched Output (Q)*
Logics . **LE66.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE67.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE67.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE67.Out**	*Signal: Latched Output (Q)*
Logics . **LE67.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE68.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE68.Timer Out**	*Signal: Timer Output*
Logics . **LE68.Out**	*Signal: Latched Output (Q)*
Logics . **LE68.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE69.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE69.Timer Out**	*Signal: Timer Output*
Logics . **LE69.Out**	*Signal: Latched Output (Q)*
Logics . **LE69.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE70.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE70.Timer Out**	*Signal: Timer Output*
Logics . **LE70.Out**	*Signal: Latched Output (Q)*
Logics . **LE70.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE71.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE71.Timer Out**	*Signal: Timer Output*
Logics . **LE71.Out**	*Signal: Latched Output (Q)*
Logics . **LE71.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE72.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE72.Timer Out**	*Signal: Timer Output*
Logics . **LE72.Out**	*Signal: Latched Output (Q)*
Logics . **LE72.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE73.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE73.Timer Out**	*Signal: Timer Output*
Logics . **LE73.Out**	*Signal: Latched Output (Q)*
Logics . **LE73.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE74.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE74.Timer Out**	*Signal: Timer Output*

Trigger	Description
Logics . **LE74.Out**	*Signal: Latched Output (Q)*
Logics . **LE74.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE75.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE75.Timer Out**	*Signal: Timer Output*
Logics . **LE75.Out**	*Signal: Latched Output (Q)*
Logics . **LE75.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE76.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE76.Timer Out**	*Signal: Timer Output*
Logics . **LE76.Out**	*Signal: Latched Output (Q)*
Logics . **LE76.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE77.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE77.Timer Out**	*Signal: Timer Output*
Logics . **LE77.Out**	*Signal: Latched Output (Q)*
Logics . **LE77.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE78.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE78.Timer Out**	*Signal: Timer Output*
Logics . **LE78.Out**	*Signal: Latched Output (Q)*
Logics . **LE78.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE79.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE79.Timer Out**	*Signal: Timer Output*
Logics . **LE79.Out**	*Signal: Latched Output (Q)*
Logics . **LE79.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE80.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE80.Timer Out**	*Signal: Timer Output*
Logics . **LE80.Out**	*Signal: Latched Output (Q)*
Logics . **LE80.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ TCS[1] . Mode

Mode	Description
Closed	*Selects that the breaker is going to be monitored when the breaker is closed.*
Either	*Selects that the breaker is going to be monitored when the breaker is either closed or open.*

1..n, Dig Inputs

List of Digital Inputs that are available for the detection of the Circuit Breaker Position.

Selection list referenced by the following parameters:

- ╚═▷ TCS[1] . Input 1
- ╚═▷ TCS[1] . Input 2

1..n, Dig Inputs	Description
“-”	*No assignment*
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*

1..n, Dig Inputs	Description
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*

PSet-Switch

Switching Parameter Set

Selection list referenced by the following parameters:

- ⮱▷ Sys . PSet-Switch

PSet-Switch	Description
PS1	*The currently active Parameter Set is PS1*
PS2	*The currently active Parameter Set is PS2*
PS3	*The currently active Parameter Set is PS3*
PS4	*The currently active Parameter Set is PS4*
PSS via Inp fct	*Parameter Set Switch via input function*
PSS via Scada	*Parameter Set Switch via Scada. Write into this output byte the integer of the parameter set that should become active (e.g. 4 => Switch onto parameter set 4).*

1..n, PSS

List of the available Parameter Setting Group Switching Signals

Selection list referenced by the following parameters:

- ⮱▷ Sys . PS1: activated by

1..n, PSS	Description
“-”	*No assignment*
CTS[1] . **Alarm**	*Signal: Alarm Current Transformer Measuring Circuit Supervision*
CTS[2] . **Alarm**	*Signal: Alarm Current Transformer Measuring Circuit Supervision*
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*

1..n, PSS	Description
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*
Logics . **LE1.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE1.Timer Out**	*Signal: Timer Output*
Logics . **LE1.Out**	*Signal: Latched Output (Q)*
Logics . **LE1.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE2.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE2.Timer Out**	*Signal: Timer Output*
Logics . **LE2.Out**	*Signal: Latched Output (Q)*
Logics . **LE2.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE3.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE3.Timer Out**	*Signal: Timer Output*
Logics . **LE3.Out**	*Signal: Latched Output (Q)*
Logics . **LE3.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE4.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE4.Timer Out**	*Signal: Timer Output*
Logics . **LE4.Out**	*Signal: Latched Output (Q)*
Logics . **LE4.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE5.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE5.Timer Out**	*Signal: Timer Output*
Logics . **LE5.Out**	*Signal: Latched Output (Q)*
Logics . **LE5.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE6.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE6.Timer Out**	*Signal: Timer Output*
Logics . **LE6.Out**	*Signal: Latched Output (Q)*
Logics . **LE6.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE7.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE7.Timer Out**	*Signal: Timer Output*
Logics . **LE7.Out**	*Signal: Latched Output (Q)*
Logics . **LE7.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE8.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE8.Timer Out**	*Signal: Timer Output*
Logics . **LE8.Out**	*Signal: Latched Output (Q)*
Logics . **LE8.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE9.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE9.Timer Out**	*Signal: Timer Output*
Logics . **LE9.Out**	*Signal: Latched Output (Q)*
Logics . **LE9.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE10.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE10.Timer Out**	*Signal: Timer Output*
Logics . **LE10.Out**	*Signal: Latched Output (Q)*
Logics . **LE10.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE11.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE11.Timer Out**	*Signal: Timer Output*
Logics . **LE11.Out**	*Signal: Latched Output (Q)*
Logics . **LE11.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE12.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE12.Timer Out**	*Signal: Timer Output*
Logics . **LE12.Out**	*Signal: Latched Output (Q)*
Logics . **LE12.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE13.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE13.Timer Out**	*Signal: Timer Output*
Logics . **LE13.Out**	*Signal: Latched Output (Q)*
Logics . **LE13.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE14.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE14.Timer Out**	*Signal: Timer Output*
Logics . **LE14.Out**	*Signal: Latched Output (Q)*
Logics . **LE14.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE15.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE15.Timer Out**	*Signal: Timer Output*
Logics . **LE15.Out**	*Signal: Latched Output (Q)*
Logics . **LE15.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE16.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE16.Timer Out**	*Signal: Timer Output*
Logics . **LE16.Out**	*Signal: Latched Output (Q)*
Logics . **LE16.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE17.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE17.Timer Out**	*Signal: Timer Output*
Logics . **LE17.Out**	*Signal: Latched Output (Q)*
Logics . **LE17.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE18.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE18.Timer Out**	*Signal: Timer Output*
Logics . **LE18.Out**	*Signal: Latched Output (Q)*
Logics . **LE18.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE19.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE19.Timer Out**	*Signal: Timer Output*
Logics . **LE19.Out**	*Signal: Latched Output (Q)*
Logics . **LE19.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE20.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE20.Timer Out**	*Signal: Timer Output*
Logics . **LE20.Out**	*Signal: Latched Output (Q)*
Logics . **LE20.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE21.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE21.Timer Out**	*Signal: Timer Output*
Logics . **LE21.Out**	*Signal: Latched Output (Q)*
Logics . **LE21.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE22.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE22.Timer Out**	*Signal: Timer Output*
Logics . **LE22.Out**	*Signal: Latched Output (Q)*
Logics . **LE22.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE23.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE23.Timer Out**	*Signal: Timer Output*
Logics . **LE23.Out**	*Signal: Latched Output (Q)*
Logics . **LE23.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE24.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE24.Timer Out**	*Signal: Timer Output*
Logics . **LE24.Out**	*Signal: Latched Output (Q)*
Logics . **LE24.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE25.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE25.Timer Out**	*Signal: Timer Output*
Logics . **LE25.Out**	*Signal: Latched Output (Q)*
Logics . **LE25.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE26.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE26.Timer Out**	*Signal: Timer Output*
Logics . **LE26.Out**	*Signal: Latched Output (Q)*
Logics . **LE26.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE27.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE27.Timer Out**	*Signal: Timer Output*
Logics . **LE27.Out**	*Signal: Latched Output (Q)*
Logics . **LE27.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE28.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE28.Timer Out**	*Signal: Timer Output*
Logics . **LE28.Out**	*Signal: Latched Output (Q)*
Logics . **LE28.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE29.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE29.Timer Out**	*Signal: Timer Output*
Logics . **LE29.Out**	*Signal: Latched Output (Q)*
Logics . **LE29.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE30.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE30.Timer Out**	*Signal: Timer Output*
Logics . **LE30.Out**	*Signal: Latched Output (Q)*
Logics . **LE30.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE31.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE31.Timer Out**	*Signal: Timer Output*
Logics . **LE31.Out**	*Signal: Latched Output (Q)*
Logics . **LE31.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE32.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE32.Timer Out**	*Signal: Timer Output*
Logics . **LE32.Out**	*Signal: Latched Output (Q)*
Logics . **LE32.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE33.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE33.Timer Out**	*Signal: Timer Output*
Logics . **LE33.Out**	*Signal: Latched Output (Q)*
Logics . **LE33.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE34.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE34.Timer Out**	*Signal: Timer Output*
Logics . **LE34.Out**	*Signal: Latched Output (Q)*
Logics . **LE34.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE35.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE35.Timer Out**	*Signal: Timer Output*
Logics . **LE35.Out**	*Signal: Latched Output (Q)*
Logics . **LE35.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE36.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE36.Timer Out**	*Signal: Timer Output*
Logics . **LE36.Out**	*Signal: Latched Output (Q)*
Logics . **LE36.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE37.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE37.Timer Out**	*Signal: Timer Output*
Logics . **LE37.Out**	*Signal: Latched Output (Q)*
Logics . **LE37.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE38.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE38.Timer Out**	*Signal: Timer Output*
Logics . **LE38.Out**	*Signal: Latched Output (Q)*
Logics . **LE38.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE39.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE39.Timer Out**	*Signal: Timer Output*
Logics . **LE39.Out**	*Signal: Latched Output (Q)*
Logics . **LE39.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE40.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE40.Timer Out**	*Signal: Timer Output*
Logics . **LE40.Out**	*Signal: Latched Output (Q)*
Logics . **LE40.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE41.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE41.Timer Out**	*Signal: Timer Output*
Logics . **LE41.Out**	*Signal: Latched Output (Q)*
Logics . **LE41.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE42.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE42.Timer Out**	*Signal: Timer Output*
Logics . **LE42.Out**	*Signal: Latched Output (Q)*
Logics . **LE42.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE43.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE43.Timer Out**	*Signal: Timer Output*
Logics . **LE43.Out**	*Signal: Latched Output (Q)*
Logics . **LE43.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE44.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE44.Timer Out**	*Signal: Timer Output*
Logics . **LE44.Out**	*Signal: Latched Output (Q)*
Logics . **LE44.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE45.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE45.Timer Out**	*Signal: Timer Output*
Logics . **LE45.Out**	*Signal: Latched Output (Q)*
Logics . **LE45.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE46.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE46.Timer Out**	*Signal: Timer Output*
Logics . **LE46.Out**	*Signal: Latched Output (Q)*
Logics . **LE46.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE47.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE47.Timer Out**	*Signal: Timer Output*
Logics . **LE47.Out**	*Signal: Latched Output (Q)*
Logics . **LE47.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE48.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE48.Timer Out**	*Signal: Timer Output*
Logics . **LE48.Out**	*Signal: Latched Output (Q)*
Logics . **LE48.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE49.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE49.Timer Out**	*Signal: Timer Output*
Logics . **LE49.Out**	*Signal: Latched Output (Q)*
Logics . **LE49.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE50.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE50.Timer Out**	*Signal: Timer Output*
Logics . **LE50.Out**	*Signal: Latched Output (Q)*
Logics . **LE50.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE51.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE51.Timer Out**	*Signal: Timer Output*
Logics . **LE51.Out**	*Signal: Latched Output (Q)*
Logics . **LE51.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE52.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE52.Timer Out**	*Signal: Timer Output*
Logics . **LE52.Out**	*Signal: Latched Output (Q)*
Logics . **LE52.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE53.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE53.Timer Out**	*Signal: Timer Output*
Logics . **LE53.Out**	*Signal: Latched Output (Q)*
Logics . **LE53.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE54.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE54.Timer Out**	*Signal: Timer Output*
Logics . **LE54.Out**	*Signal: Latched Output (Q)*
Logics . **LE54.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE55.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE55.Timer Out**	*Signal: Timer Output*
Logics . **LE55.Out**	*Signal: Latched Output (Q)*
Logics . **LE55.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE56.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE56.Timer Out**	*Signal: Timer Output*
Logics . **LE56.Out**	*Signal: Latched Output (Q)*
Logics . **LE56.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE57.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE57.Timer Out**	*Signal: Timer Output*
Logics . **LE57.Out**	*Signal: Latched Output (Q)*
Logics . **LE57.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE58.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE58.Timer Out**	*Signal: Timer Output*
Logics . **LE58.Out**	*Signal: Latched Output (Q)*
Logics . **LE58.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE59.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE59.Timer Out**	*Signal: Timer Output*
Logics . **LE59.Out**	*Signal: Latched Output (Q)*
Logics . **LE59.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE60.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE60.Timer Out**	*Signal: Timer Output*
Logics . **LE60.Out**	*Signal: Latched Output (Q)*
Logics . **LE60.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE61.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE61.Timer Out**	*Signal: Timer Output*
Logics . **LE61.Out**	*Signal: Latched Output (Q)*
Logics . **LE61.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE62.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE62.Timer Out**	*Signal: Timer Output*
Logics . **LE62.Out**	*Signal: Latched Output (Q)*
Logics . **LE62.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE63.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE63.Timer Out**	*Signal: Timer Output*
Logics . **LE63.Out**	*Signal: Latched Output (Q)*
Logics . **LE63.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE64.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE64.Timer Out**	*Signal: Timer Output*
Logics . **LE64.Out**	*Signal: Latched Output (Q)*
Logics . **LE64.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE65.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE65.Timer Out**	*Signal: Timer Output*
Logics . **LE65.Out**	*Signal: Latched Output (Q)*
Logics . **LE65.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE66.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE66.Timer Out**	*Signal: Timer Output*
Logics . **LE66.Out**	*Signal: Latched Output (Q)*
Logics . **LE66.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE67.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE67.Timer Out**	*Signal: Timer Output*
Logics . **LE67.Out**	*Signal: Latched Output (Q)*
Logics . **LE67.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE68.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE68.Timer Out**	*Signal: Timer Output*
Logics . **LE68.Out**	*Signal: Latched Output (Q)*
Logics . **LE68.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE69.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE69.Timer Out**	*Signal: Timer Output*
Logics . **LE69.Out**	*Signal: Latched Output (Q)*
Logics . **LE69.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE70.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE70.Timer Out**	*Signal: Timer Output*
Logics . **LE70.Out**	*Signal: Latched Output (Q)*
Logics . **LE70.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE71.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE71.Timer Out**	*Signal: Timer Output*
Logics . **LE71.Out**	*Signal: Latched Output (Q)*
Logics . **LE71.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE72.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE72.Timer Out**	*Signal: Timer Output*
Logics . **LE72.Out**	*Signal: Latched Output (Q)*
Logics . **LE72.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE73.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE73.Timer Out**	*Signal: Timer Output*
Logics . **LE73.Out**	*Signal: Latched Output (Q)*
Logics . **LE73.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE74.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE74.Timer Out**	*Signal: Timer Output*
Logics . **LE74.Out**	*Signal: Latched Output (Q)*
Logics . **LE74.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

1..n, PSS	Description
Logics . **LE75.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE75.Timer Out**	*Signal: Timer Output*
Logics . **LE75.Out**	*Signal: Latched Output (Q)*
Logics . **LE75.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE76.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE76.Timer Out**	*Signal: Timer Output*
Logics . **LE76.Out**	*Signal: Latched Output (Q)*
Logics . **LE76.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE77.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE77.Timer Out**	*Signal: Timer Output*
Logics . **LE77.Out**	*Signal: Latched Output (Q)*
Logics . **LE77.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE78.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE78.Timer Out**	*Signal: Timer Output*
Logics . **LE78.Out**	*Signal: Latched Output (Q)*
Logics . **LE78.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE79.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE79.Timer Out**	*Signal: Timer Output*
Logics . **LE79.Out**	*Signal: Latched Output (Q)*
Logics . **LE79.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE80.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE80.Timer Out**	*Signal: Timer Output*
Logics . **LE80.Out**	*Signal: Latched Output (Q)*
Logics . **LE80.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

Measuring method

Measuring method: fundamental or rms or 3rd harmonic (only generator protection relays)

Selection list referenced by the following parameters:

- ╘═▷ I[1] . Measuring method

Measuring method	Description
Fundamental	*Protection is based on Fundamental (1st. Harmonic)*
True RMS	*Protection is based on root-mean-square value (True RMS)*
I2	*Protection is based on negative phase sequence current*

Char

Characteristic

Selection list referenced by the following parameters:

- ╘═▷ I[1] . Char

Char	Description
DEFT	*DEFT*
IEC NINV	*IEC Normal Inverse*
IEC VINV	*IEC Very Inverse [VINV]*
IEC EINV	*IEC Extremely Inverse - Characteristic*
IEC LINV	*IEC Long Time Inverse - Characteristic [LINV]*
RINV	*R Inverse [RINV] - Characteristic*
ANSI MINV	*ANSI Moderately Inverse [MINV] - Characteristic*
ANSI VINV	*ANSI Very Inverse [VINV]*
ANSI EINV	*ANSI Extremely Inverse - Characteristic*
Therm Flat	*Therm Flat [TF] - Characteristic*
IT	*IT - Characteristic*
I2T	*I2T - Characteristic*
I4T	*I4T - Characteristic*

Reset Mode

Selection list referenced by the following parameters:

- ╚═▷ I[1] . Reset Mode

Reset Mode	Description
instantaneous	*Instantaneous reset: when the current drops below the pickup setting, the TOC time resets to zero within 2 cycles.*
delayed	*Reset after a fixed time.* *(Remark: This delay is then defined by the parameter »t-reset delay«.)*
calculated	*Calculated reset, which is defined by ANSI C37.112 and IEC.*

IH2 Blo

Blocking the trip command, if an inrush is detected.

Selection list referenced by the following parameters:

- ╚═▷ I[1] . IH2 Blo
- ╚═▷ IG[1] . IH2 Blo

IH2 Blo	Description
Sys . **inactive**	*inactive*
Sys . **active**	*active*

Measuring Channel

Selection list referenced by the following parameters:

- ╚═▷ IG[1] . IG Source

Measuring Channel	Description
CT W1 . **sensitive measurement**	*sensitive measurement*
CT W1 . **measured**	*measured*
CT W1 . **calculated**	*calculated*
CT W2 . **measured (X4)**	*measured (slot X4)*

Measuring Channel	Description
CT W2 . **sensitive measurement (X4)**	*sensitive measurement (slot X4)*

Measuring method

Measuring method: fundamental or rms or 3rd harmonic (only generator protection relays)

Selection list referenced by the following parameters:

- ⮡ IG[1] . Measuring method

Measuring method	Description
Fundamental	*Protection is based on Fundamental (1st. Harmonic)*
True RMS	*Protection is based on root-mean-square value (True RMS)*

VTS Block

Blocking of the module if the voltage transformer supervision detects a fault.

Selection list referenced by the following parameters:

- ⮡ IG[1] . Meas Circuit Superv

VTS Block	Description
Sys . **inactive**	*inactive*

Char

Characteristic

Selection list referenced by the following parameters:

- ⮡ IG[1] . Char

Char	Description
DEFT	*DEFT*
IEC NINV	*IEC Normal Inverse*

Char	Description
IEC VINV	*IEC Very Inverse [VINV]*
IEC EINV	*IEC Extremely Inverse - Characteristic*
IEC LINV	*IEC Long Time Inverse - Characteristic [LINV]*
RINV	*R Inverse [RINV] - Characteristic*
ANSI MINV	*ANSI Moderately Inverse [MINV] - Characteristic*
ANSI VINV	*ANSI Very Inverse [VINV]*
ANSI EINV	*ANSI Extremely Inverse - Characteristic*
Therm Flat	*Therm Flat [TF] - Characteristic*
IT	*IT - Characteristic*
I2T	*I2T - Characteristic*
I4T	*I4T - Characteristic*
RXIDG	*Special Overcurrent Curve*

Reset Mode

Selection list referenced by the following parameters:

- ╚═▷ IG[1] . Reset Mode

Reset Mode	Description
instantaneous	*Instantaneous reset: when the current drops below the pickup setting, the TOC time resets to zero within 2 cycles.*
delayed	*Reset after a fixed time.* *(Remark: This delay is then defined by the parameter »t-reset delay«.)*
calculated	*Calculated reset, which is defined by ANSI C37.112 and IEC.*

Char

Characteristic

Selection list referenced by the following parameters:

- ╚═▷ I2>[1] . Char

Char	Description
DEFT	*DEFT*
INV	*INV*

block mode

Selection list referenced by the following parameters:

- ⮡ IH2[1] . block mode

block mode	Description
1-ph Blo	*1-ph Blo: If an inrush is detected in one phase, the corresponding phase of those modules will be blocked, where inrush blocking is set to active.*
3-ph Blo	*3-ph Blo: If an inrush is detected in at least one phase, all three phases of those modules where inrush blocking is set to active will be blocked (cross blocking).*

NonIL ResetMode

Non-Interlocking ResetMode

Selection list referenced by the following parameters:

- ⮡ Ctrl . Res NonIL

NonIL ResetMode	Description
single Operation	*single Operation*
timeout	*timeout*
permanent	*permanent*

Manipulate Position

WARNING! Fake Position - Manual Position Manipulation

Selection list referenced by the following parameters:

- ⮡ SG[1] . Manipulate Position

Manipulate Position	Description
inactive	*inactive*
Pos OFF	*Signal: Circuit Breaker is in OFF-Position*
Pos ON	*Signal: Circuit Breaker is in ON-Position*

1..n, Trip Cmds

List of available Trip Commands

Selection list referenced by the following parameters:

- ╚═▷ SG[1] . Off Cmd1
- ╚═▷ SG[1] . Off Cmd2
- ╚═▷ SG[1] . Off Cmd3
- ╚═▷ SG[1] . Off Cmd4

1..n, Trip Cmds	Description
"-"	*No assignment*
Id . **TripCmd**	*Signal: Trip Command*
IdH . **TripCmd**	*Signal: Trip Command*
IdG[1] . **TripCmd**	*Signal: Trip Command*
IdGH[1] . **TripCmd**	*Signal: Trip Command*
IdG[2] . **TripCmd**	*Signal: Trip Command*
IdGH[2] . **TripCmd**	*Signal: Trip Command*
I[1] . **TripCmd**	*Signal: Trip Command*
I[2] . **TripCmd**	*Signal: Trip Command*
I[3] . **TripCmd**	*Signal: Trip Command*
I[4] . **TripCmd**	*Signal: Trip Command*
I[5] . **TripCmd**	*Signal: Trip Command*
I[6] . **TripCmd**	*Signal: Trip Command*
IG[1] . **TripCmd**	*Signal: Trip Command*
IG[2] . **TripCmd**	*Signal: Trip Command*
IG[3] . **TripCmd**	*Signal: Trip Command*
IG[4] . **TripCmd**	*Signal: Trip Command*

1..n, Trip Cmds	Description
ThR . **TripCmd**	*Signal: Trip Command*
I2>[1] . **TripCmd**	*Signal: Trip Command*
I2>[2] . **TripCmd**	*Signal: Trip Command*
ExP[1] . **TripCmd**	*Signal: Trip Command*
ExP[2] . **TripCmd**	*Signal: Trip Command*
ExP[3] . **TripCmd**	*Signal: Trip Command*
ExP[4] . **TripCmd**	*Signal: Trip Command*
Ext Sudd Press . **TripCmd**	*Signal: Trip Command*
Ext Oil Temp . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[1] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[2] . **TripCmd**	*Signal: Trip Command*
Ext Temp Superv[3] . **TripCmd**	*Signal: Trip Command*
RTD . **TripCmd**	*Signal: Trip Command*

1..n, In-SyncList

Selection list referenced by the following parameters:

- ╚═▷ SG[1] . Synchronism

1..n, In-SyncList	Description
“-”	*No assignment*
DI Slot X1 . **DI 1**	*Signal: Digital Input*
DI Slot X1 . **DI 2**	*Signal: Digital Input*
DI Slot X1 . **DI 3**	*Signal: Digital Input*
DI Slot X1 . **DI 4**	*Signal: Digital Input*
DI Slot X1 . **DI 5**	*Signal: Digital Input*
DI Slot X1 . **DI 6**	*Signal: Digital Input*
DI Slot X1 . **DI 7**	*Signal: Digital Input*
DI Slot X1 . **DI 8**	*Signal: Digital Input*
DI Slot X6 . **DI 1**	*Signal: Digital Input*

1..n, In-SyncList	Description
DI Slot X6 . **DI 2**	*Signal: Digital Input*
DI Slot X6 . **DI 3**	*Signal: Digital Input*
DI Slot X6 . **DI 4**	*Signal: Digital Input*
DI Slot X6 . **DI 5**	*Signal: Digital Input*
DI Slot X6 . **DI 6**	*Signal: Digital Input*
DI Slot X6 . **DI 7**	*Signal: Digital Input*
DI Slot X6 . **DI 8**	*Signal: Digital Input*
Logics . **LE1.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE1.Timer Out**	*Signal: Timer Output*
Logics . **LE1.Out**	*Signal: Latched Output (Q)*
Logics . **LE1.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE2.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE2.Timer Out**	*Signal: Timer Output*
Logics . **LE2.Out**	*Signal: Latched Output (Q)*
Logics . **LE2.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE3.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE3.Timer Out**	*Signal: Timer Output*
Logics . **LE3.Out**	*Signal: Latched Output (Q)*
Logics . **LE3.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE4.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE4.Timer Out**	*Signal: Timer Output*
Logics . **LE4.Out**	*Signal: Latched Output (Q)*
Logics . **LE4.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE5.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE5.Timer Out**	*Signal: Timer Output*
Logics . **LE5.Out**	*Signal: Latched Output (Q)*
Logics . **LE5.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE6.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE6.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE6.Out**	*Signal: Latched Output (Q)*
Logics . **LE6.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE7.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE7.Timer Out**	*Signal: Timer Output*
Logics . **LE7.Out**	*Signal: Latched Output (Q)*
Logics . **LE7.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE8.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE8.Timer Out**	*Signal: Timer Output*
Logics . **LE8.Out**	*Signal: Latched Output (Q)*
Logics . **LE8.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE9.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE9.Timer Out**	*Signal: Timer Output*
Logics . **LE9.Out**	*Signal: Latched Output (Q)*
Logics . **LE9.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE10.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE10.Timer Out**	*Signal: Timer Output*
Logics . **LE10.Out**	*Signal: Latched Output (Q)*
Logics . **LE10.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE11.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE11.Timer Out**	*Signal: Timer Output*
Logics . **LE11.Out**	*Signal: Latched Output (Q)*
Logics . **LE11.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE12.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE12.Timer Out**	*Signal: Timer Output*
Logics . **LE12.Out**	*Signal: Latched Output (Q)*
Logics . **LE12.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE13.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE13.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE13.Out**	*Signal: Latched Output (Q)*
Logics . **LE13.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE14.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE14.Timer Out**	*Signal: Timer Output*
Logics . **LE14.Out**	*Signal: Latched Output (Q)*
Logics . **LE14.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE15.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE15.Timer Out**	*Signal: Timer Output*
Logics . **LE15.Out**	*Signal: Latched Output (Q)*
Logics . **LE15.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE16.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE16.Timer Out**	*Signal: Timer Output*
Logics . **LE16.Out**	*Signal: Latched Output (Q)*
Logics . **LE16.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE17.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE17.Timer Out**	*Signal: Timer Output*
Logics . **LE17.Out**	*Signal: Latched Output (Q)*
Logics . **LE17.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE18.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE18.Timer Out**	*Signal: Timer Output*
Logics . **LE18.Out**	*Signal: Latched Output (Q)*
Logics . **LE18.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE19.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE19.Timer Out**	*Signal: Timer Output*
Logics . **LE19.Out**	*Signal: Latched Output (Q)*
Logics . **LE19.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE20.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE20.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE20.Out**	*Signal: Latched Output (Q)*
Logics . **LE20.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE21.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE21.Timer Out**	*Signal: Timer Output*
Logics . **LE21.Out**	*Signal: Latched Output (Q)*
Logics . **LE21.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE22.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE22.Timer Out**	*Signal: Timer Output*
Logics . **LE22.Out**	*Signal: Latched Output (Q)*
Logics . **LE22.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE23.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE23.Timer Out**	*Signal: Timer Output*
Logics . **LE23.Out**	*Signal: Latched Output (Q)*
Logics . **LE23.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE24.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE24.Timer Out**	*Signal: Timer Output*
Logics . **LE24.Out**	*Signal: Latched Output (Q)*
Logics . **LE24.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE25.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE25.Timer Out**	*Signal: Timer Output*
Logics . **LE25.Out**	*Signal: Latched Output (Q)*
Logics . **LE25.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE26.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE26.Timer Out**	*Signal: Timer Output*
Logics . **LE26.Out**	*Signal: Latched Output (Q)*
Logics . **LE26.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE27.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE27.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE27.Out**	*Signal: Latched Output (Q)*
Logics . **LE27.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE28.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE28.Timer Out**	*Signal: Timer Output*
Logics . **LE28.Out**	*Signal: Latched Output (Q)*
Logics . **LE28.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE29.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE29.Timer Out**	*Signal: Timer Output*
Logics . **LE29.Out**	*Signal: Latched Output (Q)*
Logics . **LE29.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE30.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE30.Timer Out**	*Signal: Timer Output*
Logics . **LE30.Out**	*Signal: Latched Output (Q)*
Logics . **LE30.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE31.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE31.Timer Out**	*Signal: Timer Output*
Logics . **LE31.Out**	*Signal: Latched Output (Q)*
Logics . **LE31.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE32.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE32.Timer Out**	*Signal: Timer Output*
Logics . **LE32.Out**	*Signal: Latched Output (Q)*
Logics . **LE32.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE33.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE33.Timer Out**	*Signal: Timer Output*
Logics . **LE33.Out**	*Signal: Latched Output (Q)*
Logics . **LE33.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE34.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE34.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE34.Out**	*Signal: Latched Output (Q)*
Logics . **LE34.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE35.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE35.Timer Out**	*Signal: Timer Output*
Logics . **LE35.Out**	*Signal: Latched Output (Q)*
Logics . **LE35.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE36.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE36.Timer Out**	*Signal: Timer Output*
Logics . **LE36.Out**	*Signal: Latched Output (Q)*
Logics . **LE36.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE37.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE37.Timer Out**	*Signal: Timer Output*
Logics . **LE37.Out**	*Signal: Latched Output (Q)*
Logics . **LE37.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE38.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE38.Timer Out**	*Signal: Timer Output*
Logics . **LE38.Out**	*Signal: Latched Output (Q)*
Logics . **LE38.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE39.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE39.Timer Out**	*Signal: Timer Output*
Logics . **LE39.Out**	*Signal: Latched Output (Q)*
Logics . **LE39.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE40.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE40.Timer Out**	*Signal: Timer Output*
Logics . **LE40.Out**	*Signal: Latched Output (Q)*
Logics . **LE40.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE41.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE41.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE41.Out**	*Signal: Latched Output (Q)*
Logics . **LE41.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE42.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE42.Timer Out**	*Signal: Timer Output*
Logics . **LE42.Out**	*Signal: Latched Output (Q)*
Logics . **LE42.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE43.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE43.Timer Out**	*Signal: Timer Output*
Logics . **LE43.Out**	*Signal: Latched Output (Q)*
Logics . **LE43.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE44.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE44.Timer Out**	*Signal: Timer Output*
Logics . **LE44.Out**	*Signal: Latched Output (Q)*
Logics . **LE44.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE45.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE45.Timer Out**	*Signal: Timer Output*
Logics . **LE45.Out**	*Signal: Latched Output (Q)*
Logics . **LE45.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE46.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE46.Timer Out**	*Signal: Timer Output*
Logics . **LE46.Out**	*Signal: Latched Output (Q)*
Logics . **LE46.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE47.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE47.Timer Out**	*Signal: Timer Output*
Logics . **LE47.Out**	*Signal: Latched Output (Q)*
Logics . **LE47.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE48.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE48.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE48.Out**	*Signal: Latched Output (Q)*
Logics . **LE48.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE49.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE49.Timer Out**	*Signal: Timer Output*
Logics . **LE49.Out**	*Signal: Latched Output (Q)*
Logics . **LE49.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE50.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE50.Timer Out**	*Signal: Timer Output*
Logics . **LE50.Out**	*Signal: Latched Output (Q)*
Logics . **LE50.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE51.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE51.Timer Out**	*Signal: Timer Output*
Logics . **LE51.Out**	*Signal: Latched Output (Q)*
Logics . **LE51.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE52.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE52.Timer Out**	*Signal: Timer Output*
Logics . **LE52.Out**	*Signal: Latched Output (Q)*
Logics . **LE52.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE53.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE53.Timer Out**	*Signal: Timer Output*
Logics . **LE53.Out**	*Signal: Latched Output (Q)*
Logics . **LE53.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE54.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE54.Timer Out**	*Signal: Timer Output*
Logics . **LE54.Out**	*Signal: Latched Output (Q)*
Logics . **LE54.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE55.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE55.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE55.Out**	*Signal: Latched Output (Q)*
Logics . **LE55.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE56.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE56.Timer Out**	*Signal: Timer Output*
Logics . **LE56.Out**	*Signal: Latched Output (Q)*
Logics . **LE56.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE57.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE57.Timer Out**	*Signal: Timer Output*
Logics . **LE57.Out**	*Signal: Latched Output (Q)*
Logics . **LE57.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE58.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE58.Timer Out**	*Signal: Timer Output*
Logics . **LE58.Out**	*Signal: Latched Output (Q)*
Logics . **LE58.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE59.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE59.Timer Out**	*Signal: Timer Output*
Logics . **LE59.Out**	*Signal: Latched Output (Q)*
Logics . **LE59.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE60.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE60.Timer Out**	*Signal: Timer Output*
Logics . **LE60.Out**	*Signal: Latched Output (Q)*
Logics . **LE60.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE61.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE61.Timer Out**	*Signal: Timer Output*
Logics . **LE61.Out**	*Signal: Latched Output (Q)*
Logics . **LE61.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE62.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE62.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE62.Out**	*Signal: Latched Output (Q)*
Logics . **LE62.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE63.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE63.Timer Out**	*Signal: Timer Output*
Logics . **LE63.Out**	*Signal: Latched Output (Q)*
Logics . **LE63.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE64.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE64.Timer Out**	*Signal: Timer Output*
Logics . **LE64.Out**	*Signal: Latched Output (Q)*
Logics . **LE64.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE65.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE65.Timer Out**	*Signal: Timer Output*
Logics . **LE65.Out**	*Signal: Latched Output (Q)*
Logics . **LE65.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE66.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE66.Timer Out**	*Signal: Timer Output*
Logics . **LE66.Out**	*Signal: Latched Output (Q)*
Logics . **LE66.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE67.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE67.Timer Out**	*Signal: Timer Output*
Logics . **LE67.Out**	*Signal: Latched Output (Q)*
Logics . **LE67.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE68.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE68.Timer Out**	*Signal: Timer Output*
Logics . **LE68.Out**	*Signal: Latched Output (Q)*
Logics . **LE68.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE69.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE69.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE69.Out**	*Signal: Latched Output (Q)*
Logics . **LE69.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE70.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE70.Timer Out**	*Signal: Timer Output*
Logics . **LE70.Out**	*Signal: Latched Output (Q)*
Logics . **LE70.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE71.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE71.Timer Out**	*Signal: Timer Output*
Logics . **LE71.Out**	*Signal: Latched Output (Q)*
Logics . **LE71.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE72.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE72.Timer Out**	*Signal: Timer Output*
Logics . **LE72.Out**	*Signal: Latched Output (Q)*
Logics . **LE72.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE73.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE73.Timer Out**	*Signal: Timer Output*
Logics . **LE73.Out**	*Signal: Latched Output (Q)*
Logics . **LE73.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE74.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE74.Timer Out**	*Signal: Timer Output*
Logics . **LE74.Out**	*Signal: Latched Output (Q)*
Logics . **LE74.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE75.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE75.Timer Out**	*Signal: Timer Output*
Logics . **LE75.Out**	*Signal: Latched Output (Q)*
Logics . **LE75.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE76.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE76.Timer Out**	*Signal: Timer Output*

1..n, In-SyncList	Description
Logics . **LE76.Out**	*Signal: Latched Output (Q)*
Logics . **LE76.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE77.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE77.Timer Out**	*Signal: Timer Output*
Logics . **LE77.Out**	*Signal: Latched Output (Q)*
Logics . **LE77.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE78.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE78.Timer Out**	*Signal: Timer Output*
Logics . **LE78.Out**	*Signal: Latched Output (Q)*
Logics . **LE78.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE79.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE79.Timer Out**	*Signal: Timer Output*
Logics . **LE79.Out**	*Signal: Latched Output (Q)*
Logics . **LE79.Out inverted**	*Signal: Negated Latched Output (Q NOT)*
Logics . **LE80.Gate Out**	*Signal: Output of the logic gate*
Logics . **LE80.Timer Out**	*Signal: Timer Output*
Logics . **LE80.Out**	*Signal: Latched Output (Q)*
Logics . **LE80.Out inverted**	*Signal: Negated Latched Output (Q NOT)*

LE1.Gate

Logic gate

Selection list referenced by the following parameters:

- └⇨ Logics . LE1.Gate

LE1.Gate	Description
AND	*AND Gate*
OR	*OR Gate*
NAND	*NAND Gate*

LE1.Gate	Description
NOR	*NOR Gate*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ BO Slot X2 . Disarm Mode
- ╚═▷ BO Slot X2 . Force Mode

Mode	Description
permanent	*permanent*
timeout	*timeout*

active/inactive

Selection list referenced by the following parameters:

- ╚═▷ BO Slot X2 . DISARMED

active/inactive	Description
inactive	*inactive*
active	*active*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╚═▷ BO Slot X5 . Disarm Mode
- ╚═▷ BO Slot X5 . Force Mode

Mode	Description
permanent	*permanent*

Mode	Description
timeout	*timeout*

active/inactive

Selection list referenced by the following parameters:

- ⮡ BO Slot X5 . DISARMED

active/inactive	Description
inactive	*inactive*
active	*active*

Relay operating modes

Selection list referenced by the following parameters:

- ⮡ BO Slot X2 . Force all Outs
- ⮡ BO Slot X2 . Force OR1

Relay operating modes	Description
Normal	*Normal*
De-Energized	*De-Energized*
Energized	*Energized*

Relay operating modes

Selection list referenced by the following parameters:

- ⮡ BO Slot X5 . Force all Outs
- ⮡ BO Slot X5 . Force OR1

Relay operating modes	Description
Normal	*Normal*
De-Energized	*De-Energized*

Relay operating modes	Description
Energized	*Energized*

Mode

general operation mode

Selection list referenced by the following parameters:

- ╘═▷ URTD . Force Mode

Mode	Description
permanent	*permanent*
timeout	*timeout*

active/inactive

Selection list referenced by the following parameters:

- ╘═▷ URTD . Function

active/inactive	Description
inactive	*inactive*
active	*active*

State

Selection list referenced by the following parameters:

- ╘═▷ Sgen . State

State	Description
Off	*Off*
PreFault	*Pre Fault Duration*
FaultSimulation	*Duration of Fault Simulation*
PostFault	*Post Fault Duration*

State	Description
Init Res	*Init Reset*

TripCmd Mode

Trip Command Mode: Select between two operating modes for the Fault Simulator: "cold simulation” (without tripping the circuit breaker), or "hot simulation” (i.e. the simulation is authorized to trip the circuit breaker)

Selection list referenced by the following parameters:

- ╚═▷ Sgen . TripCmd Mode

TripCmd Mode	Description
No TripCmd	*No Trip Command: The TripCmd of all protection functions is blocked. The protection function will possibly trip but not generate a TripCmd.*
With TripCmd	*With Trip Command: The trip of a protection function generates a TripCmd, that can open the circuit breaker.*

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Woodward Kempen GmbH
Krefelder Weg 47 • D–47906 Kempen (Germany)
Postfach 10 07 55 (P.O.Box) • D–47884 Kempen (Germany)
Telephone: : +49 (0) 21 52 145 1

Internet: — www.woodward.com

Sales
Telephone: : +49 (0) 21 52 145 331
Fax: : +49 (0) 21 52 145 354
Email: : SalesPGD_EMEA@woodward.com

Service
Telephone: : +49 (0) 21 52 145 600
Fax: : +49 (0) 21 52 145 455
Email: : SupportPGD_Europe@woodward.com

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